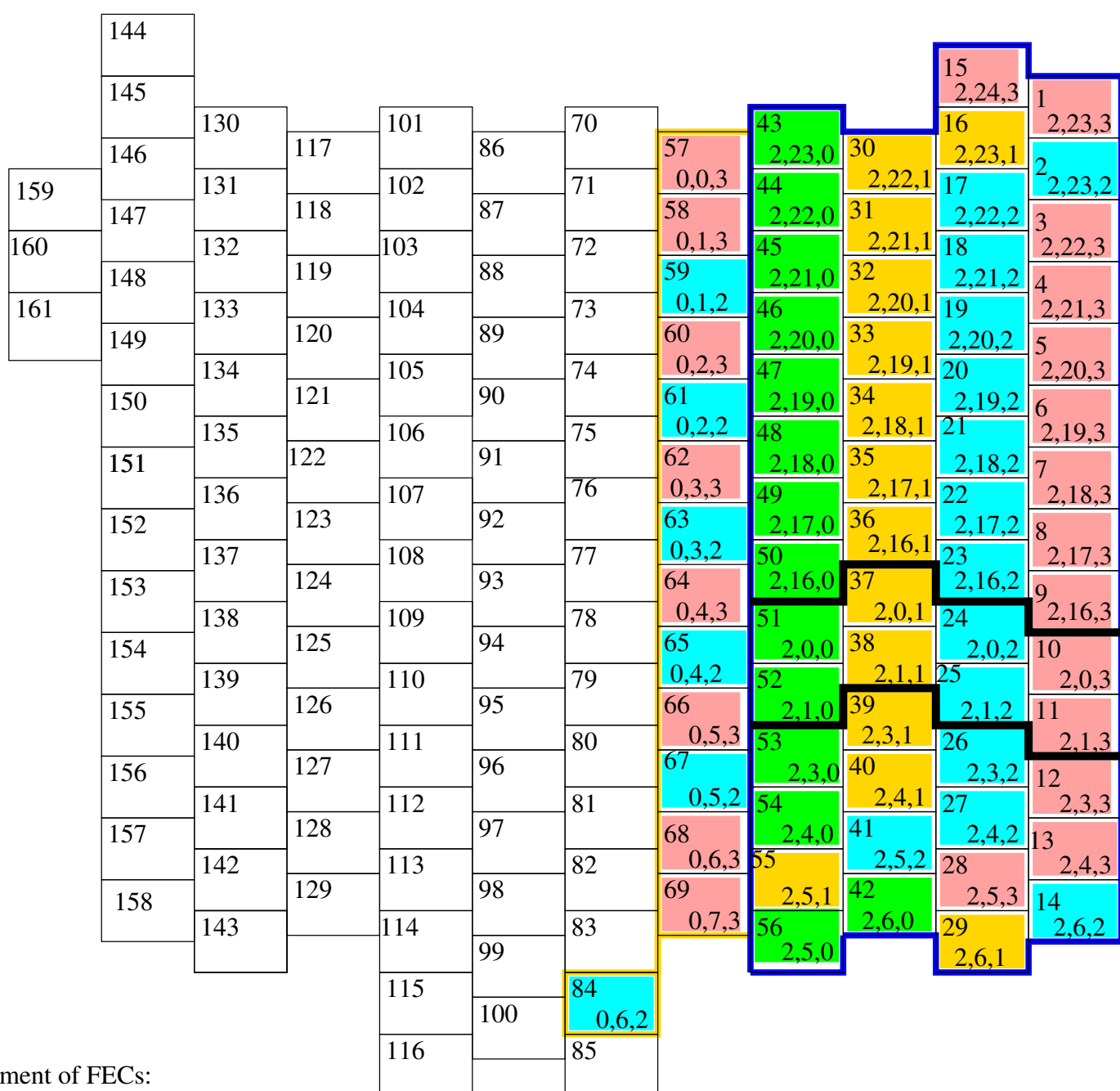
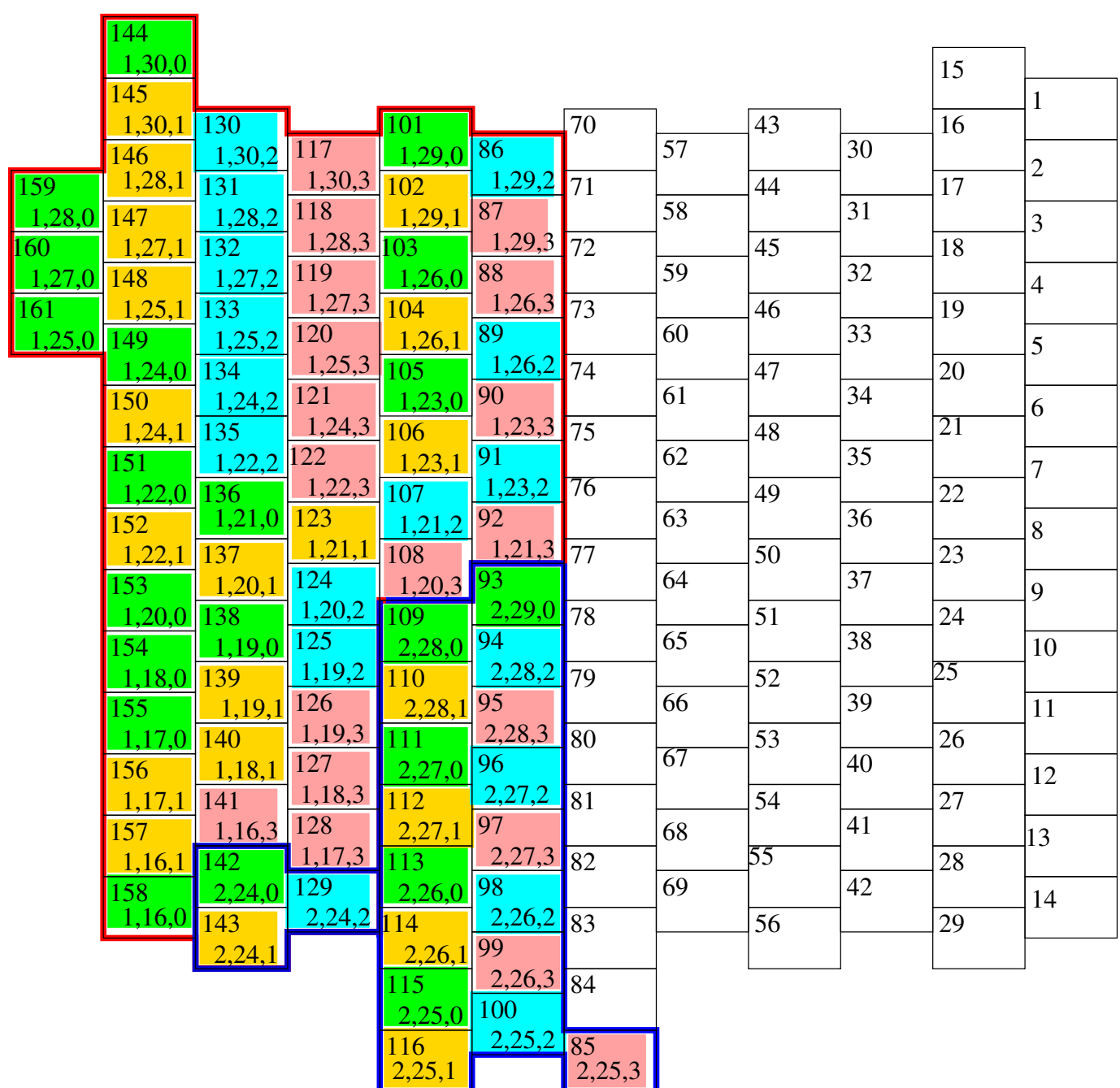
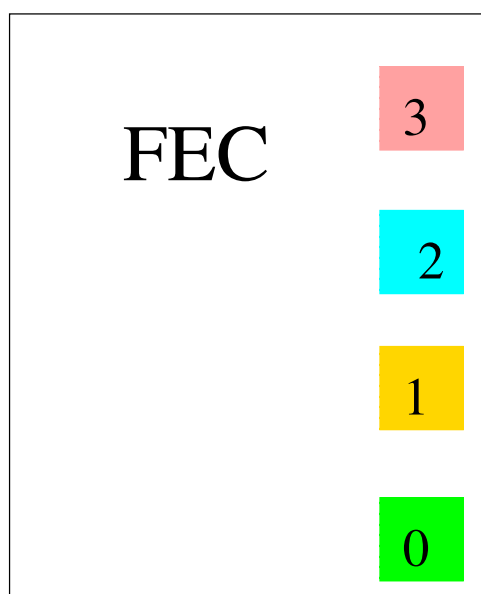
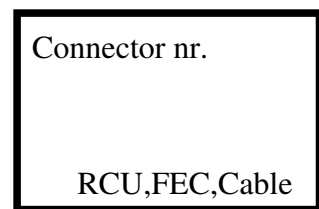
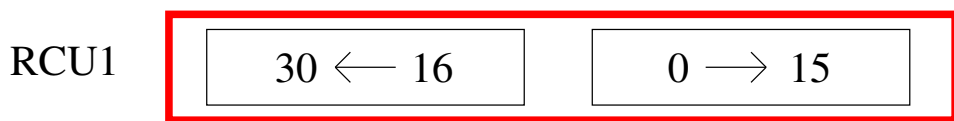
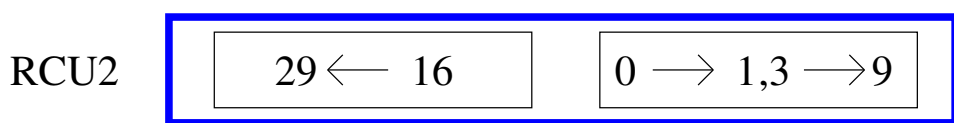




Numbering and placement of FECs:



Crude sketch of modules seen from outside of TPC:

