

20200802

The resistor values after the opamplifiers are measured to be about 25 ohm.

Measured the reference voltages after the apamplifier at the resistor.

The output from the DAC was measured at the capacitor at the input to the amplifier.

In these measurements the biascontrol was off, i.e. the ADC was switched off, and the preamplifier (PCA16) was shutdown. This was done to keep the temperature low.

At resistors

RefP	Resistor R41	Expected Level: 1.25V	page 4
InCM	Resistor R42	Expected Level: 0.75V	page 5
CmOut	Resistor R44	Expected Level: 0.75V	page 6
RefN:	Resistor R43	Expected Level: 0.25V	page 7

At capacitors

RefP	Capacitor C1	Expected Level: 1.25V	page 8
InCM	Capacitor C2	Expected Level: 0.75V	page 9
CmOut	Capacitor C79	Expected Level: 0.75V	page 10
RefN:	Capacitor C3	Expected Level: 0.25V	page 11

Some measurements were made with biasctrl on, i.e. ADC was switched on, increasing the activity on the mcm and the temperature.

At resistors

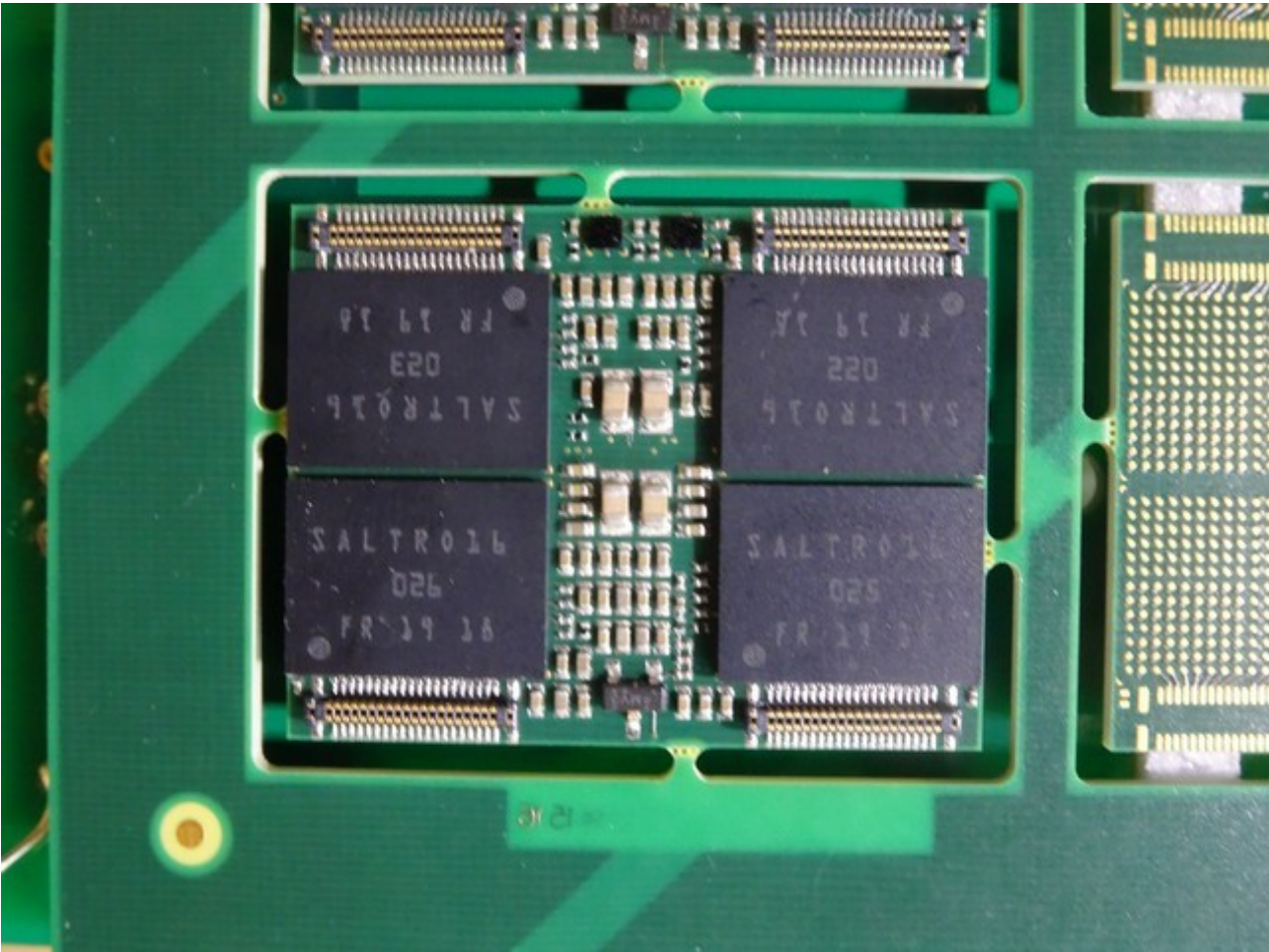
RefP	Resistor R41	Expected Level: 1.25V	page 12
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At capacitors

RefP	Capacitor C1	Expected Level: 1.25V	page 13
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The placements of the components can be found in page 2 (component placement) and page 3 (picture of mcm).

Picture of mcm



RefP

Resistor R41

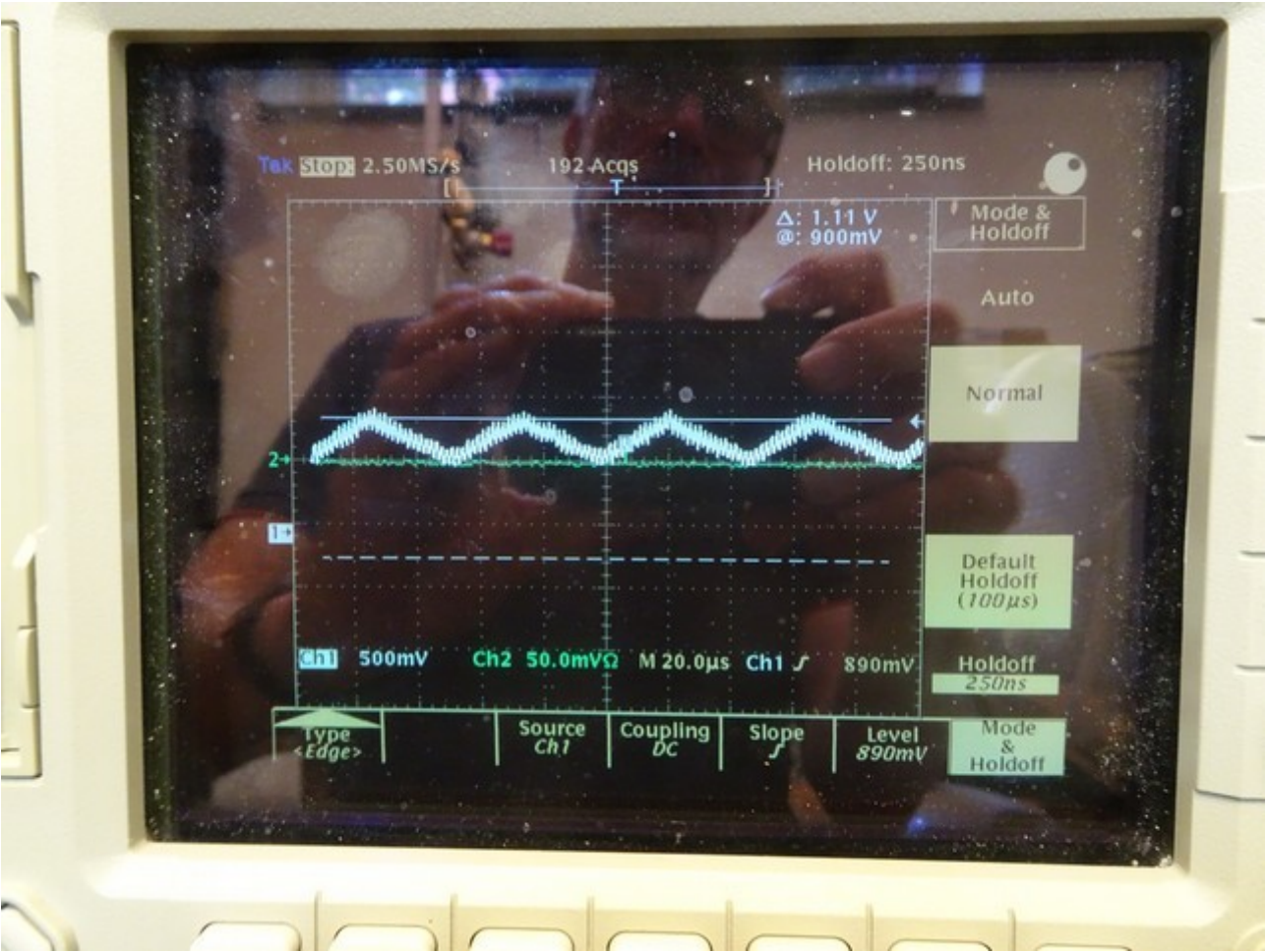
Expected Level: 1.25V



InCM

Resistor R42

Expected Level: 0.75V



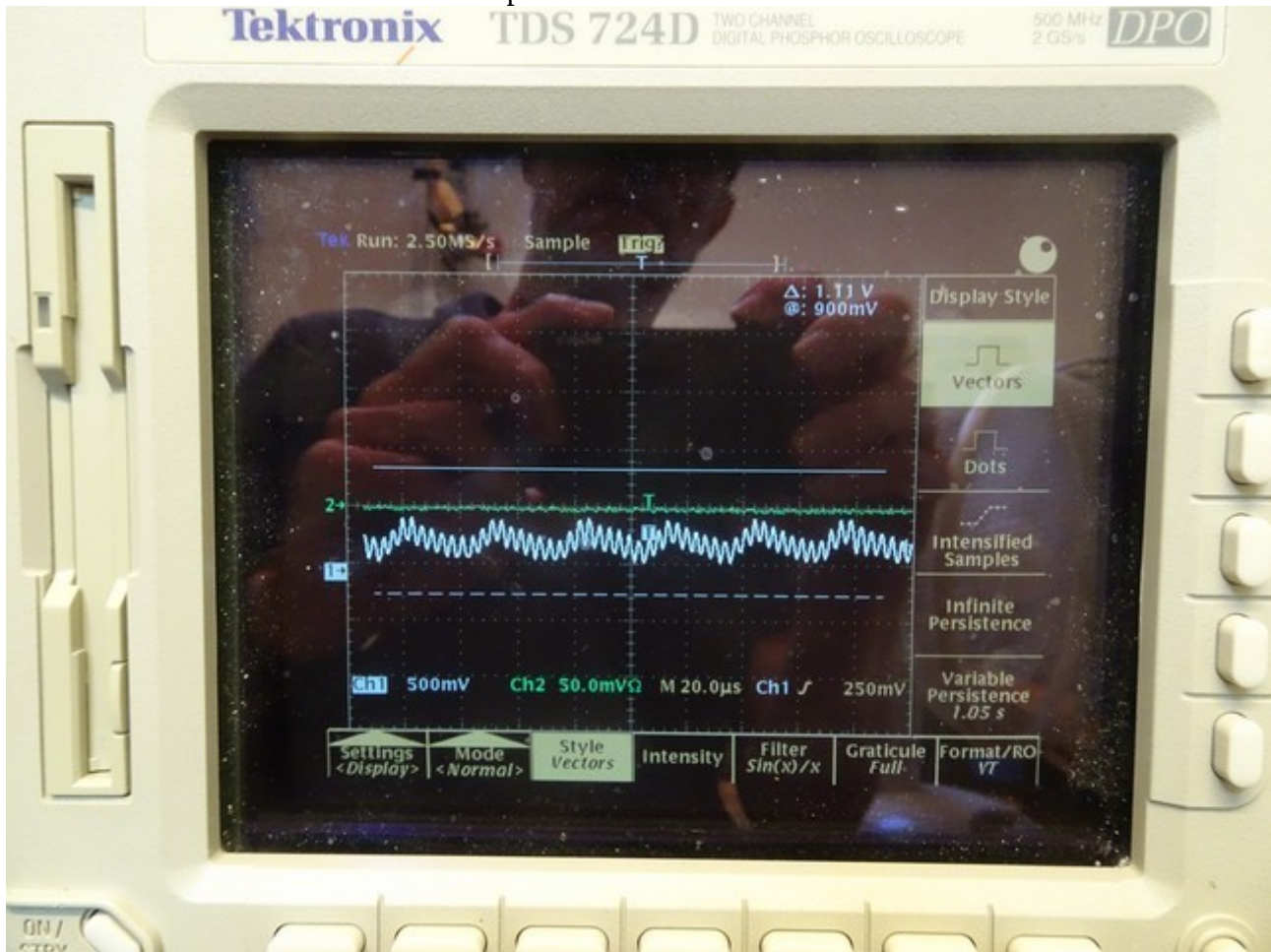
CmOut

Resistor R44

Expected Level: 0.75V



RefN: Resistor R43 Expected Level: 0.25V



RefP

Capacitor C1

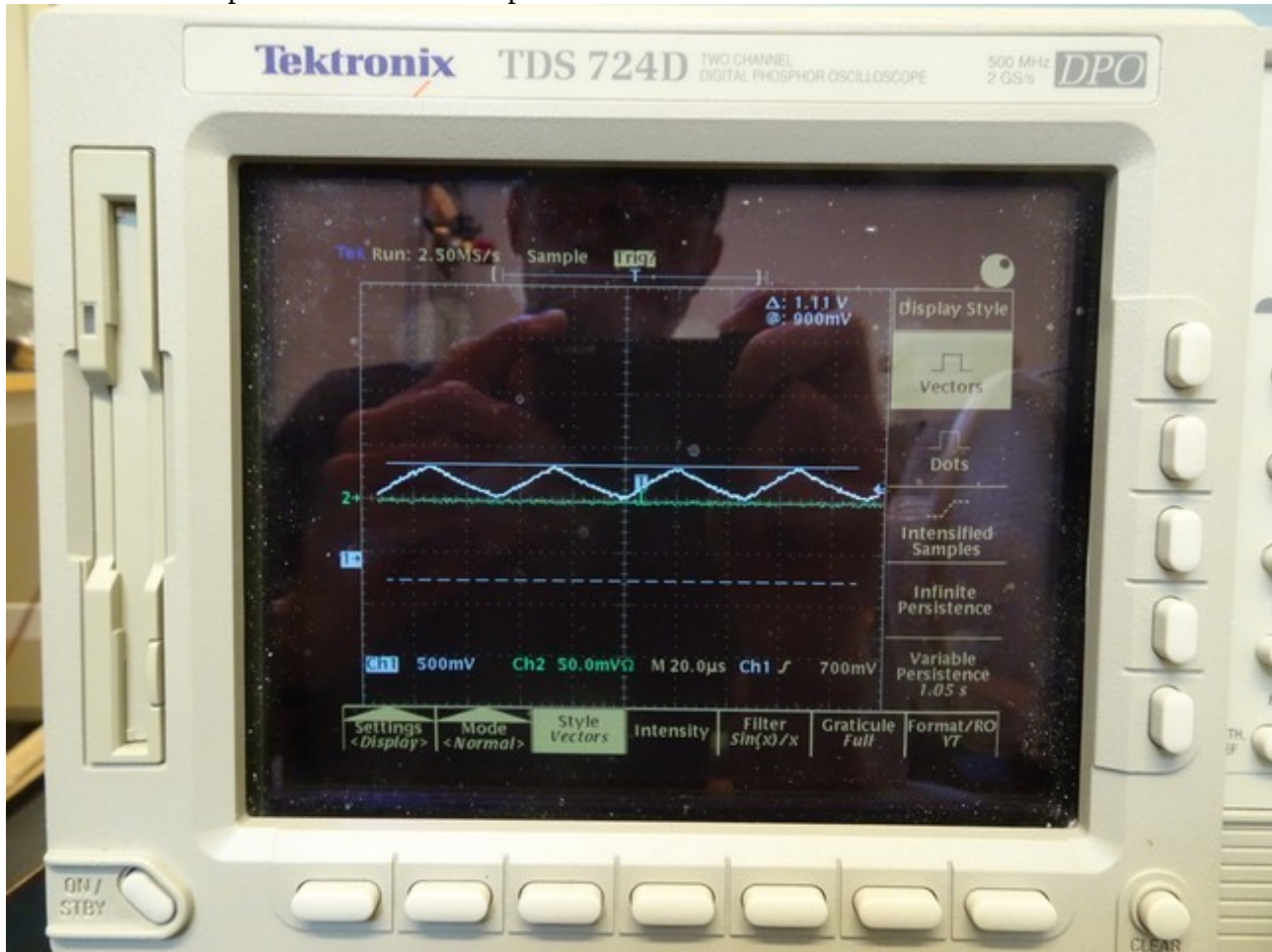
Expected Level: 1.25V



InCM

Capacitor C2

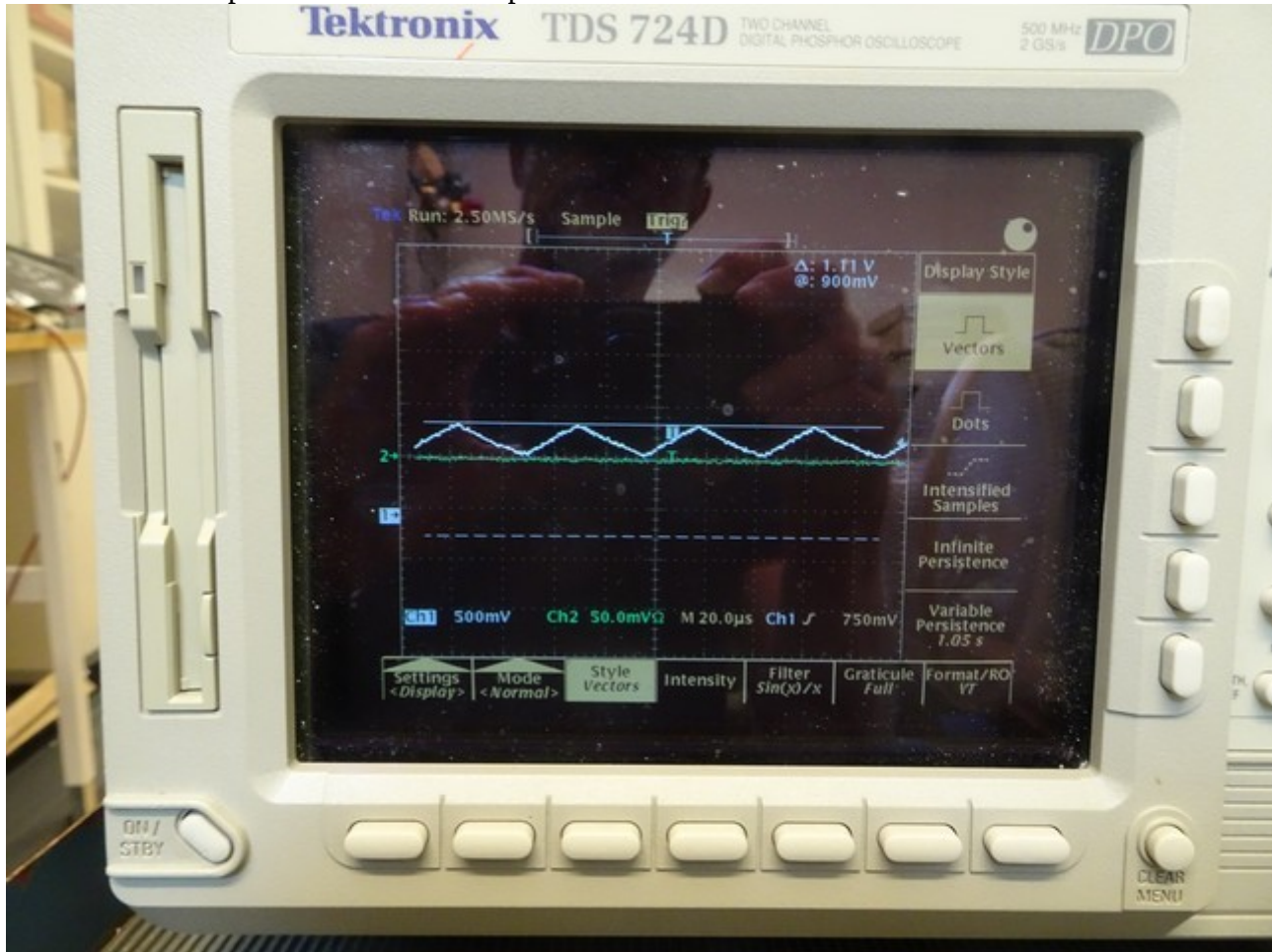
Expected Level: 0.75V



CmOut

Capacitor C79

Expected Level: 0.75V



RefN: Capacitor C3 Expected Level: 0.25V

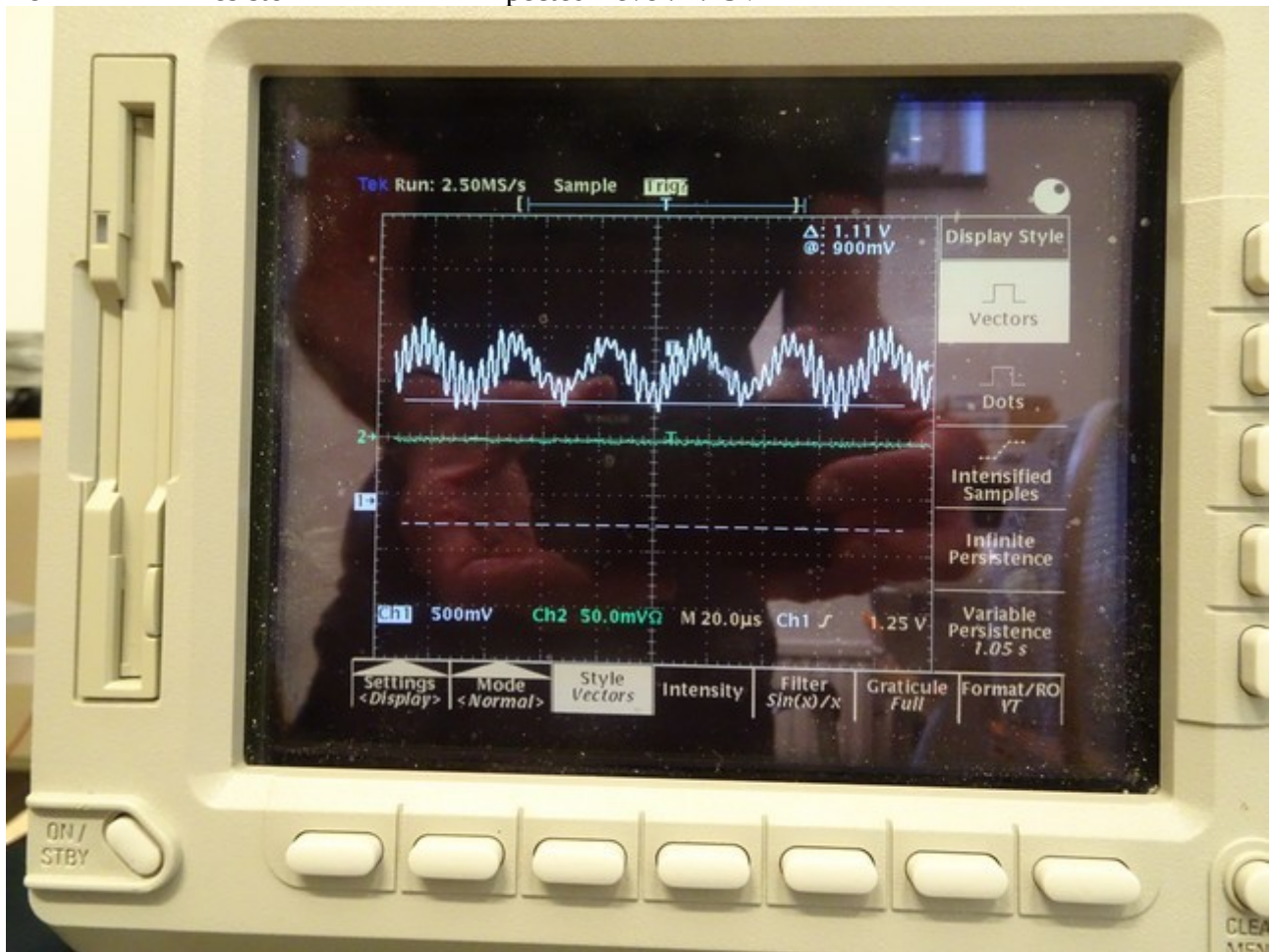


Bias control on

RefP

Resistor R41

Expected Level: 1.25V



At capacitor

RefP

Capacitor C1

Expected Level: 1.25V

