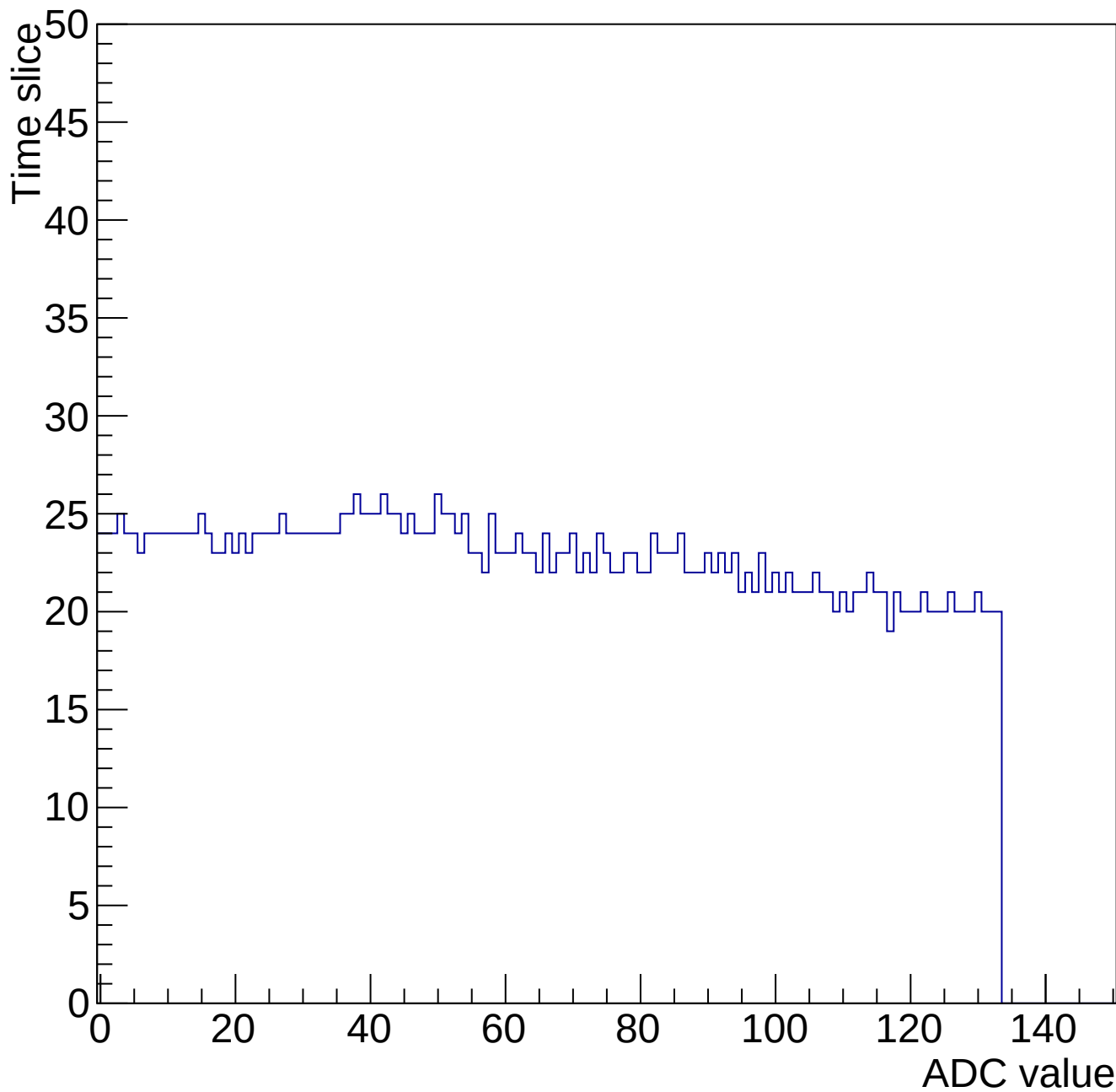
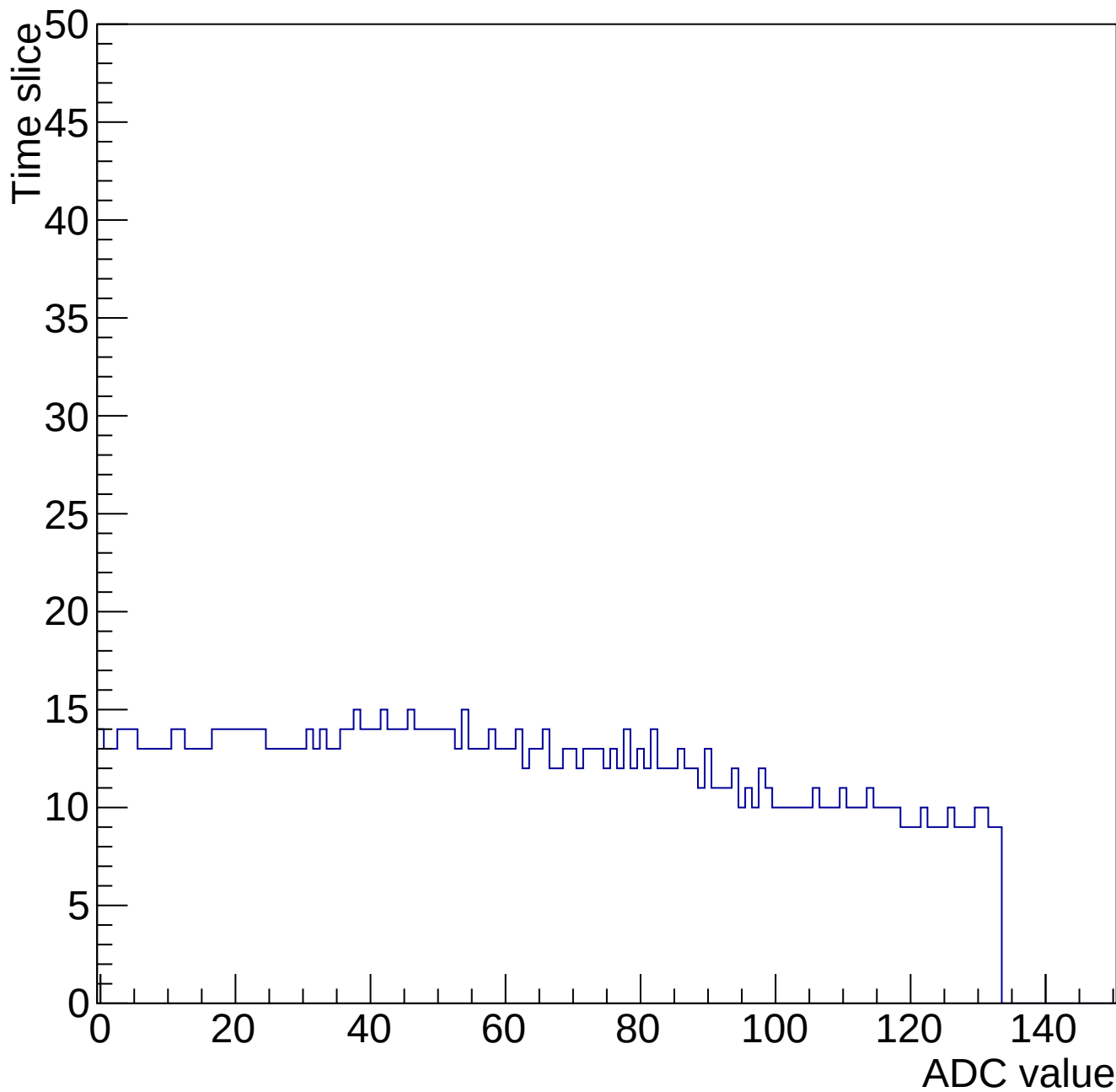


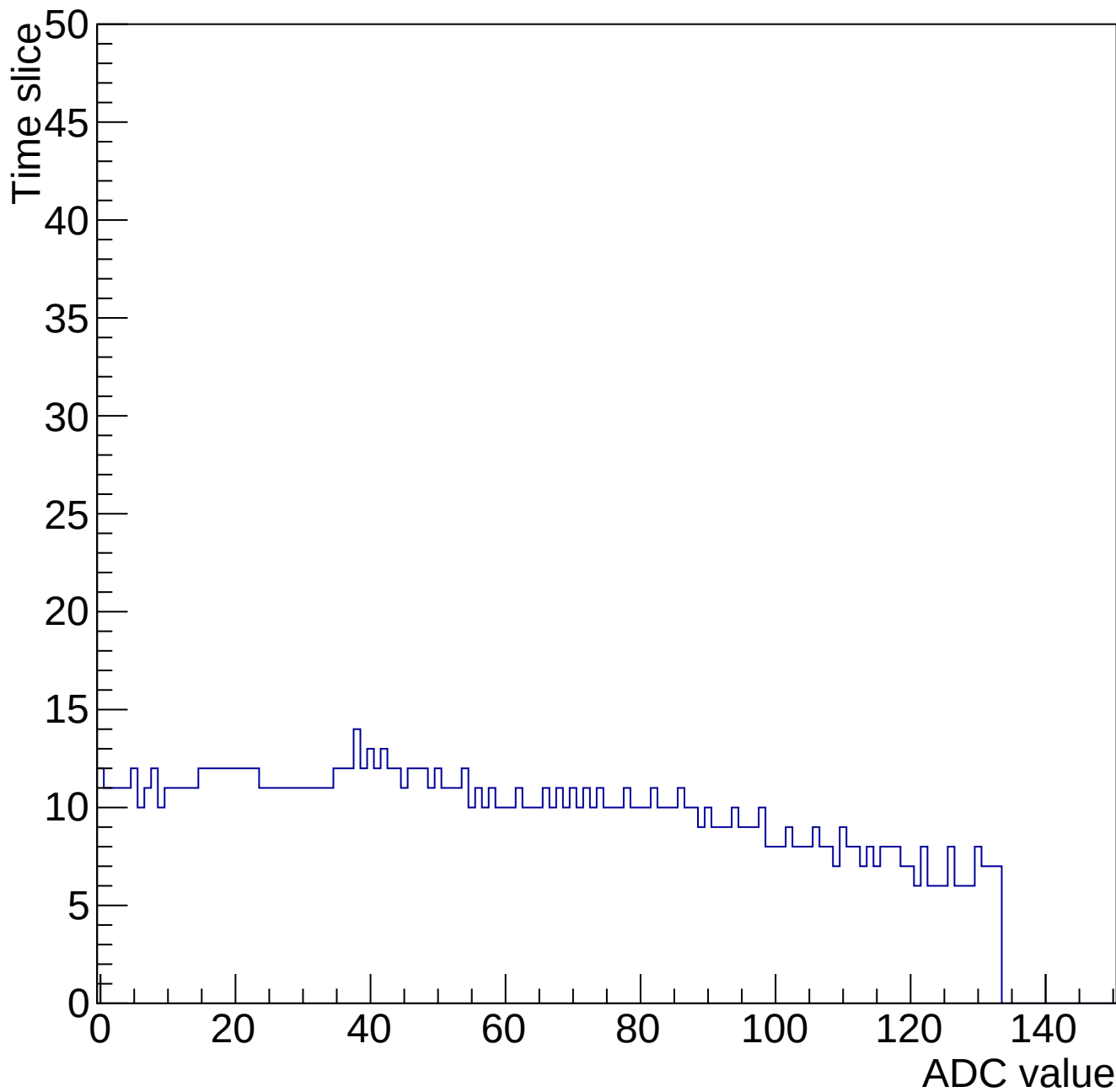
Evt 10 Time mcm 2001 chip 1 chan 0 chip1-nominal-34537



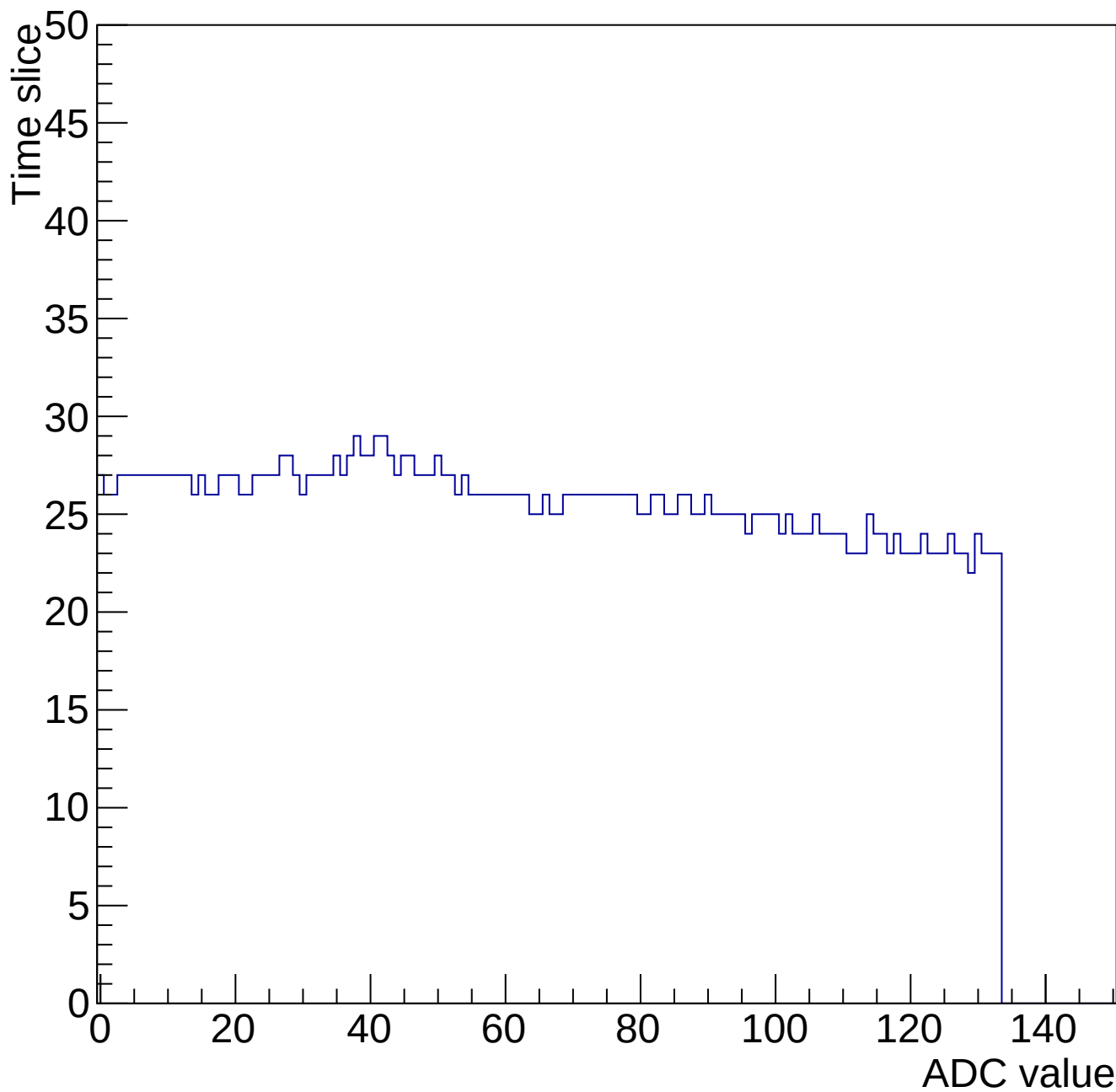
Evt 10 Time mcm 2001 chip 1 chan 1 chip1-nominal-34537



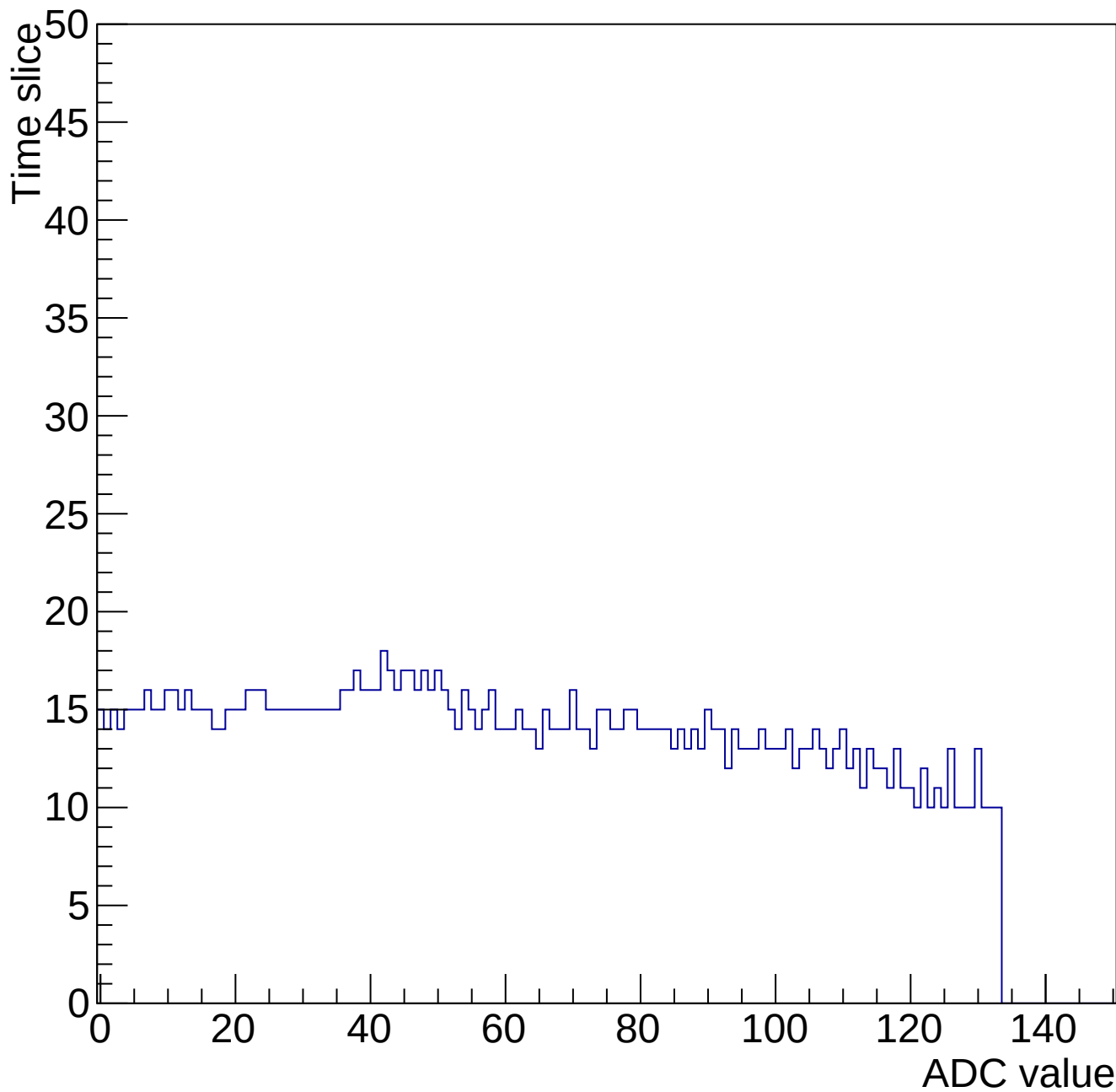
Evt 10 Time mcm 2001 chip 1 chan 2 chip1-nominal-34537



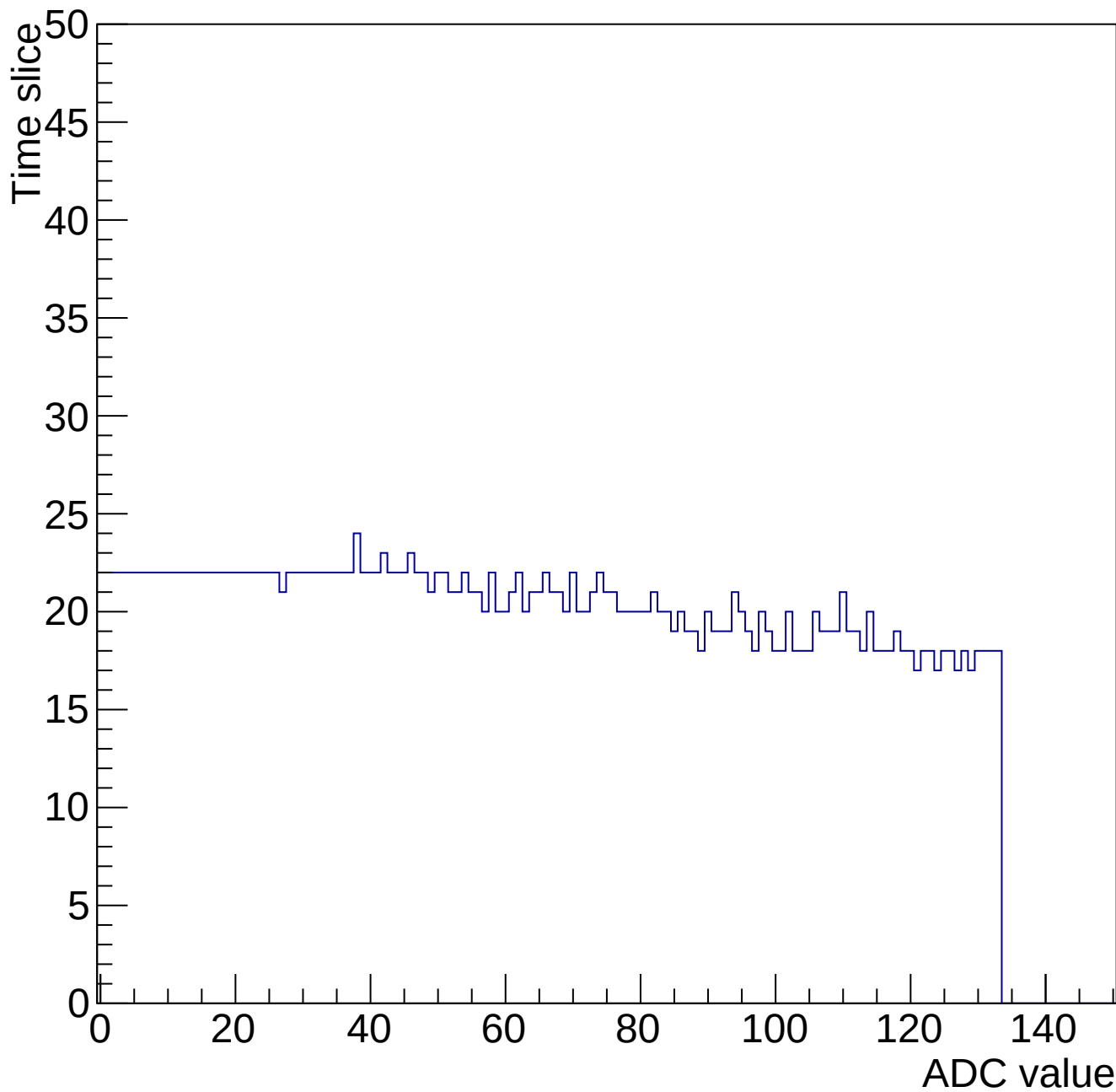
Evt 10 Time mcm 2001 chip 1 chan 3 chip1-nominal-34537



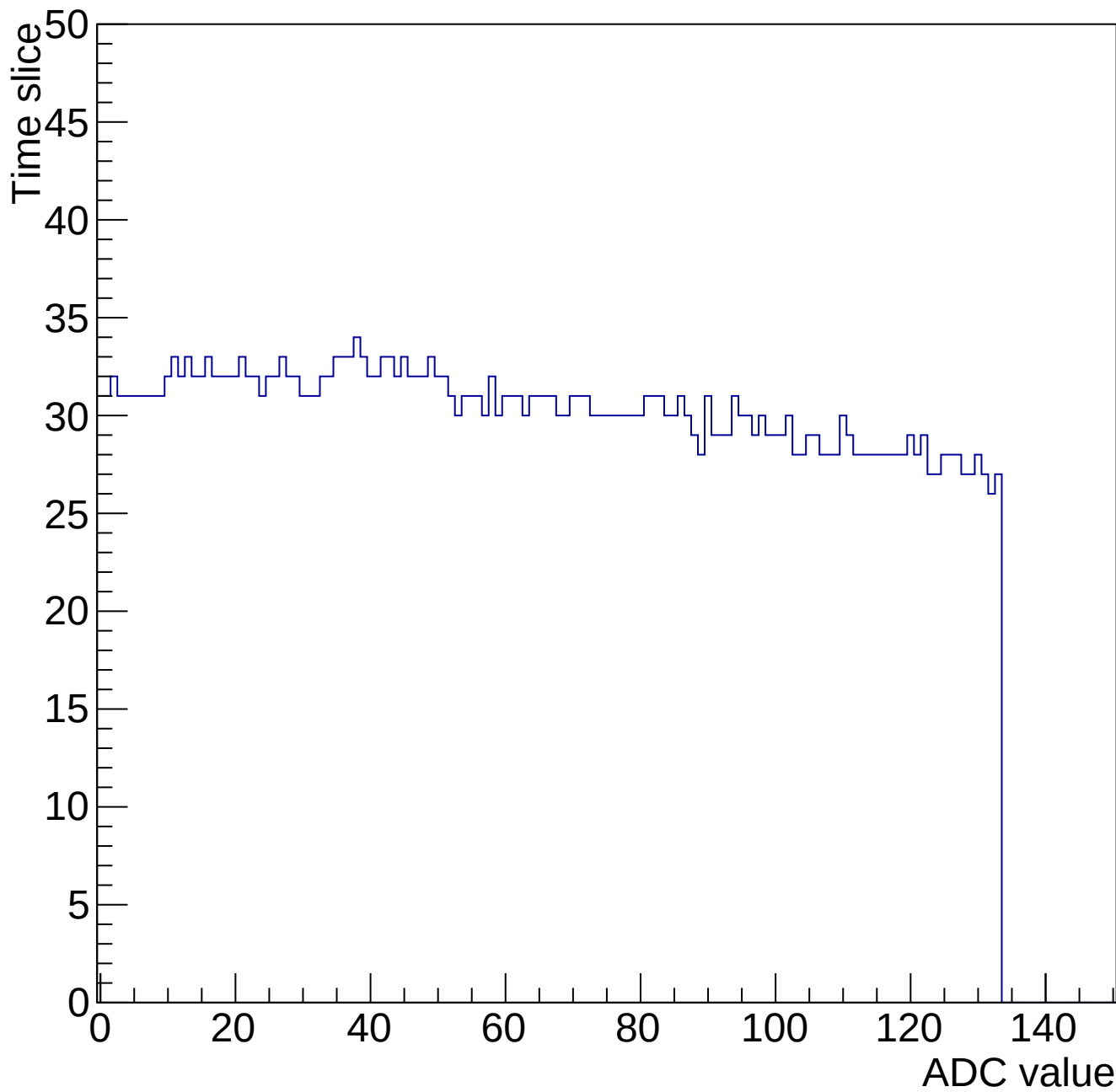
Evt 10 Time mcm 2001 chip 1 chan 4 chip1-nominal-34537



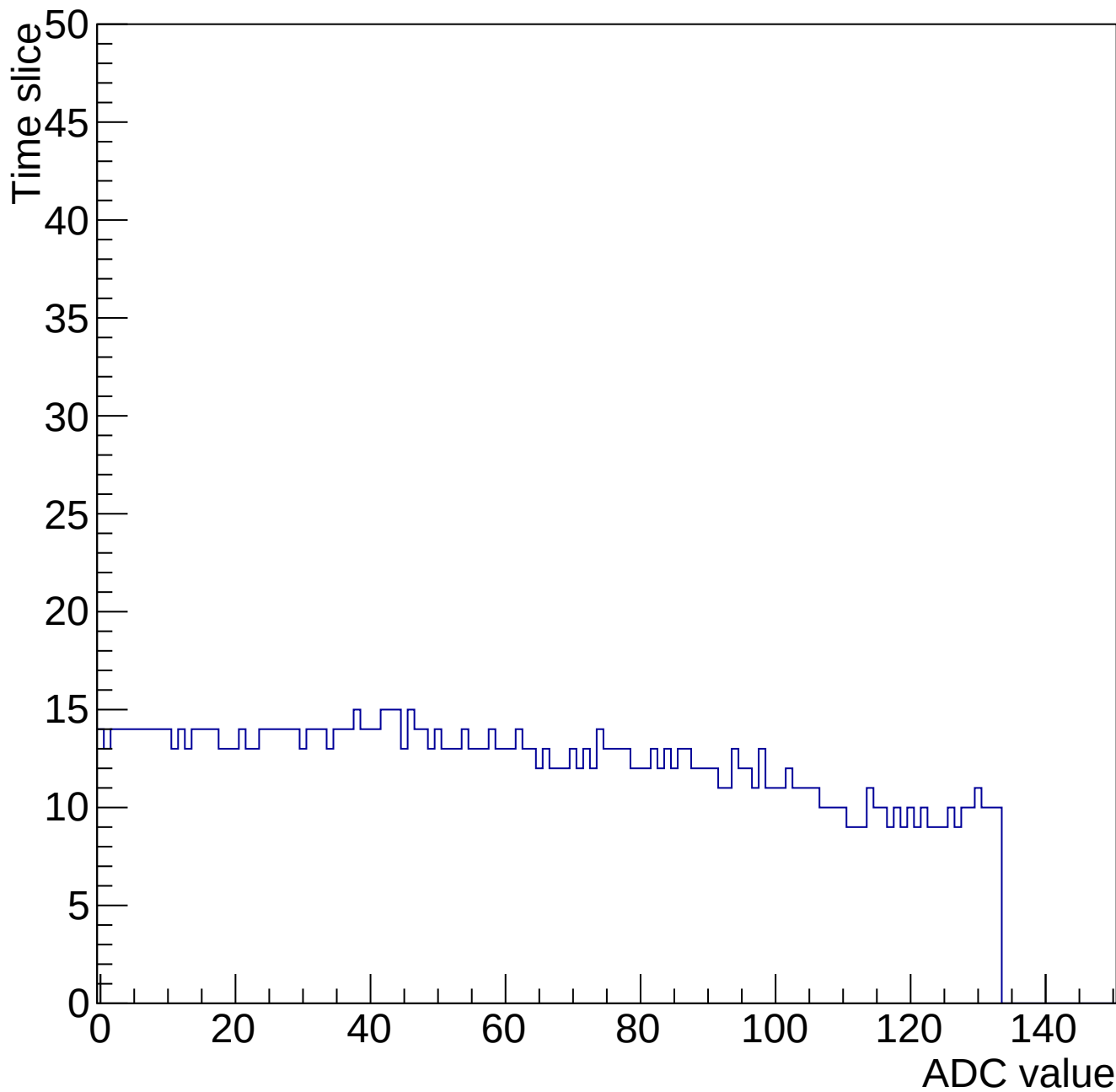
Evt 10 Time mcm 2001 chip 1 chan 5 chip1-nominal-34537



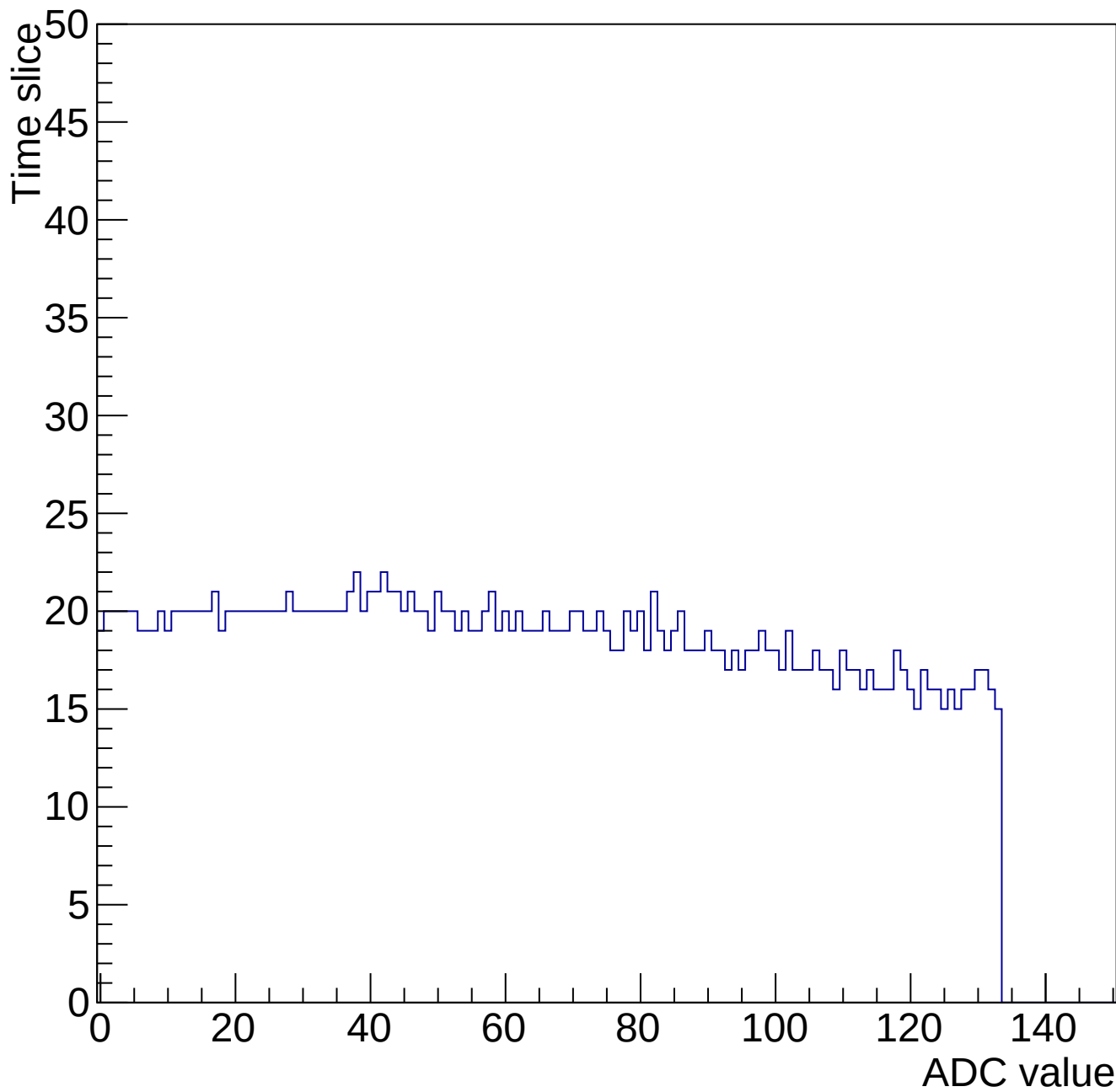
Evt 10 Time mcm 2001 chip 1 chan 6 chip1-nominal-34537



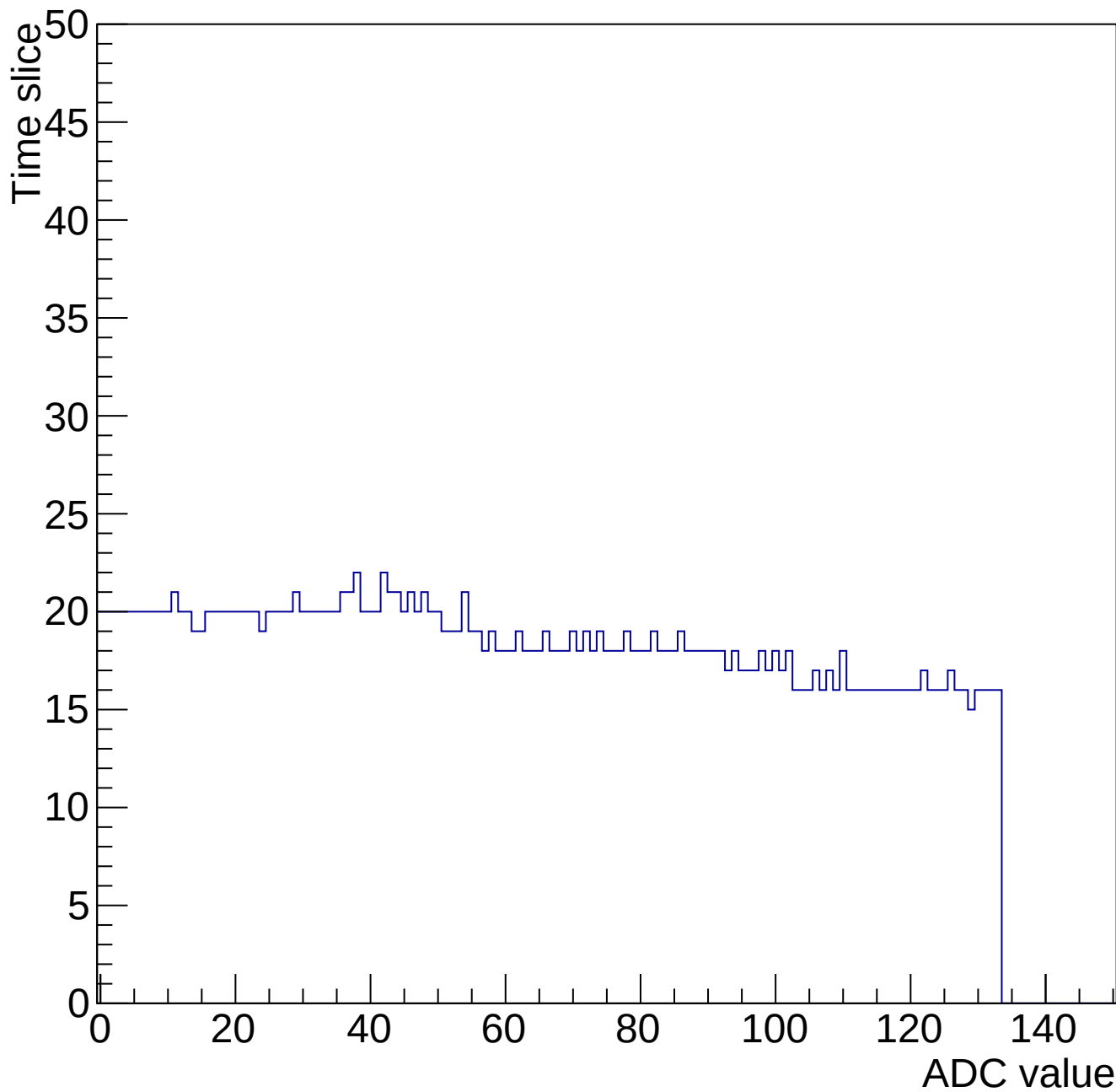
Evt 10 Time mcm 2001 chip 1 chan 7 chip1-nominal-34537



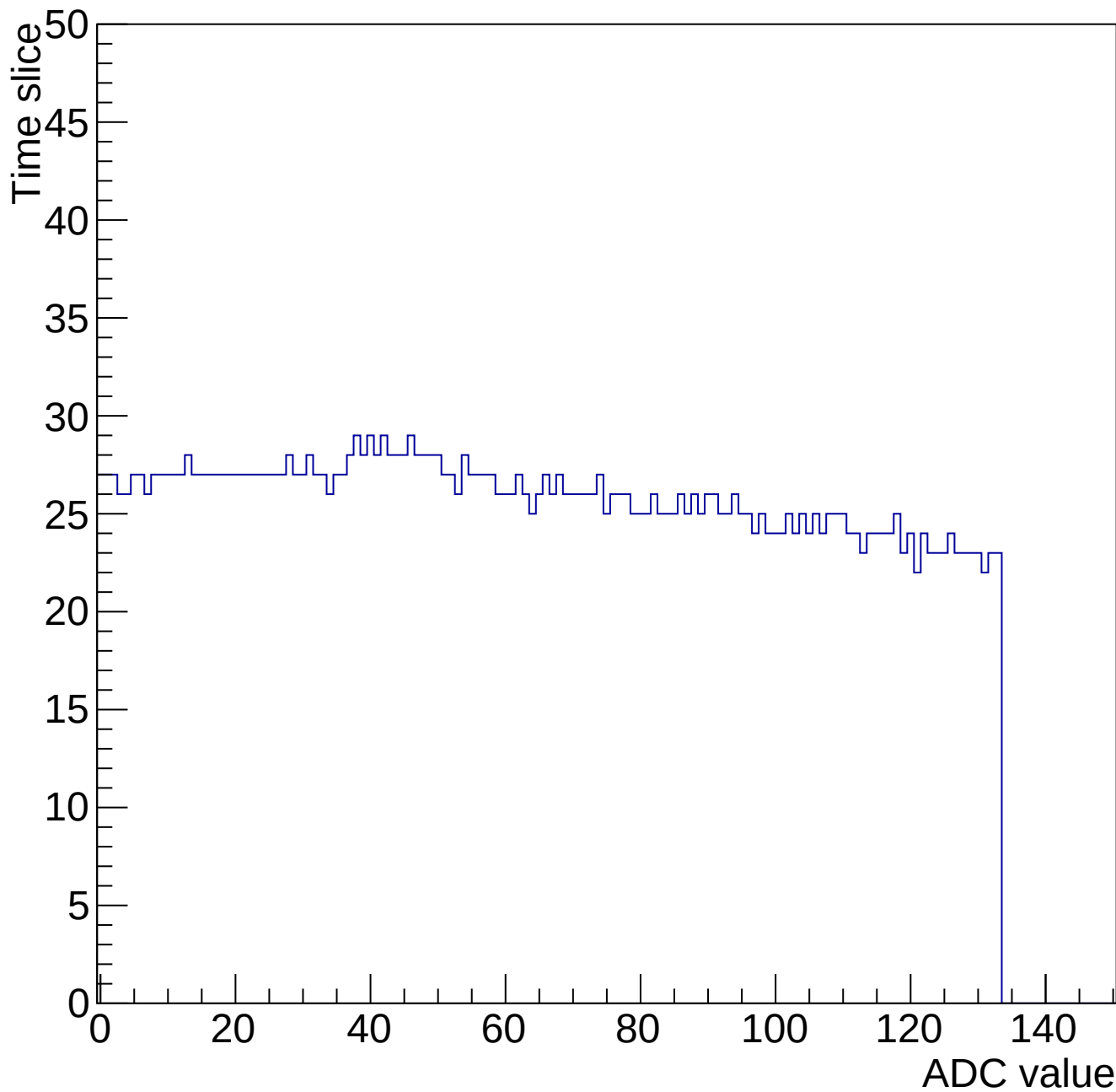
Evt 10 Time mcm 2001 chip 1 chan 8 chip1-nominal-34537



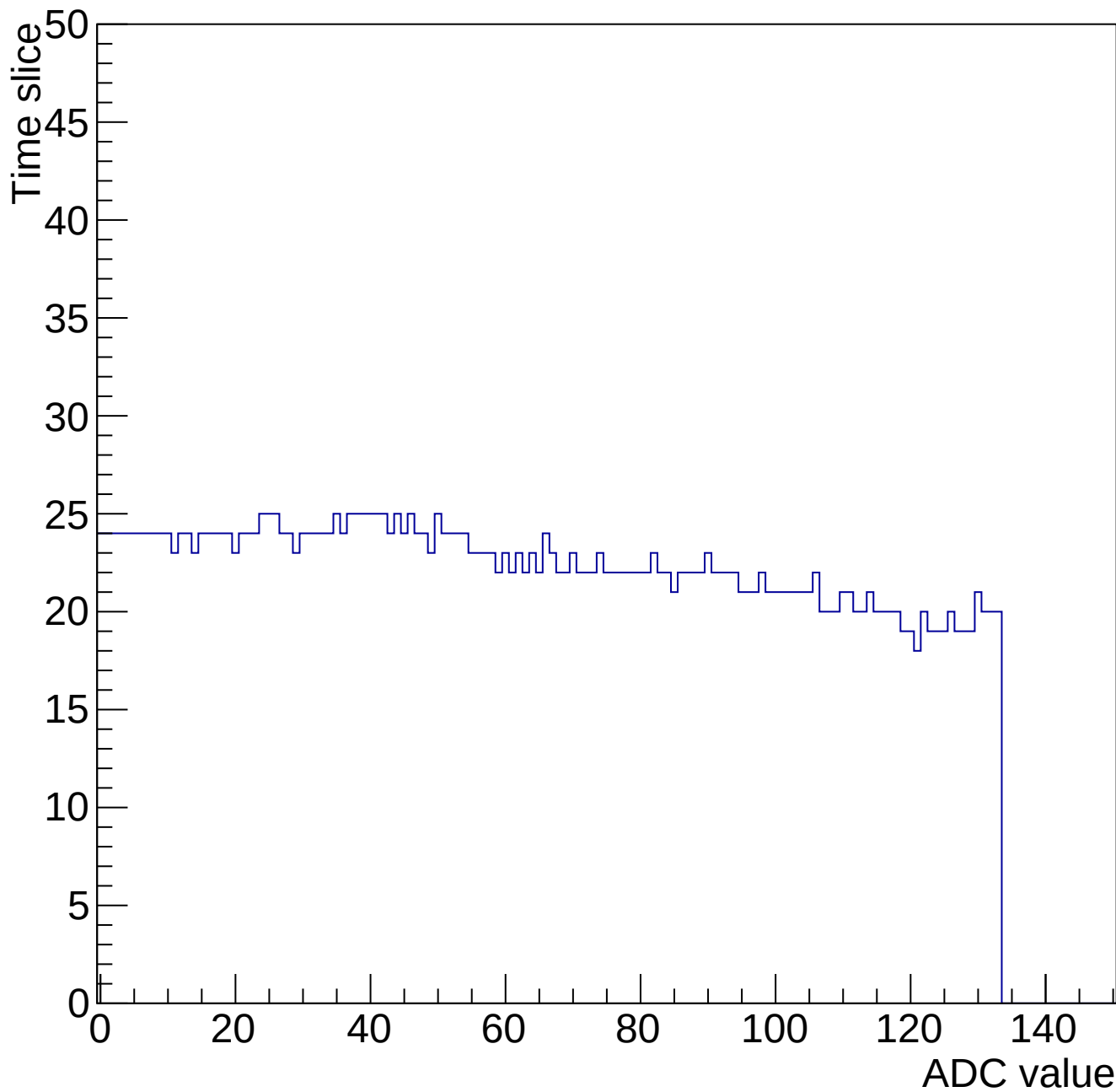
Evt 10 Time mcm 2001 chip 1 chan 9 chip1-nominal-34537



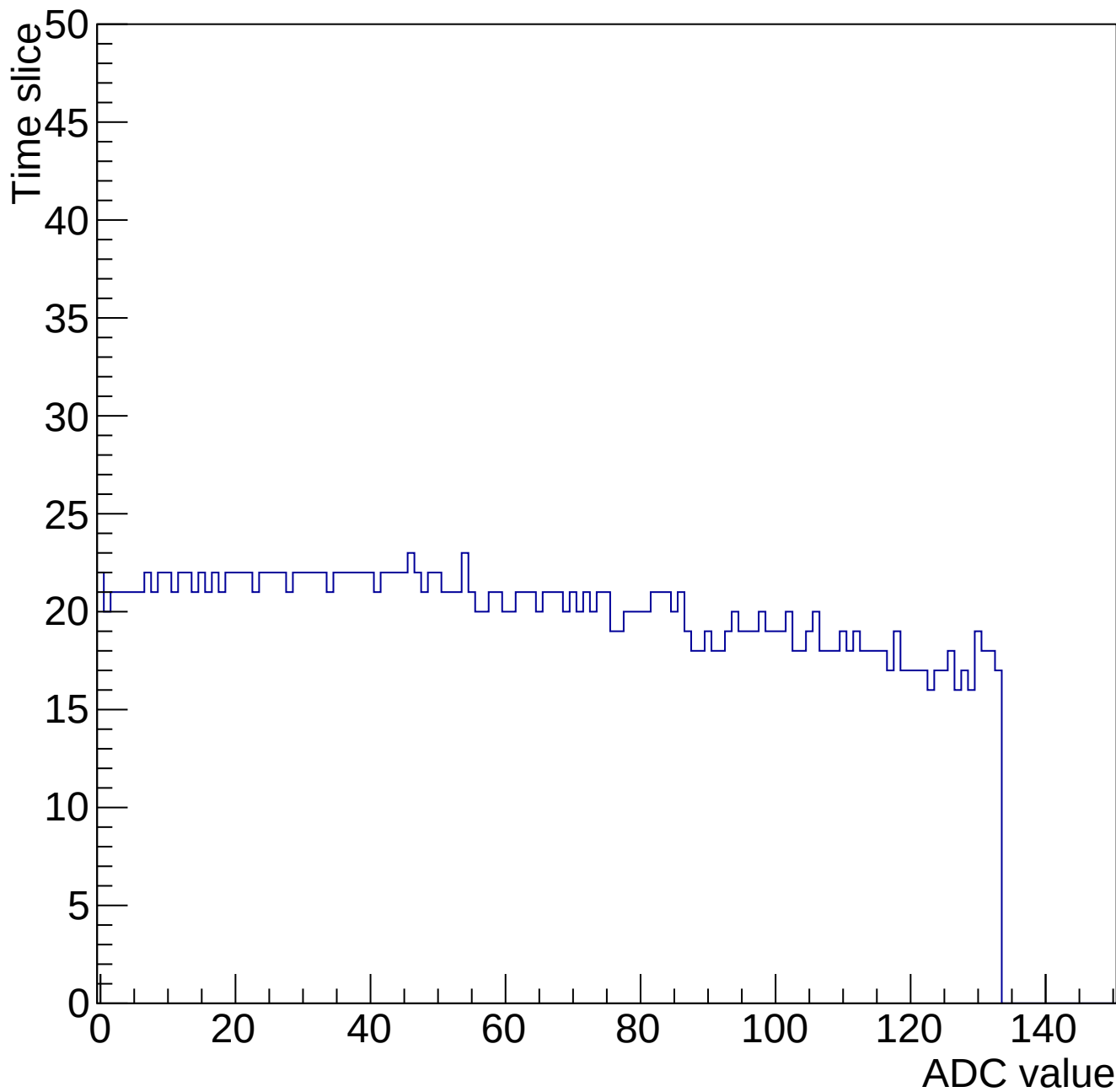
Evt 10 Time mcm 2001 chip 1 chan 10 chip1-nominal-34537



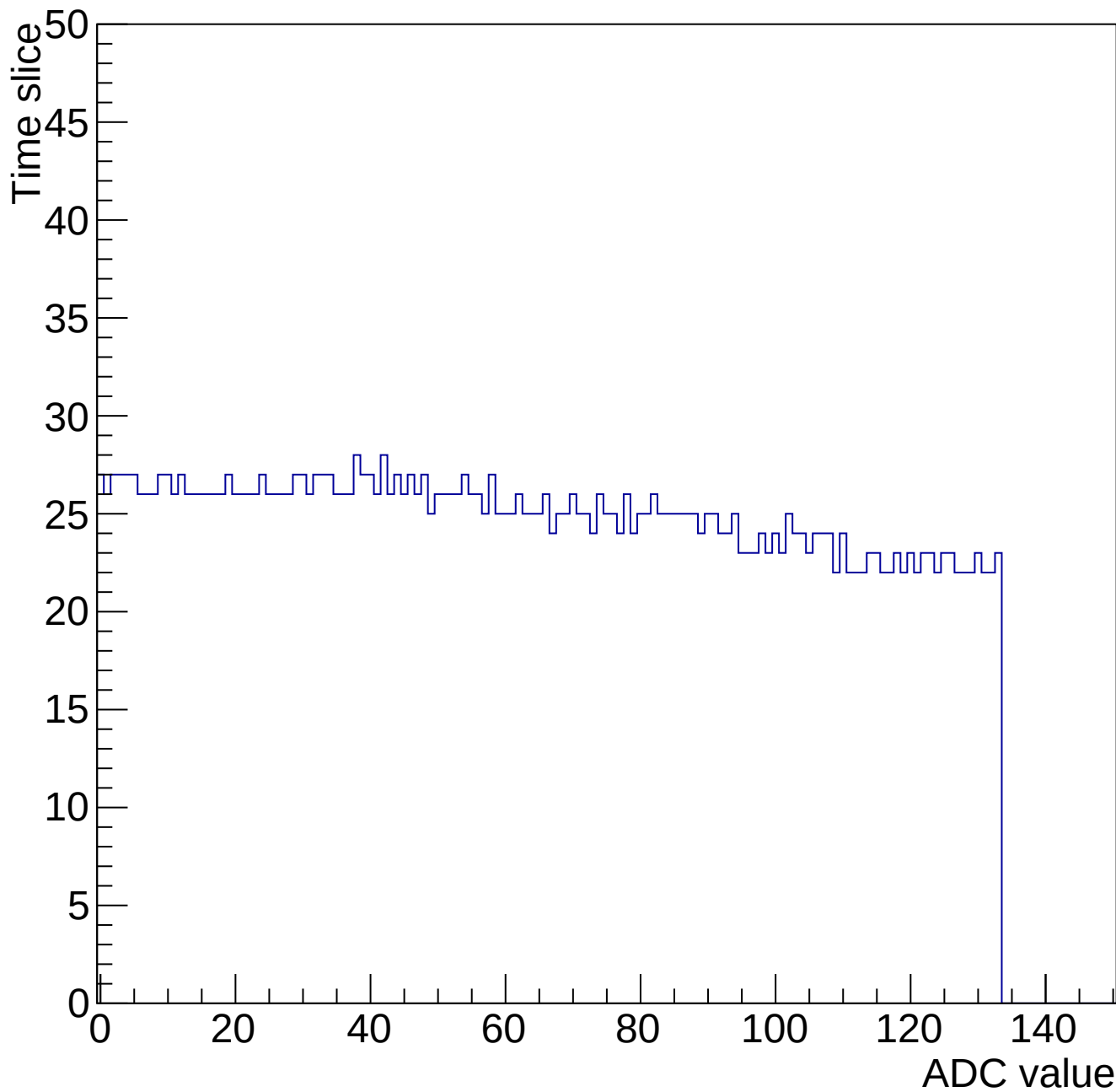
Evt 10 Time mcm 2001 chip 1 chan 11 chip1-nominal-34537



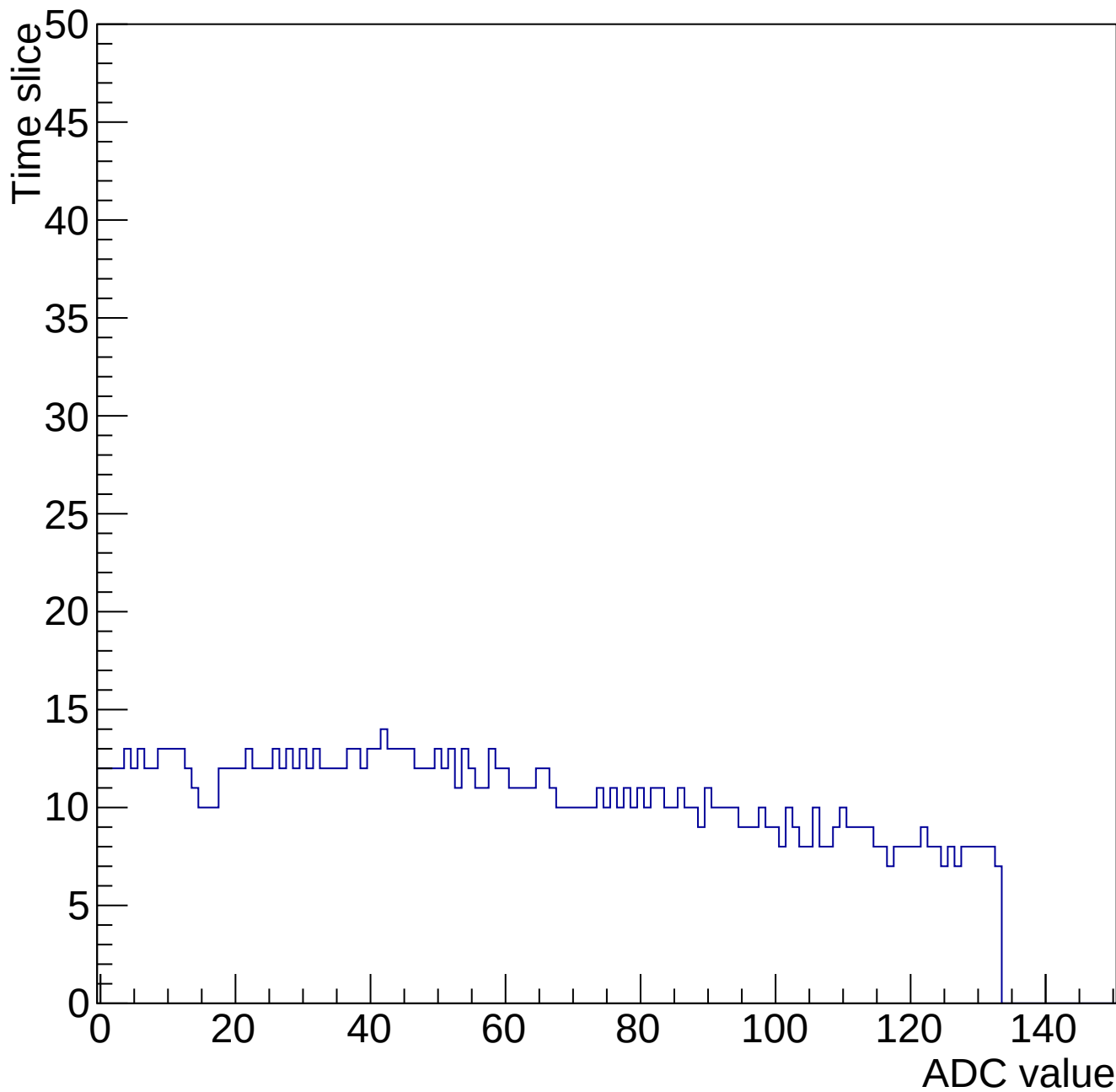
Evt 10 Time mcm 2001 chip 1 chan 12 chip1-nominal-34537



Evt 10 Time mcm 2001 chip 1 chan 13 chip1-nominal-34537



Evt 10 Time mcm 2001 chip 1 chan 14 chip1-nominal-34537



Evt 10 Time mcm 2001 chip 1 chan 15 chip1-nominal-34537

