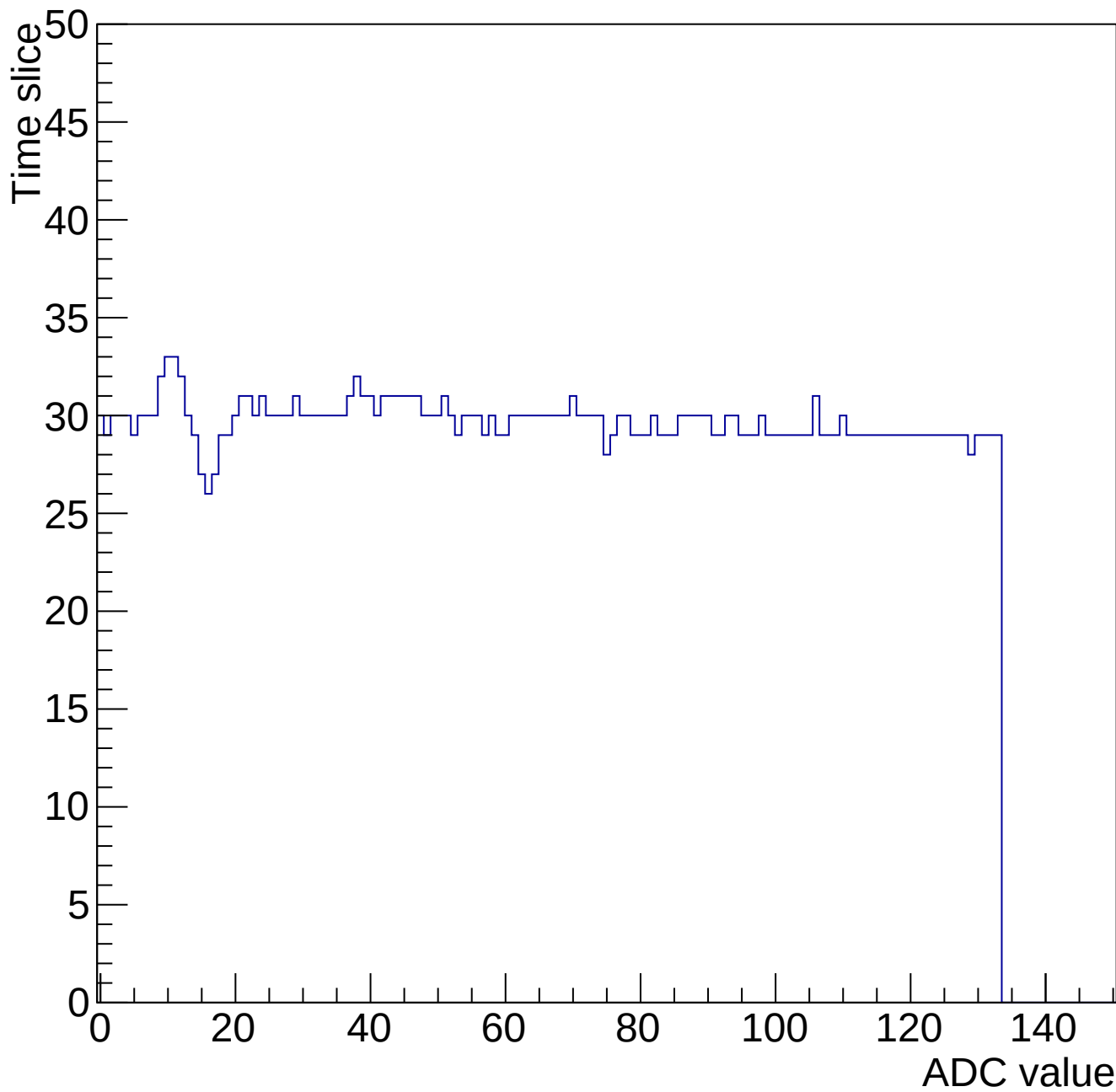
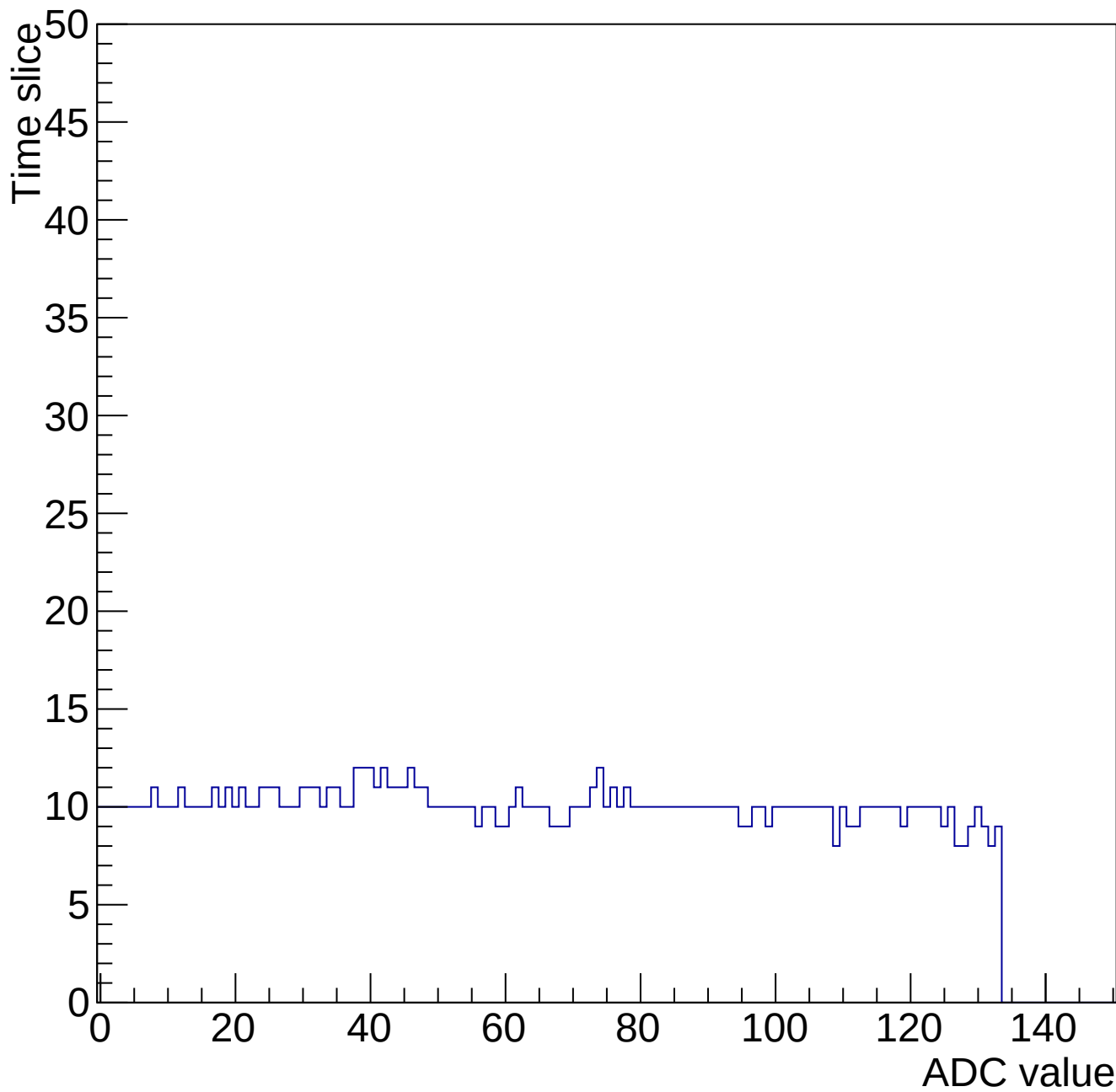


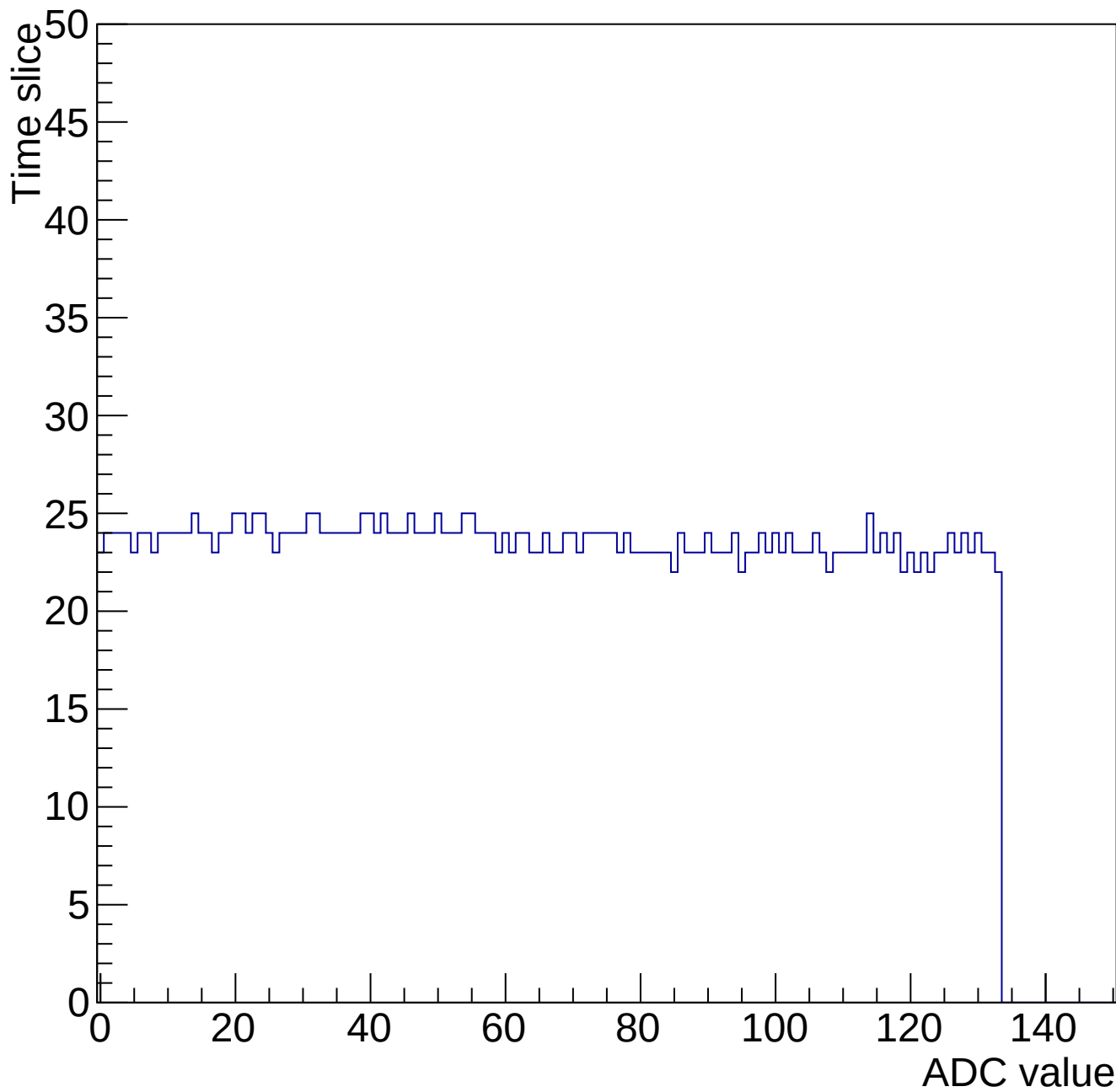
Evt 10 Time mcm 2003 chip 3 chan 0 chip3-nominal-34529



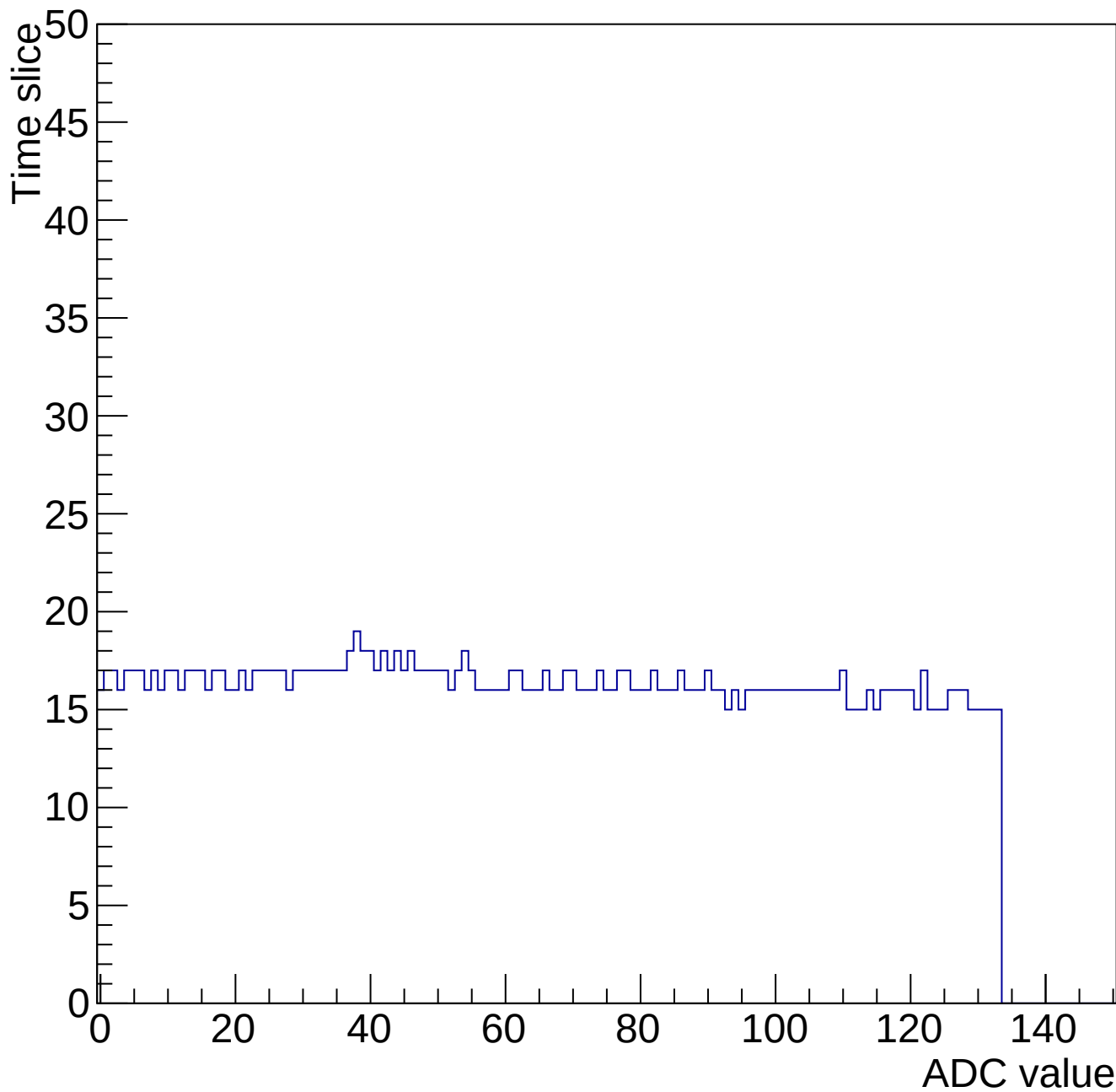
Evt 10 Time mcm 2003 chip 3 chan 1 chip3-nominal-34529



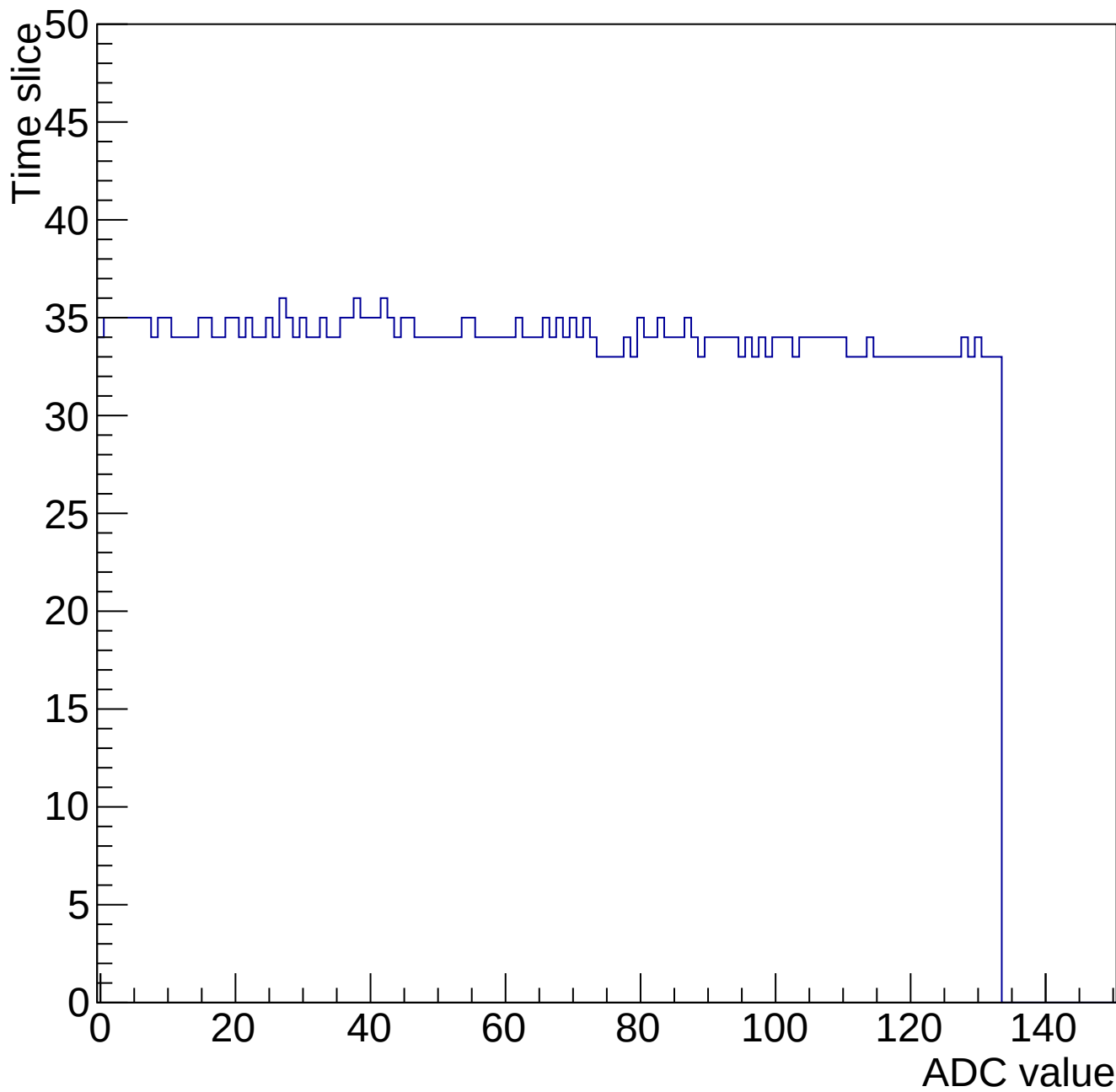
Evt 10 Time mcm 2003 chip 3 chan 2 chip3-nominal-34529



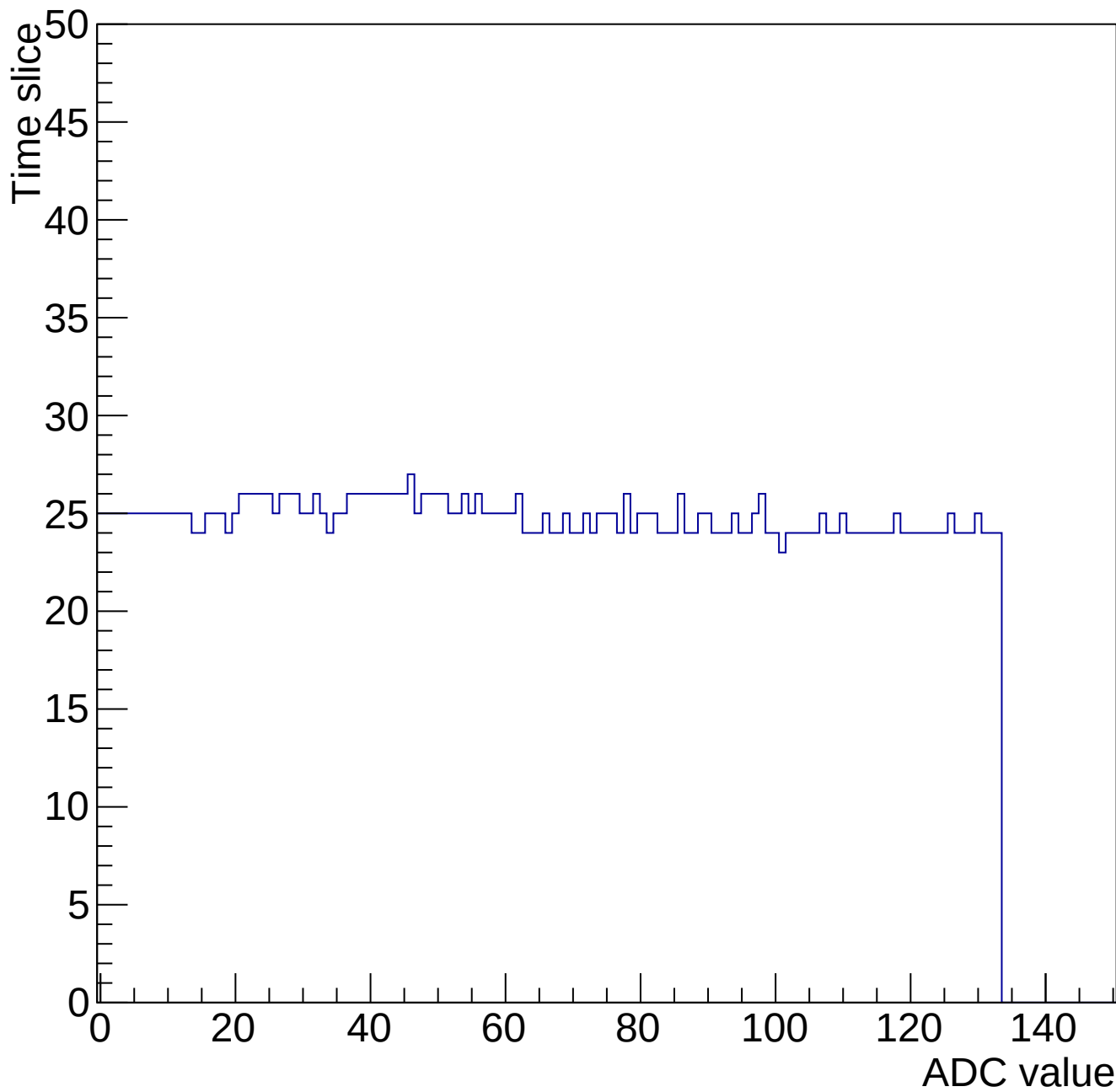
Evt 10 Time mcm 2003 chip 3 chan 3 chip3-nominal-34529



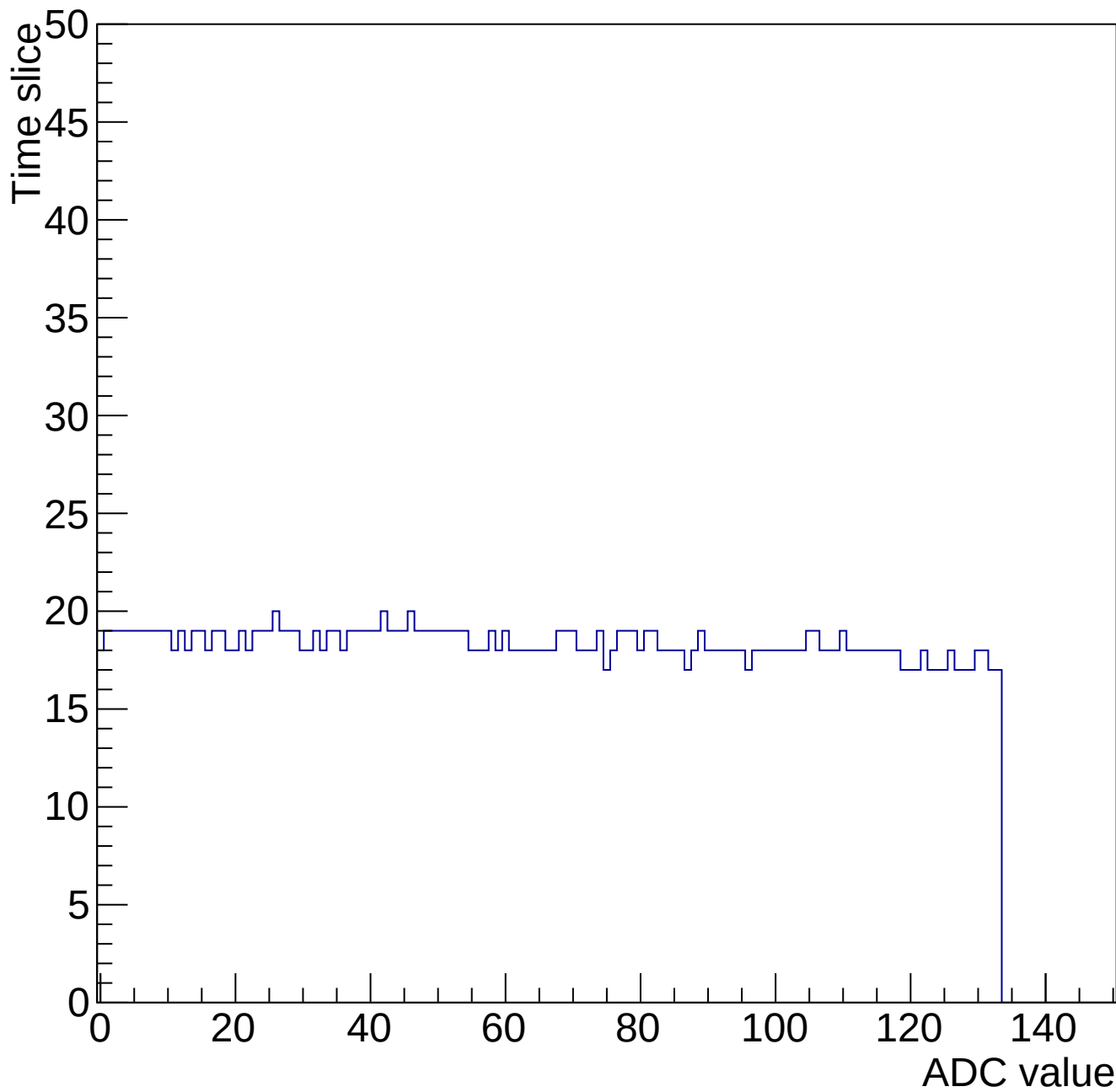
Evt 10 Time mcm 2003 chip 3 chan 4 chip3-nominal-34529



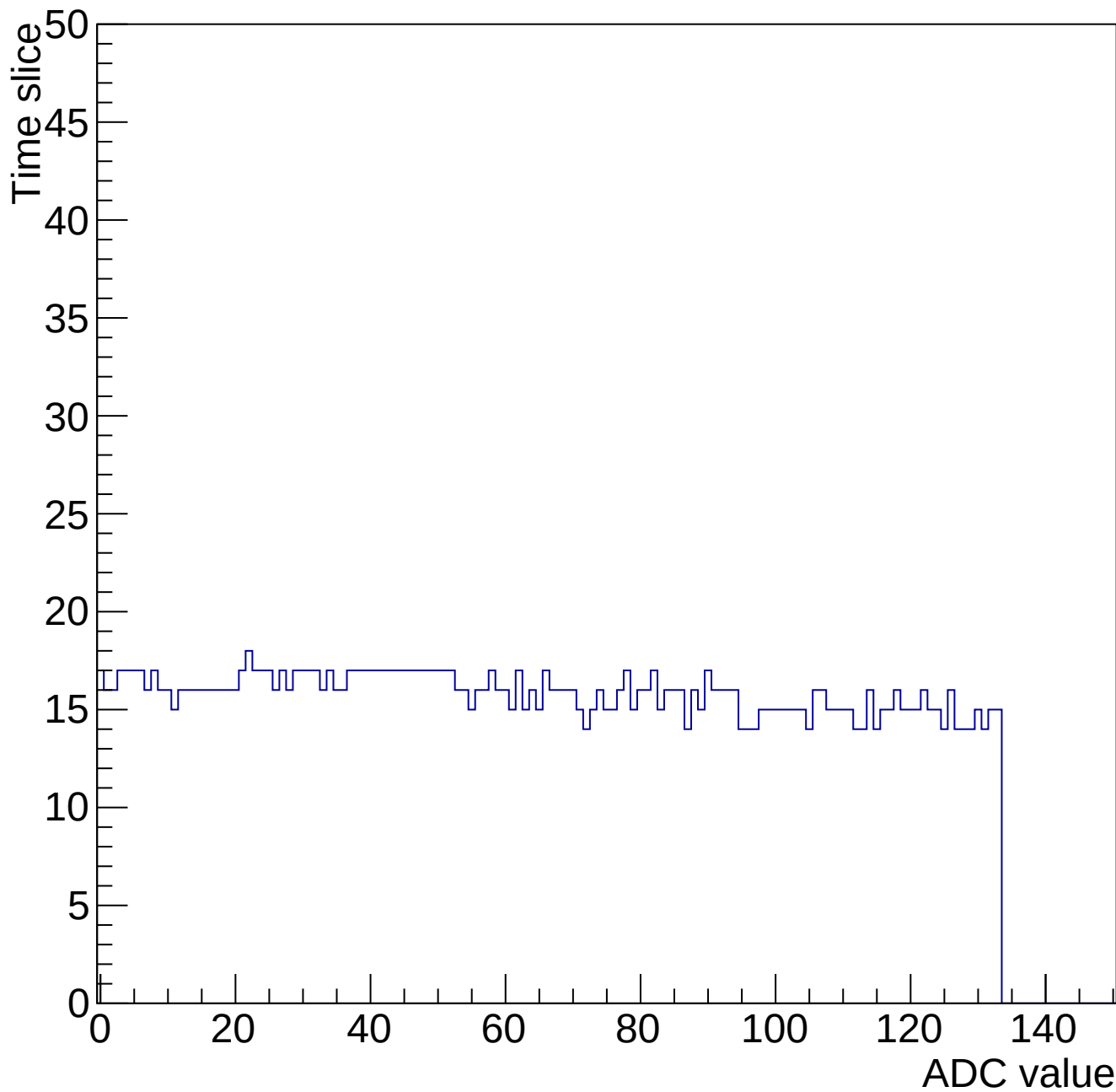
Evt 10 Time mcm 2003 chip 3 chan 5 chip3-nominal-34529



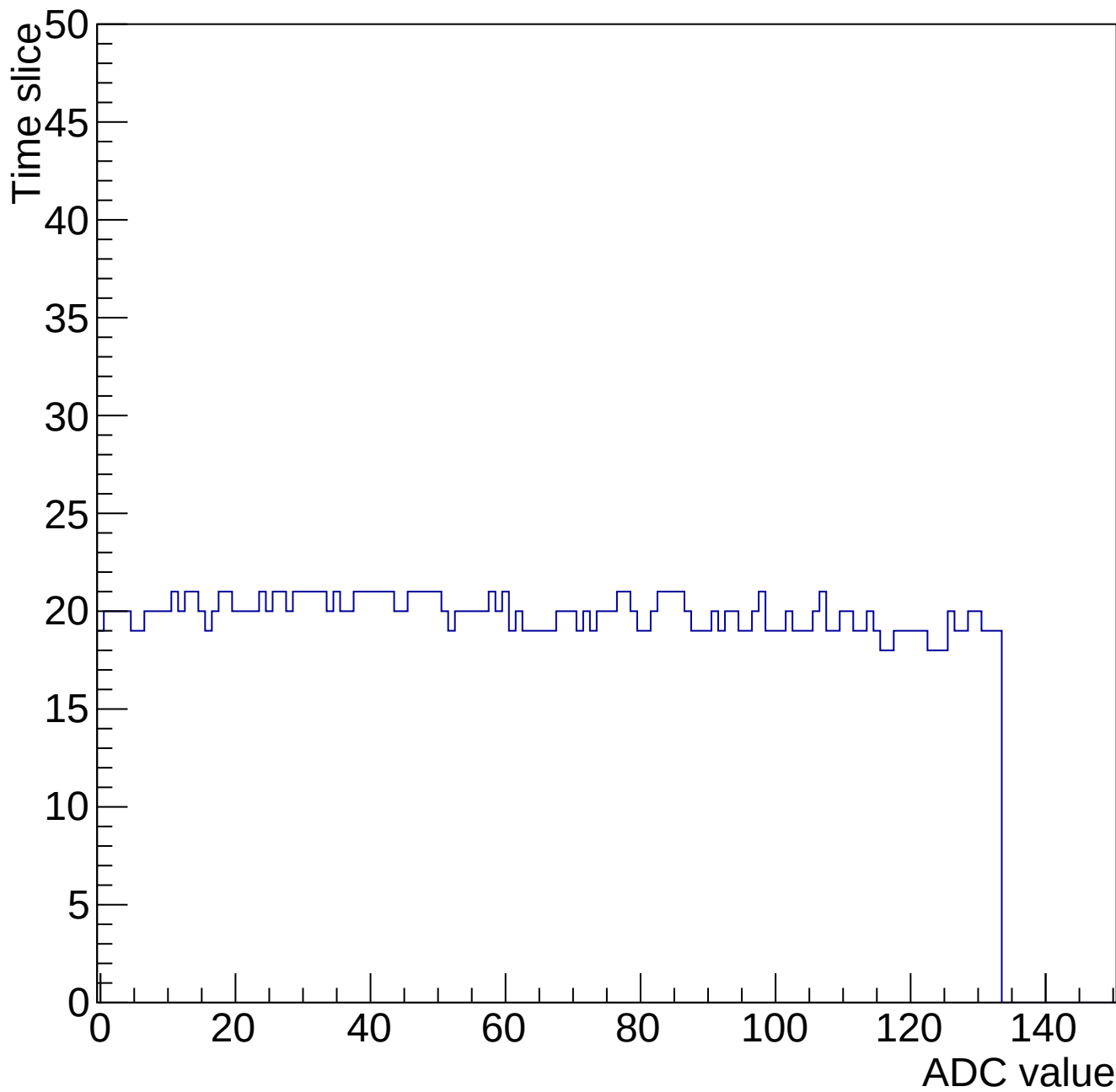
Evt 10 Time mcm 2003 chip 3 chan 6 chip3-nominal-34529



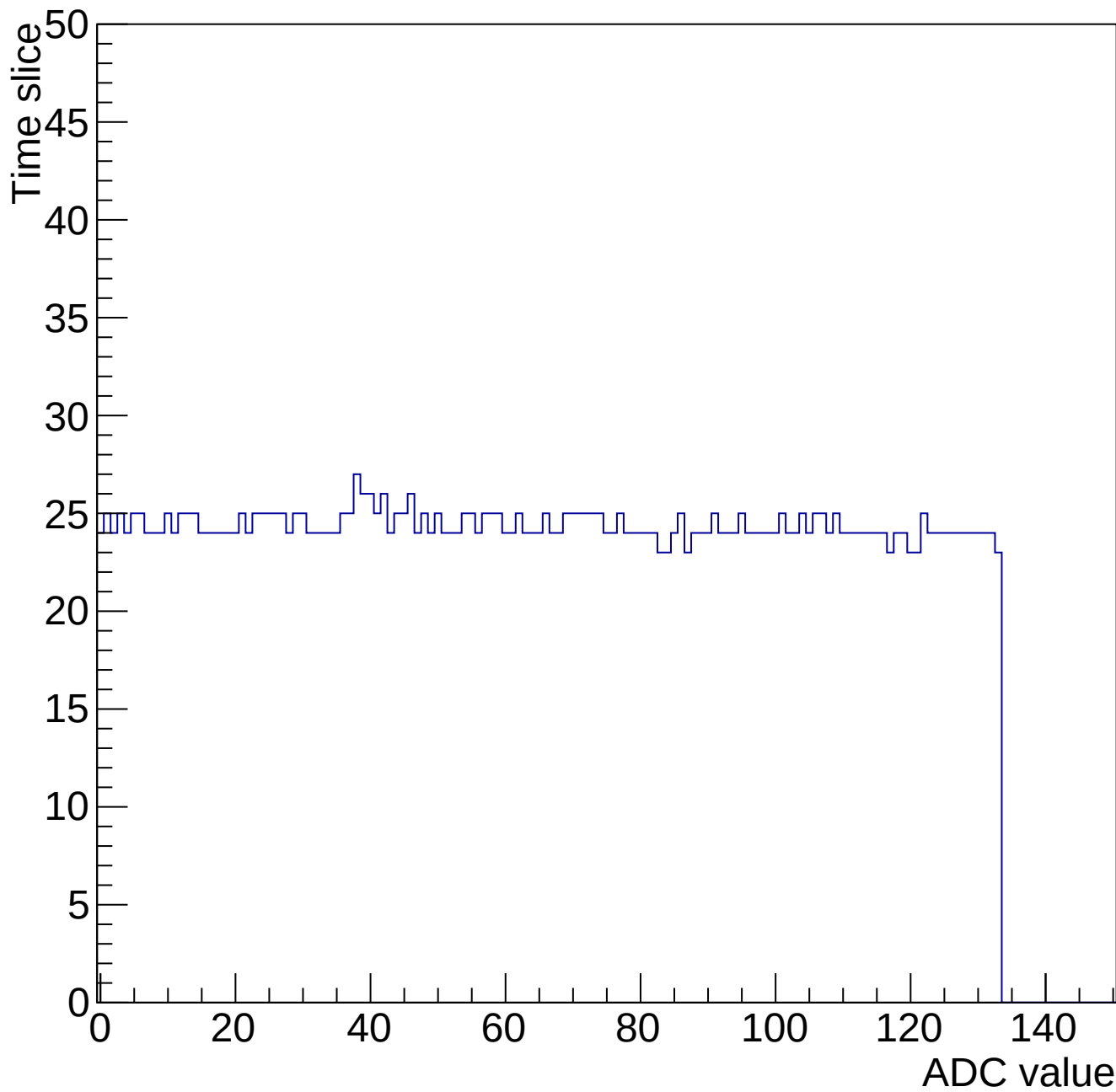
Evt 10 Time mcm 2003 chip 3 chan 7 chip3-nominal-34529



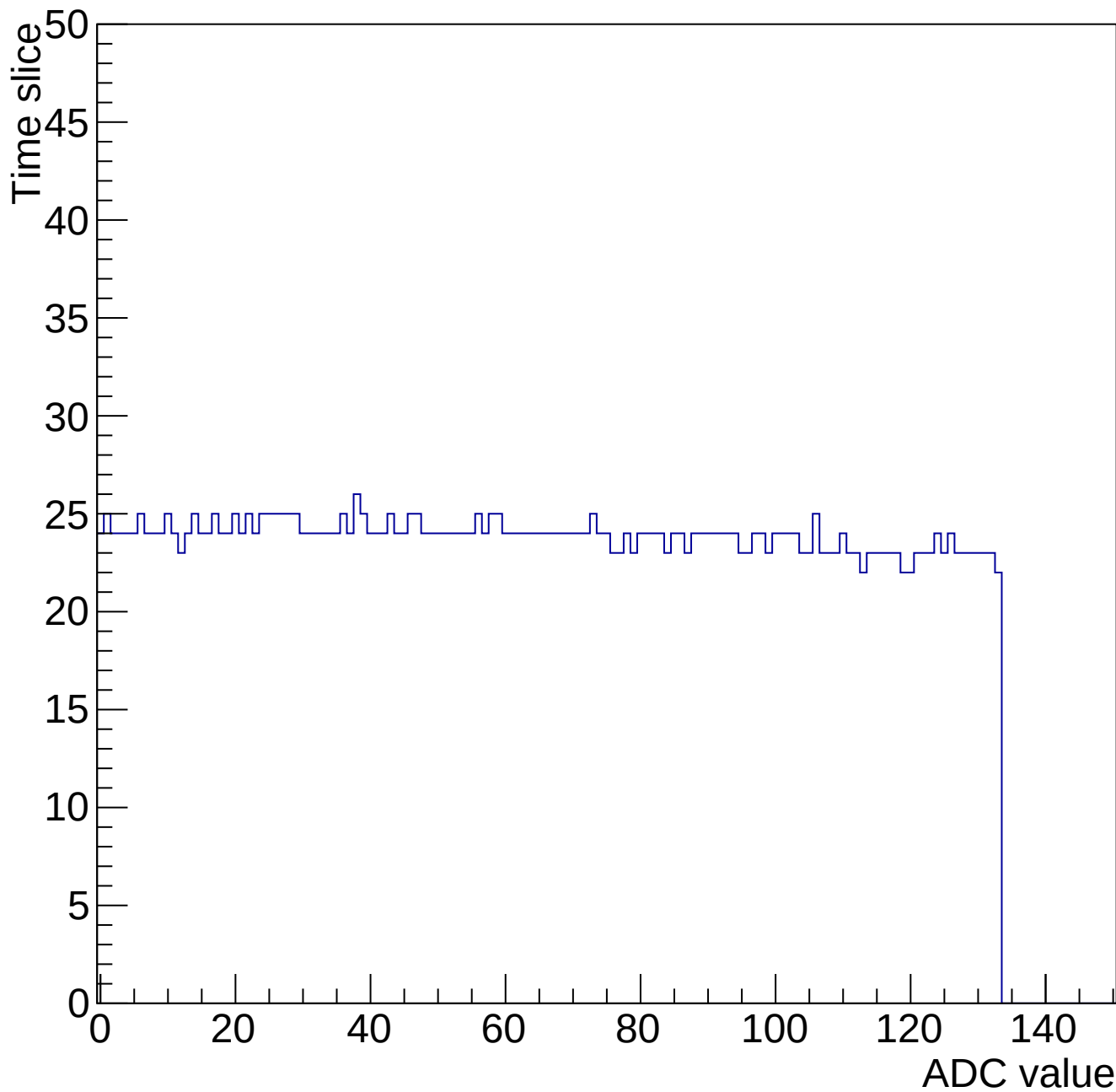
Evt 10 Time mcm 2003 chip 3 chan 8 chip3-nominal-34529



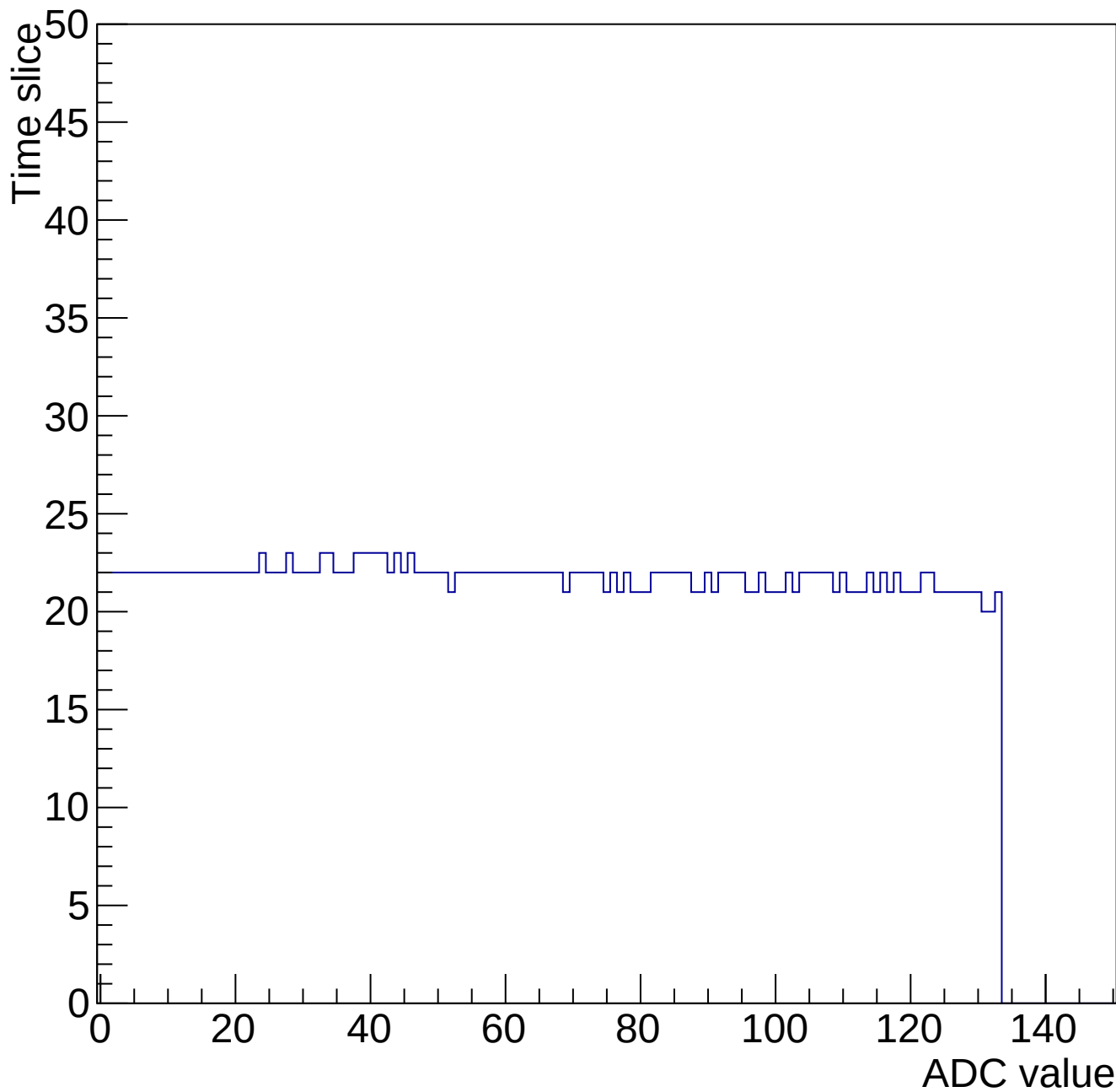
Evt 10 Time mcm 2003 chip 3 chan 9 chip3-nominal-34529



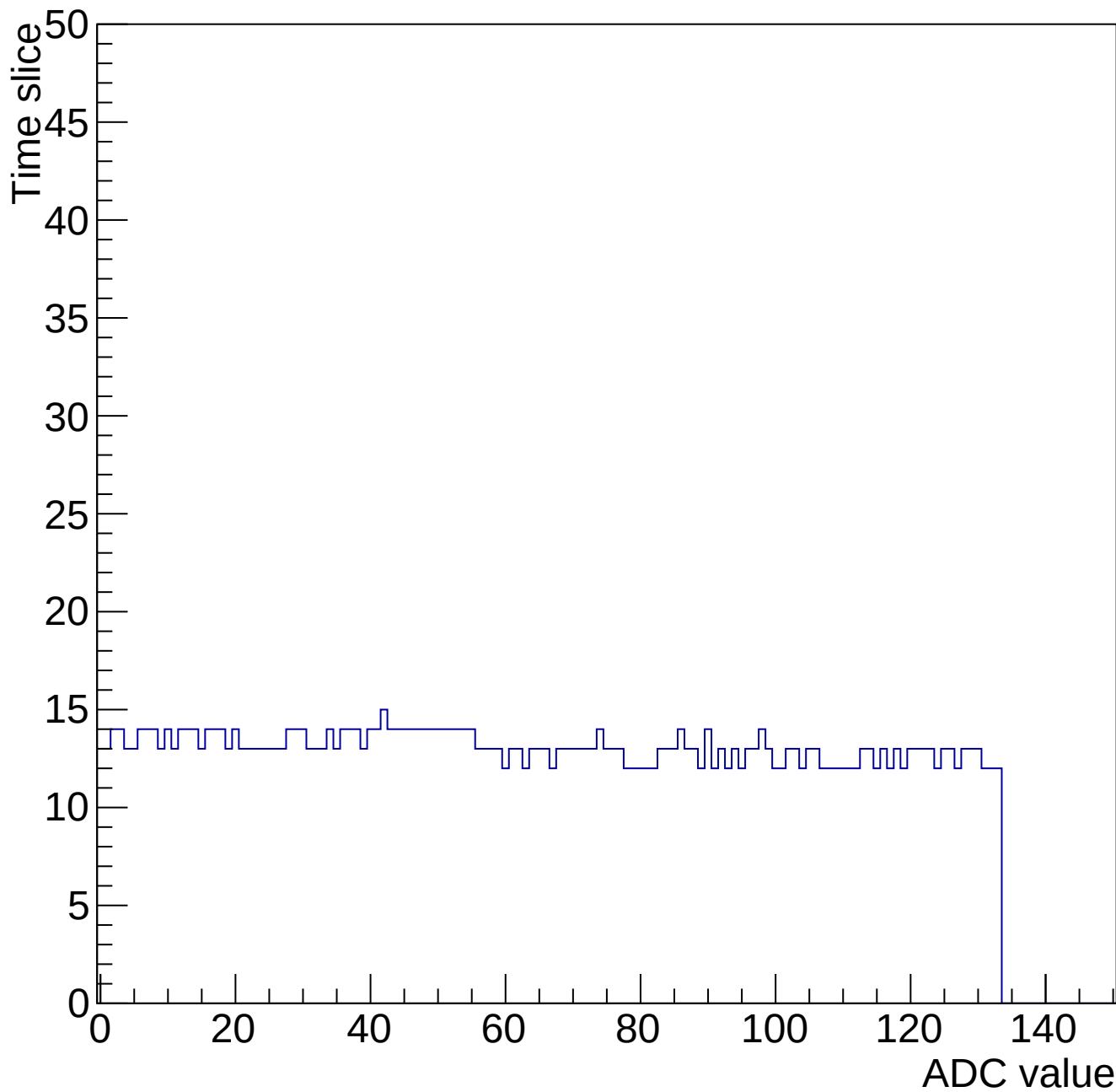
Evt 10 Time mcm 2003 chip 3 chan 10 chip3-nominal-34529



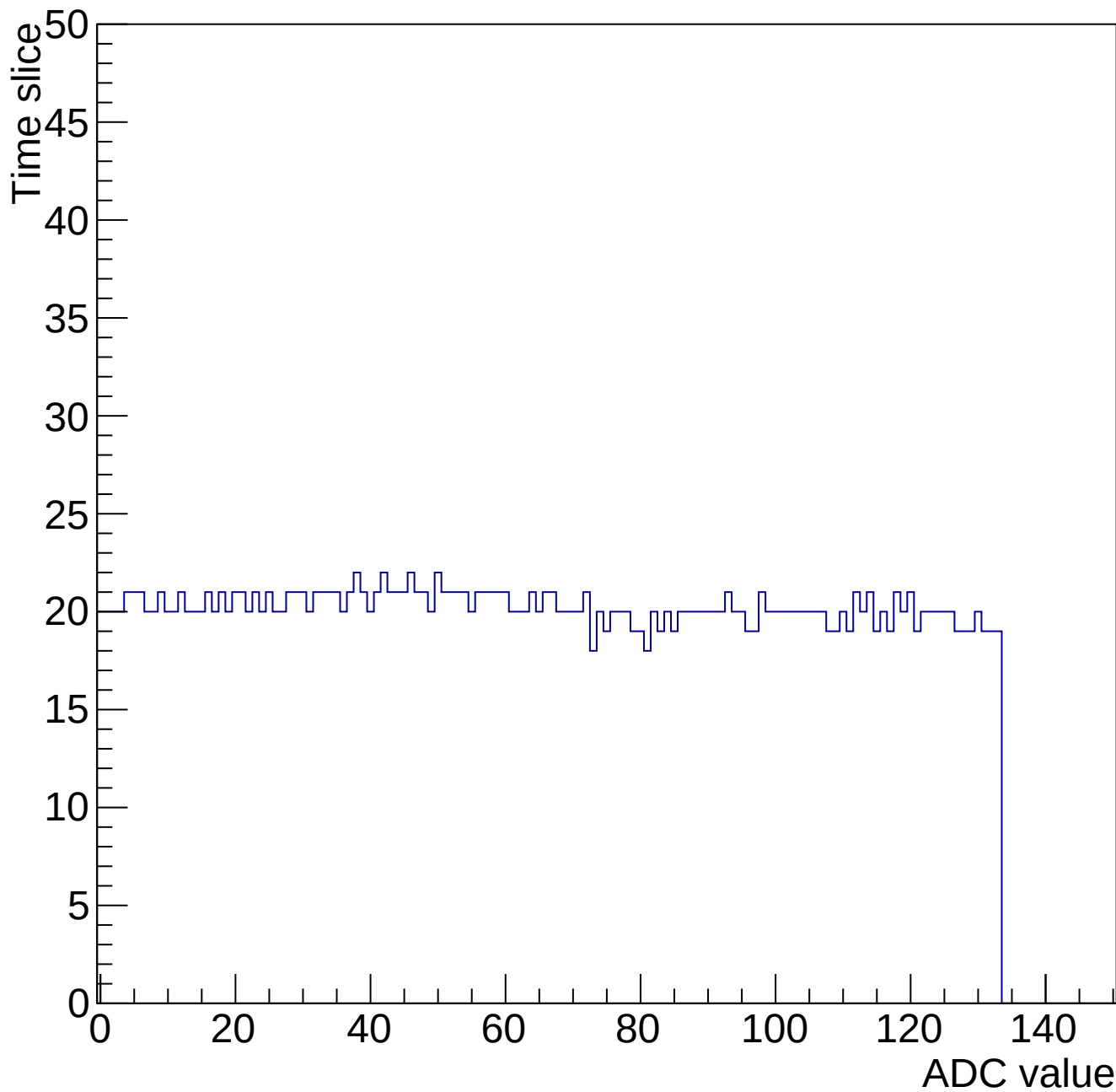
Evt 10 Time mcm 2003 chip 3 chan 11 chip3-nominal-34529



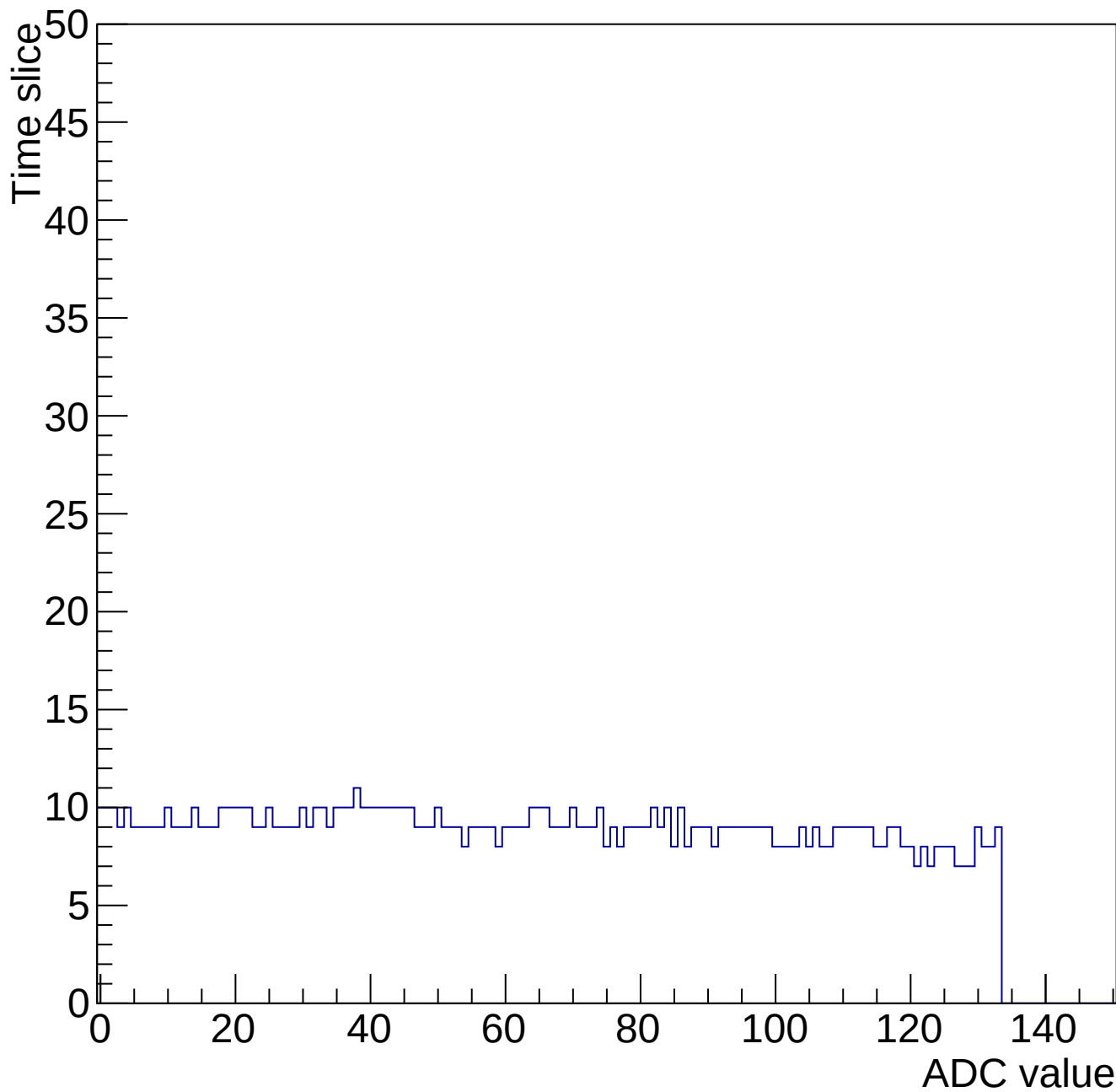
Evt 10 Time mcm 2003 chip 3 chan 12 chip3-nominal-34529



Evt 10 Time mcm 2003 chip 3 chan 13 chip3-nominal-34529



Evt 10 Time mcm 2003 chip 3 chan 14 chip3-nominal-34529



Evt 10 Time mcm 2003 chip 3 chan 15 chip3-nominal-34529

