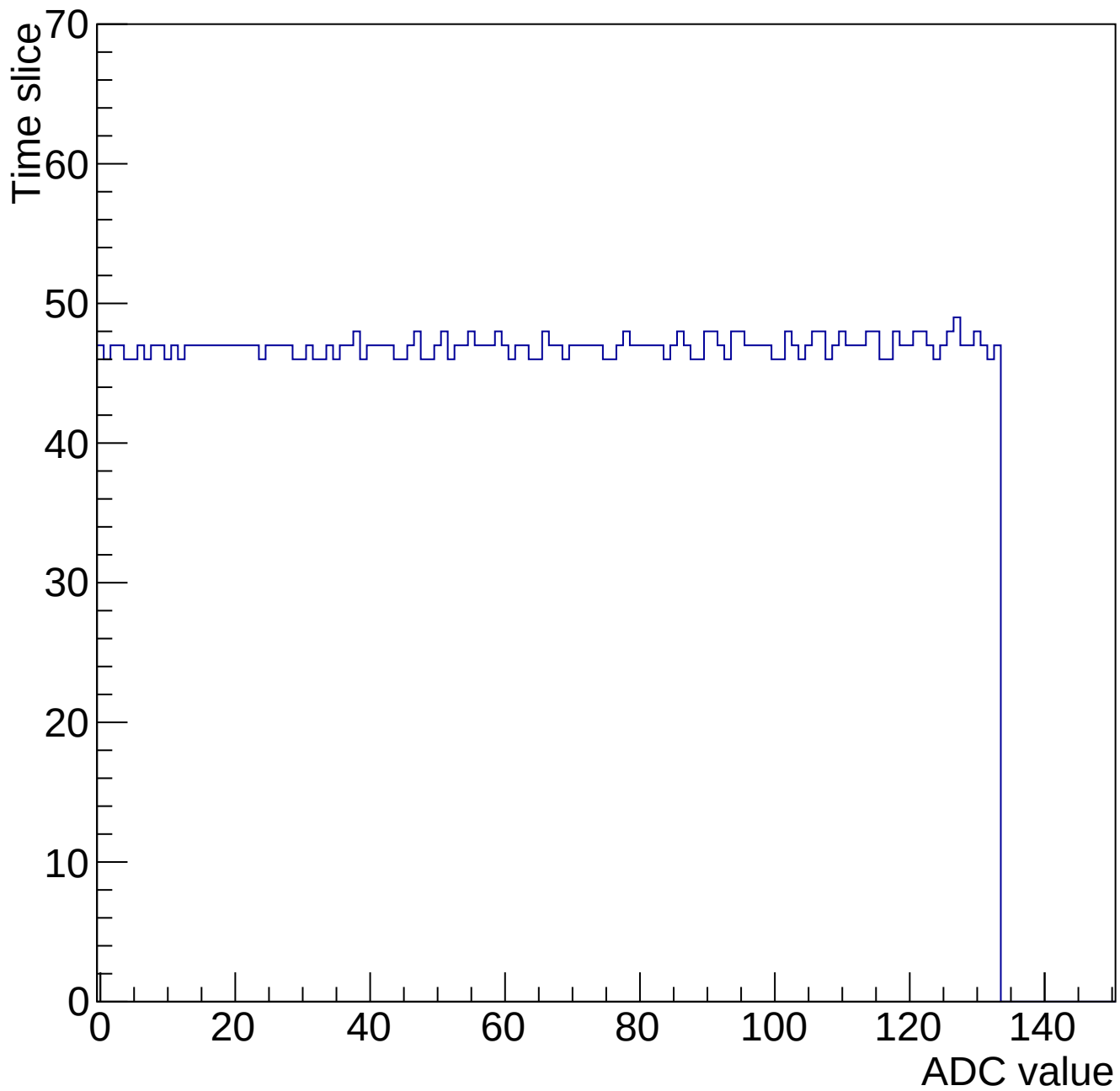
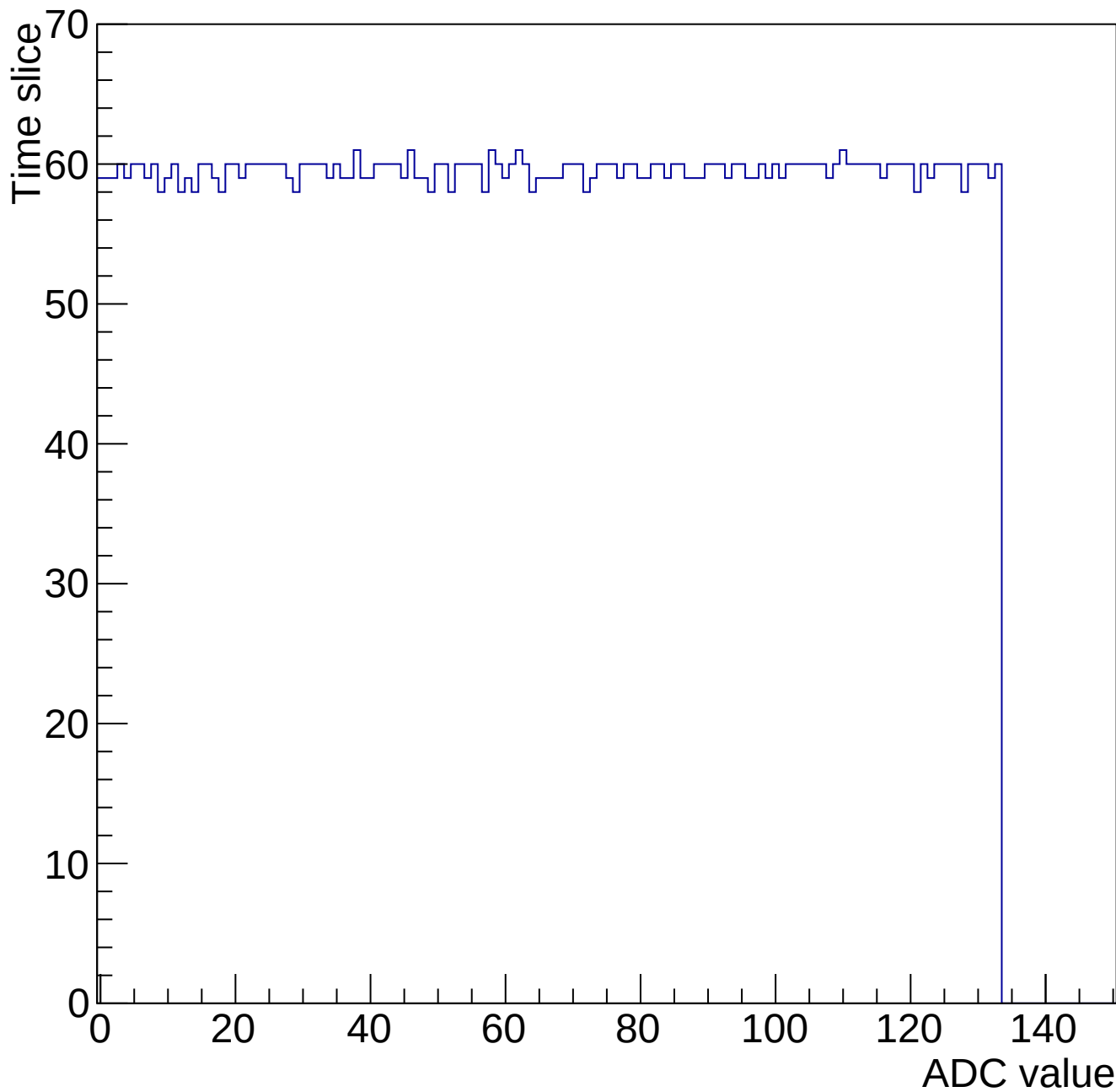


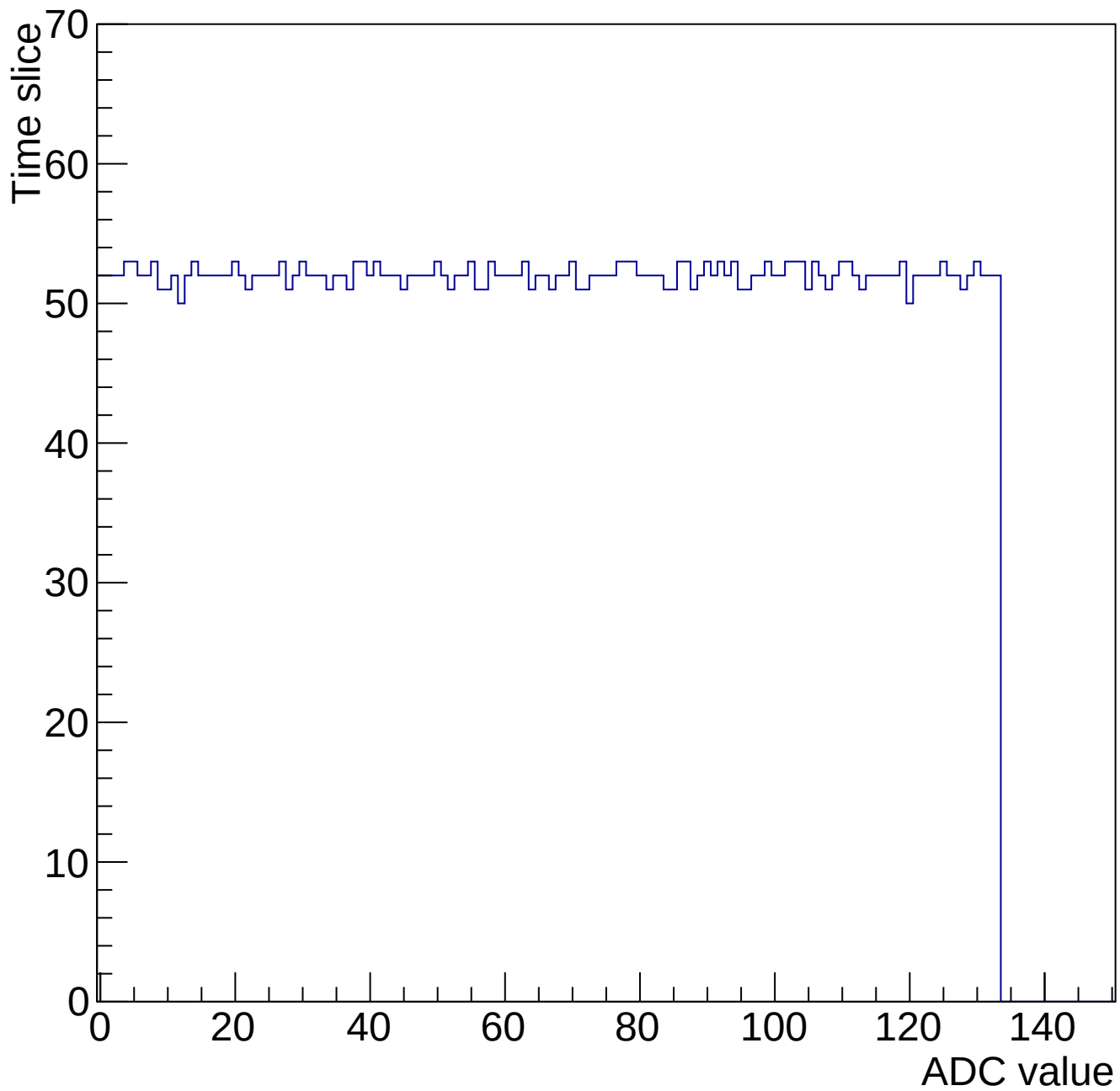
Evt 10 Time mcm 2000 chip 0 chan 0 gain0-shaper7



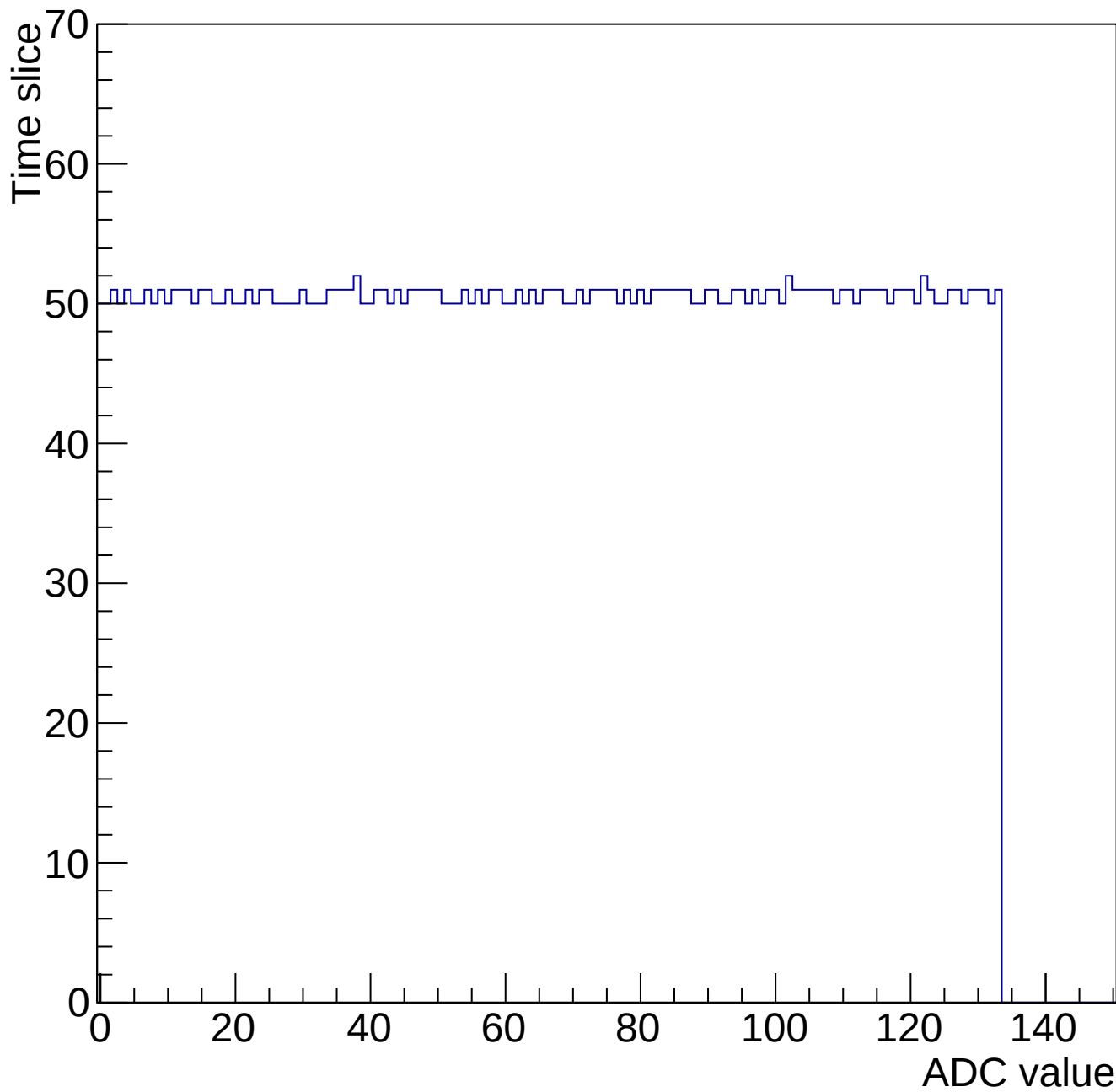
Evt 10 Time mcm 2000 chip 0 chan 1 gain0-shaper7



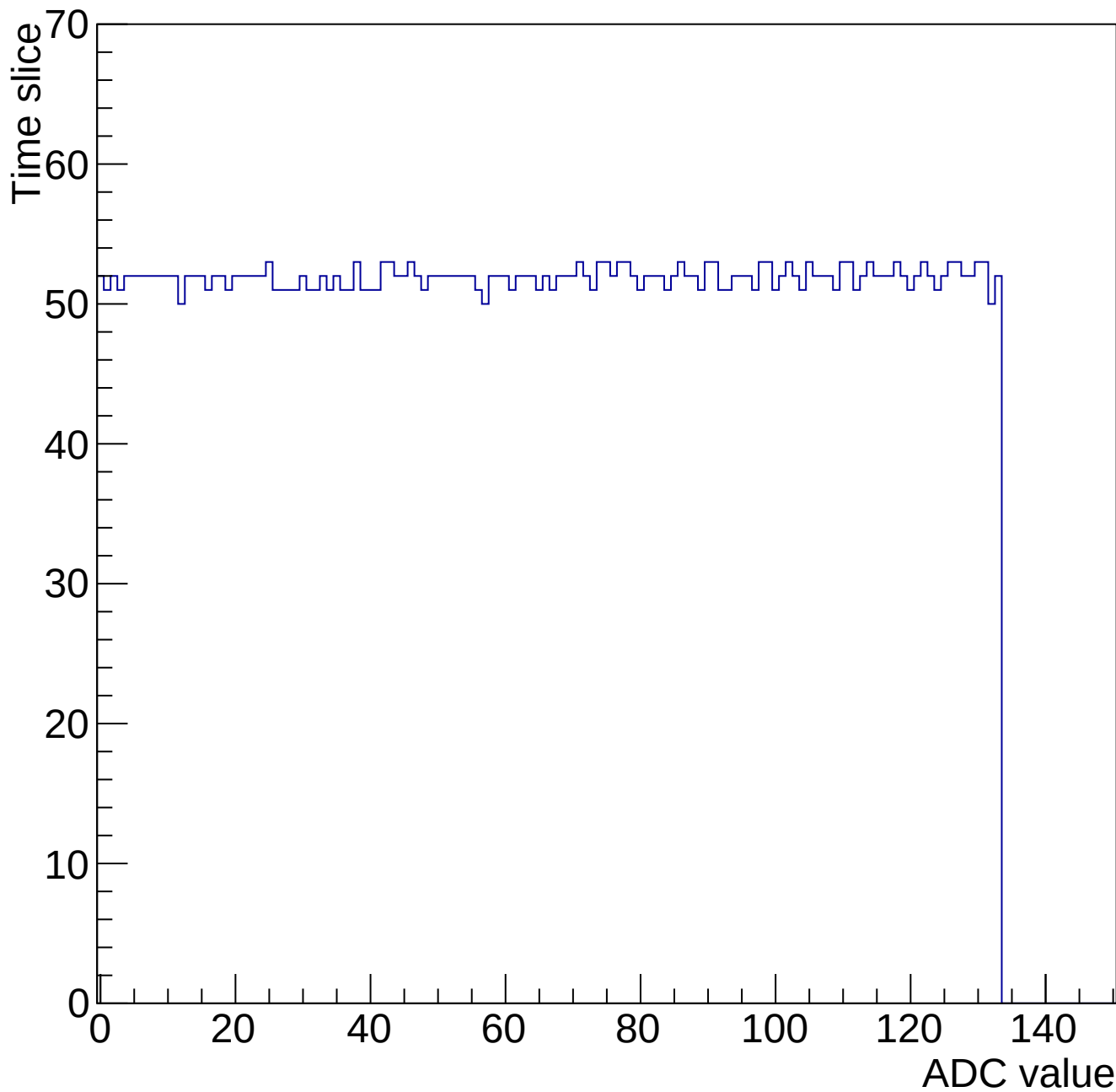
Evt 10 Time mcm 2000 chip 0 chan 2 gain0-shaper7



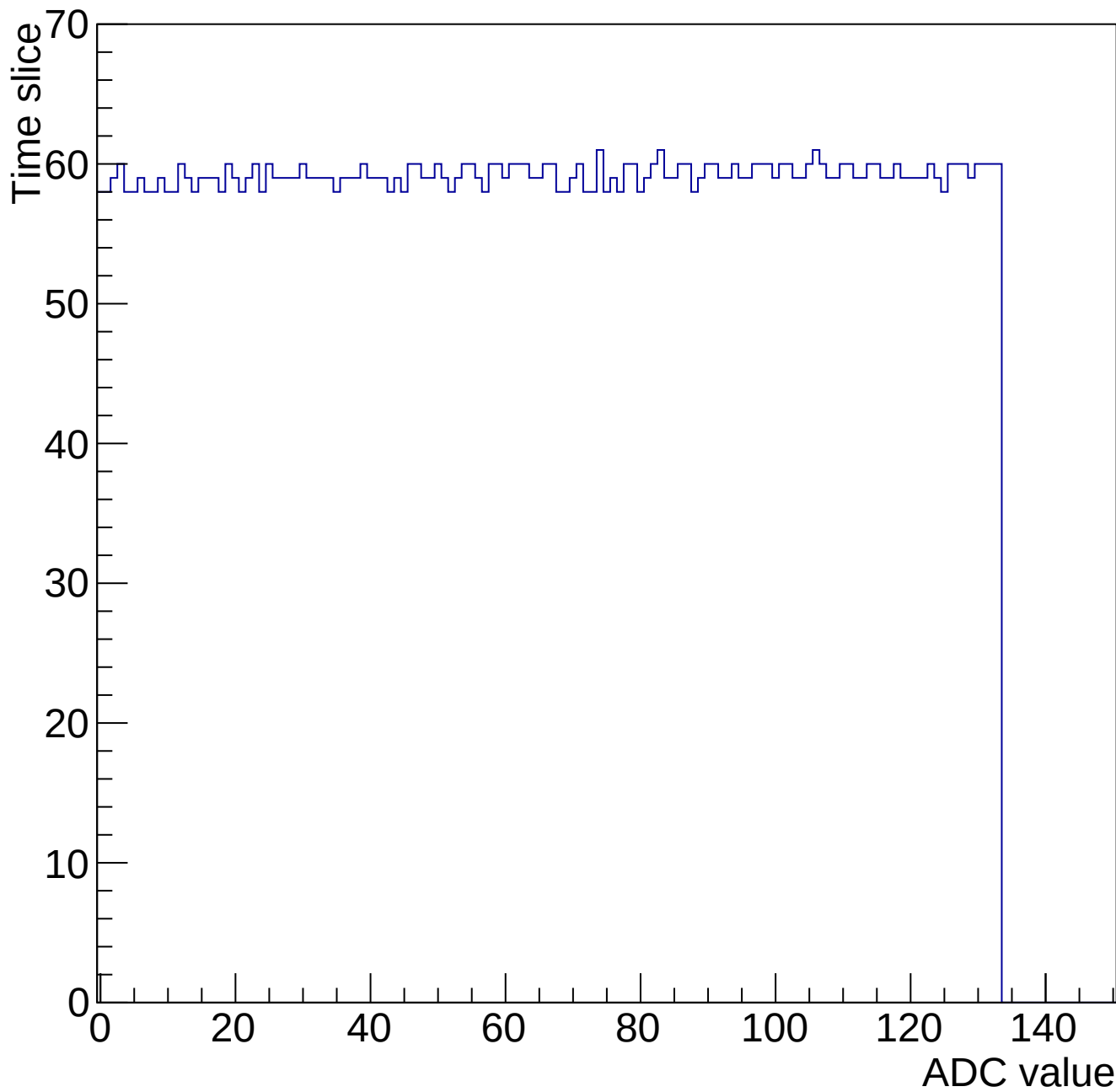
Evt 10 Time mcm 2000 chip 0 chan 3 gain0-shaper7



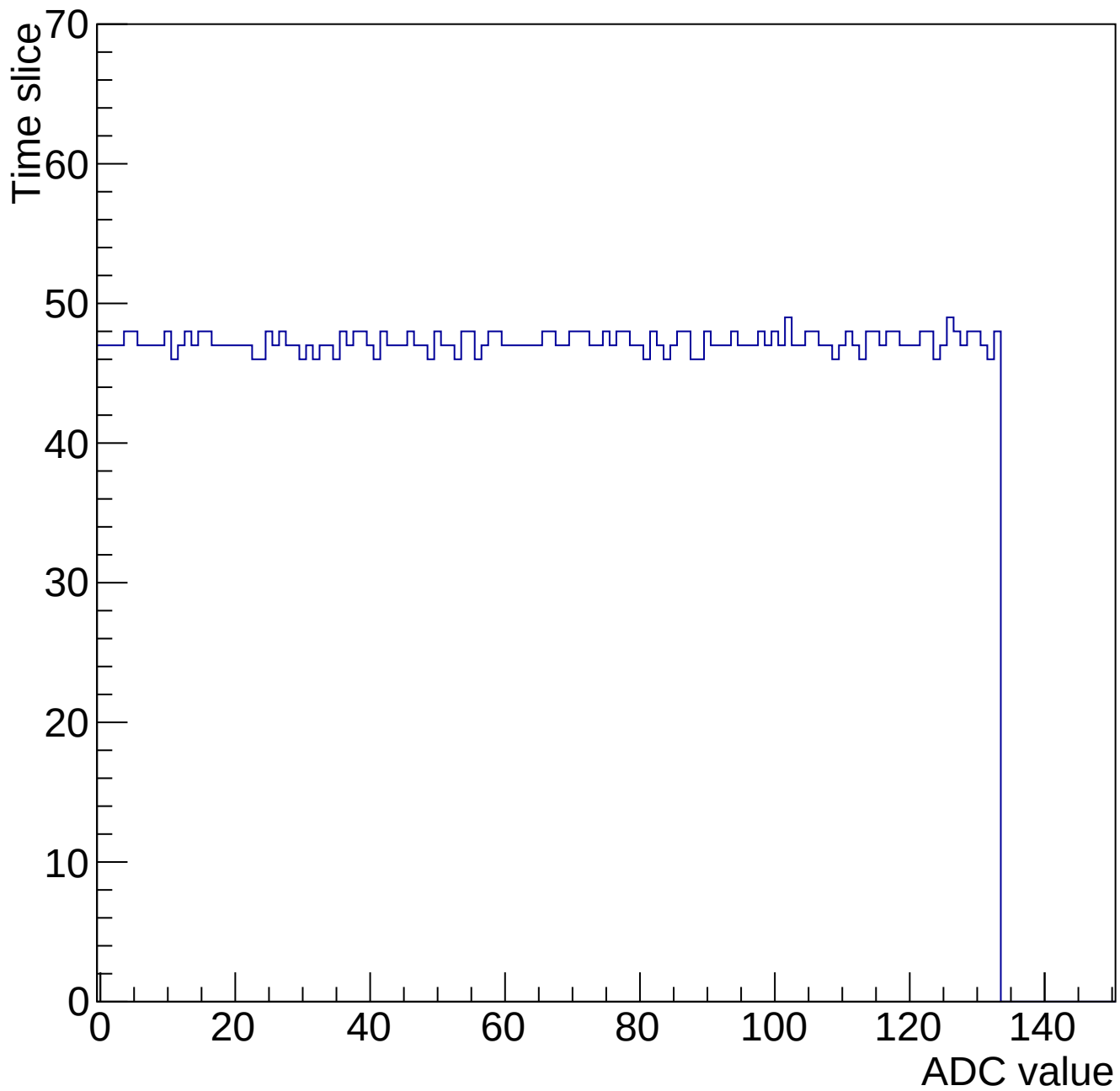
Evt 10 Time mcm 2000 chip 0 chan 4 gain0-shaper7



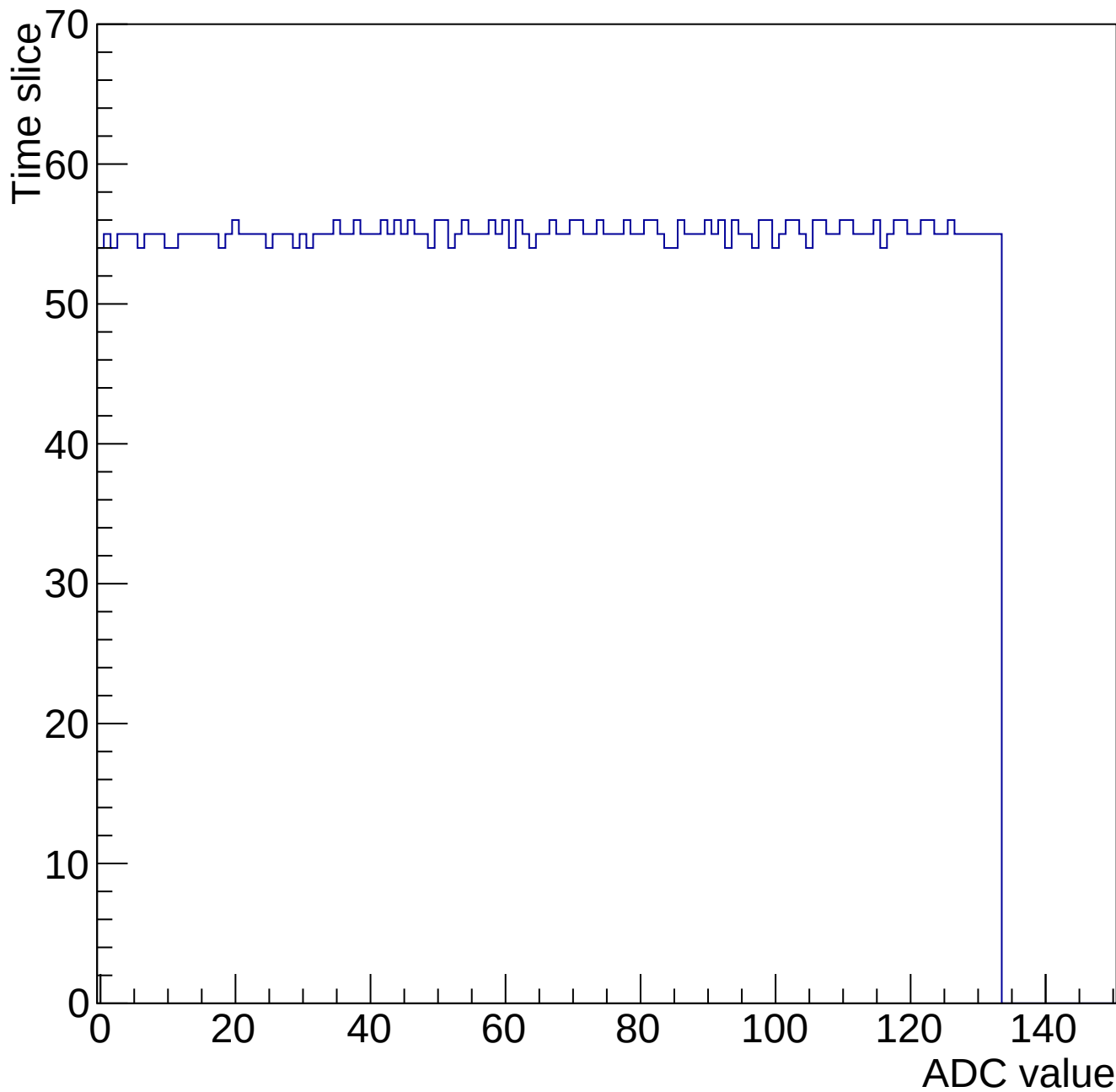
Evt 10 Time mcm 2000 chip 0 chan 5 gain0-shaper7



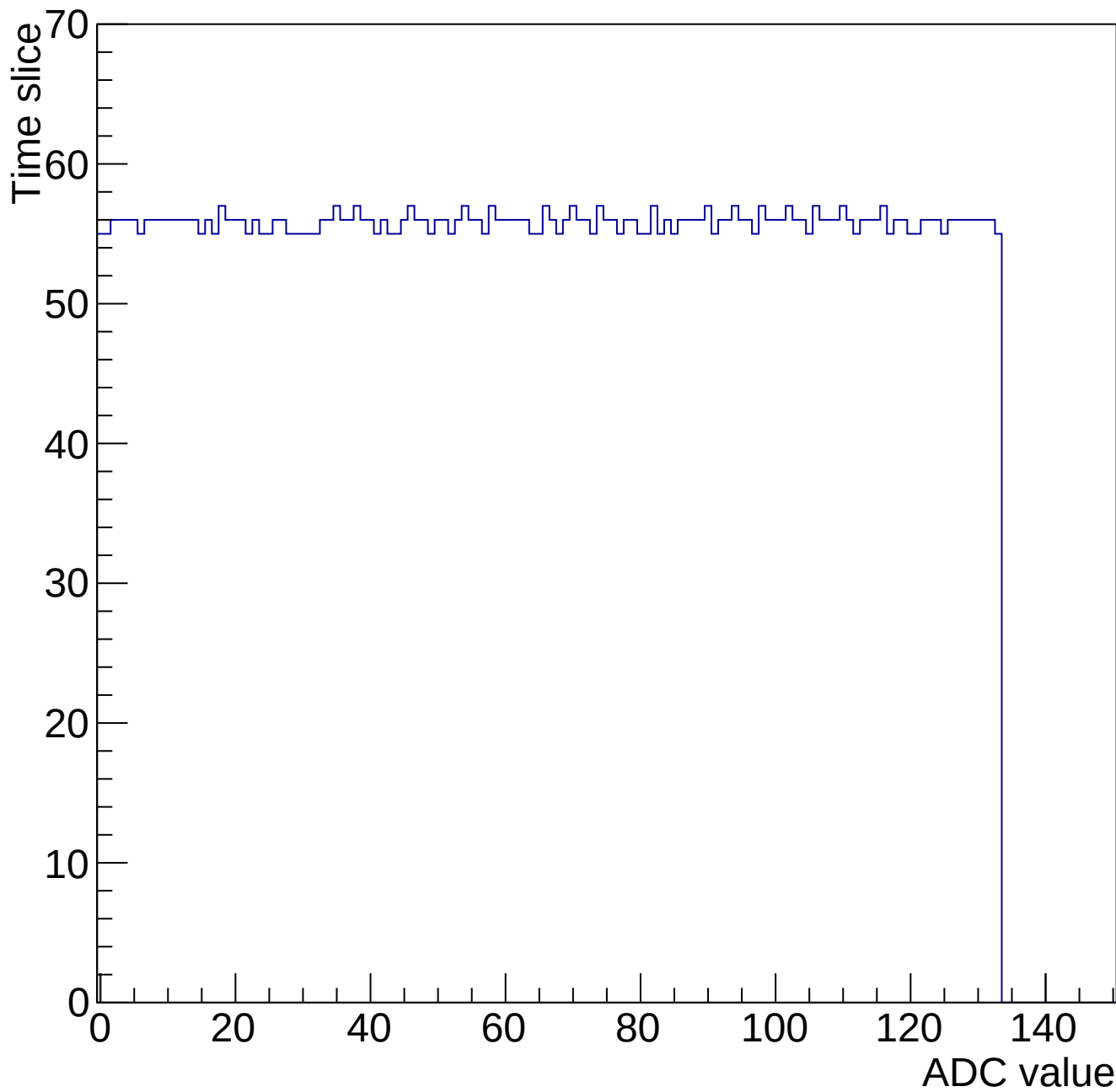
Evt 10 Time mcm 2000 chip 0 chan 6 gain0-shaper7



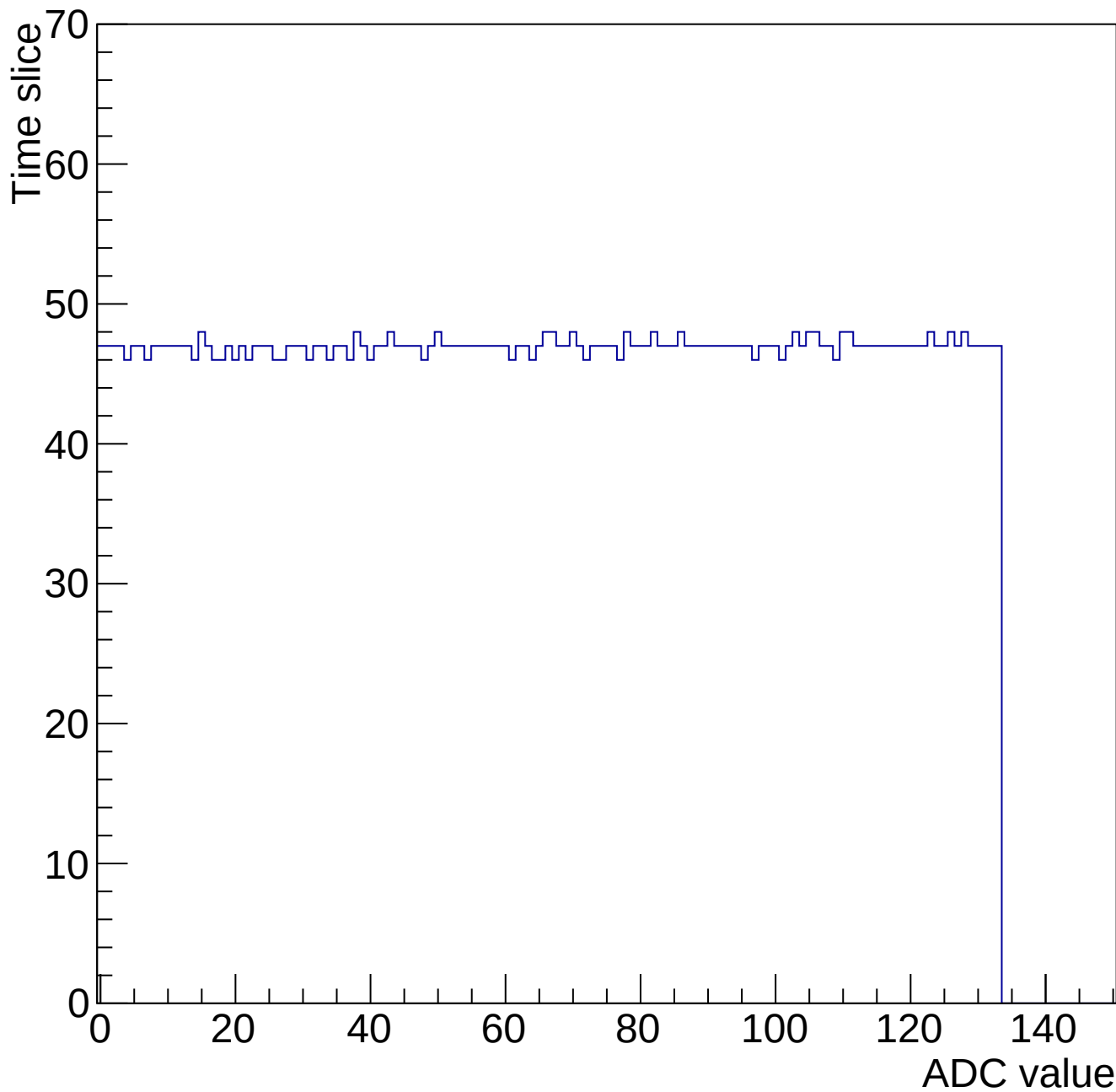
Evt 10 Time mcm 2000 chip 0 chan 7 gain0-shaper7



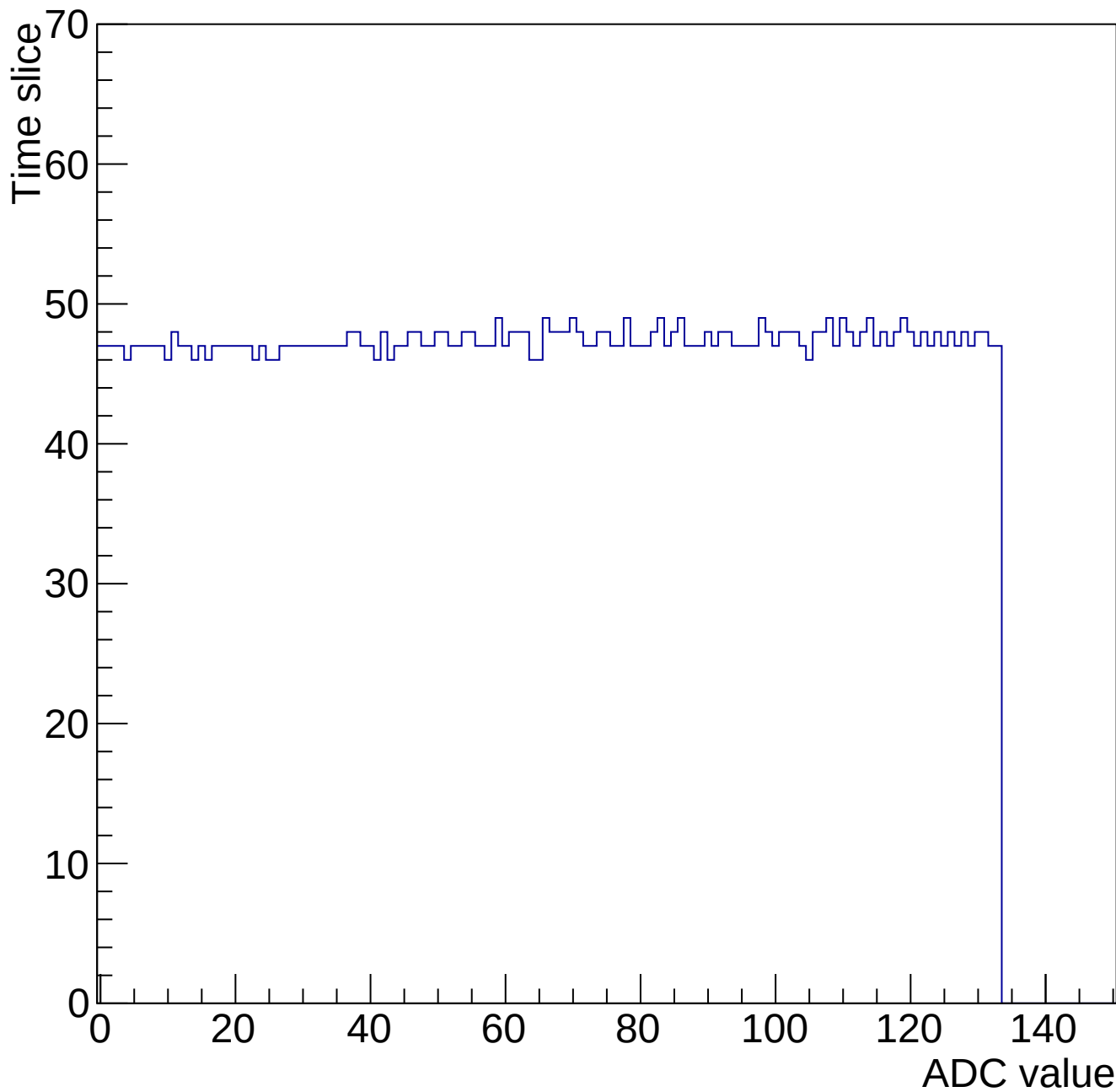
Evt 10 Time mcm 2000 chip 0 chan 8 gain0-shaper7



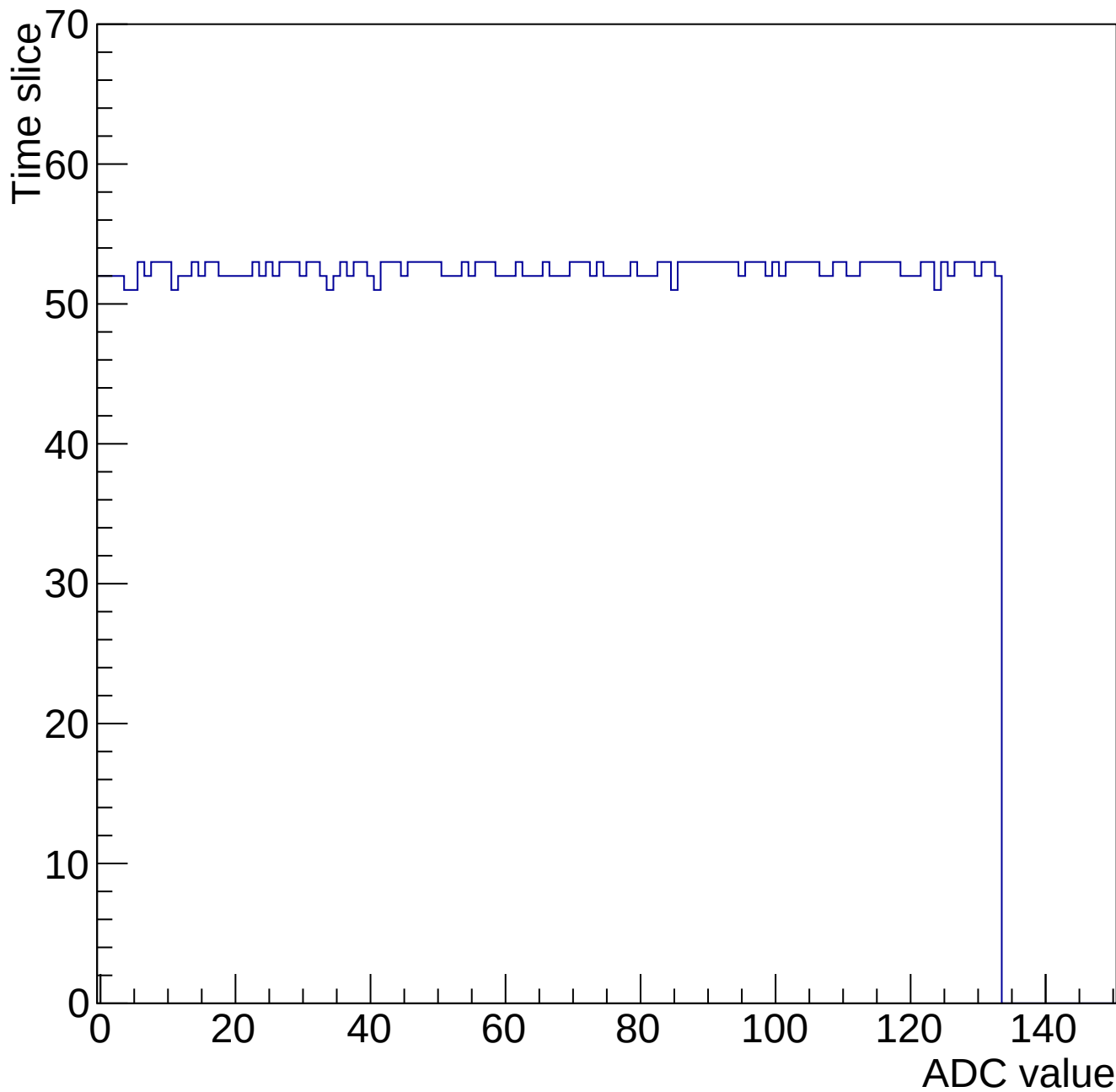
Evt 10 Time mcm 2000 chip 0 chan 9 gain0-shaper7



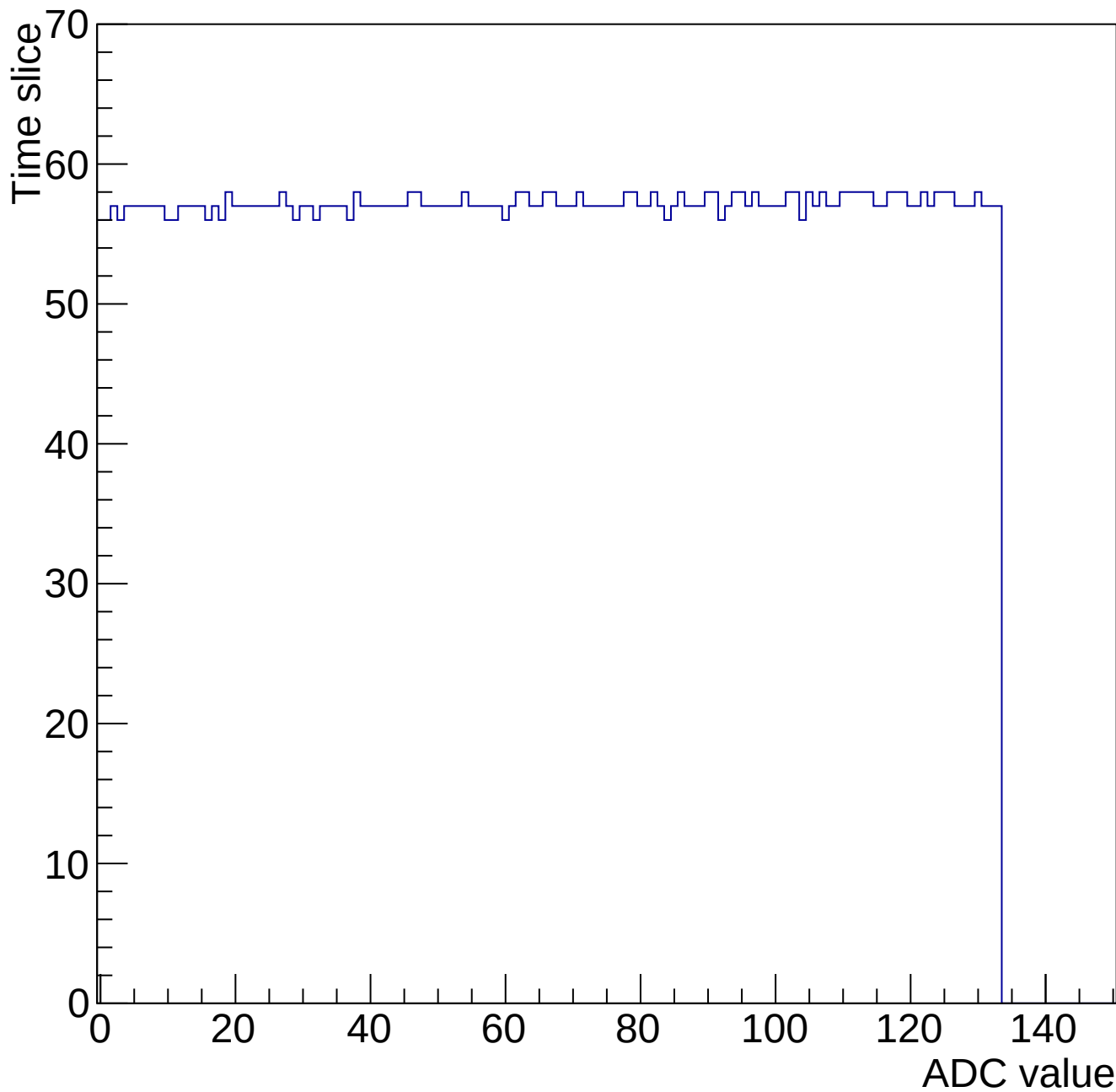
Evt 10 Time mcm 2000 chip 0 chan 10 gain0-shaper7



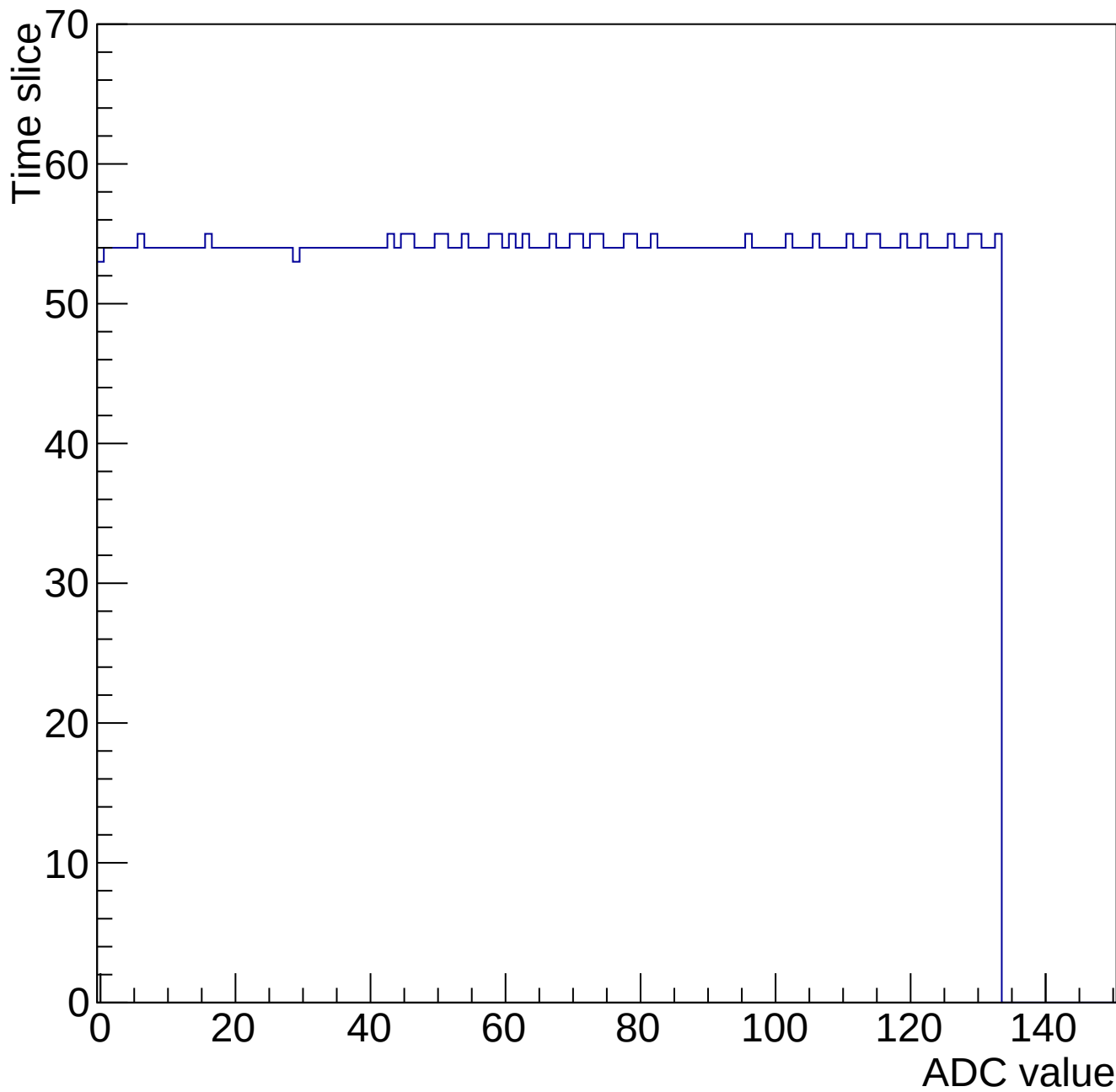
Evt 10 Time mcm 2000 chip 0 chan 11 gain0-shaper7



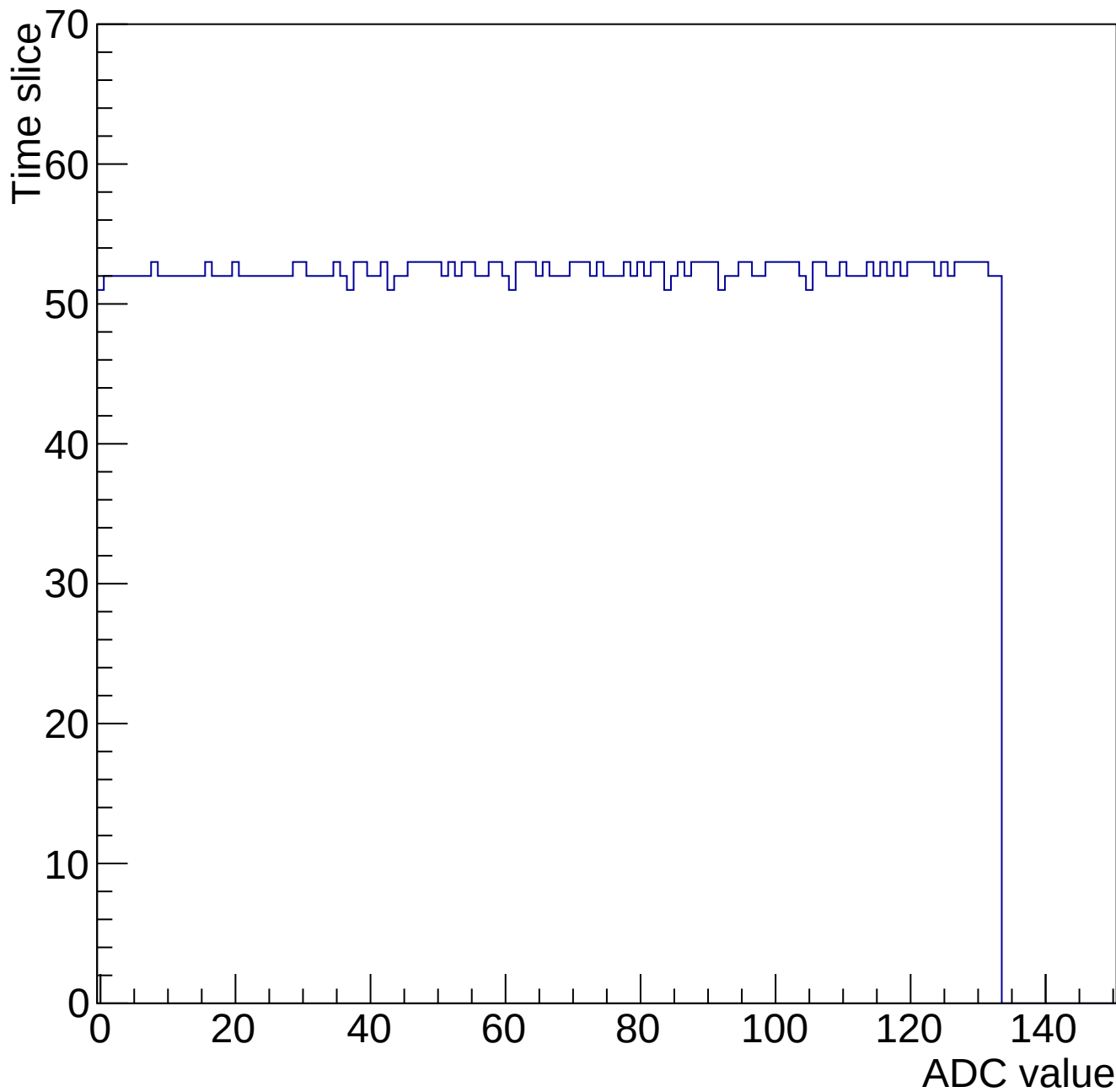
Evt 10 Time mcm 2000 chip 0 chan 12 gain0-shaper7



Evt 10 Time mcm 2000 chip 0 chan 13 gain0-shaper7



Evt 10 Time mcm 2000 chip 0 chan 14 gain0-shaper7



Evt 10 Time mcm 2000 chip 0 chan 15 gain0-shaper7

