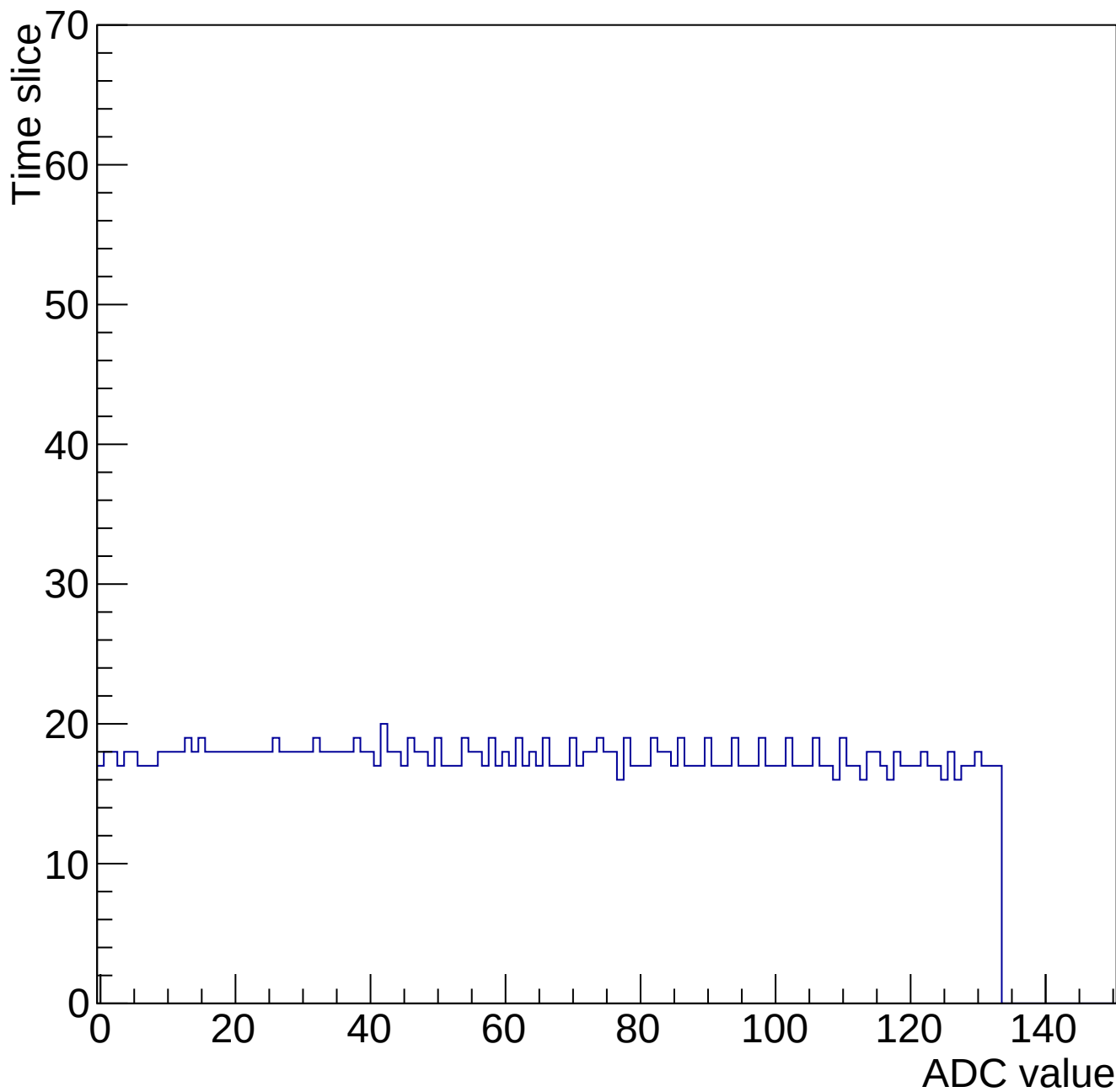
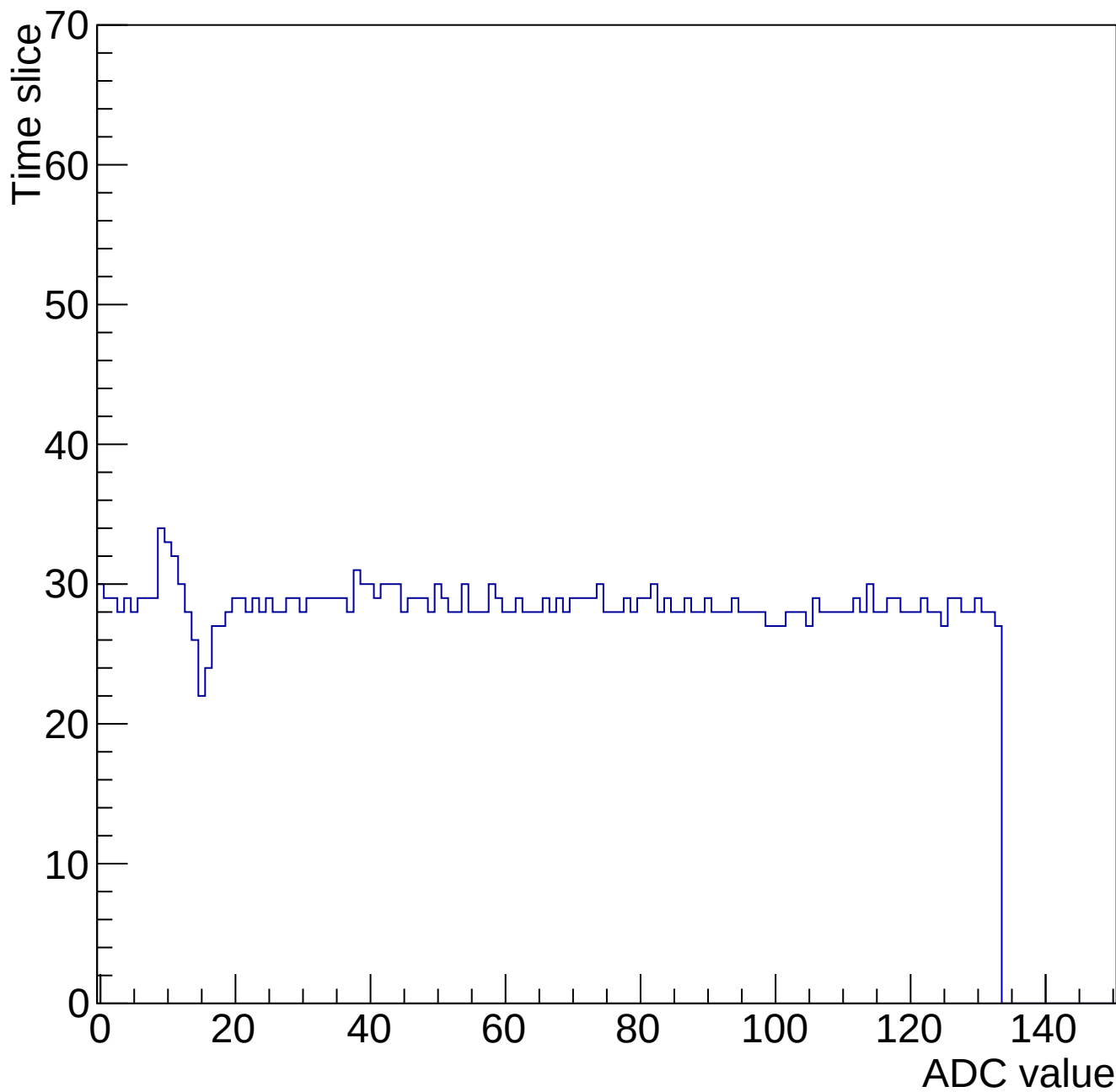


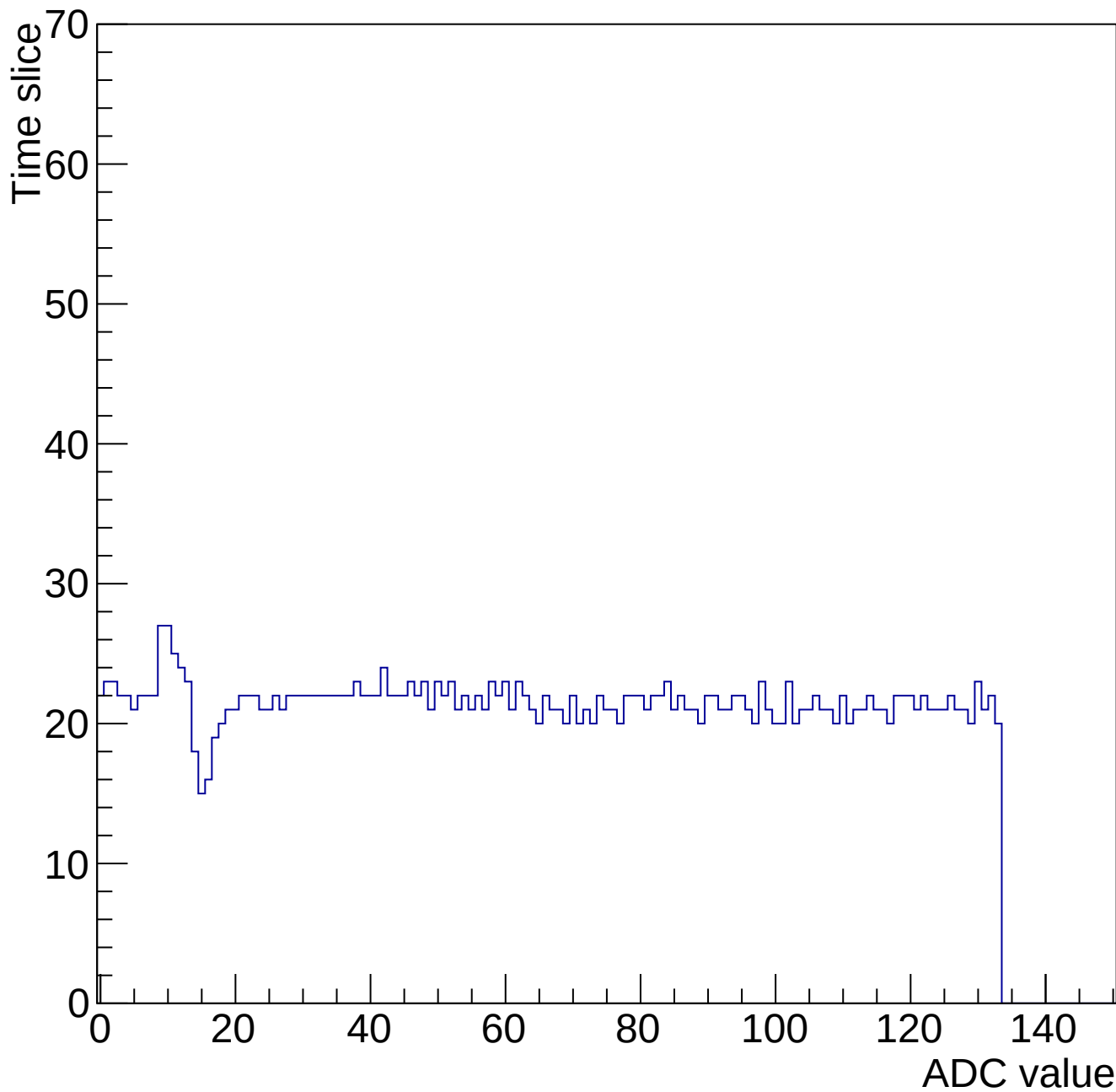
Evt 10 Time mcm 2000 chip 0 chan 0 34619-gain0-shaper0



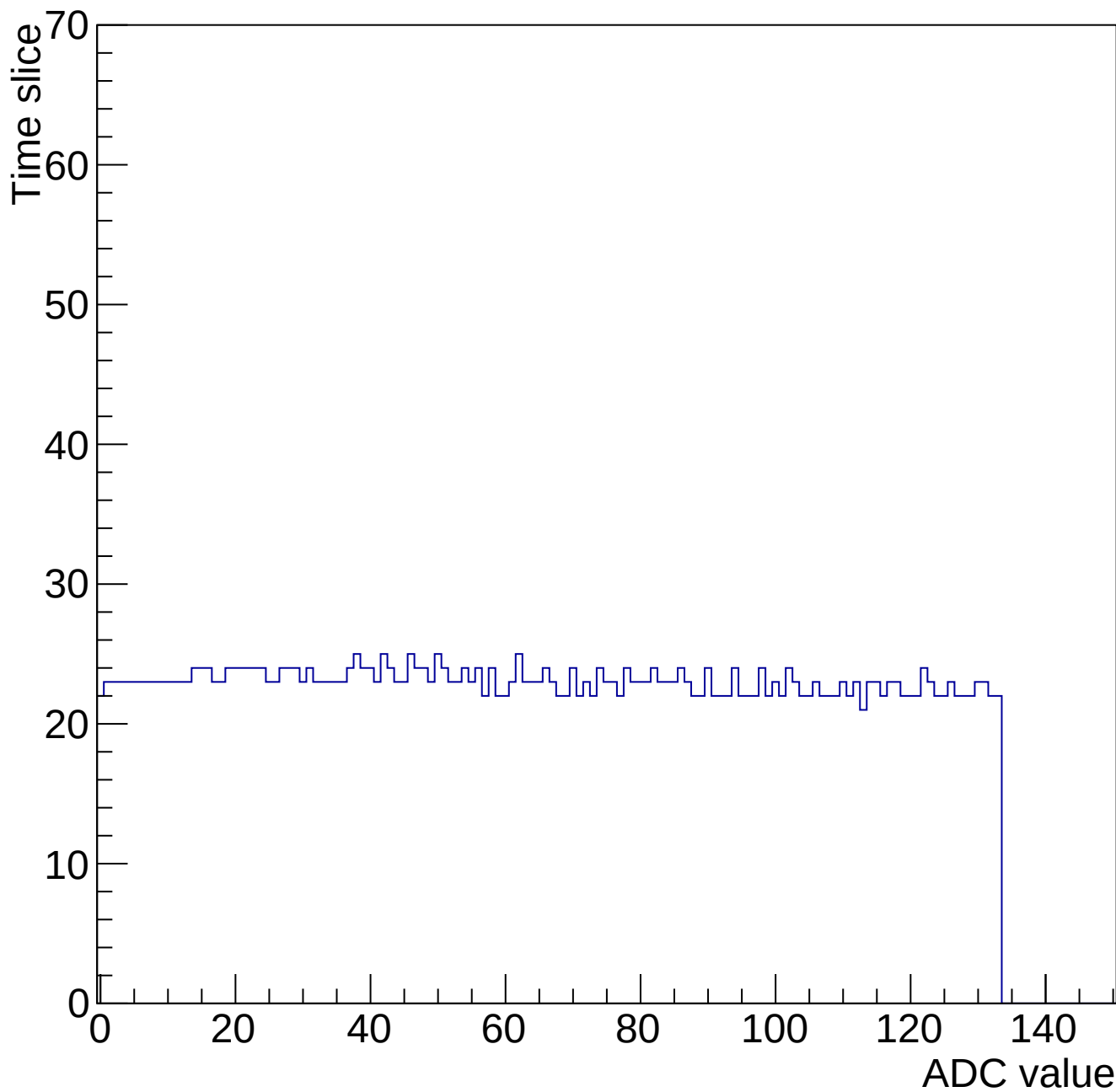
Evt 10 Time mcm 2000 chip 0 chan 1 34619-gain0-shaper0



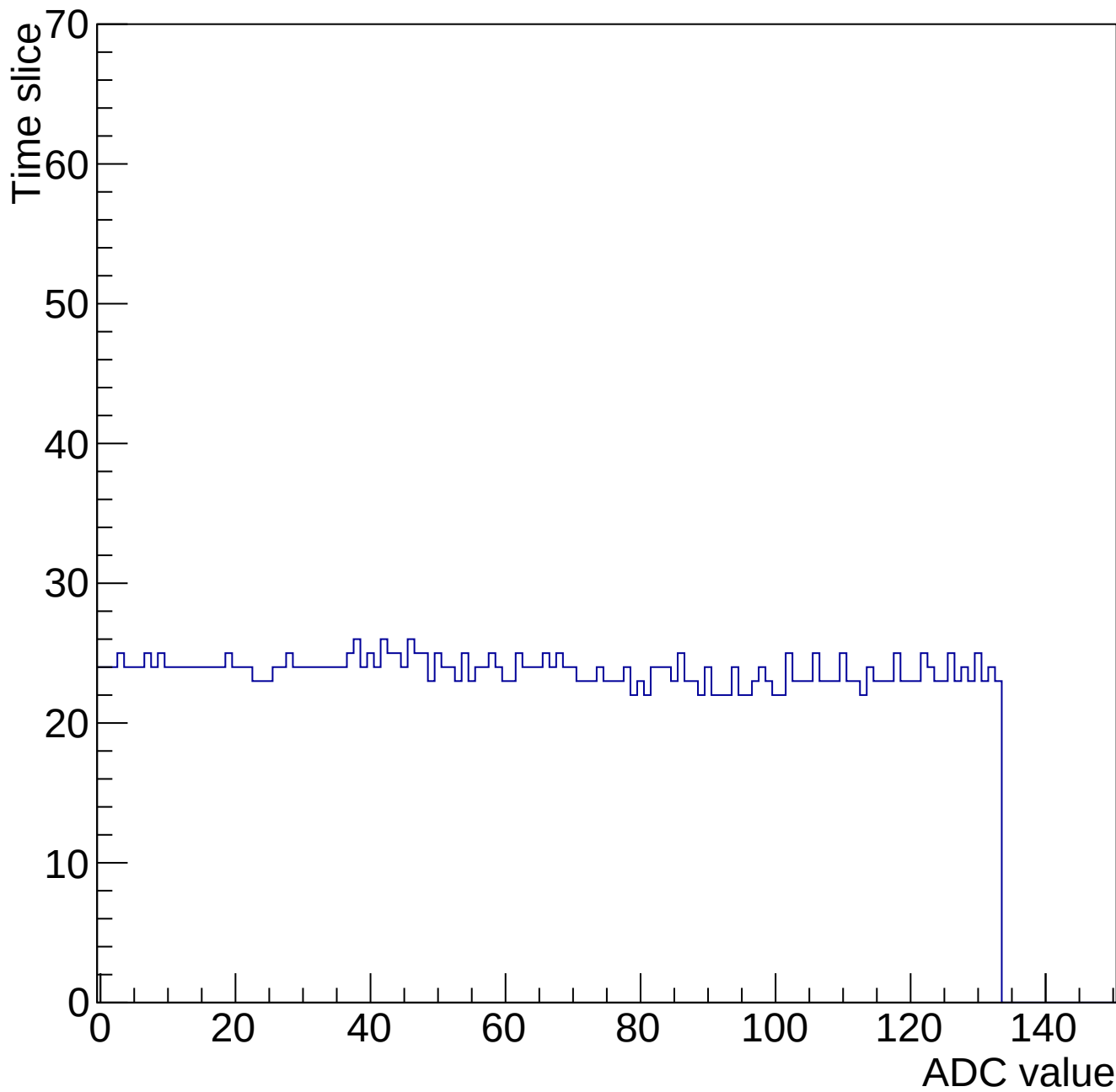
Evt 10 Time mcm 2000 chip 0 chan 2 34619-gain0-shaper0



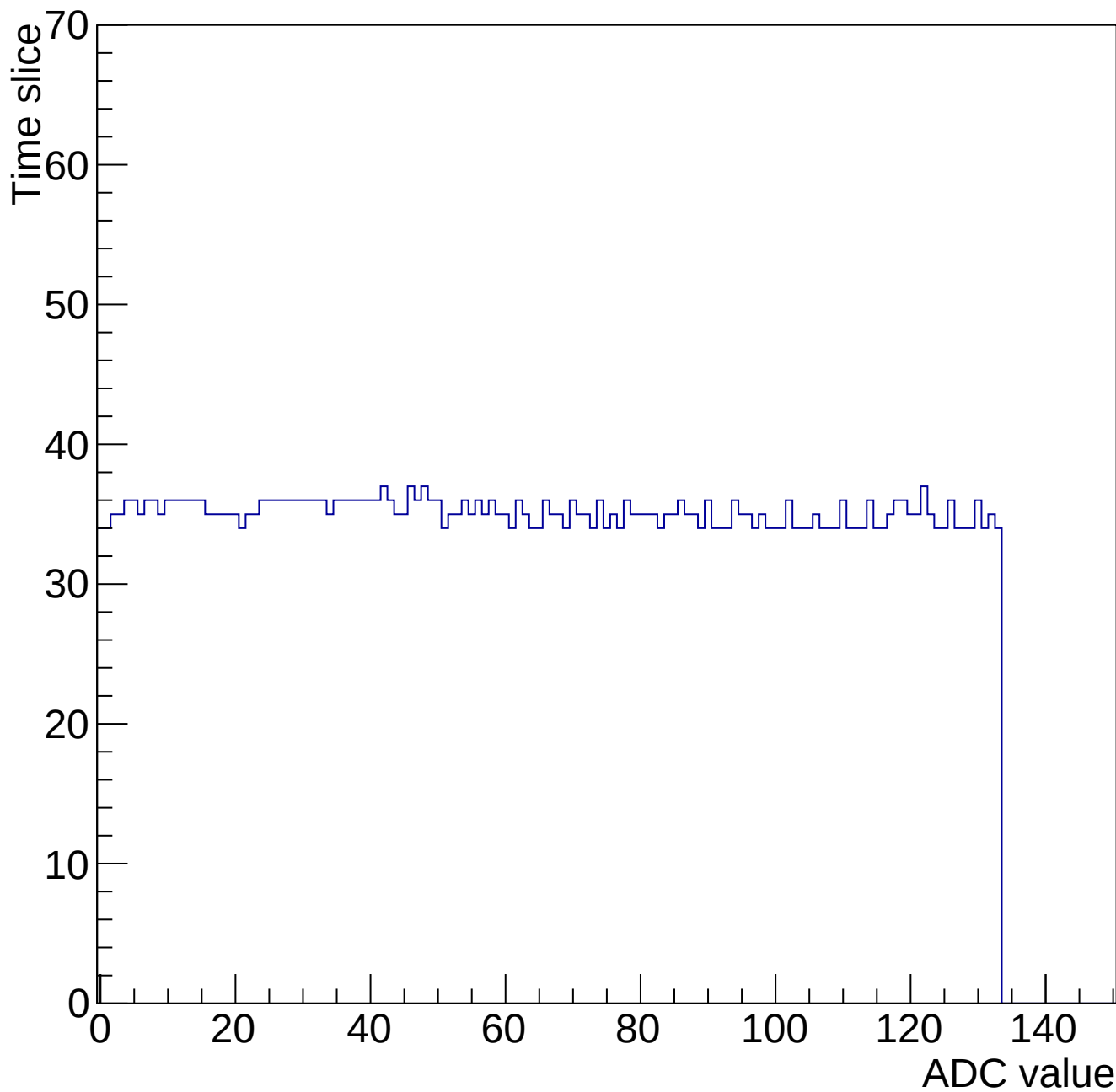
Evt 10 Time mcm 2000 chip 0 chan 3 34619-gain0-shaper0



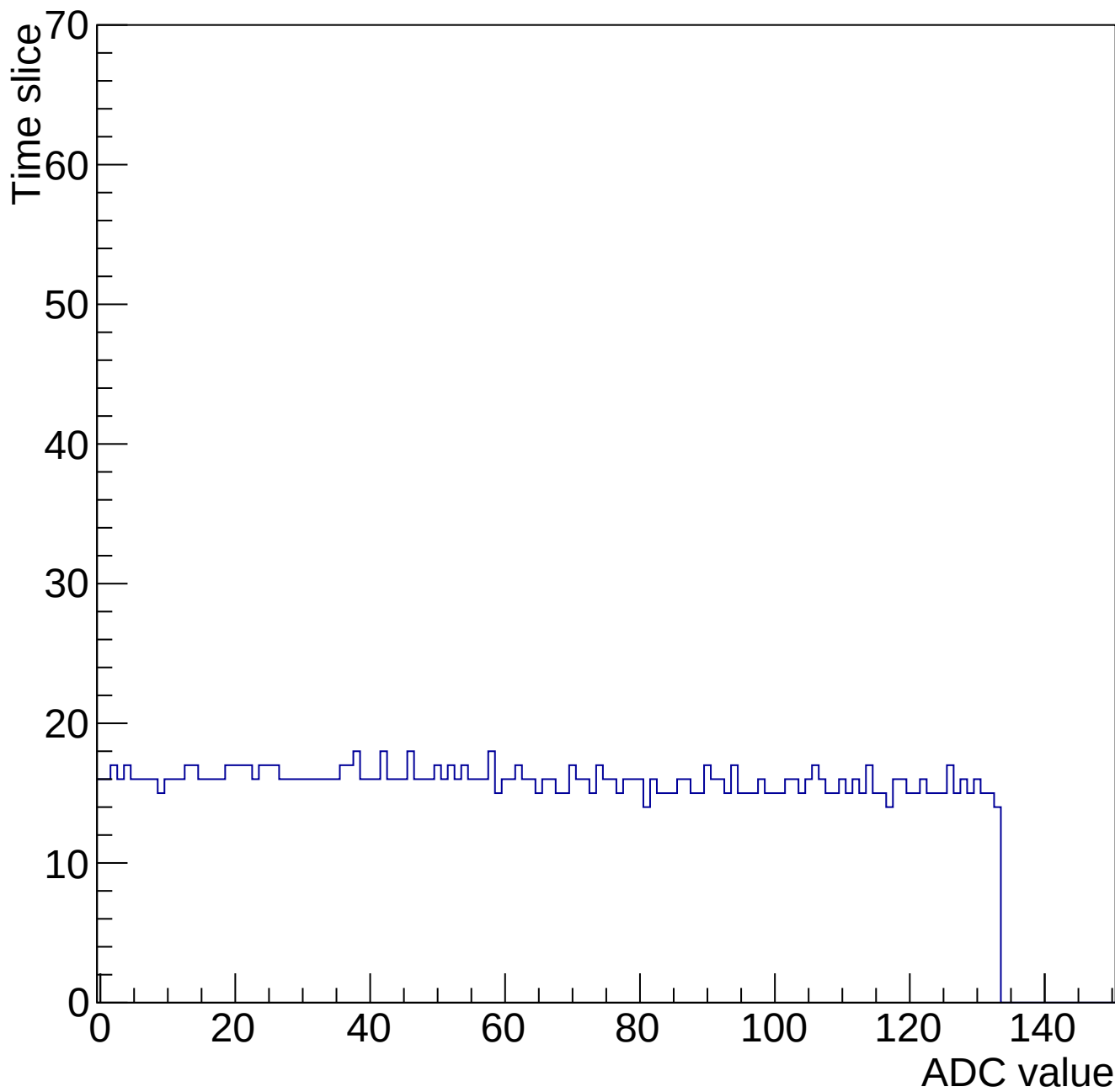
Evt 10 Time mcm 2000 chip 0 chan 4 34619-gain0-shaper0



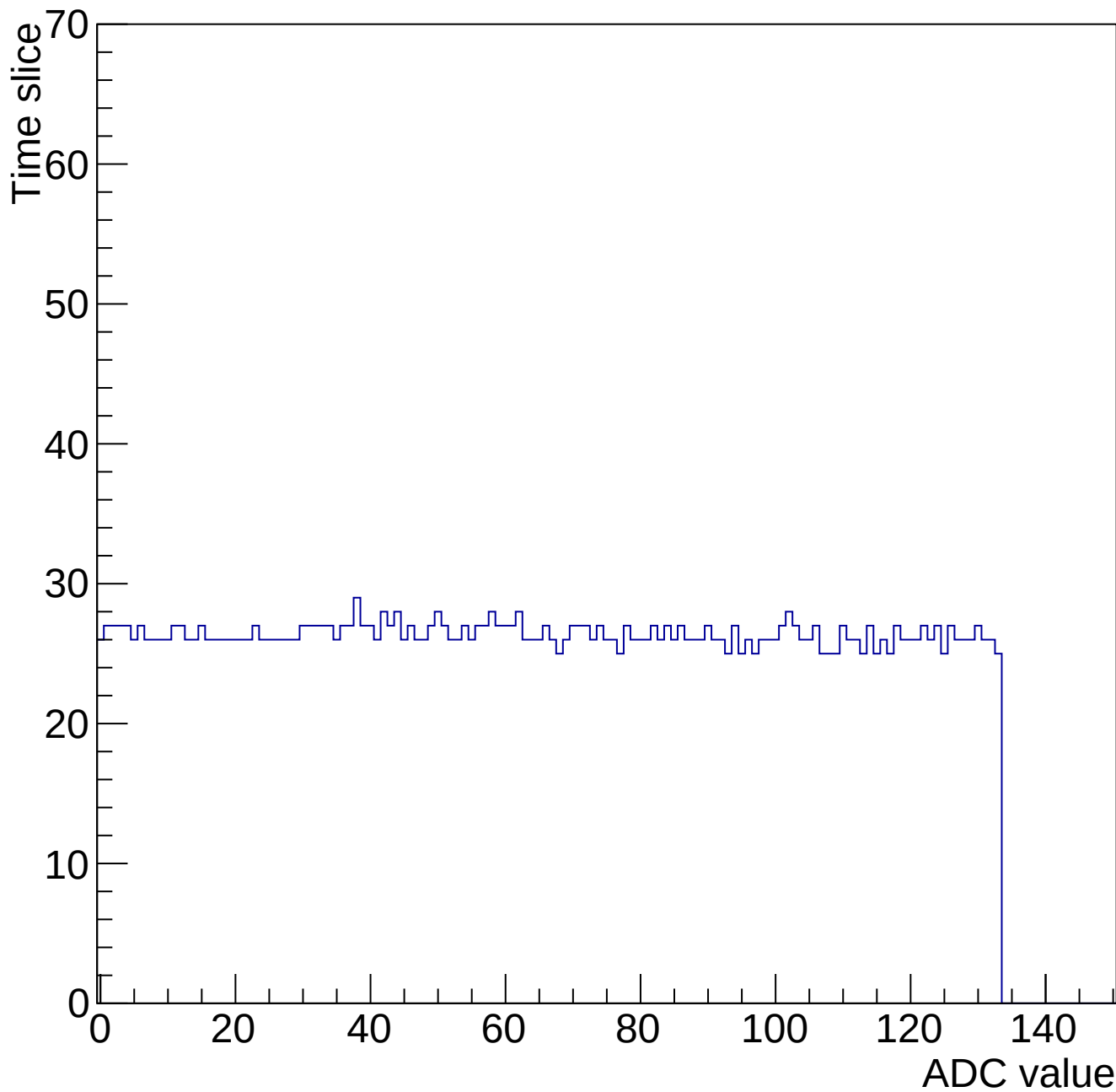
Evt 10 Time mcm 2000 chip 0 chan 5 34619-gain0-shaper0



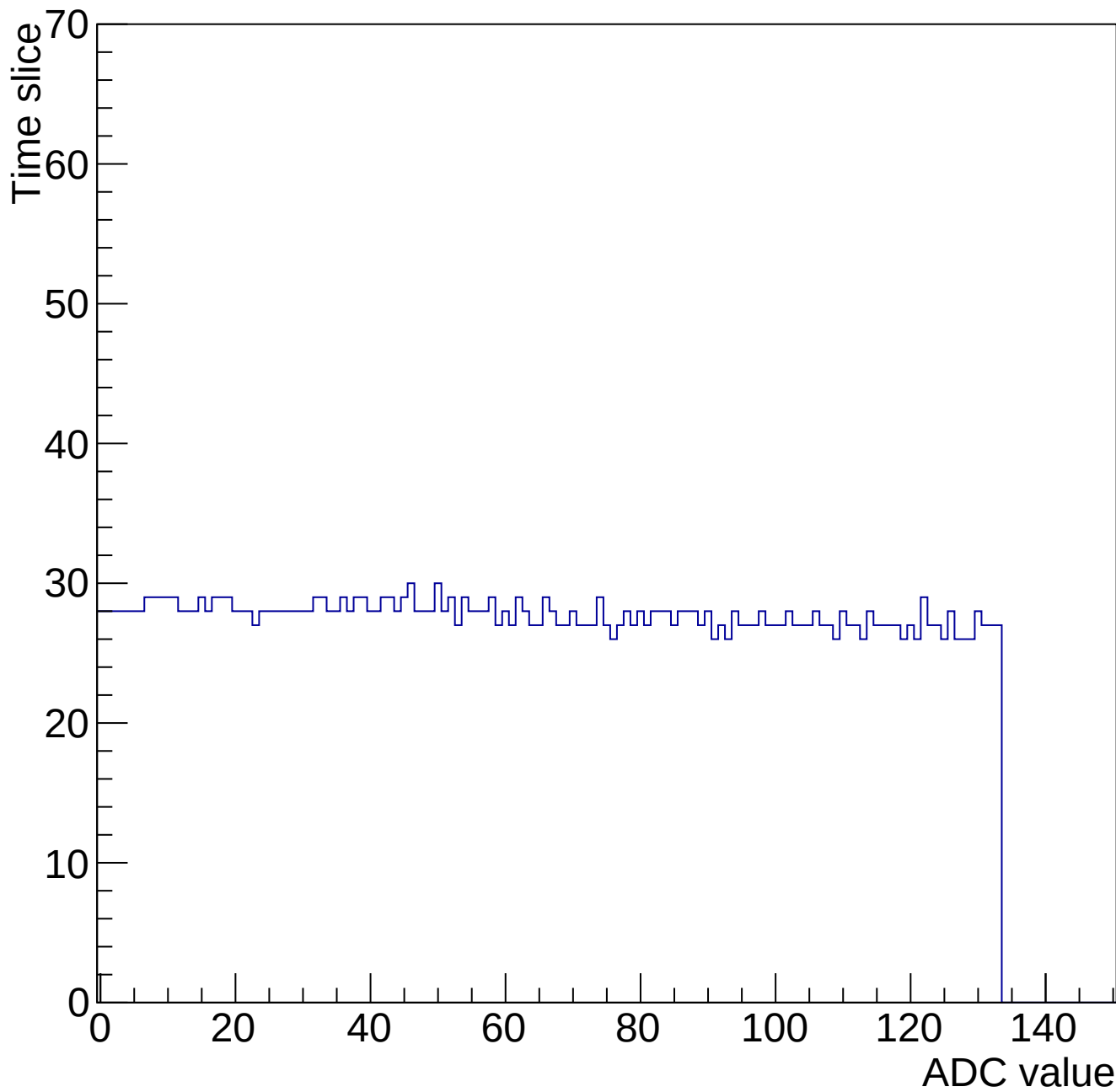
Evt 10 Time mcm 2000 chip 0 chan 6 34619-gain0-shaper0



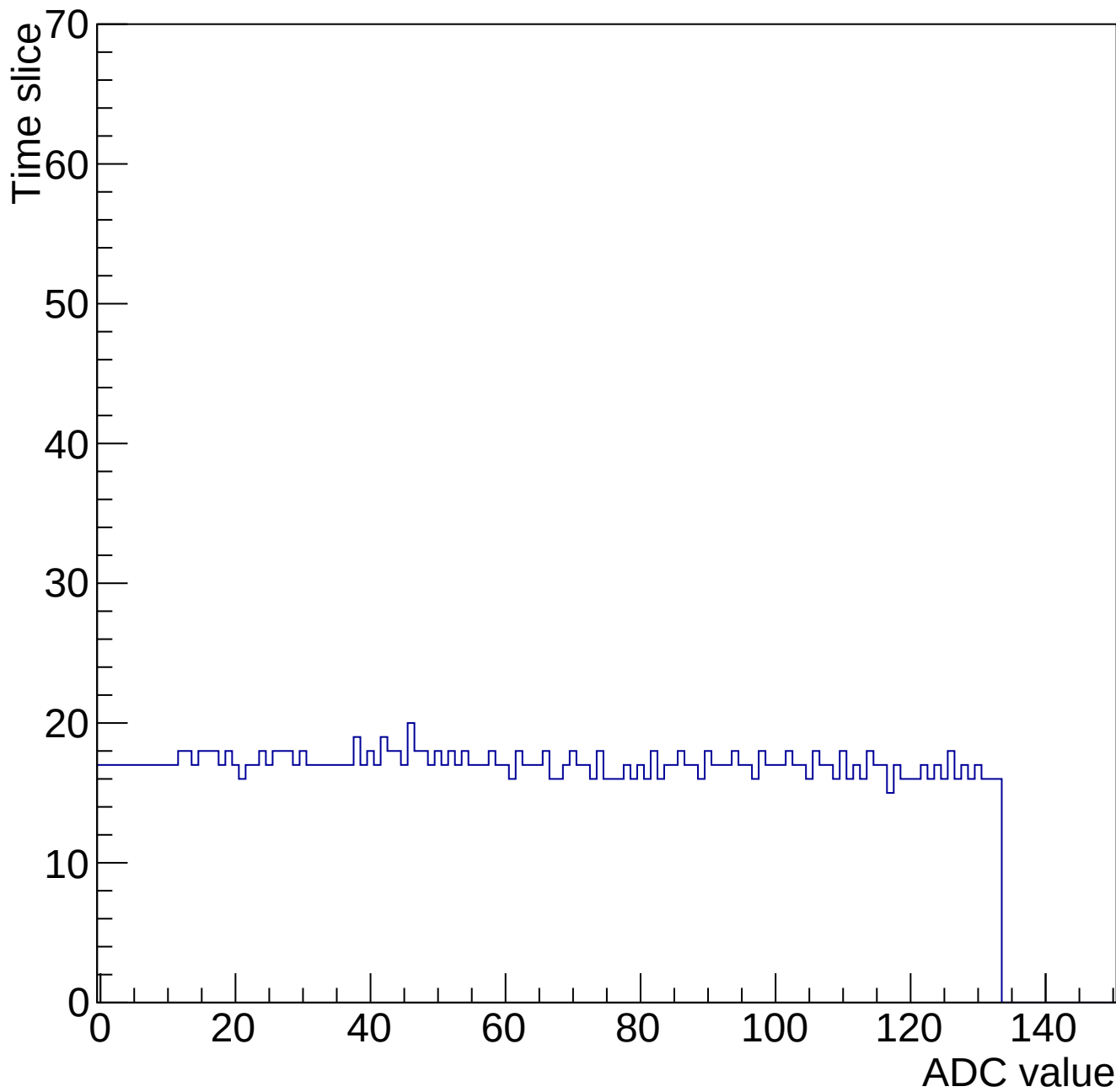
Evt 10 Time mcm 2000 chip 0 chan 7 34619-gain0-shaper0



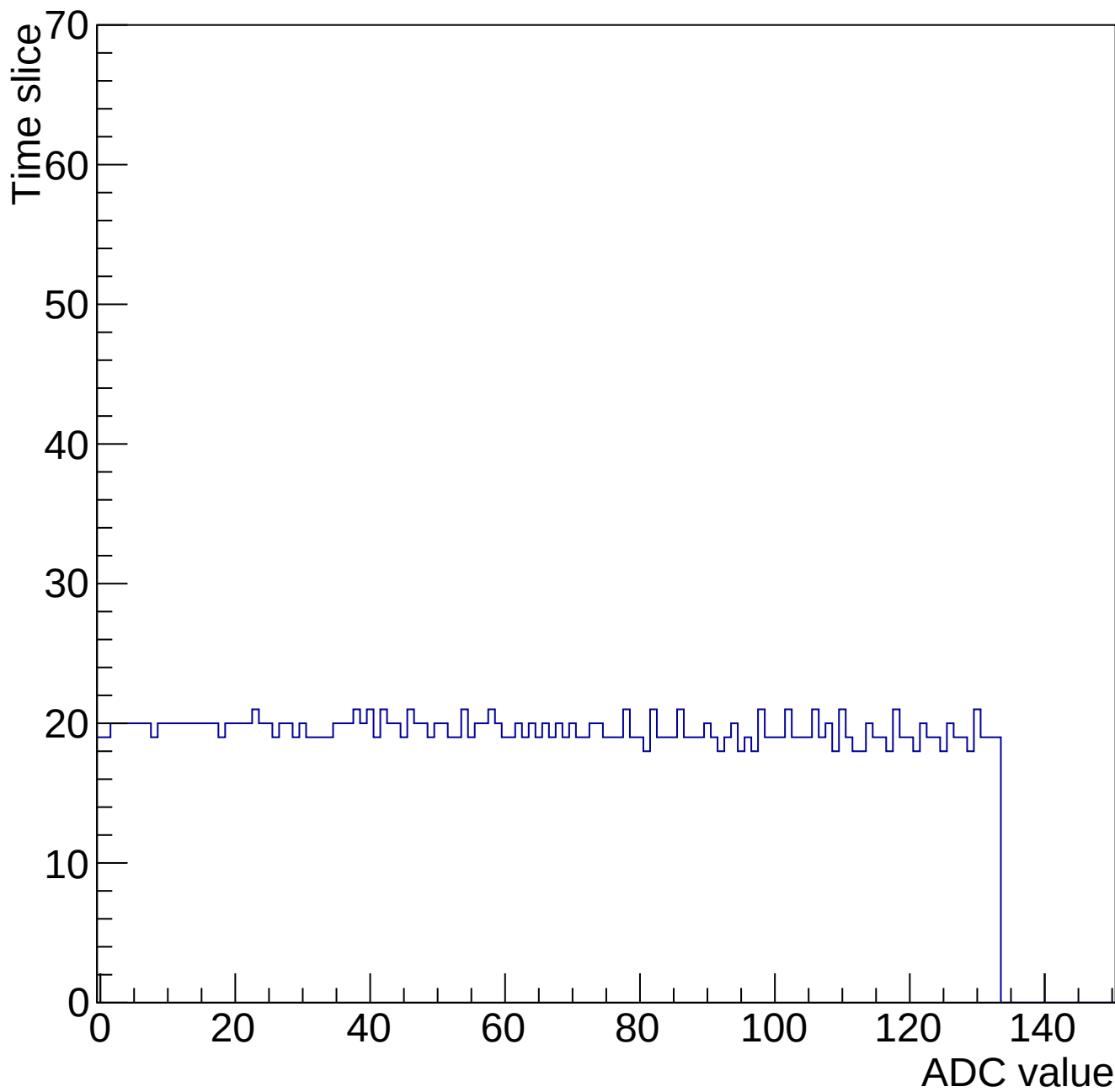
Evt 10 Time mcm 2000 chip 0 chan 8 34619-gain0-shaper0



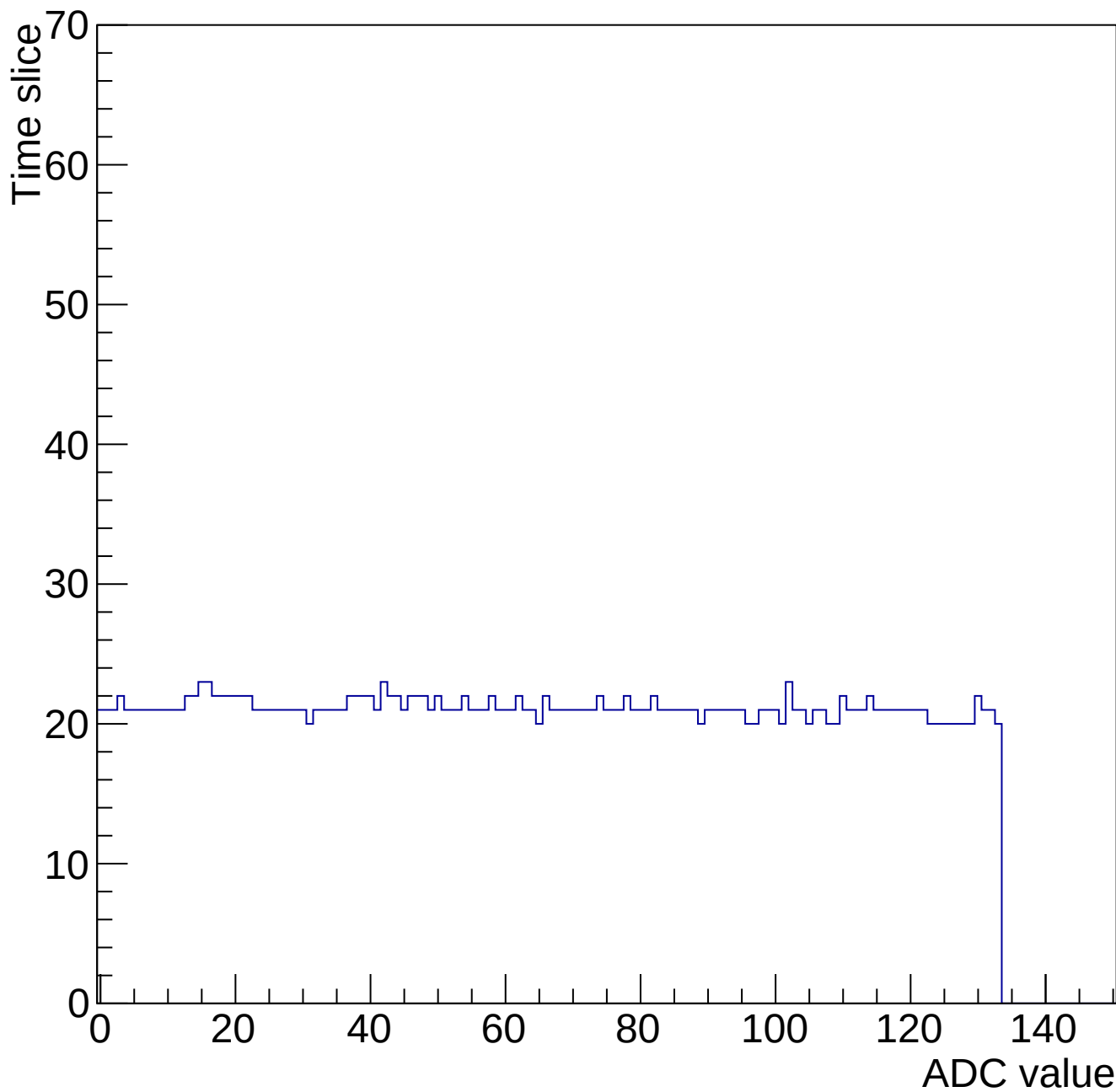
Evt 10 Time mcm 2000 chip 0 chan 9 34619-gain0-shaper0



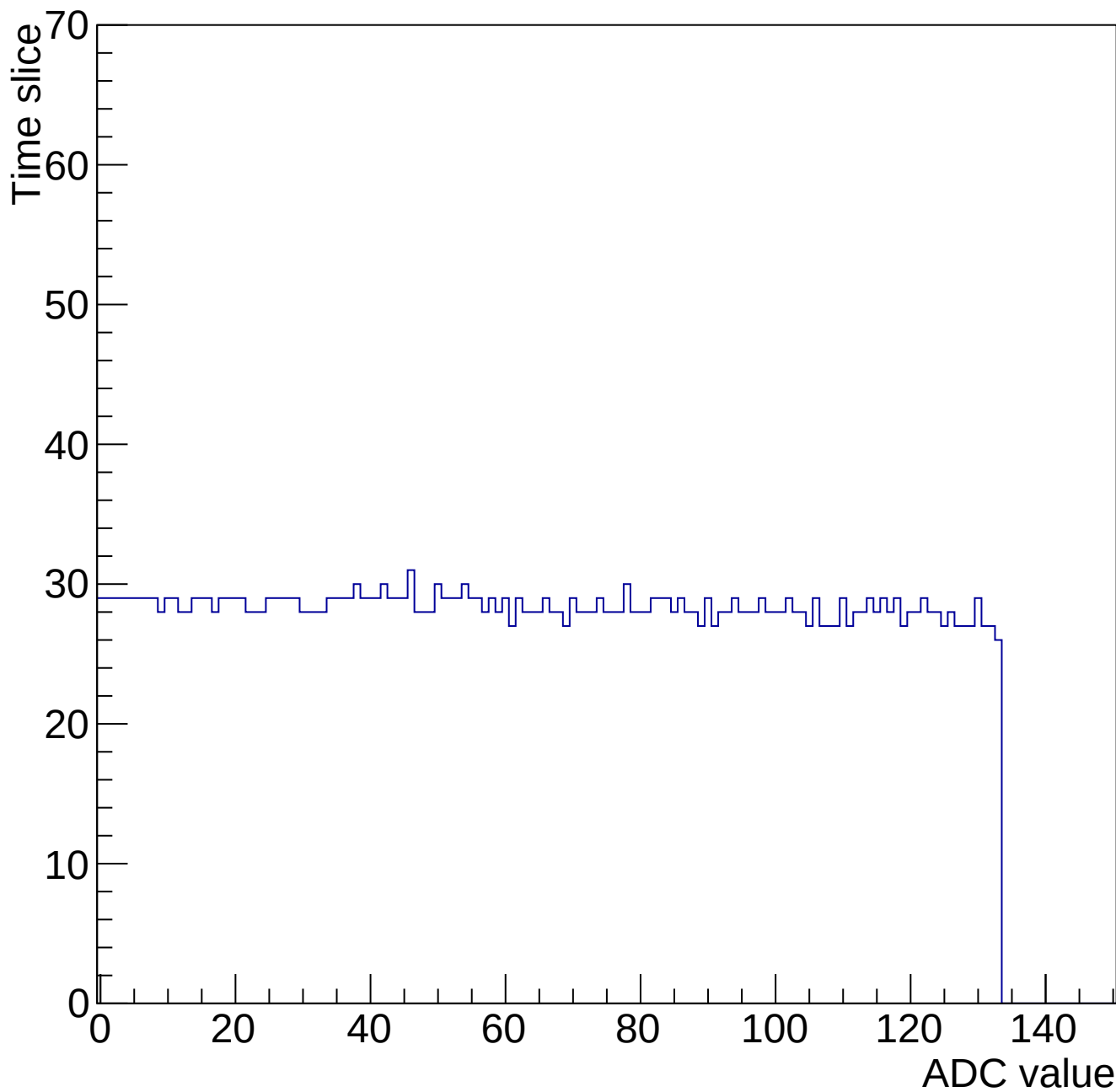
Evt 10 Time mcm 2000 chip 0 chan 10 34619-gain0-shaper0



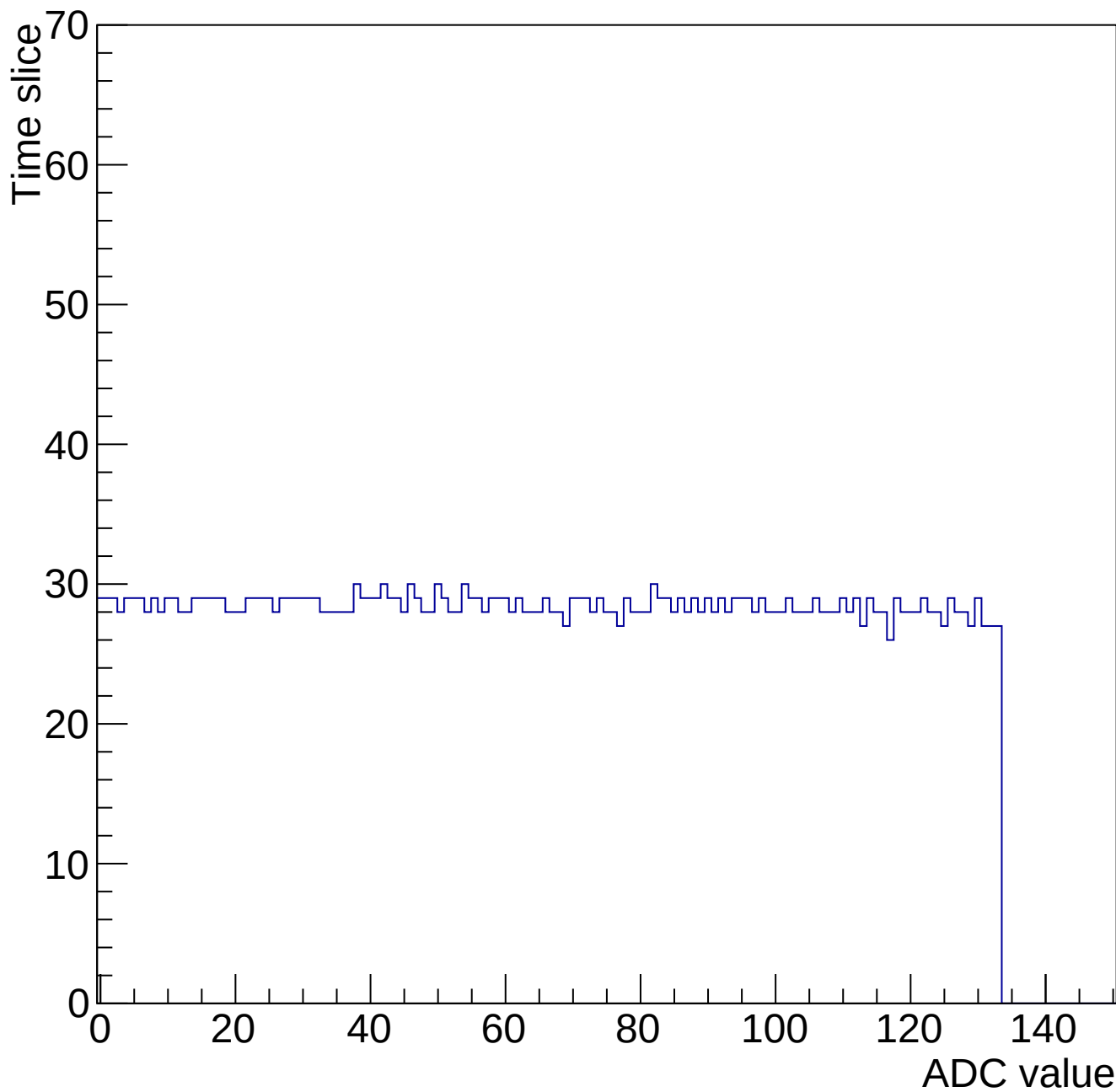
Evt 10 Time mcm 2000 chip 0 chan 11 34619-gain0-shaper0



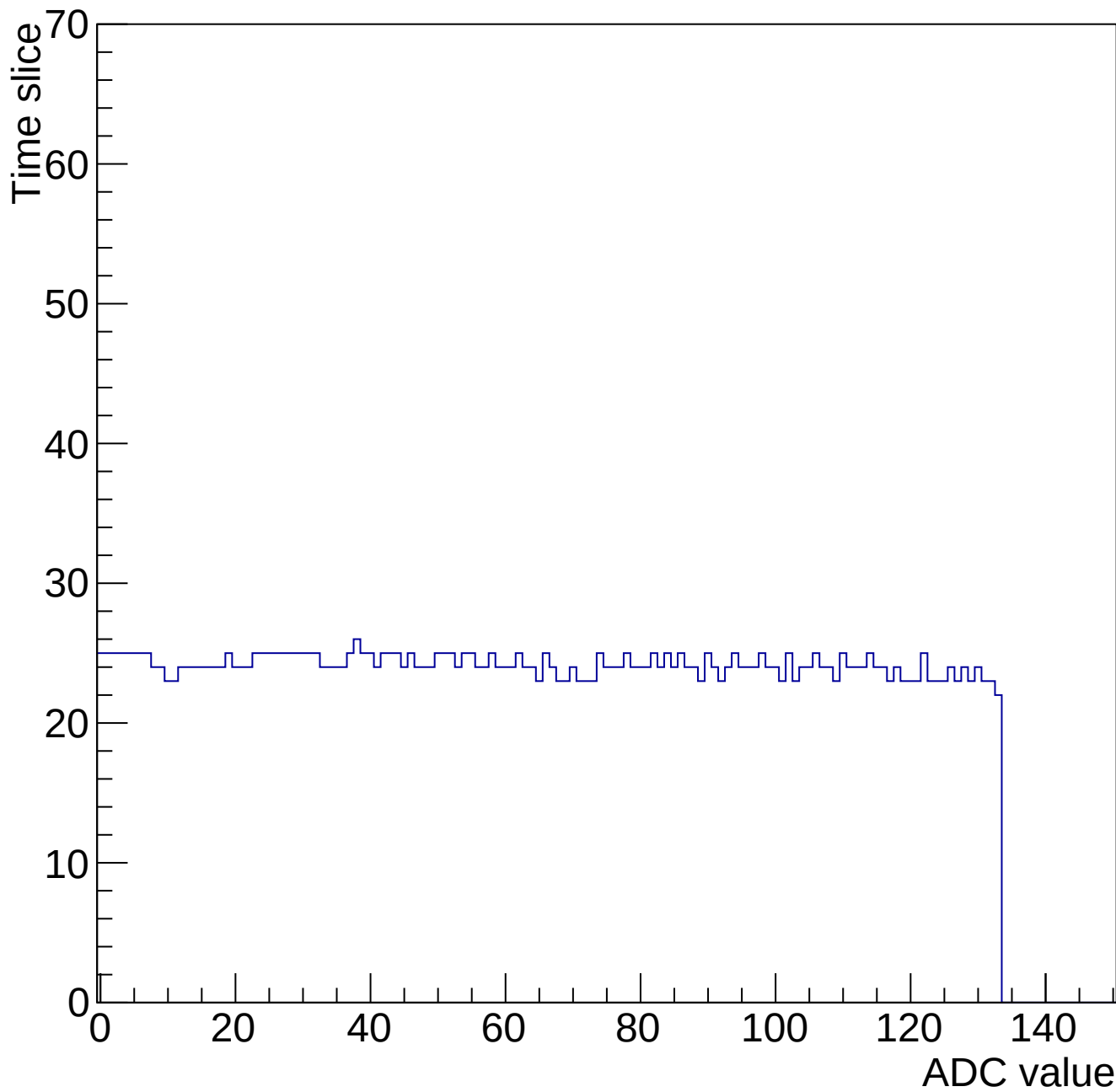
Evt 10 Time mcm 2000 chip 0 chan 12 34619-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 13 34619-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 14 34619-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 15 34619-gain0-shaper0

