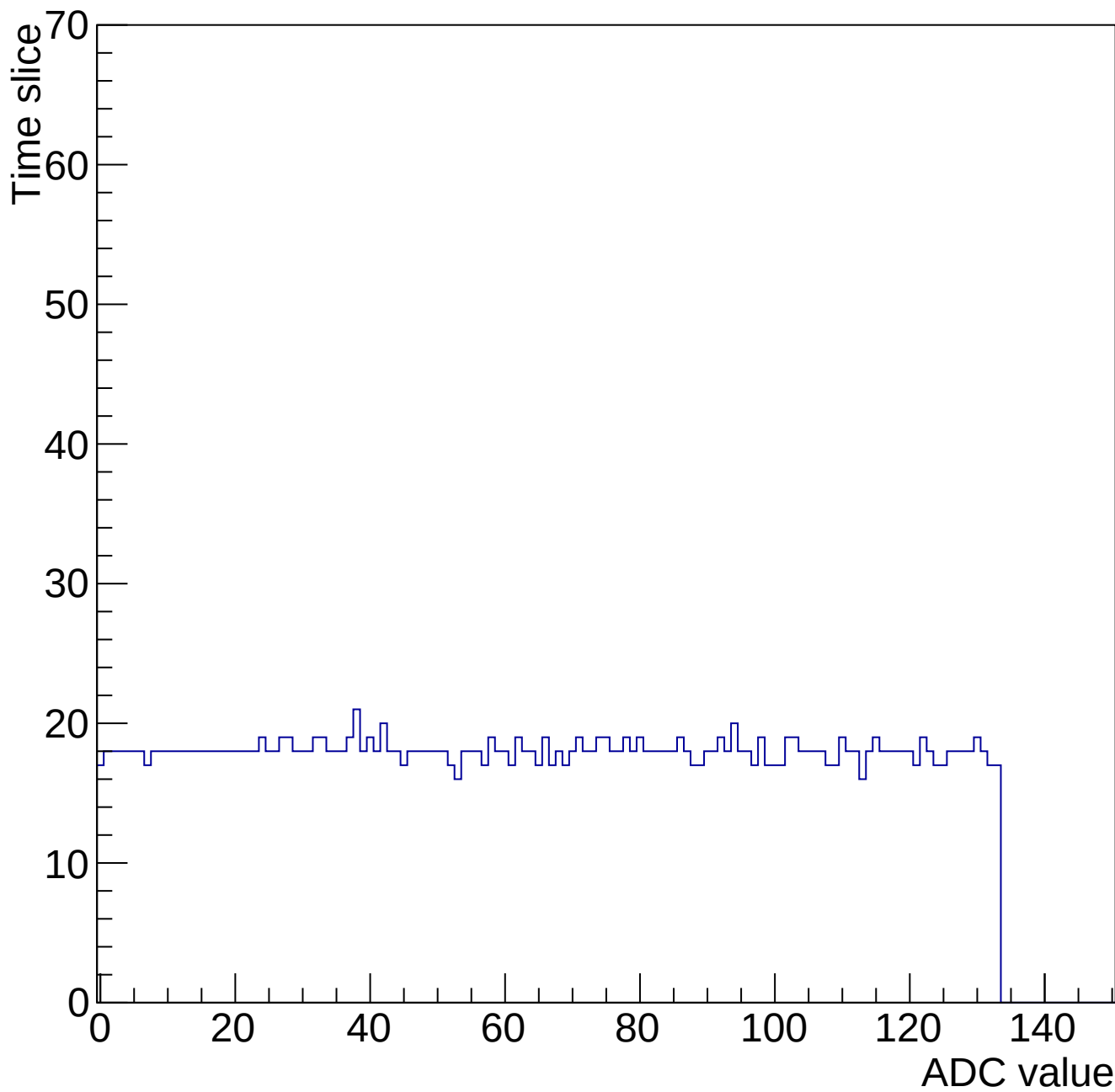
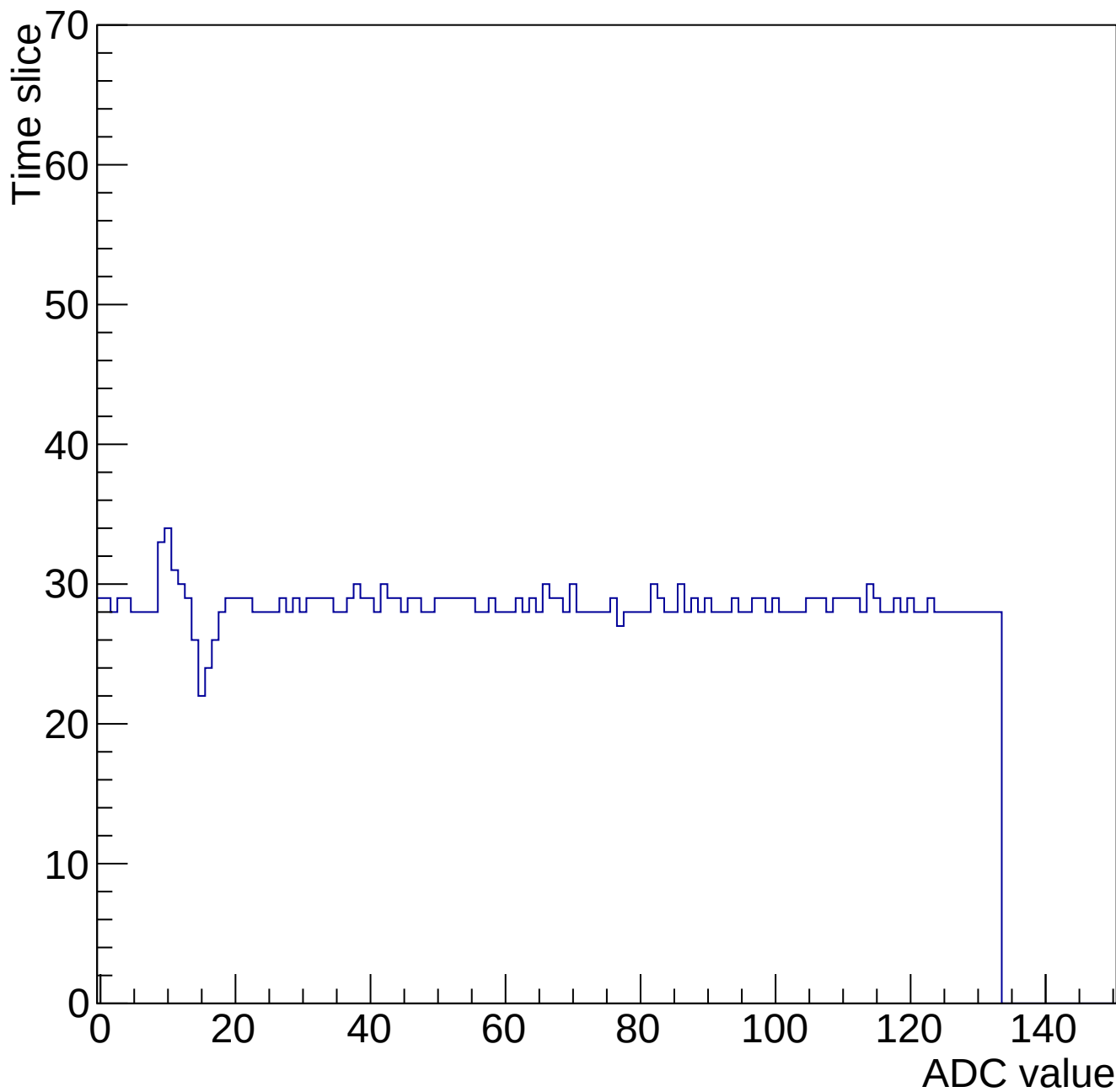


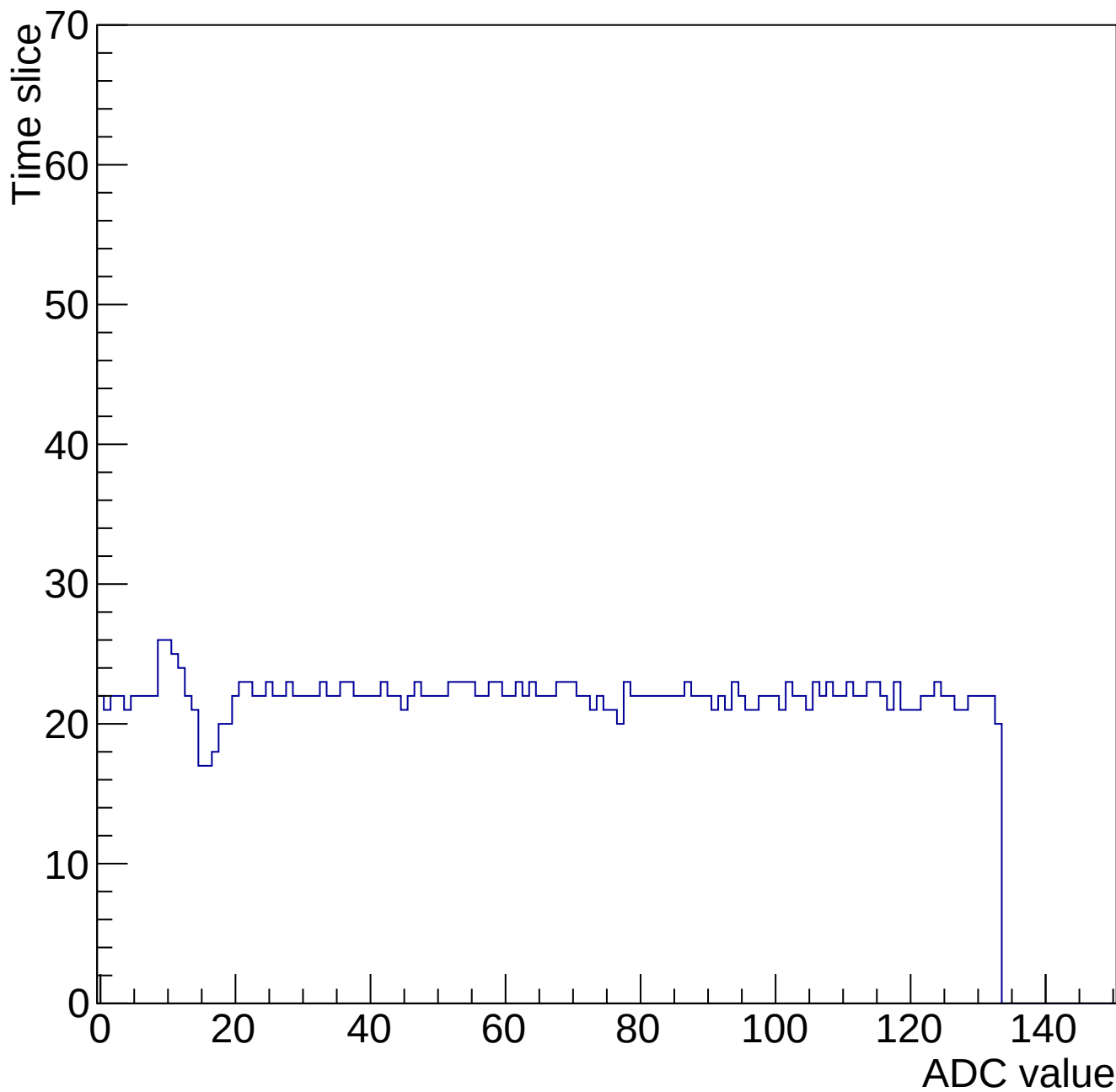
Evt 10 Time mcm 2000 chip 0 chan 0 34626-gain0-shaper0



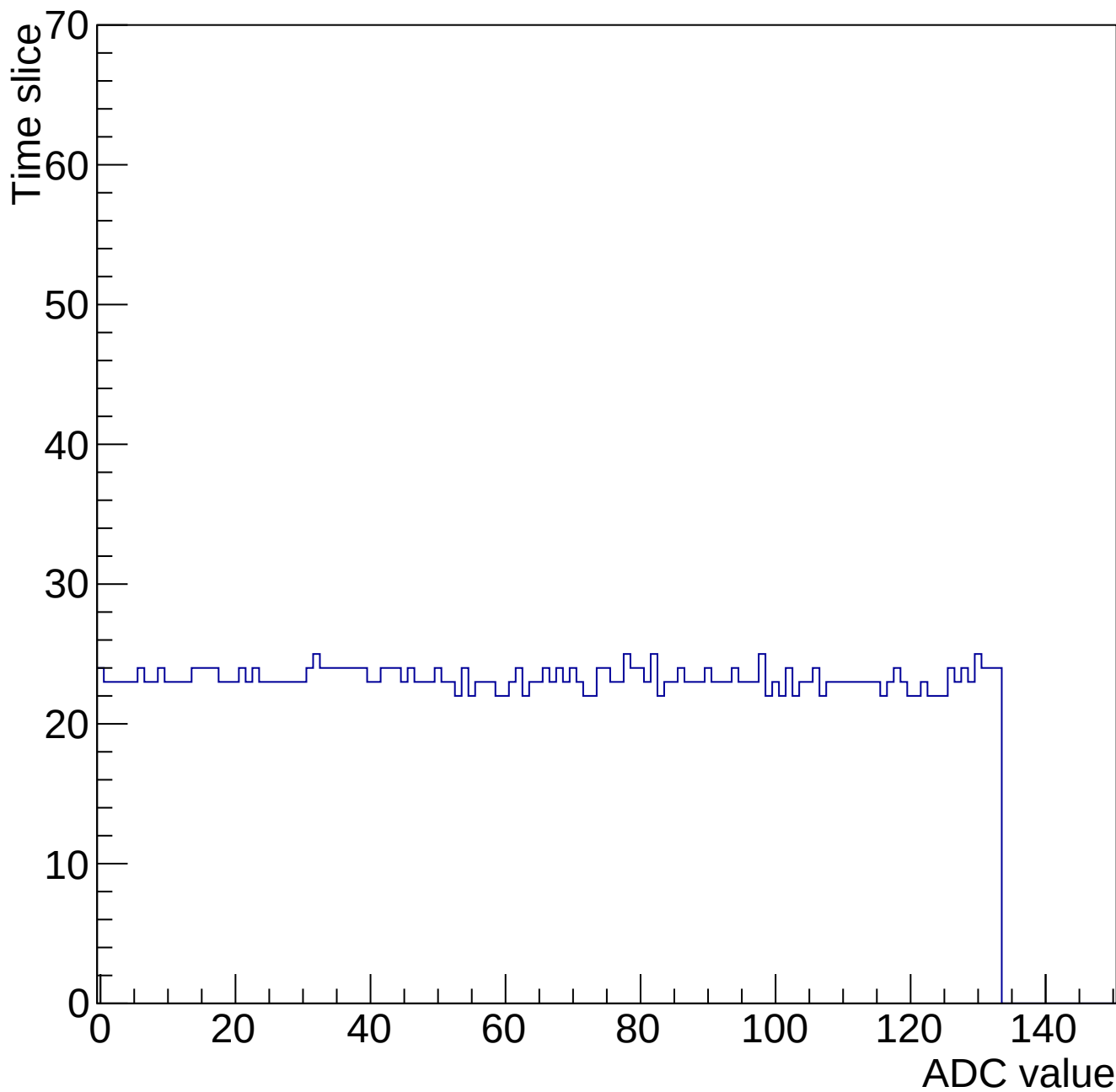
Evt 10 Time mcm 2000 chip 0 chan 1 34626-gain0-shaper0



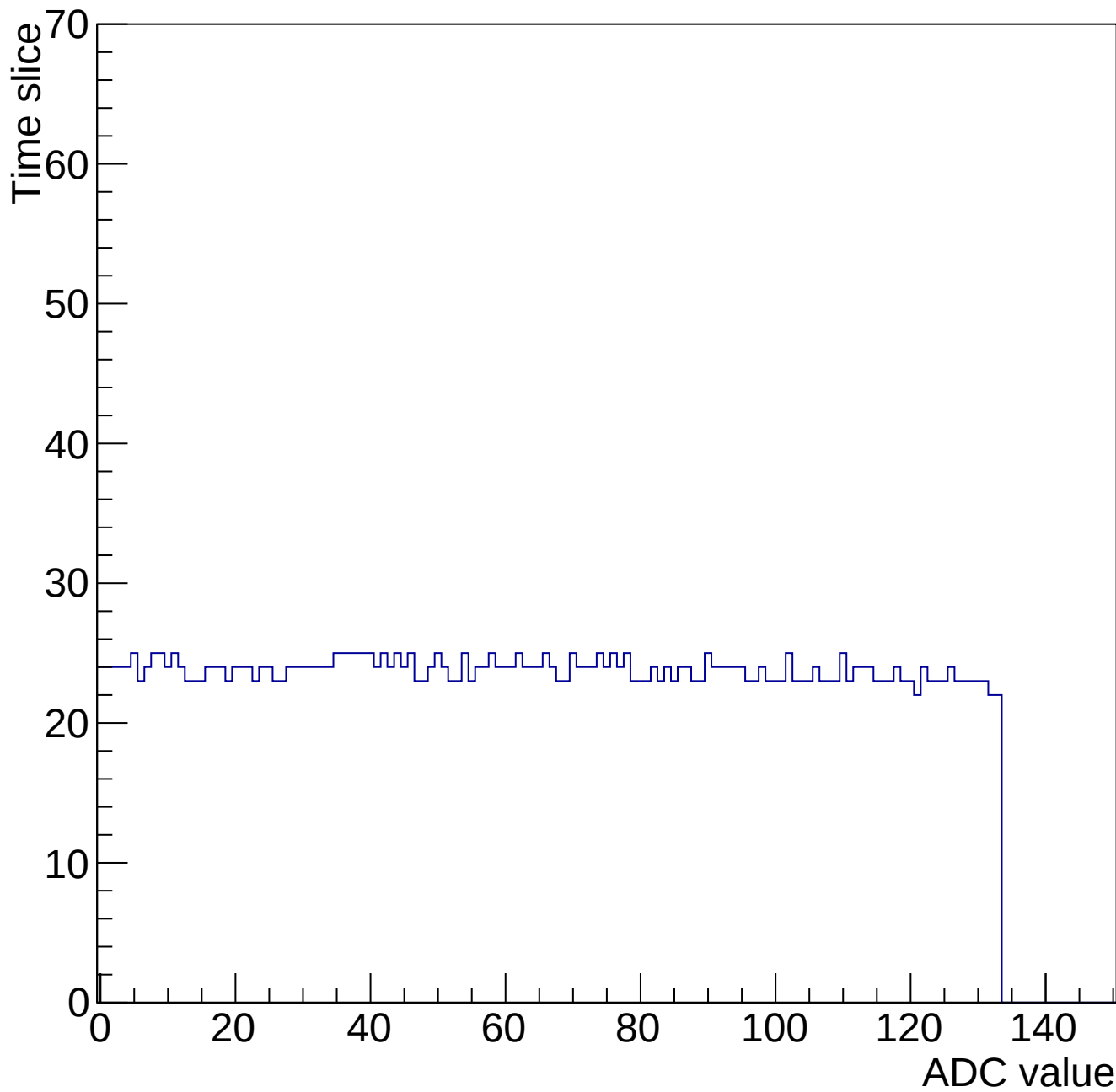
Evt 10 Time mcm 2000 chip 0 chan 2 34626-gain0-shaper0



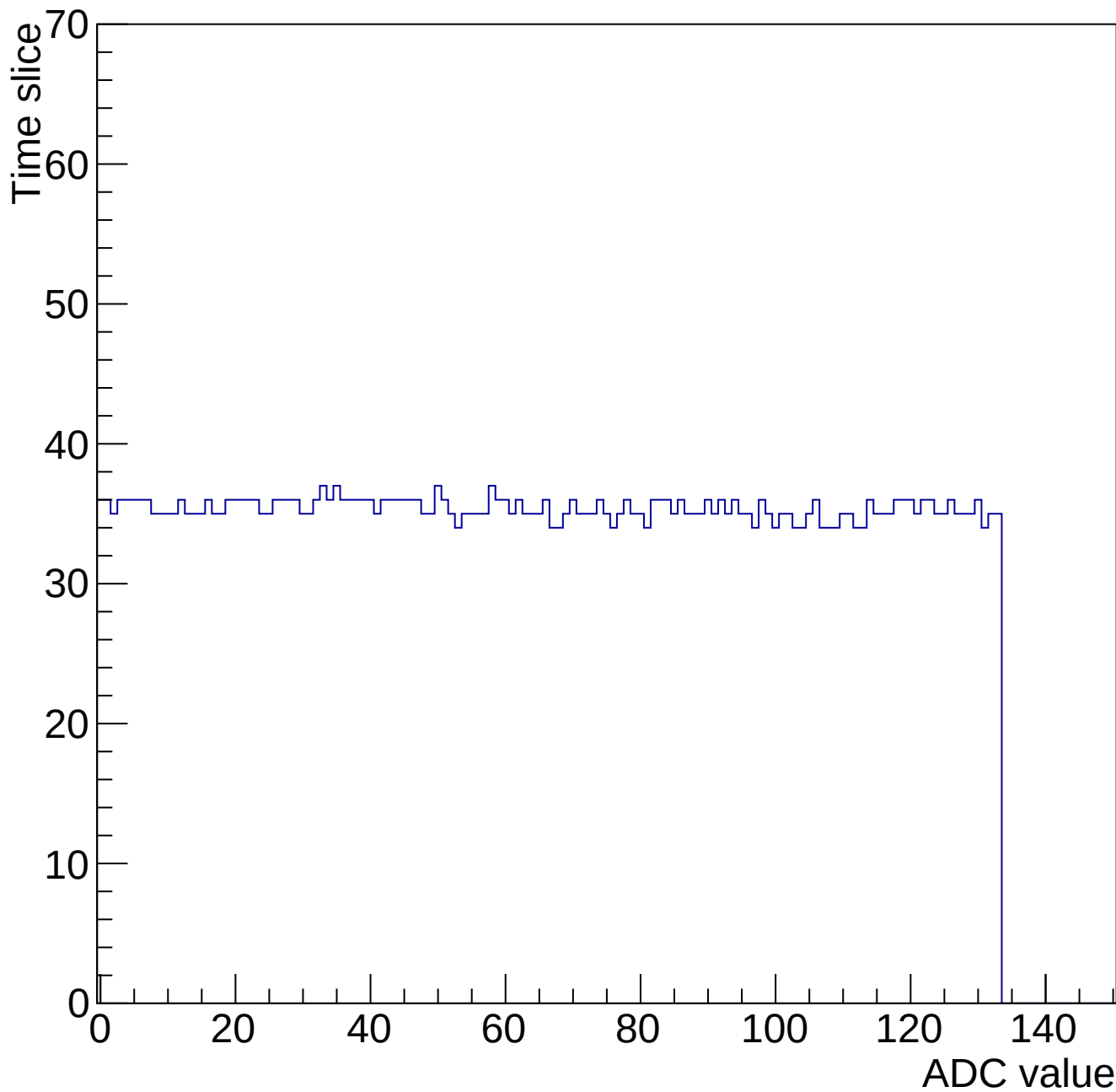
Evt 10 Time mcm 2000 chip 0 chan 3 34626-gain0-shaper0



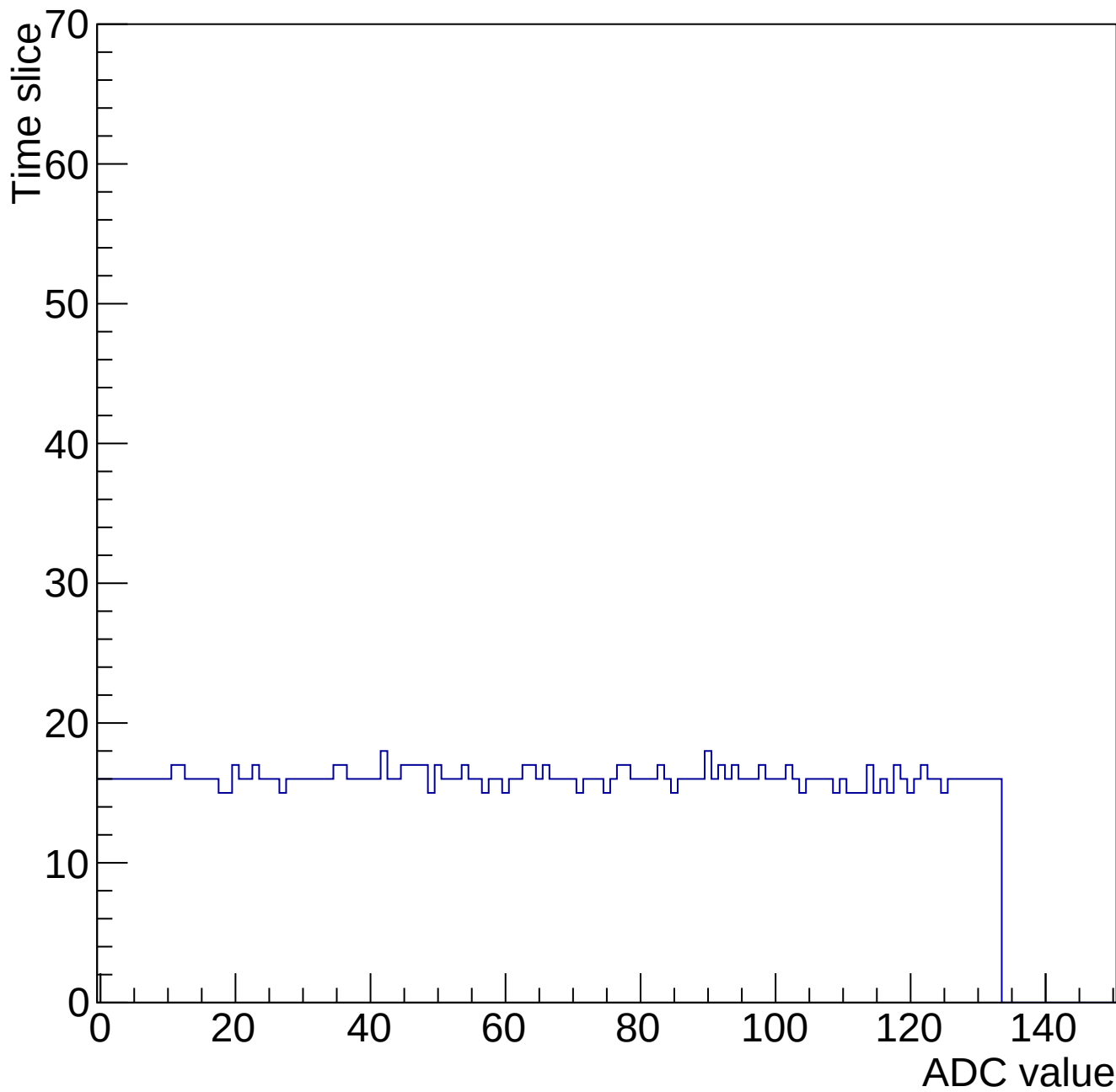
Evt 10 Time mcm 2000 chip 0 chan 4 34626-gain0-shaper0



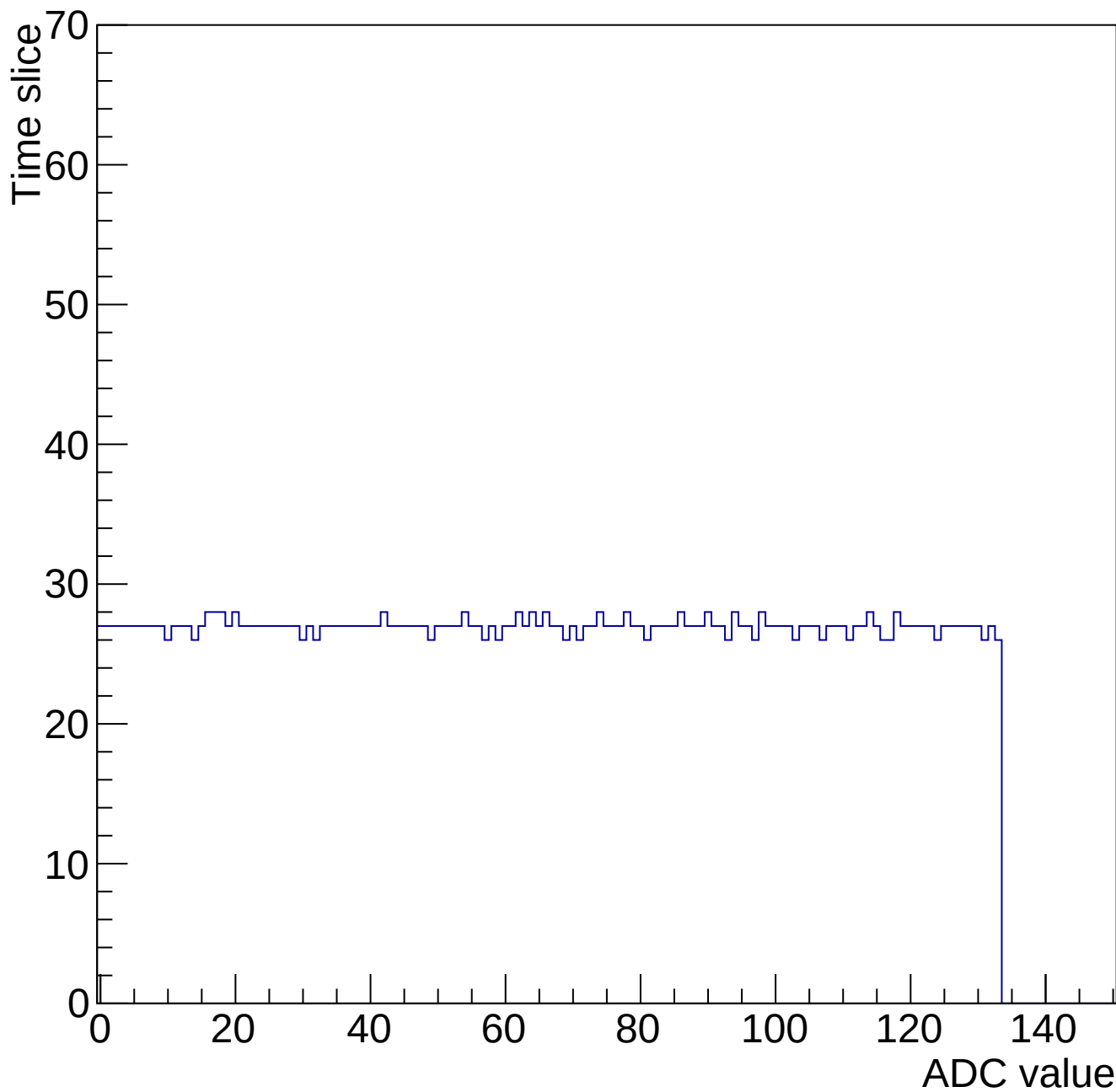
Evt 10 Time mcm 2000 chip 0 chan 5 34626-gain0-shaper0



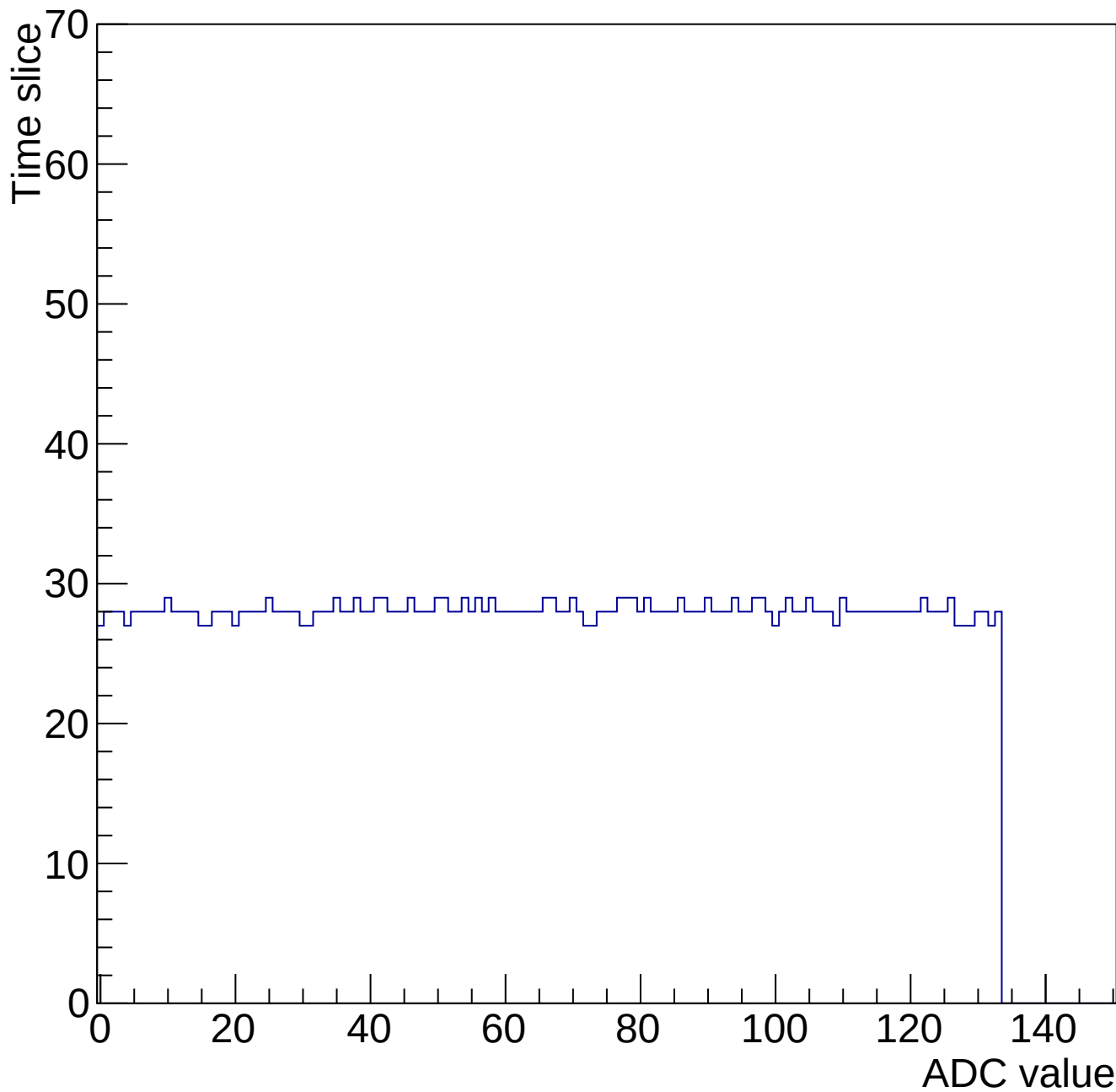
Evt 10 Time mcm 2000 chip 0 chan 6 34626-gain0-shaper0



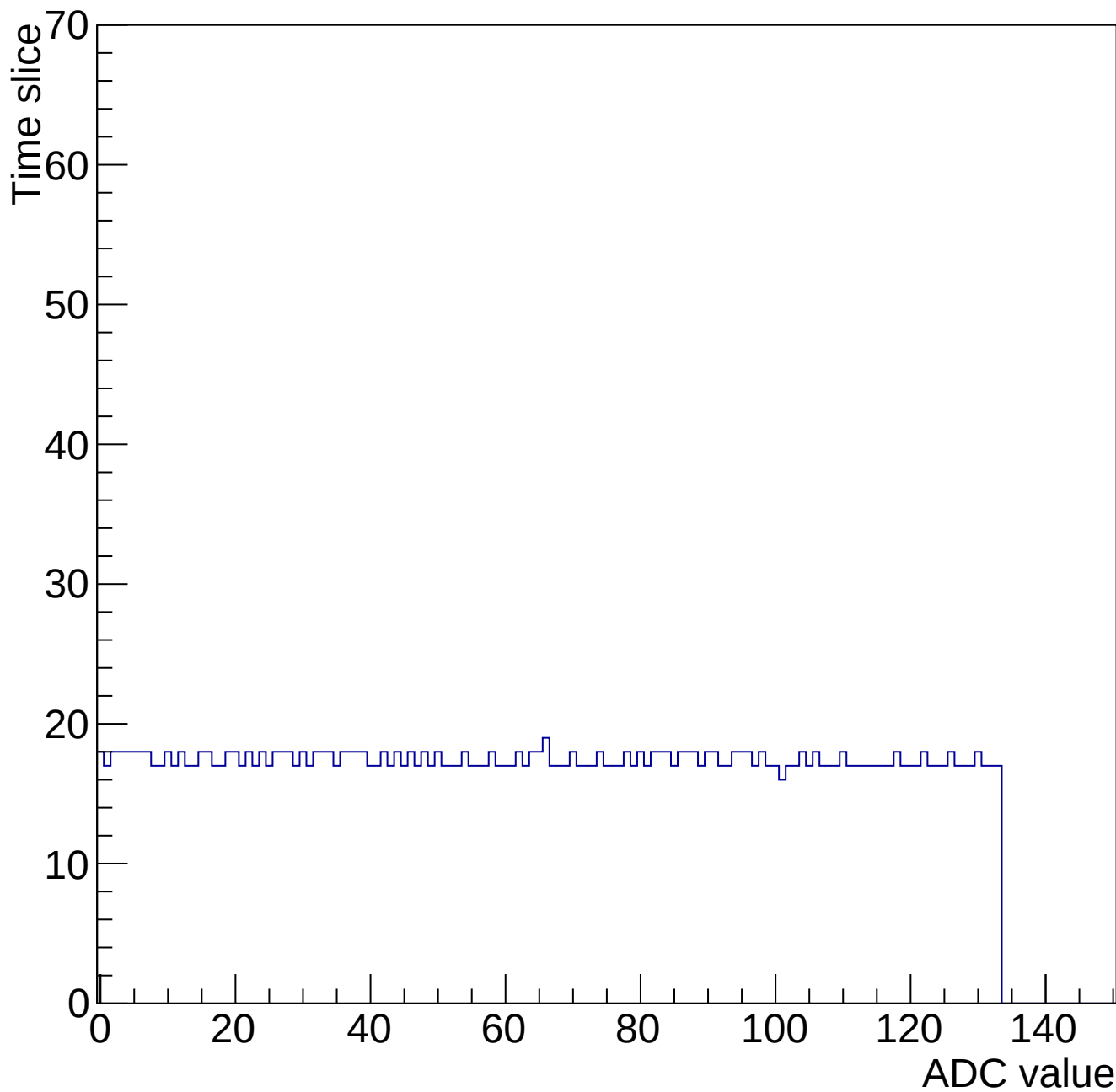
Evt 10 Time mcm 2000 chip 0 chan 7 34626-gain0-shaper0



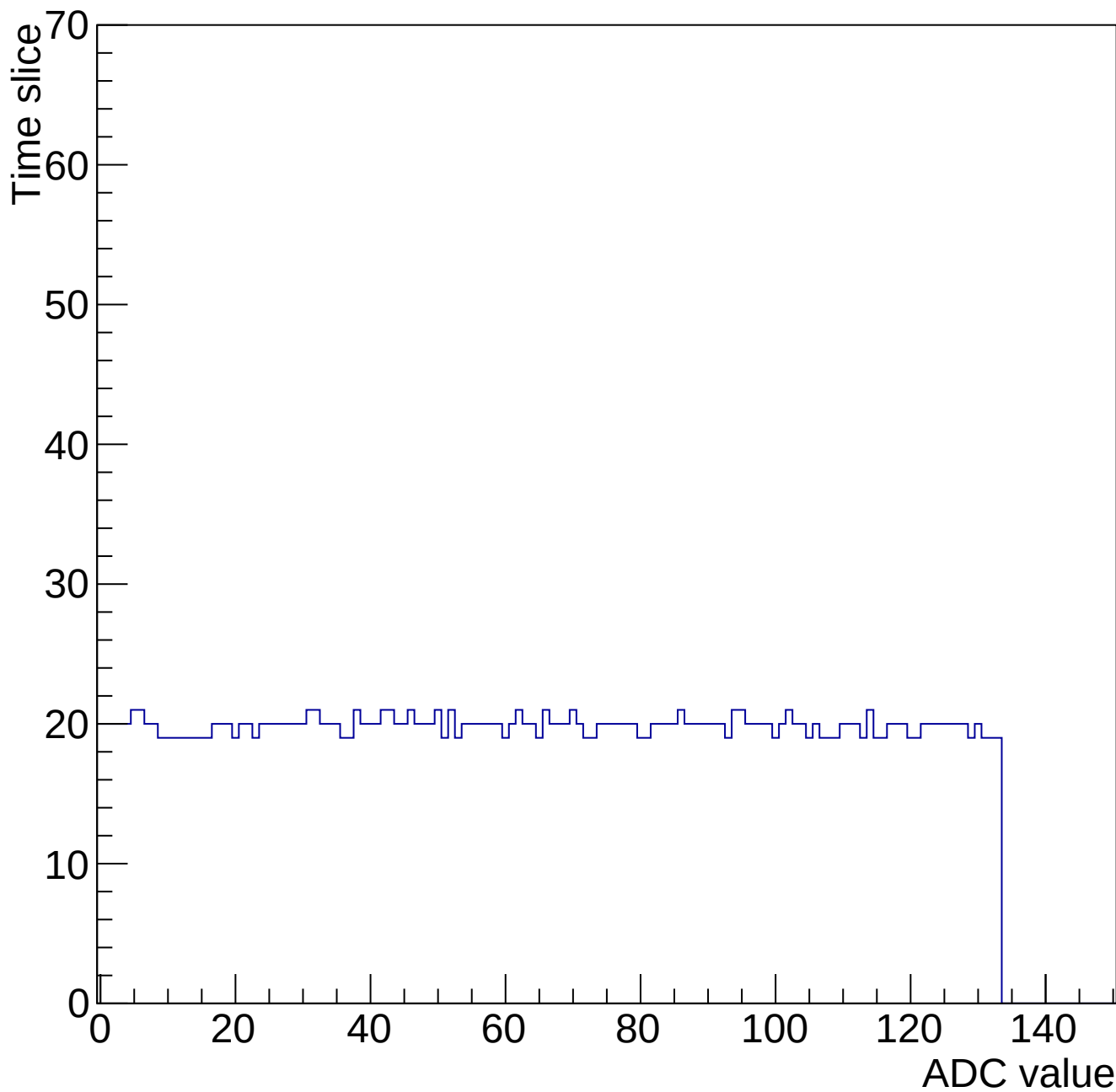
Evt 10 Time mcm 2000 chip 0 chan 8 34626-gain0-shaper0



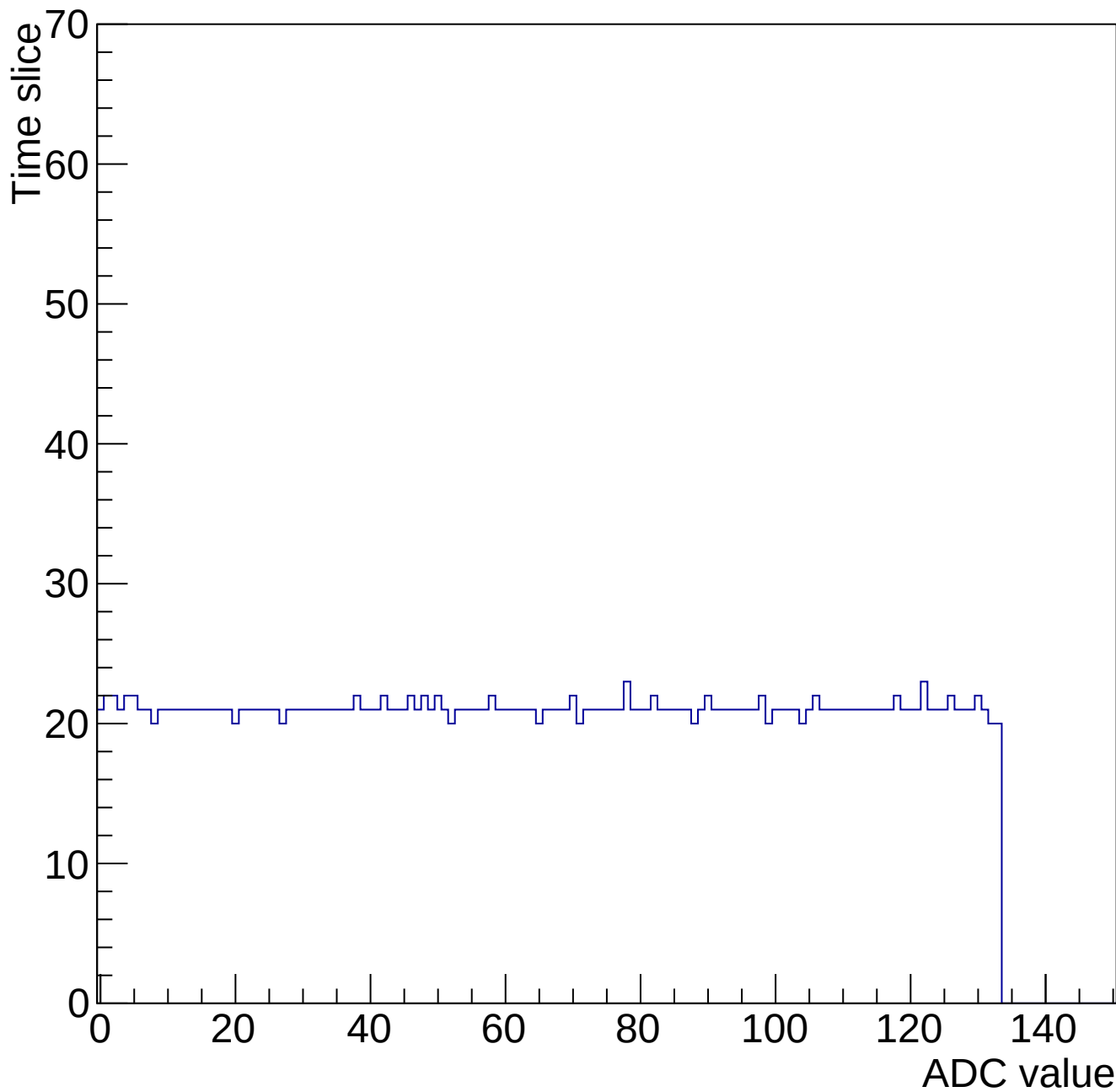
Evt 10 Time mcm 2000 chip 0 chan 9 34626-gain0-shaper0



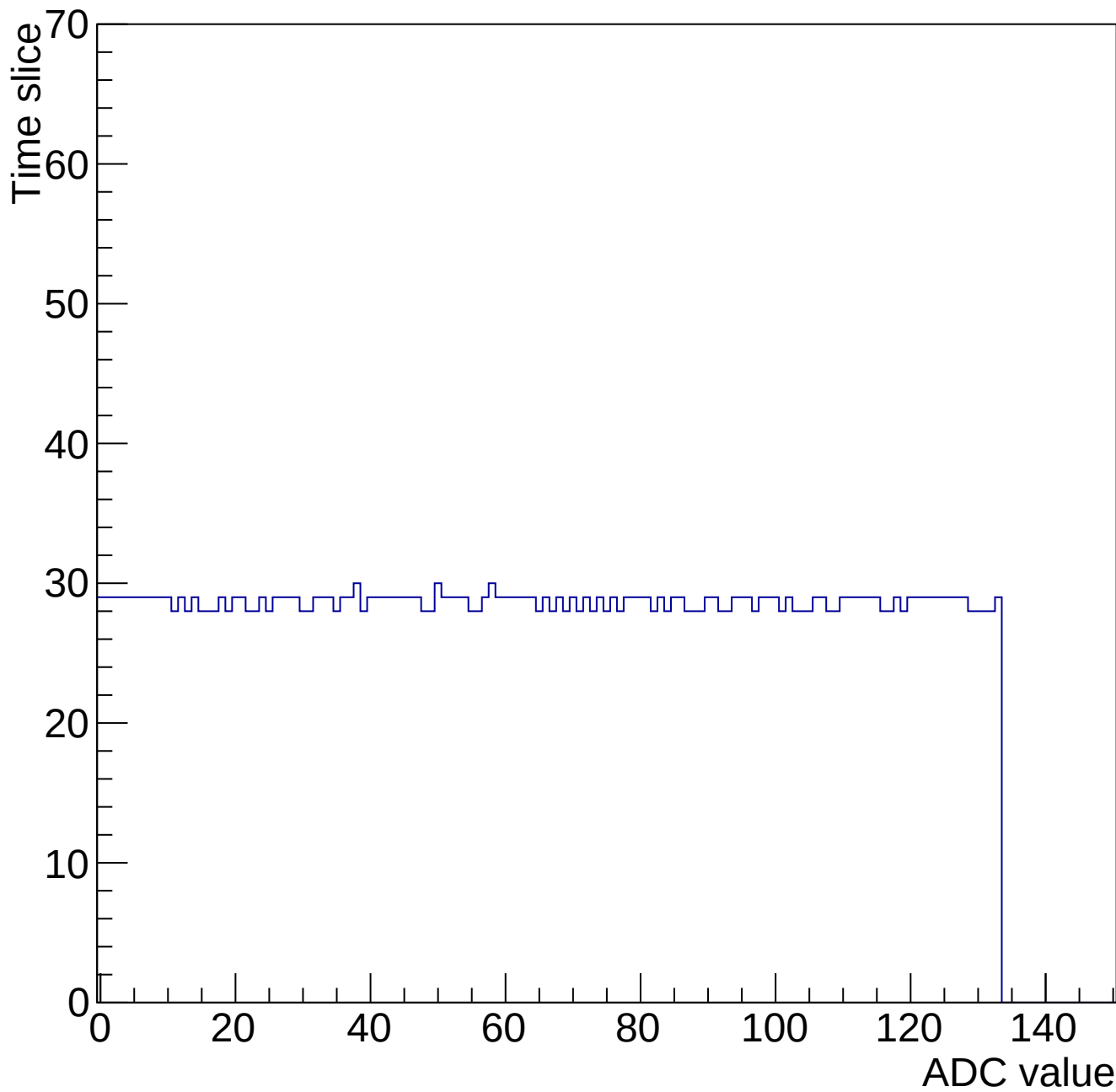
Evt 10 Time mcm 2000 chip 0 chan 10 34626-gain0-shaper0



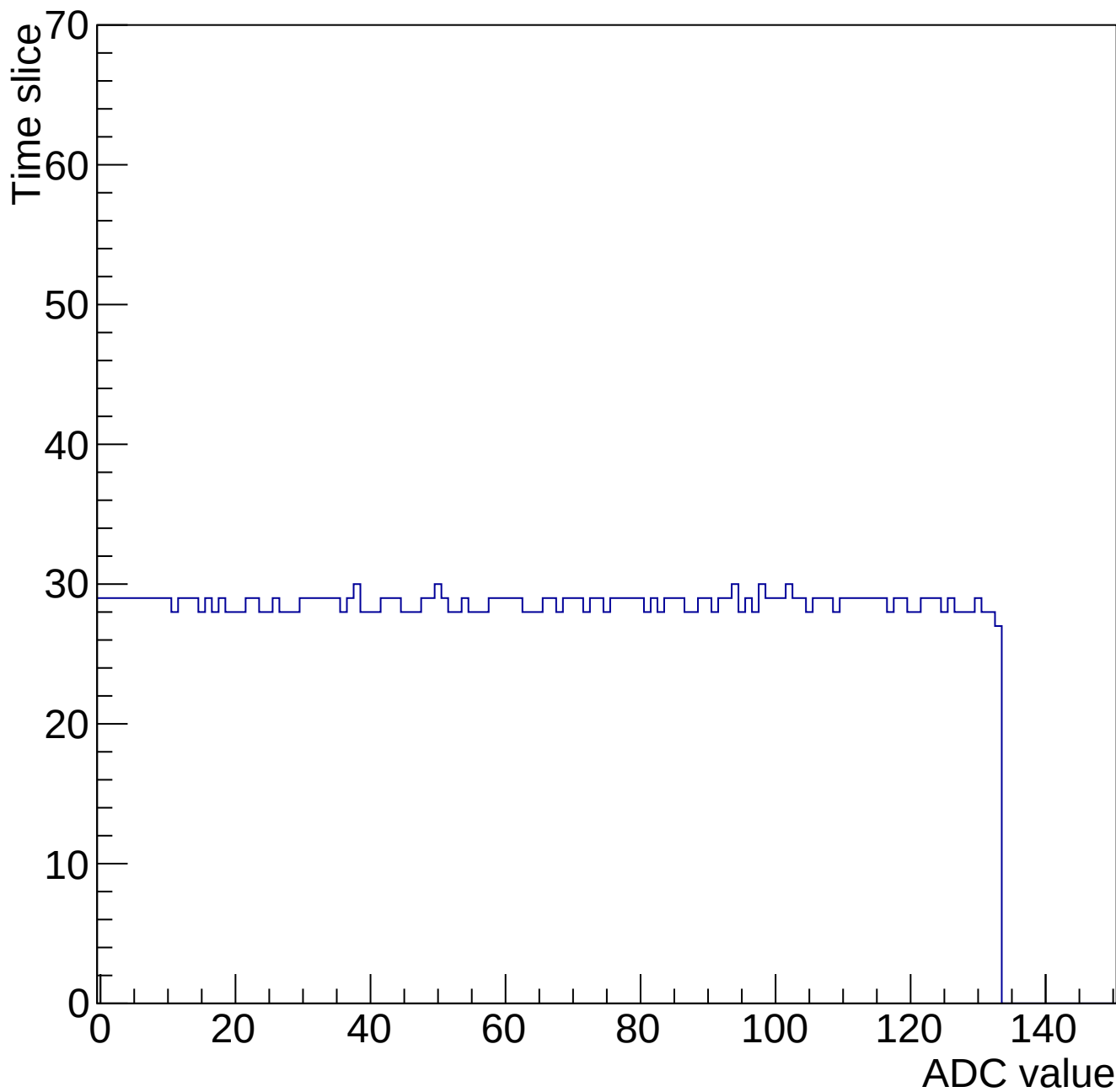
Evt 10 Time mcm 2000 chip 0 chan 11 34626-gain0-shaper0



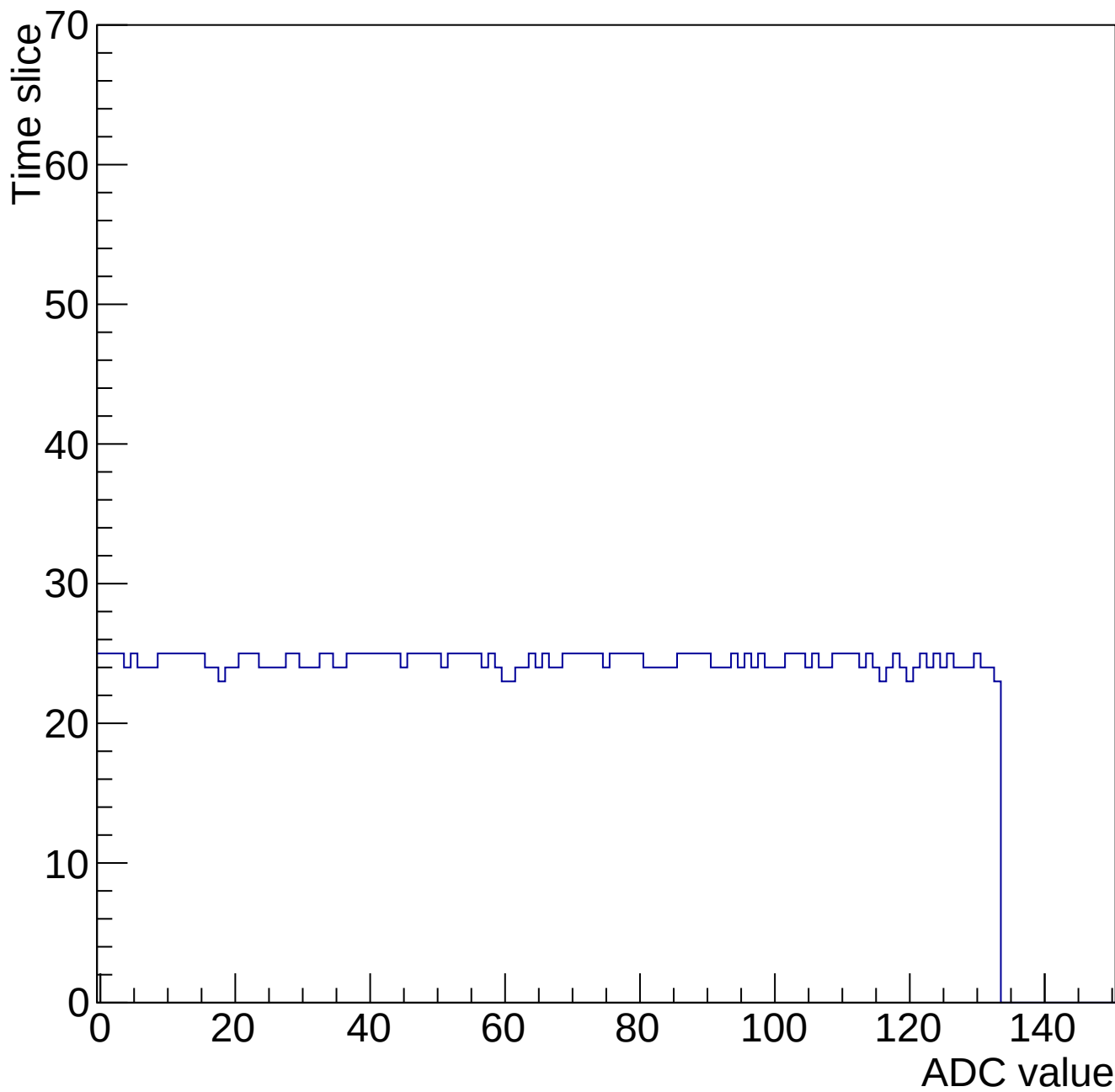
Evt 10 Time mcm 2000 chip 0 chan 12 34626-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 13 34626-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 14 34626-gain0-shaper0



Evt 10 Time mcm 2000 chip 0 chan 15 34626-gain0-shaper0

