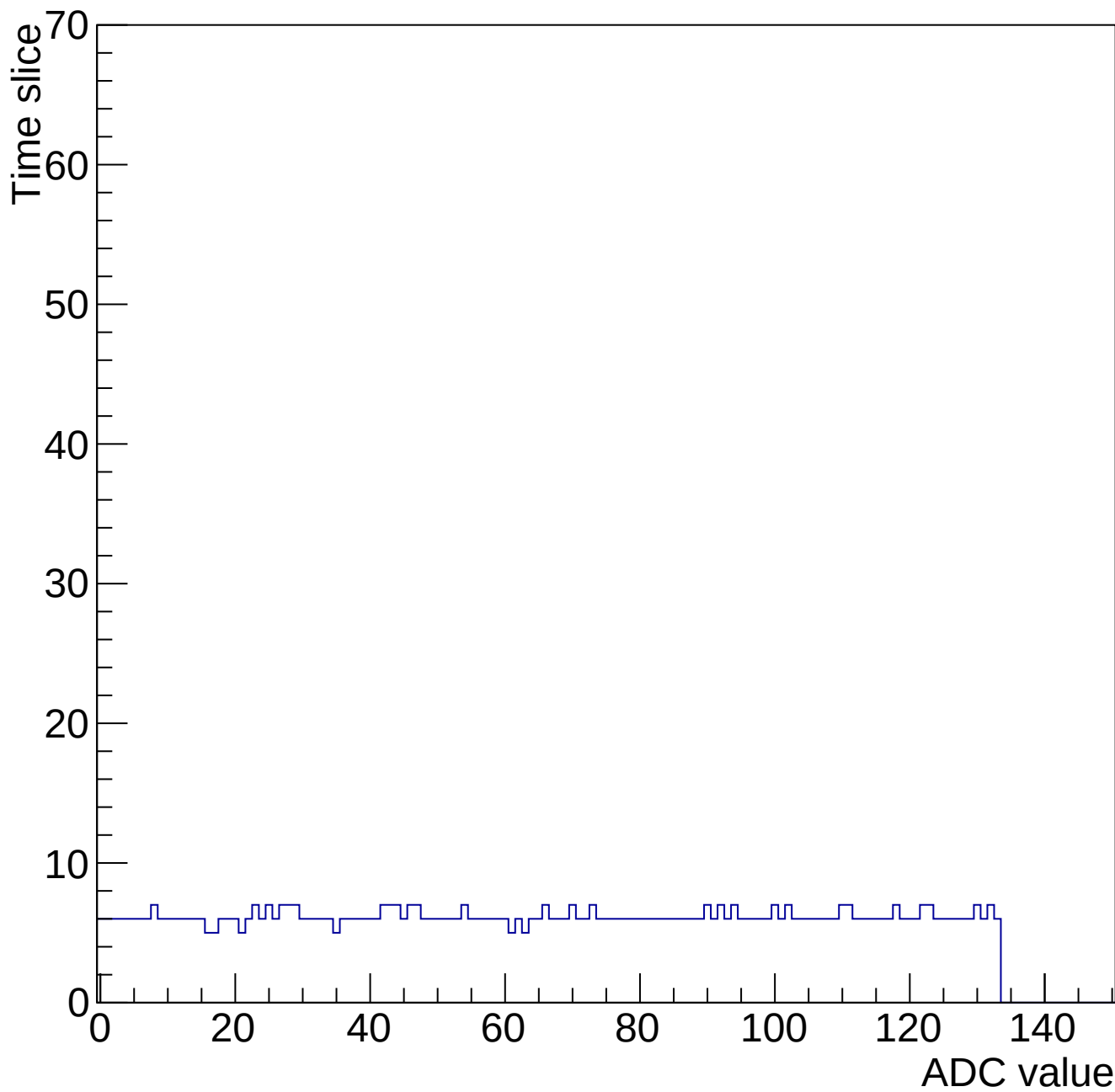
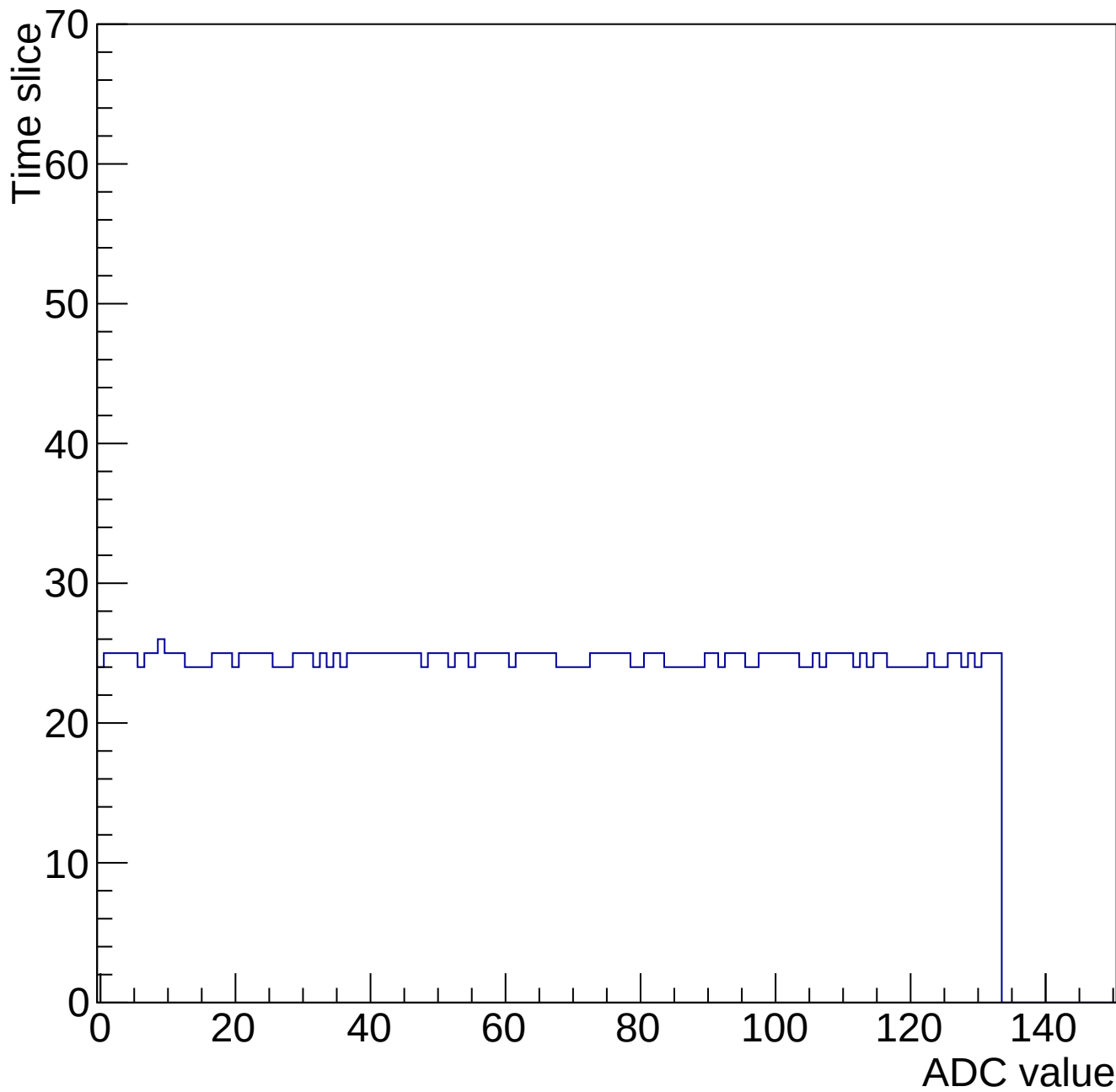


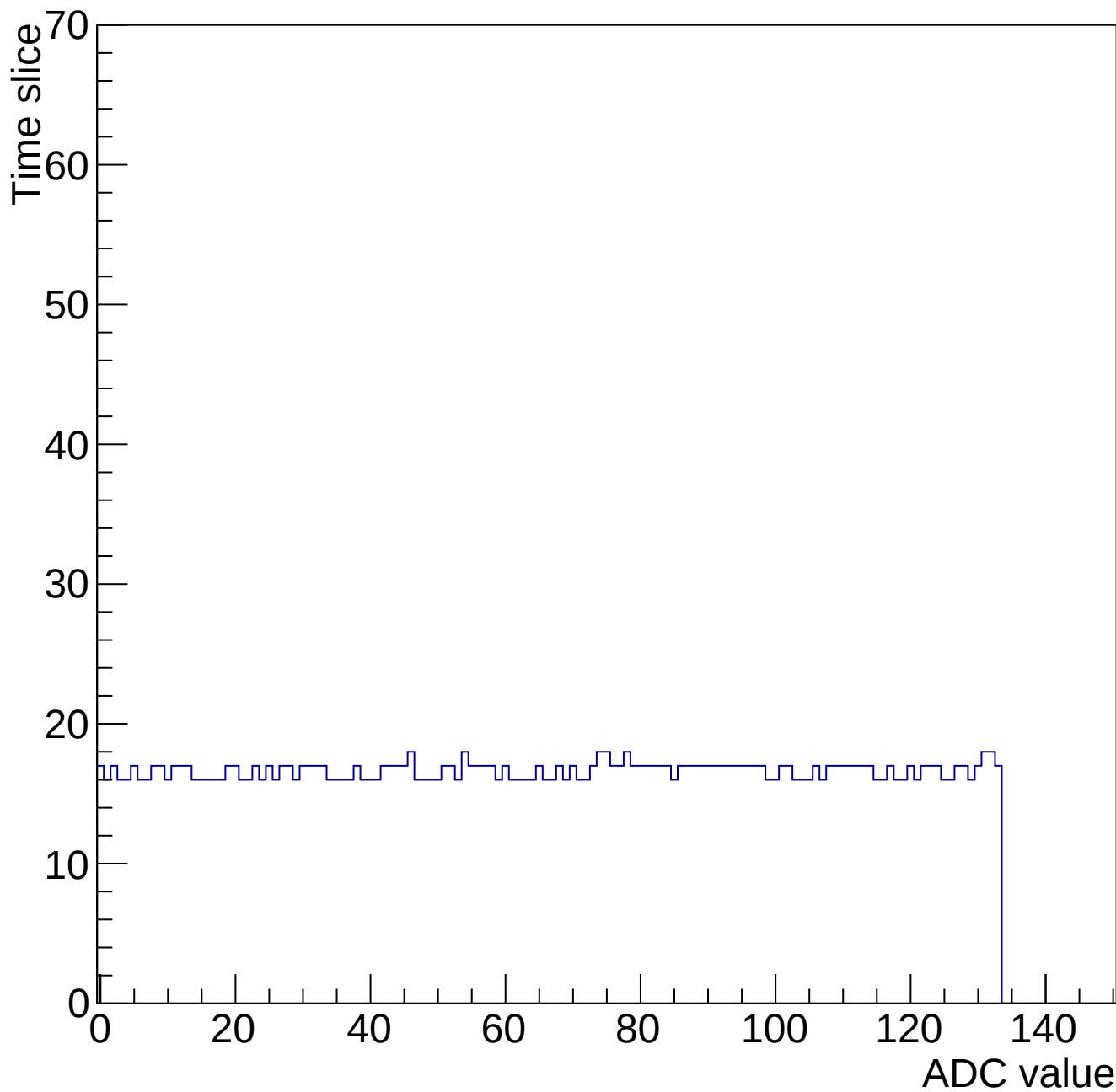
Evt 10 Time mcm 2007 chip 7 chan 0 34627-gain0-shaper0



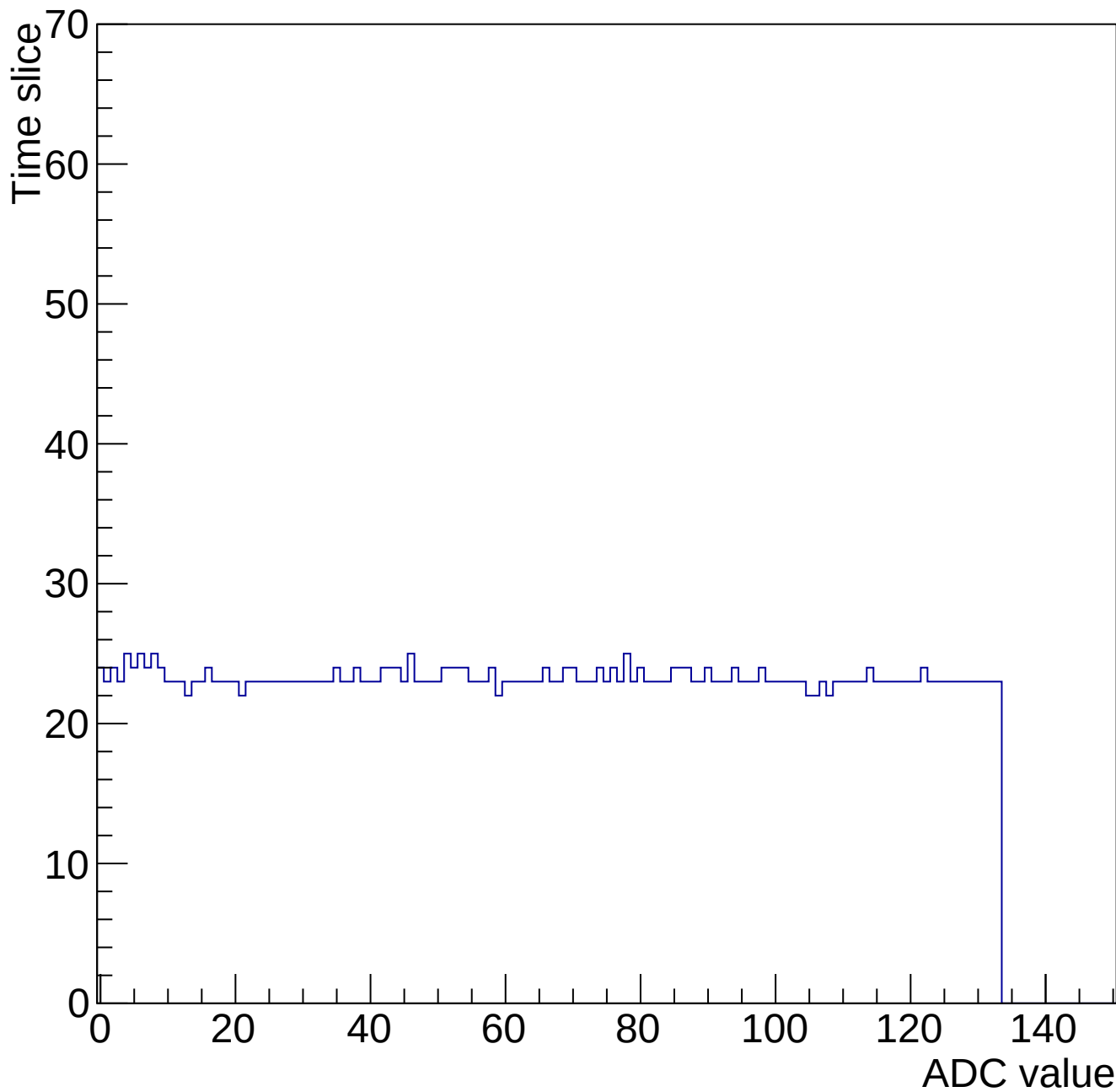
Evt 10 Time mcm 2007 chip 7 chan 1 34627-gain0-shaper0



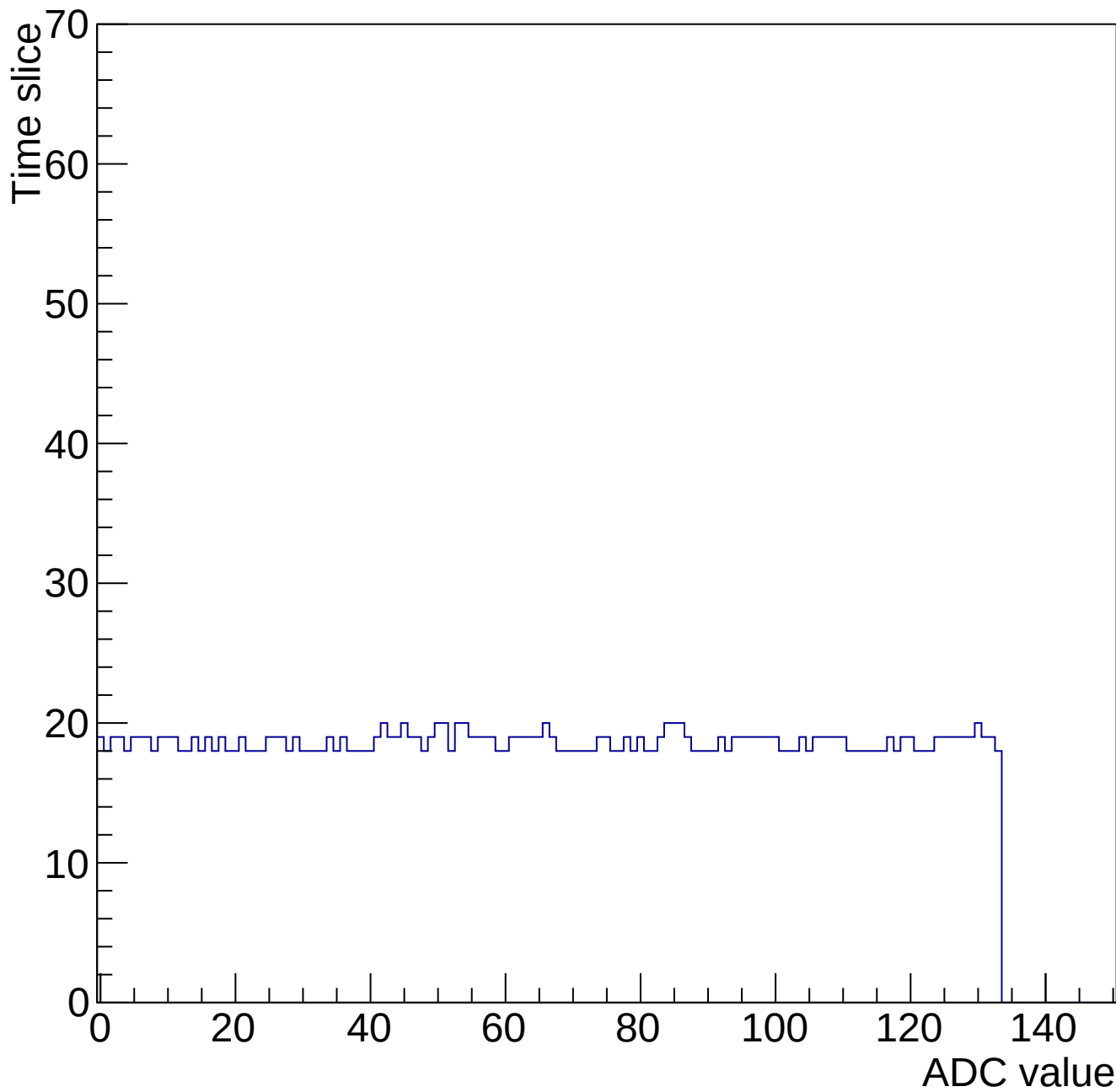
Evt 10 Time mcm 2007 chip 7 chan 2 34627-gain0-shaper0



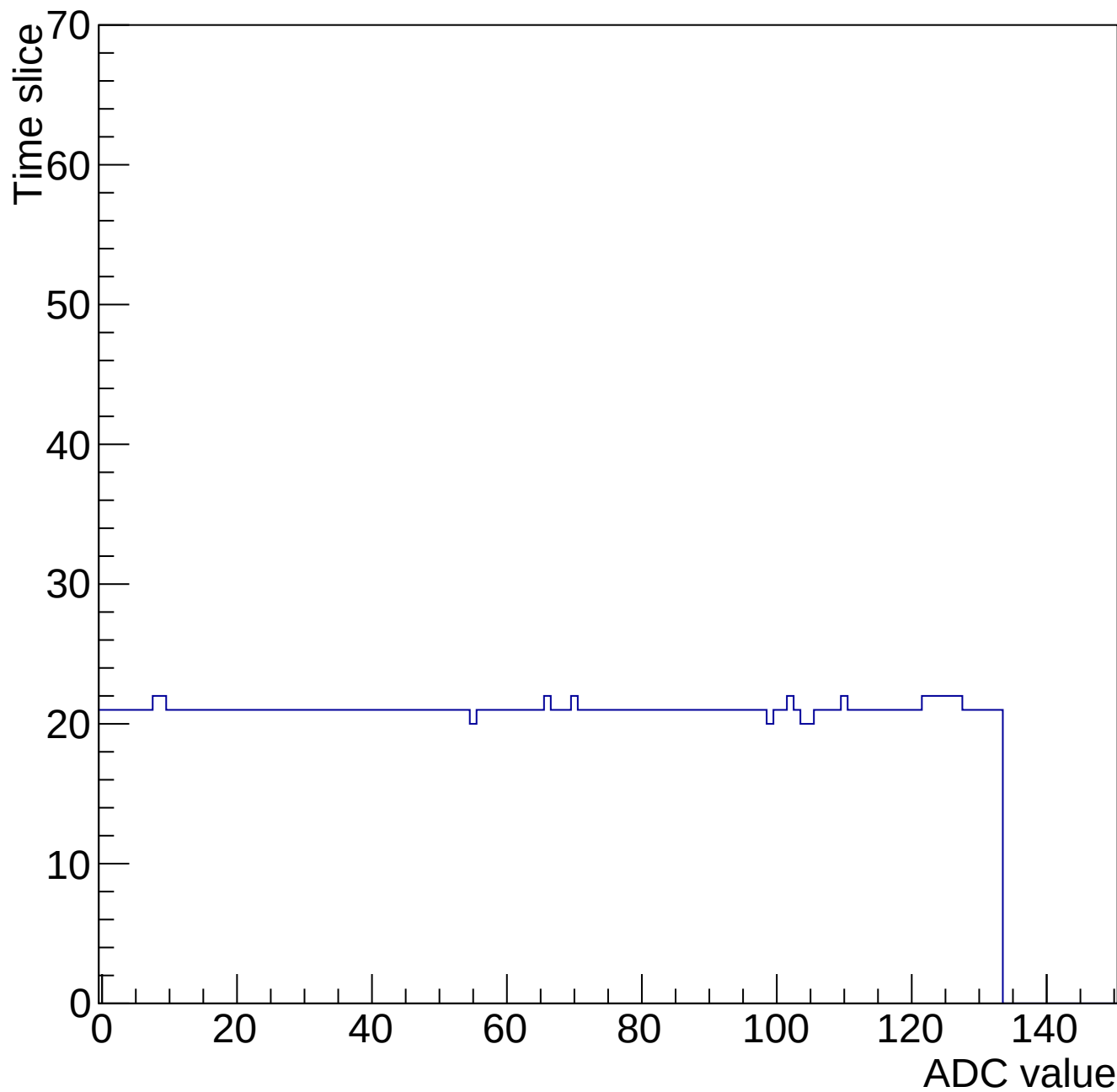
Evt 10 Time mcm 2007 chip 7 chan 3 34627-gain0-shaper0



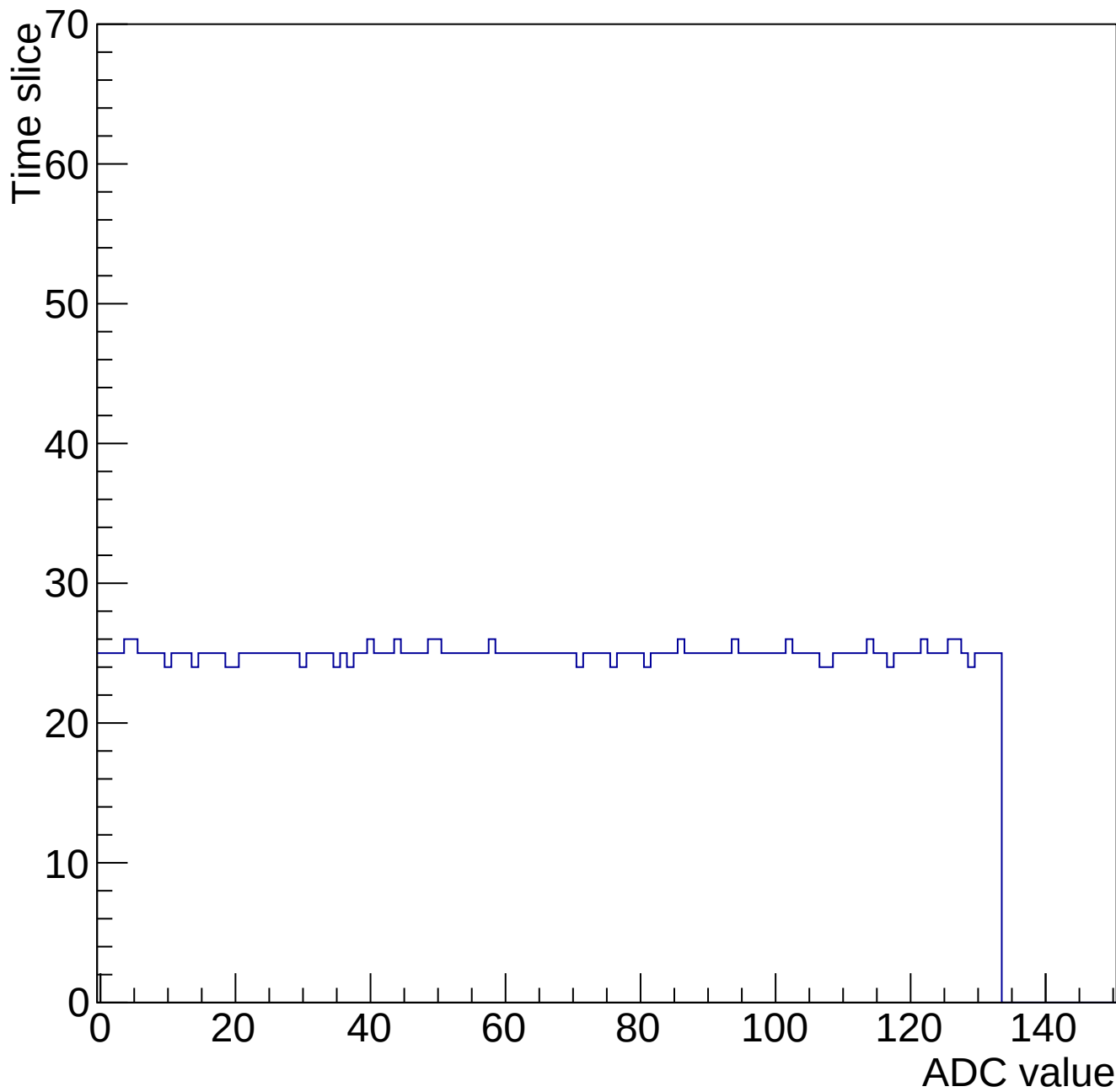
Evt 10 Time mcm 2007 chip 7 chan 4 34627-gain0-shaper0



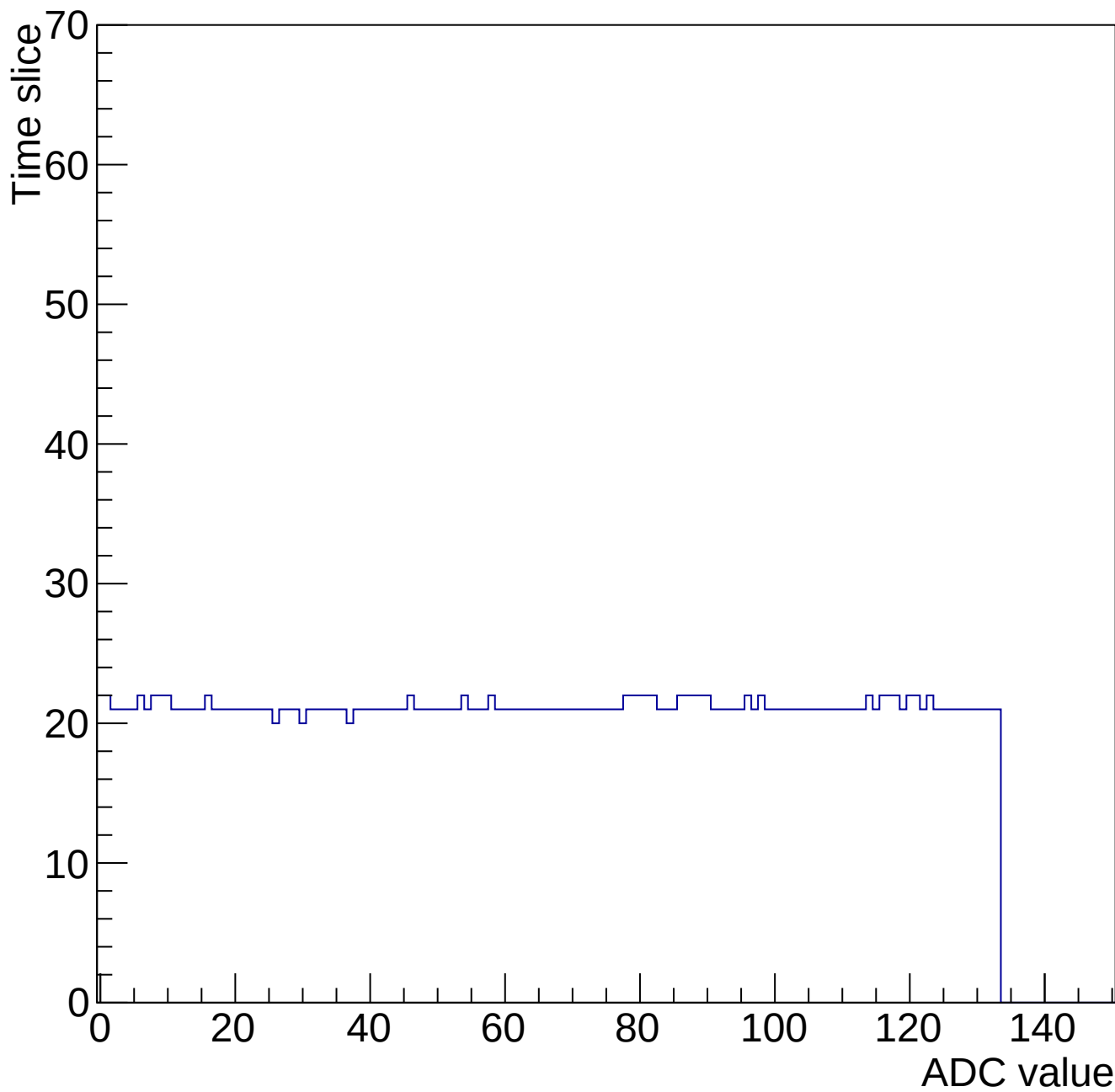
Evt 10 Time mcm 2007 chip 7 chan 5 34627-gain0-shaper0



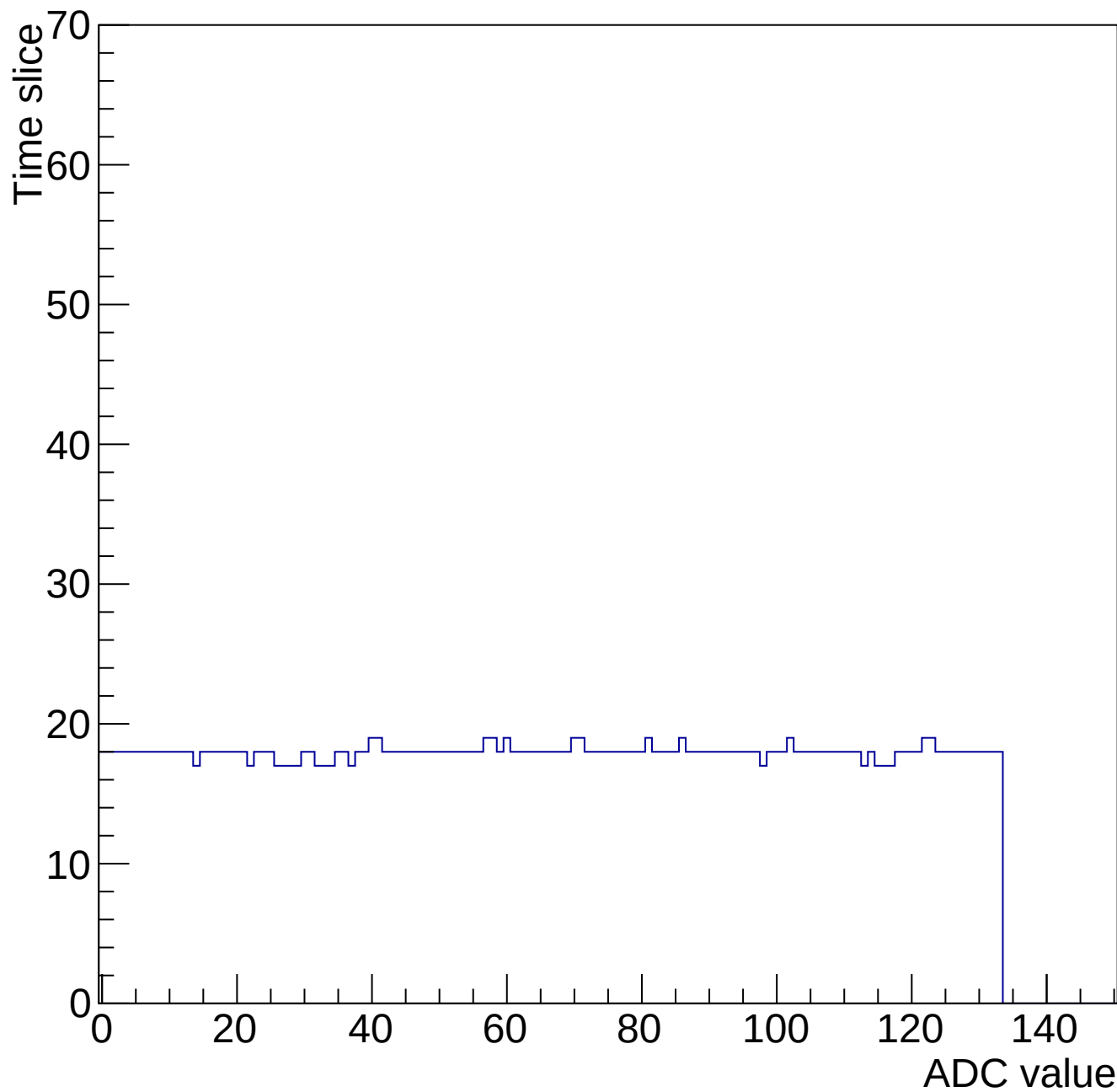
Evt 10 Time mcm 2007 chip 7 chan 6 34627-gain0-shaper0



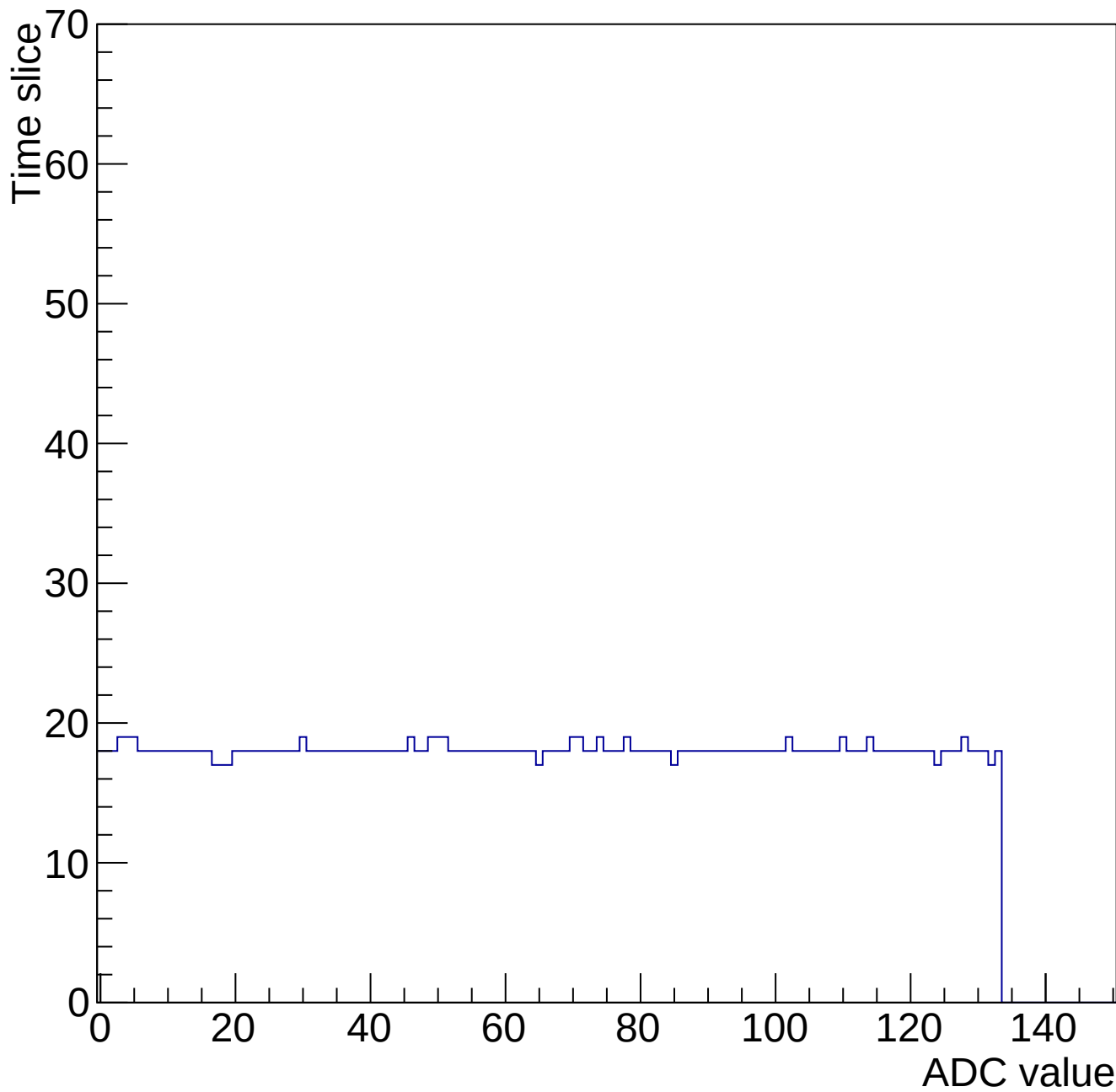
Evt 10 Time mcm 2007 chip 7 chan 7 34627-gain0-shaper0



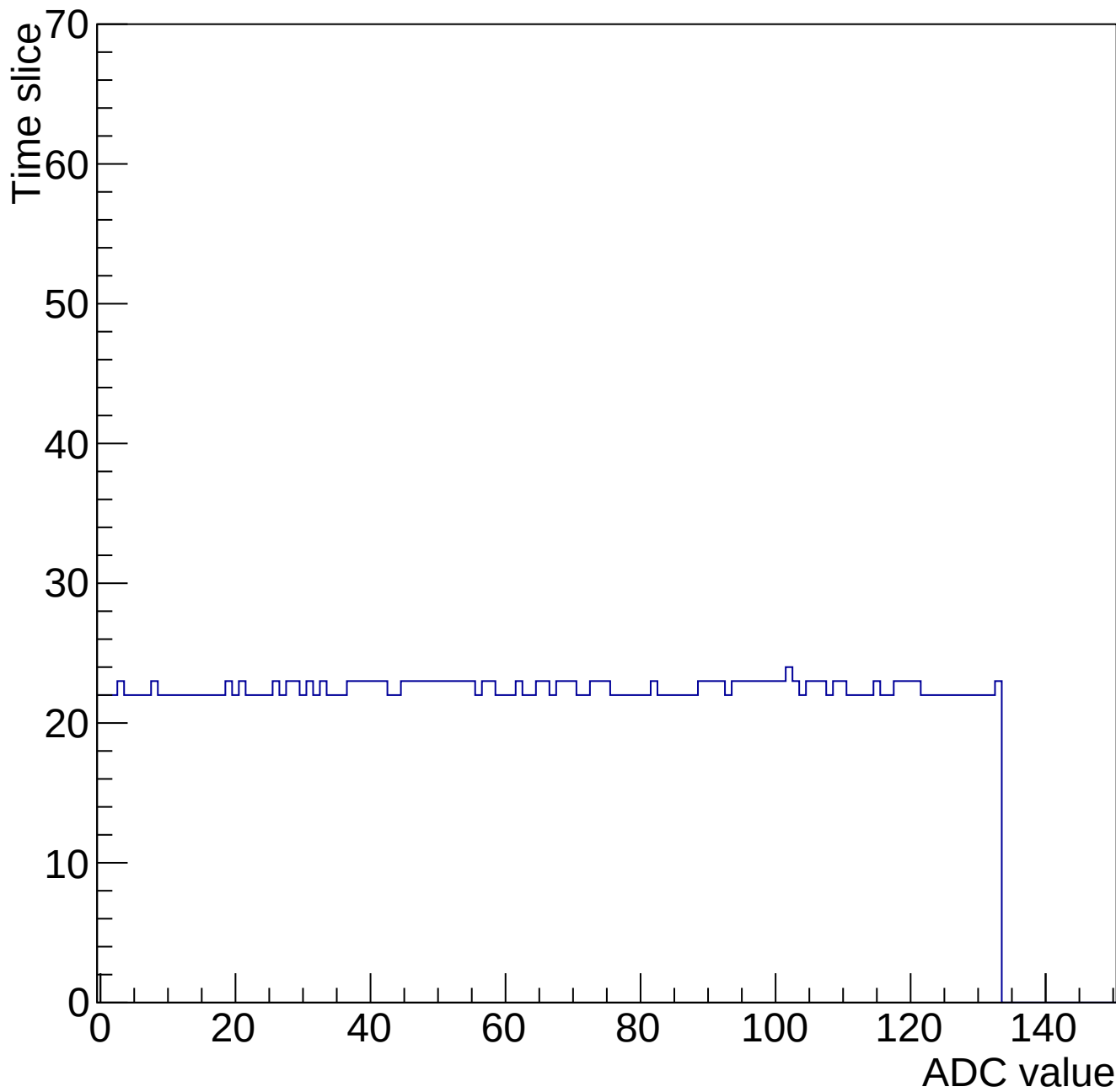
Evt 10 Time mcm 2007 chip 7 chan 8 34627-gain0-shaper0



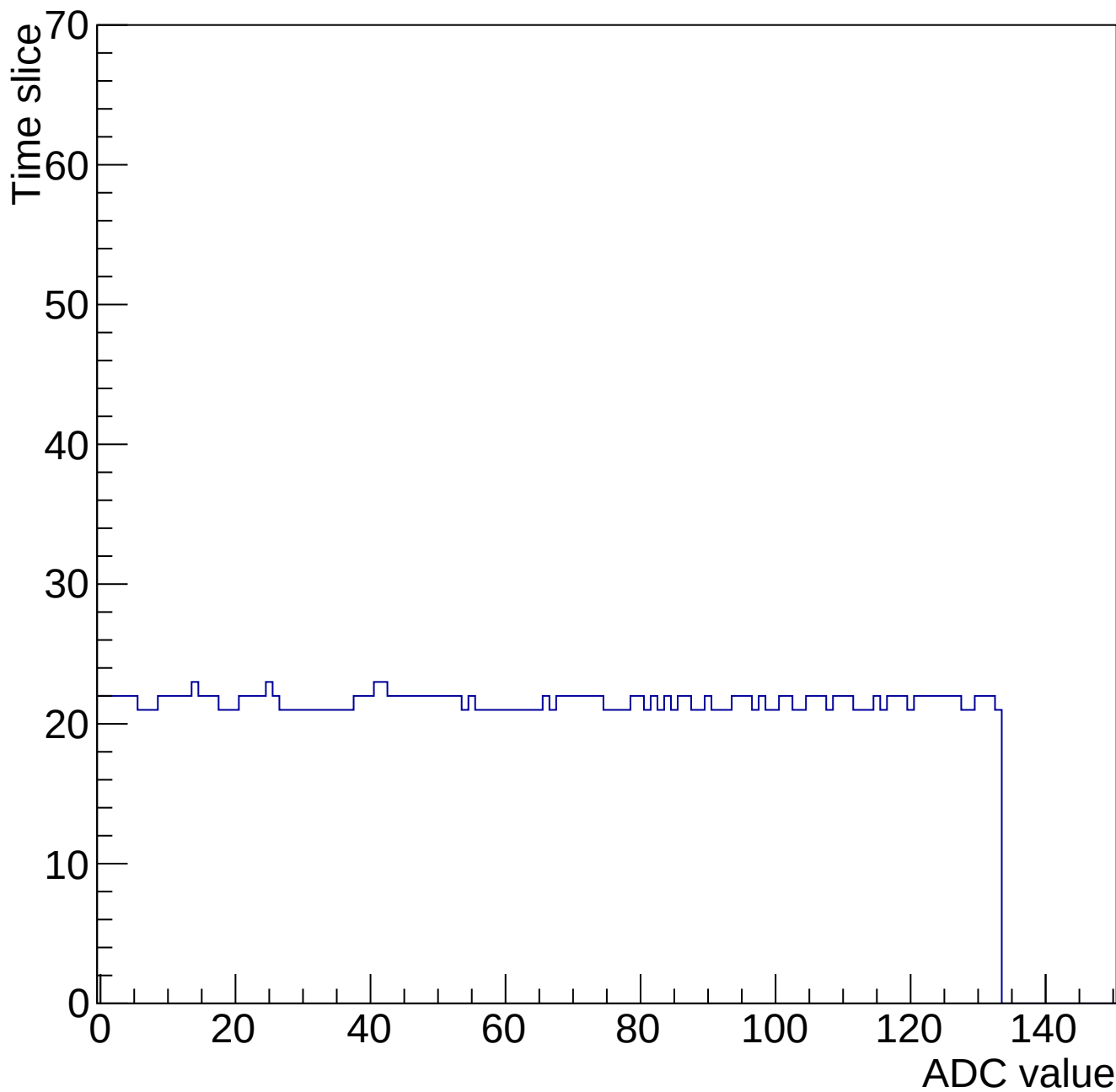
Evt 10 Time mcm 2007 chip 7 chan 9 34627-gain0-shaper0



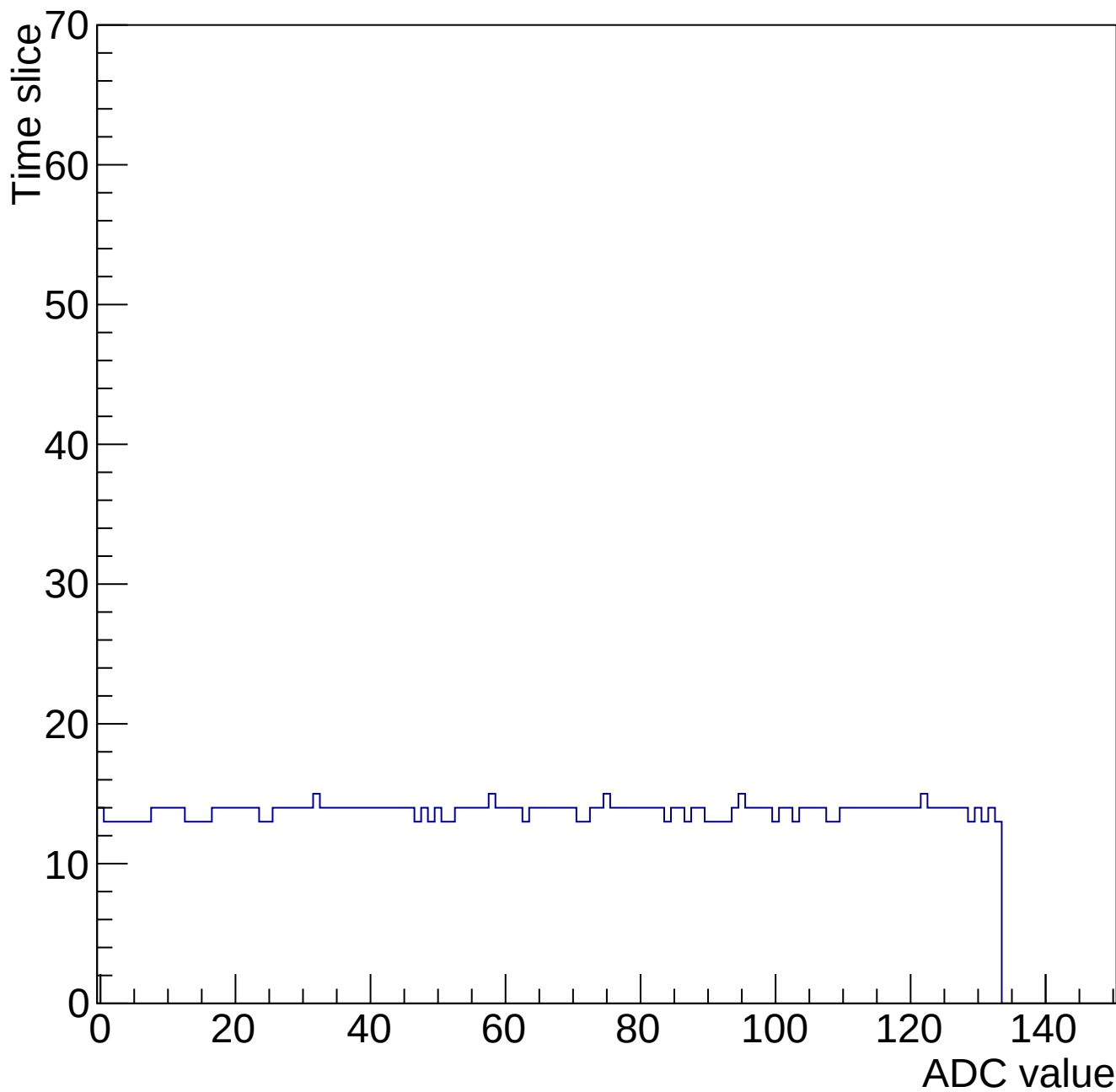
Evt 10 Time mcm 2007 chip 7 chan 10 34627-gain0-shaper0



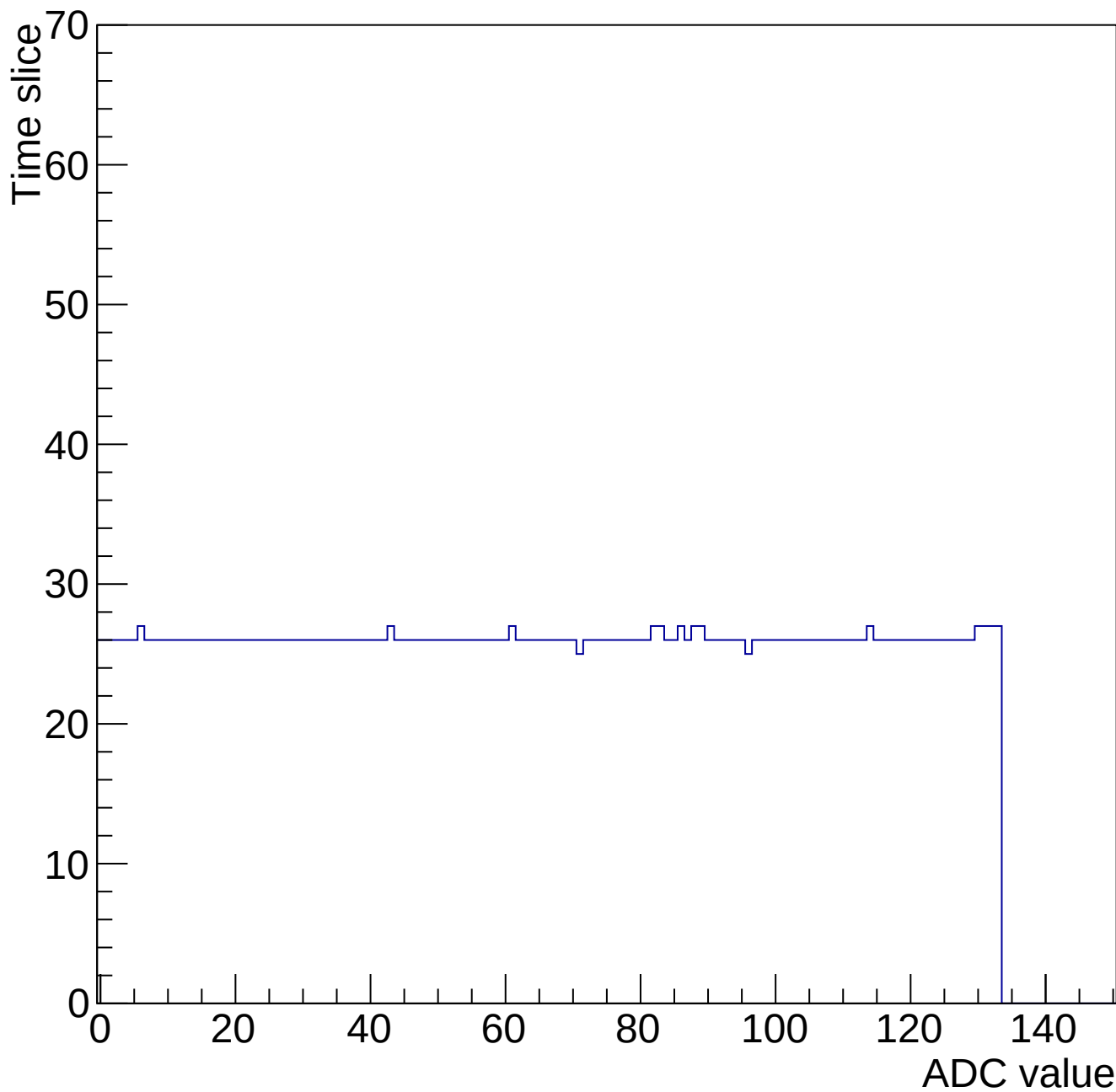
Evt 10 Time mcm 2007 chip 7 chan 11 34627-gain0-shaper0



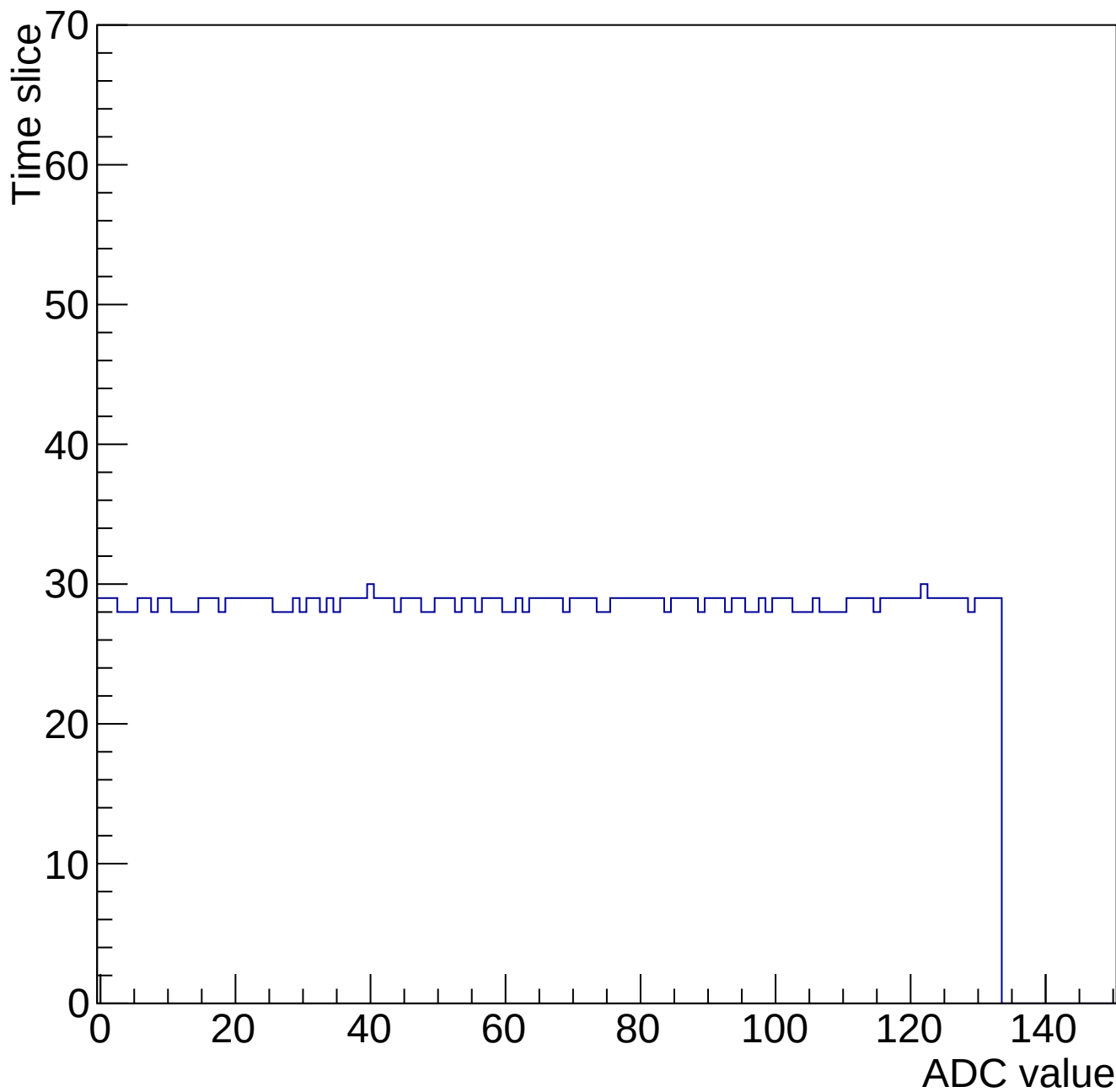
Evt 10 Time mcm 2007 chip 7 chan 12 34627-gain0-shaper0



Evt 10 Time mcm 2007 chip 7 chan 13 34627-gain0-shaper0



Evt 10 Time mcm 2007 chip 7 chan 14 34627-gain0-shaper0



Evt 10 Time mcm 2007 chip 7 chan 15 34627-gain0-shaper0

