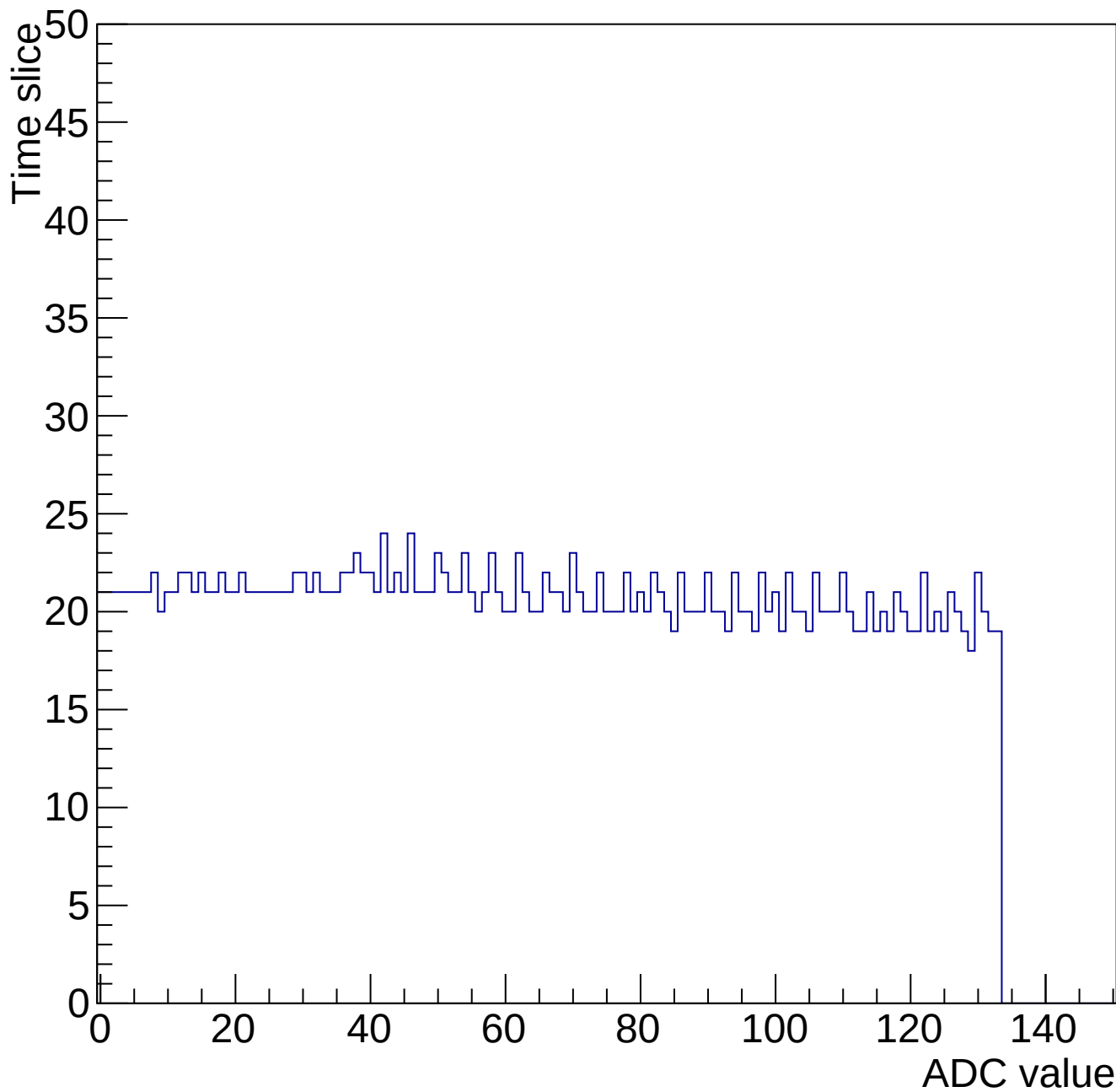
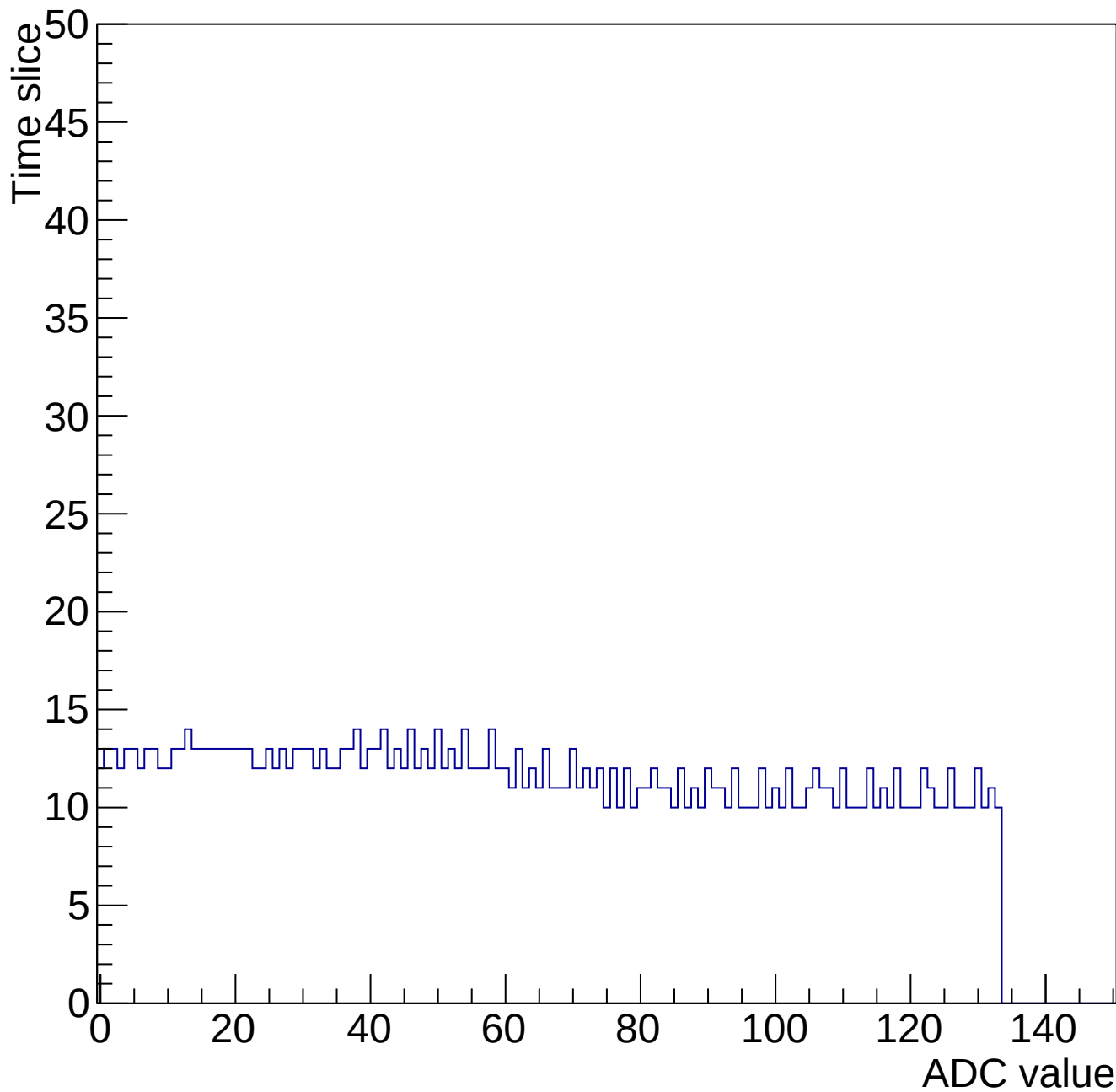


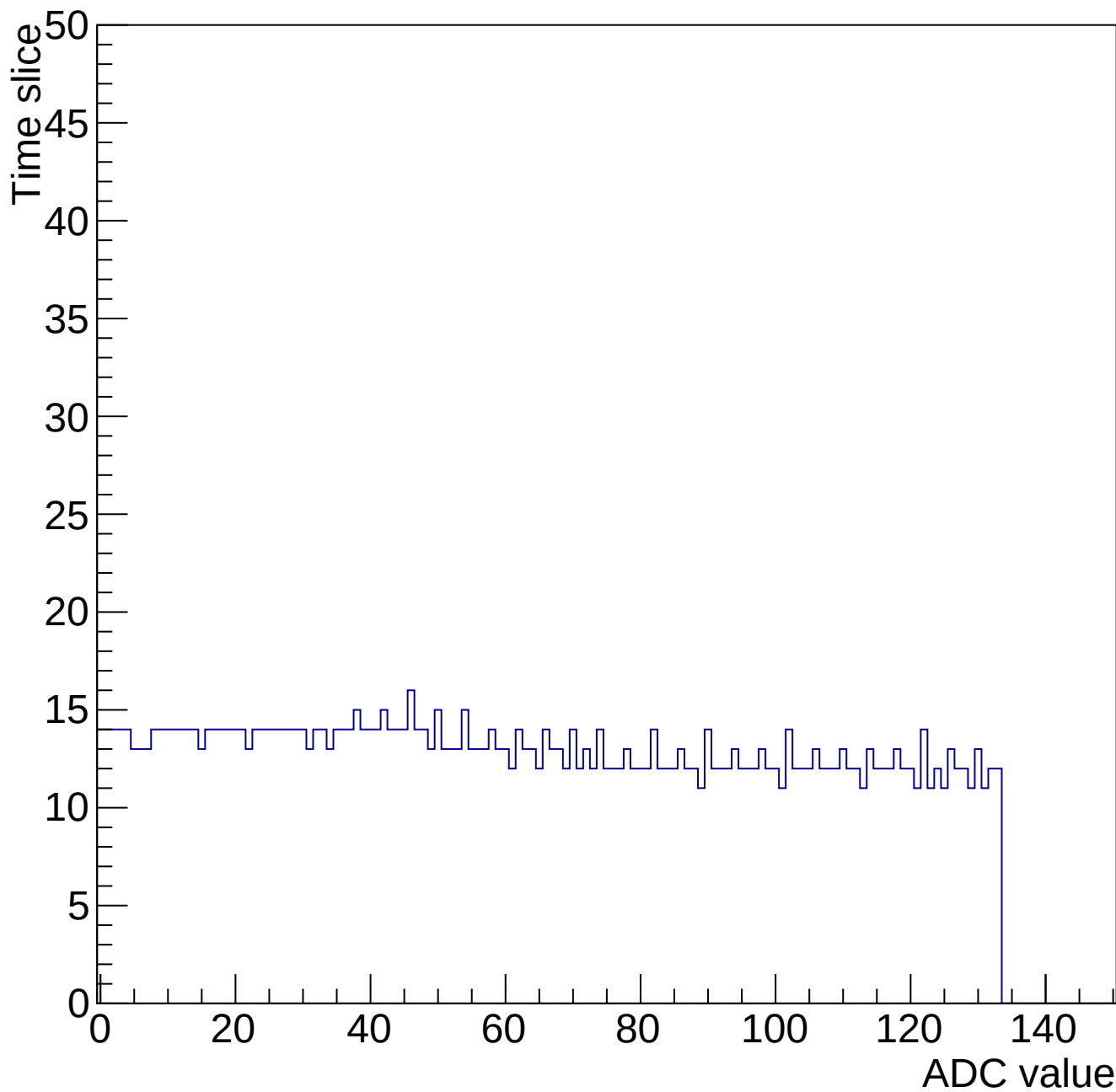
Evt 10 Time mcm 2001 chip 1 chan 0 34668-gain0-shaper6



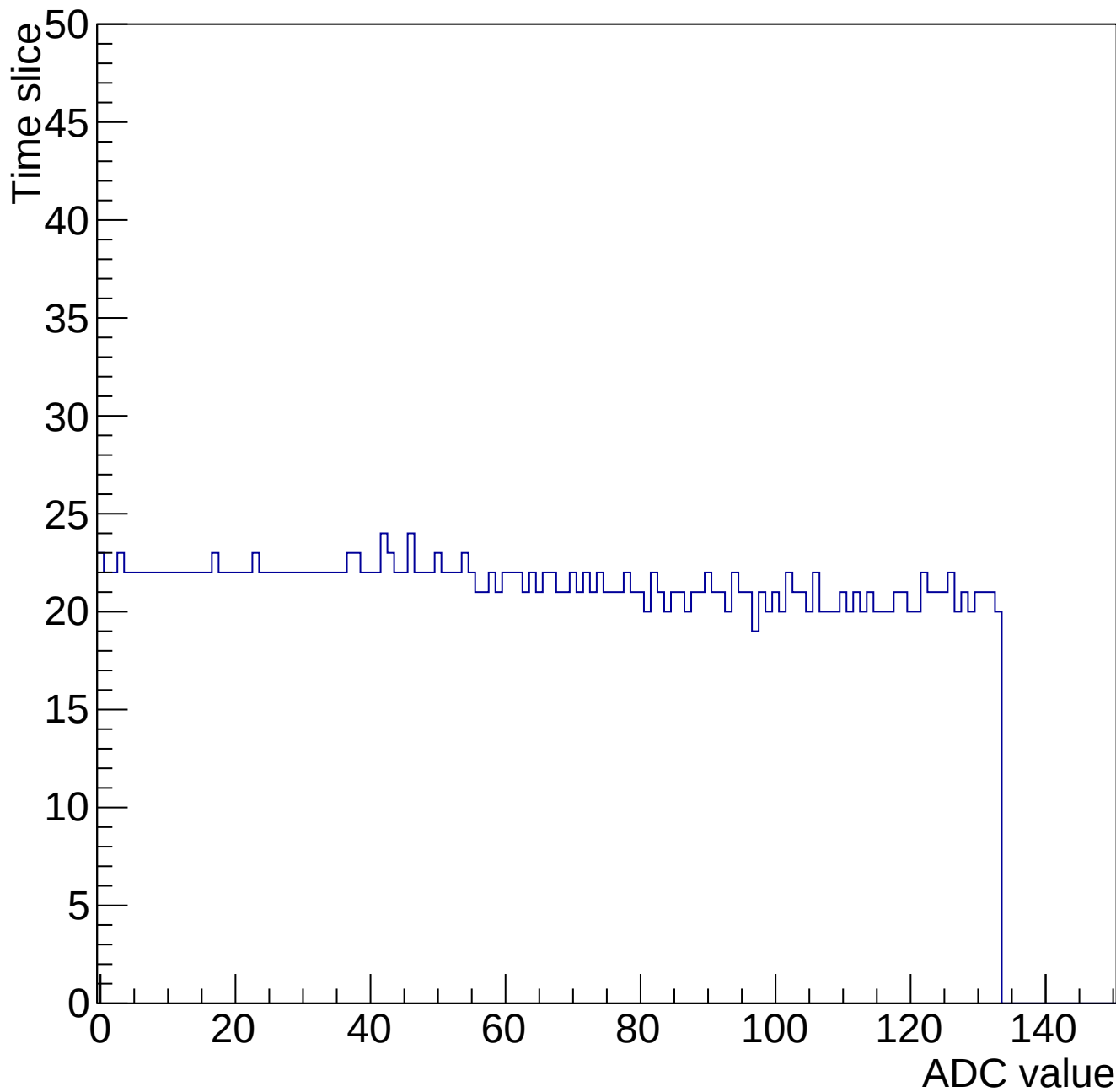
Evt 10 Time mcm 2001 chip 1 chan 1 34668-gain0-shaper6



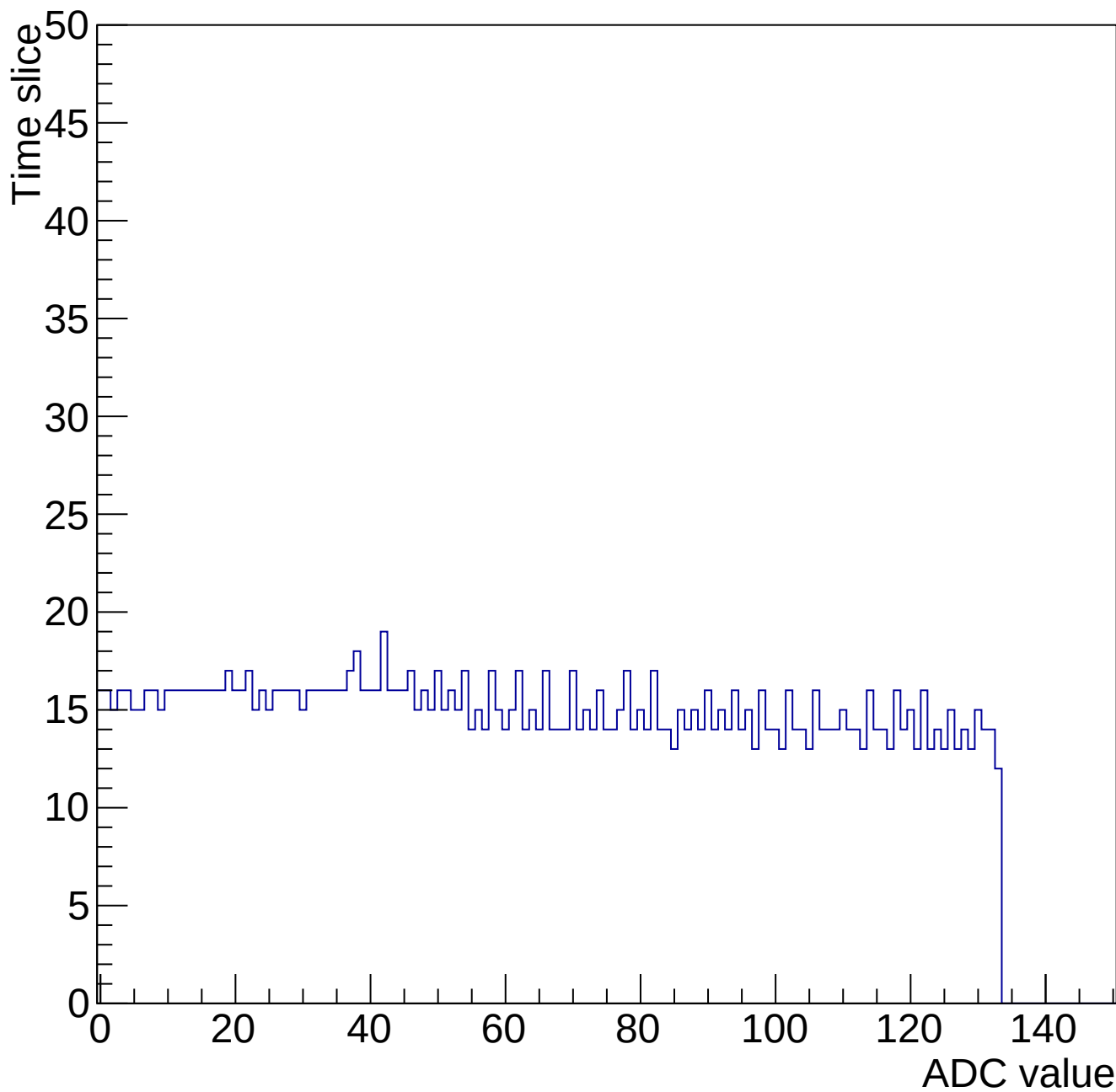
Evt 10 Time mcm 2001 chip 1 chan 2 34668-gain0-shaper6



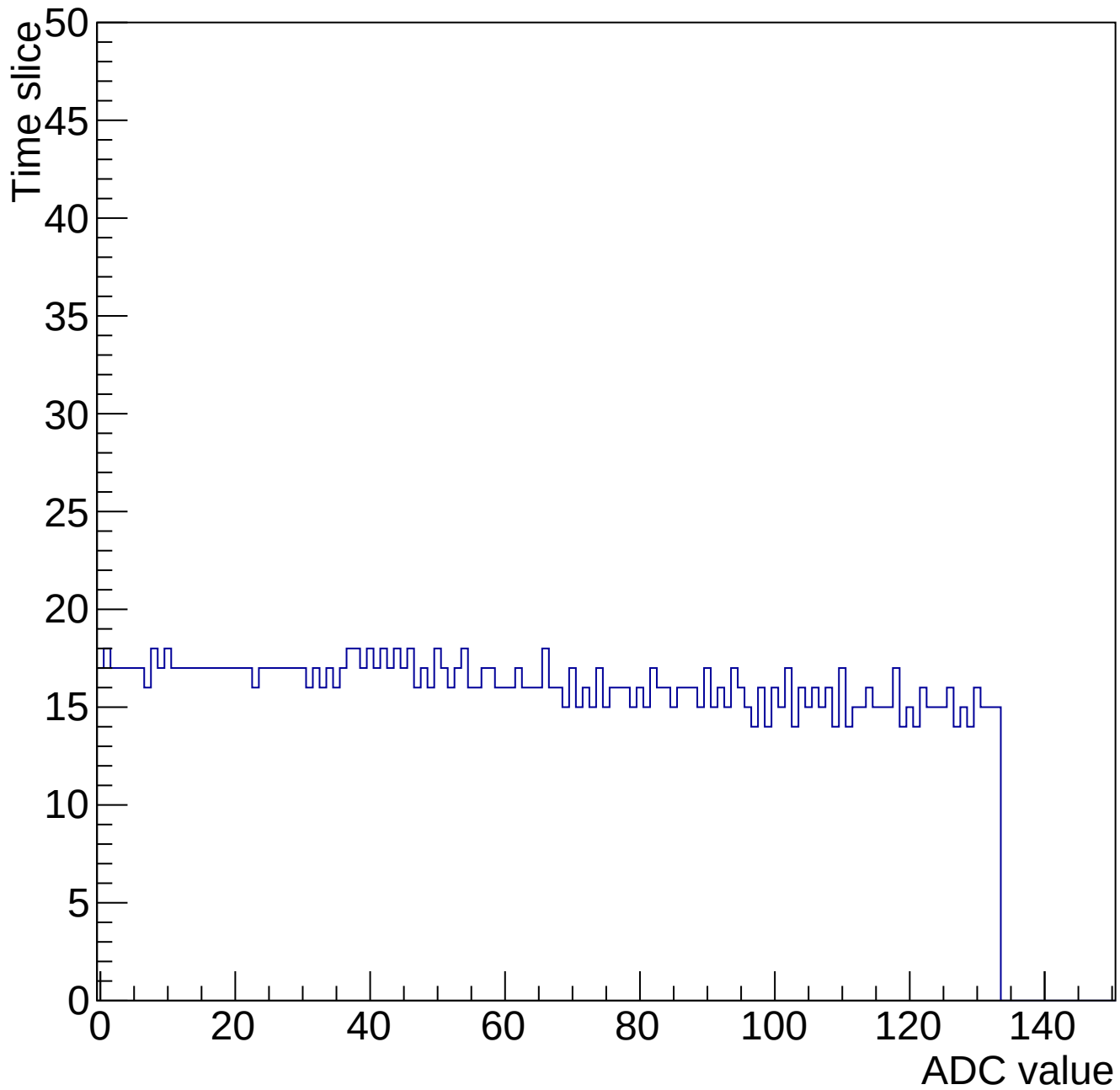
Evt 10 Time mcm 2001 chip 1 chan 3 34668-gain0-shaper6



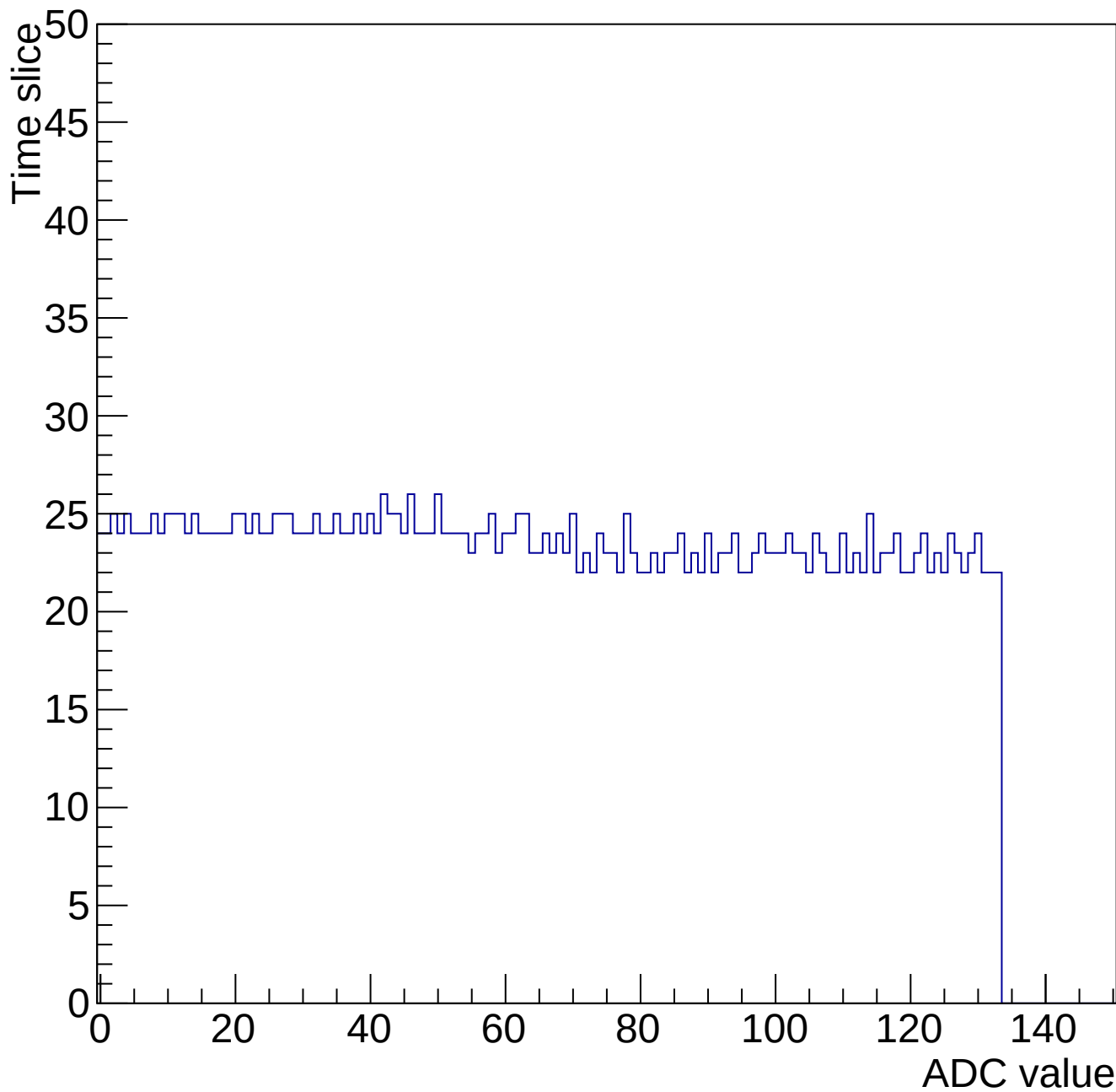
Evt 10 Time mcm 2001 chip 1 chan 4 34668-gain0-shaper6



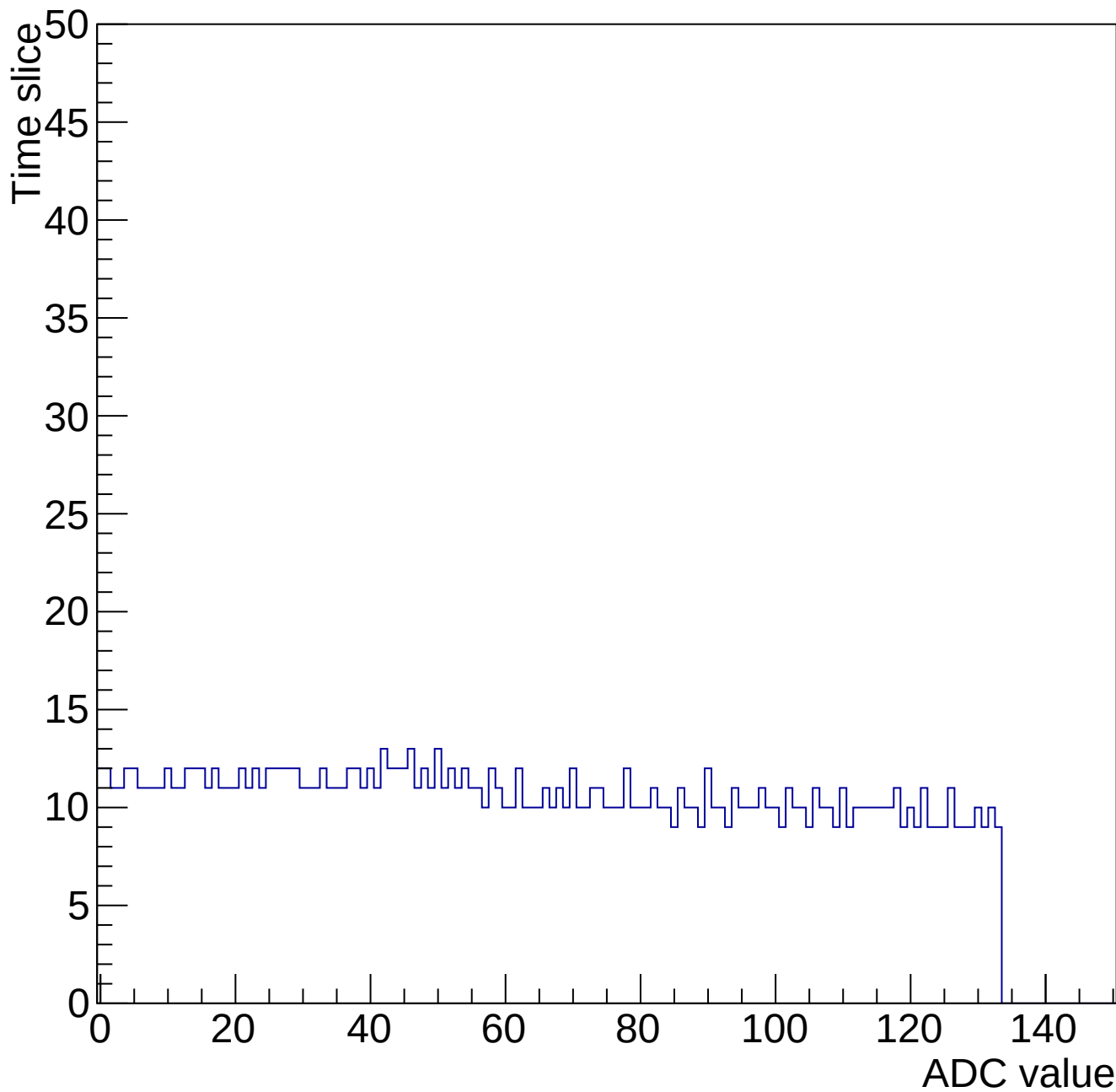
Evt 10 Time mcm 2001 chip 1 chan 5 34668-gain0-shaper6



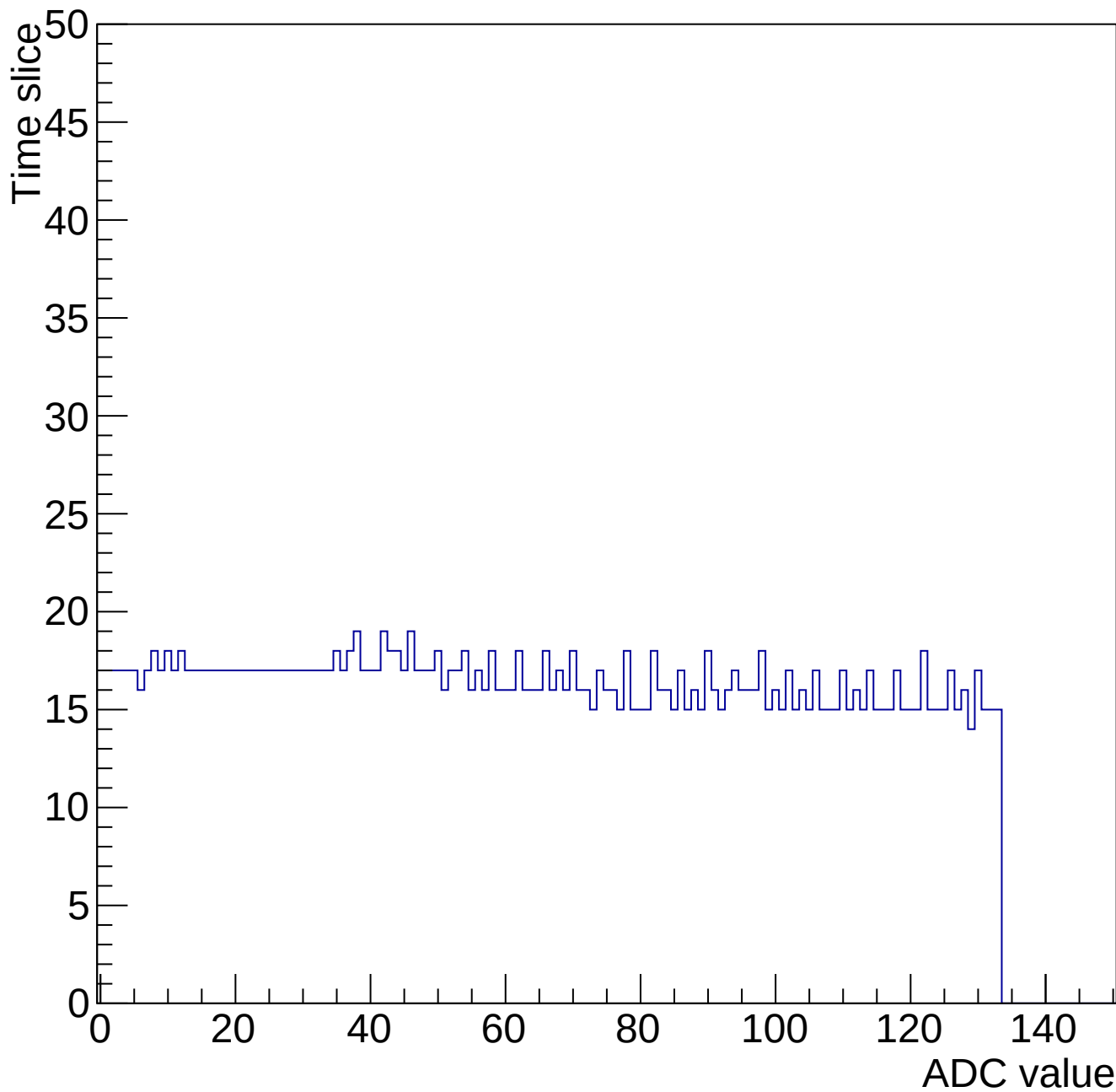
Evt 10 Time mcm 2001 chip 1 chan 6 34668-gain0-shaper6



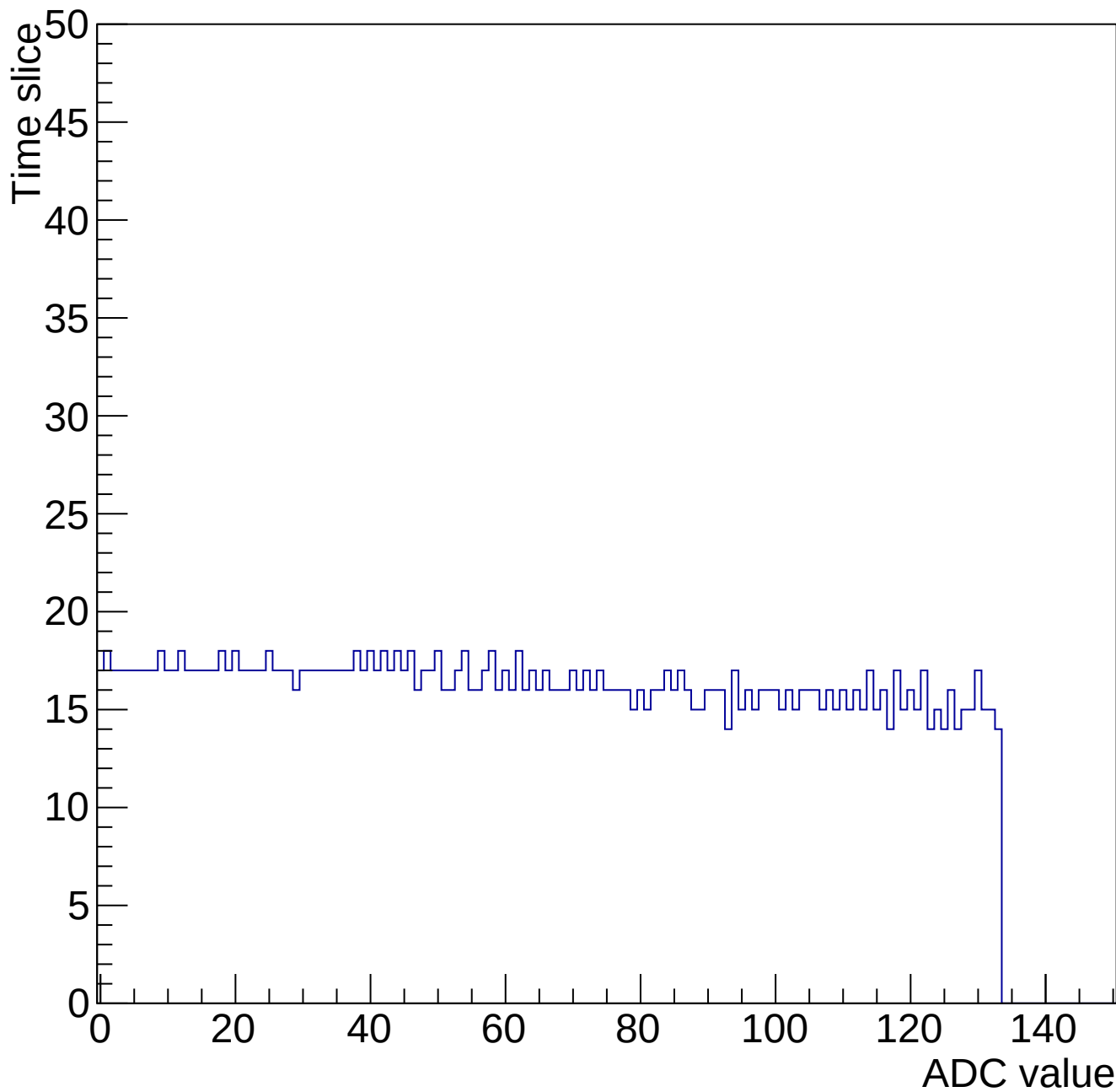
Evt 10 Time mcm 2001 chip 1 chan 7 34668-gain0-shaper6



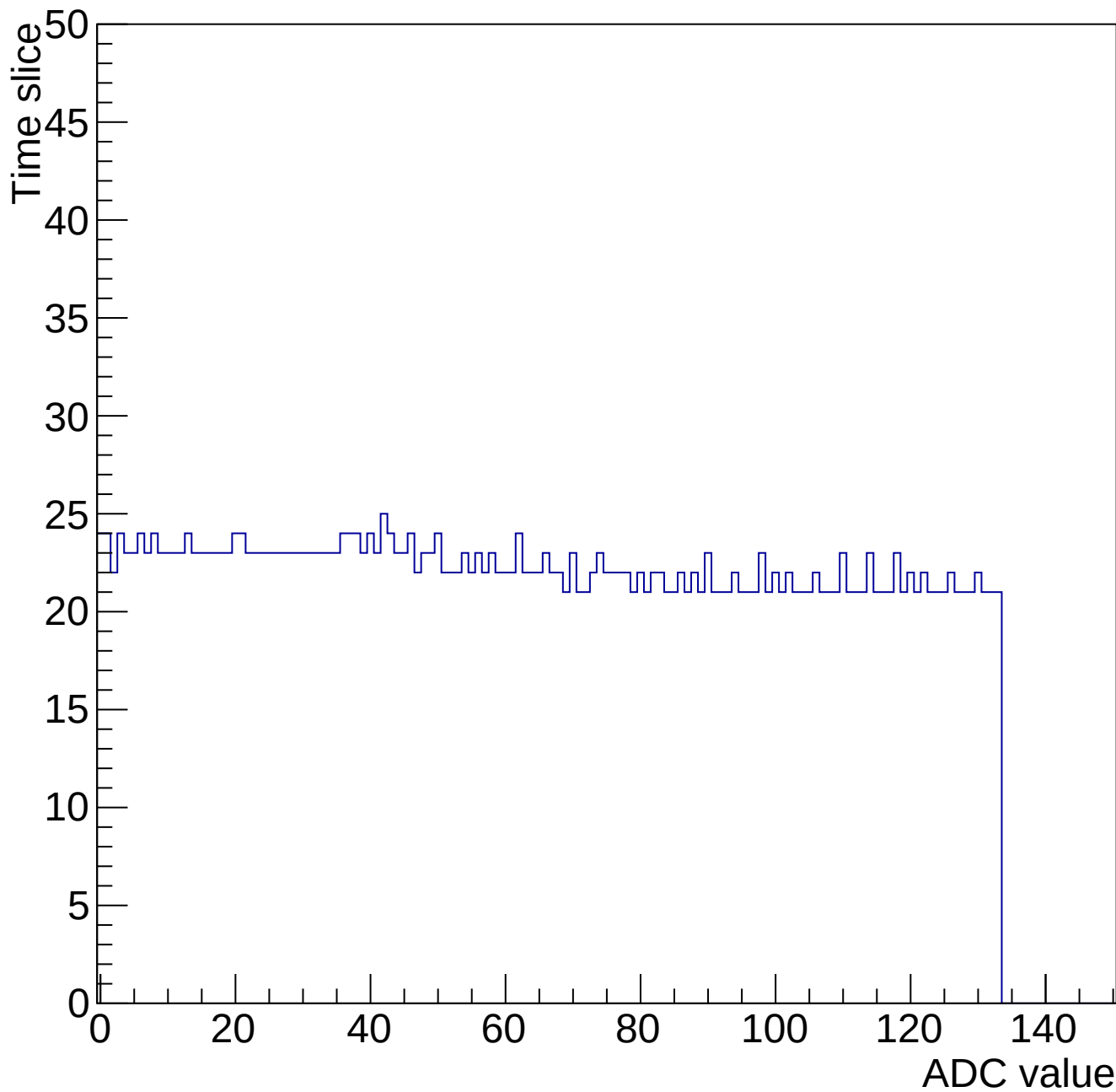
Evt 10 Time mcm 2001 chip 1 chan 8 34668-gain0-shaper6



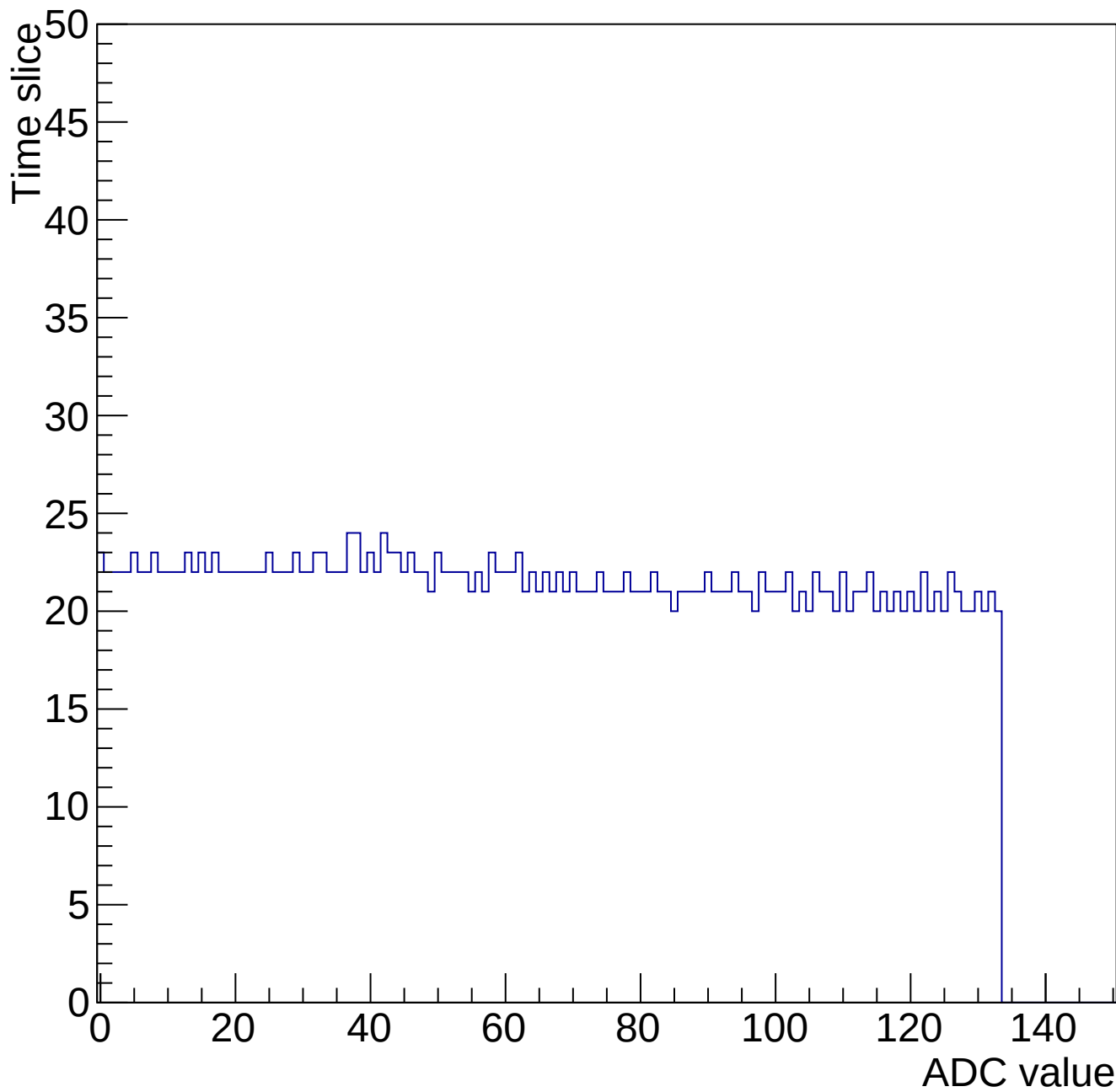
Evt 10 Time mcm 2001 chip 1 chan 9 34668-gain0-shaper6



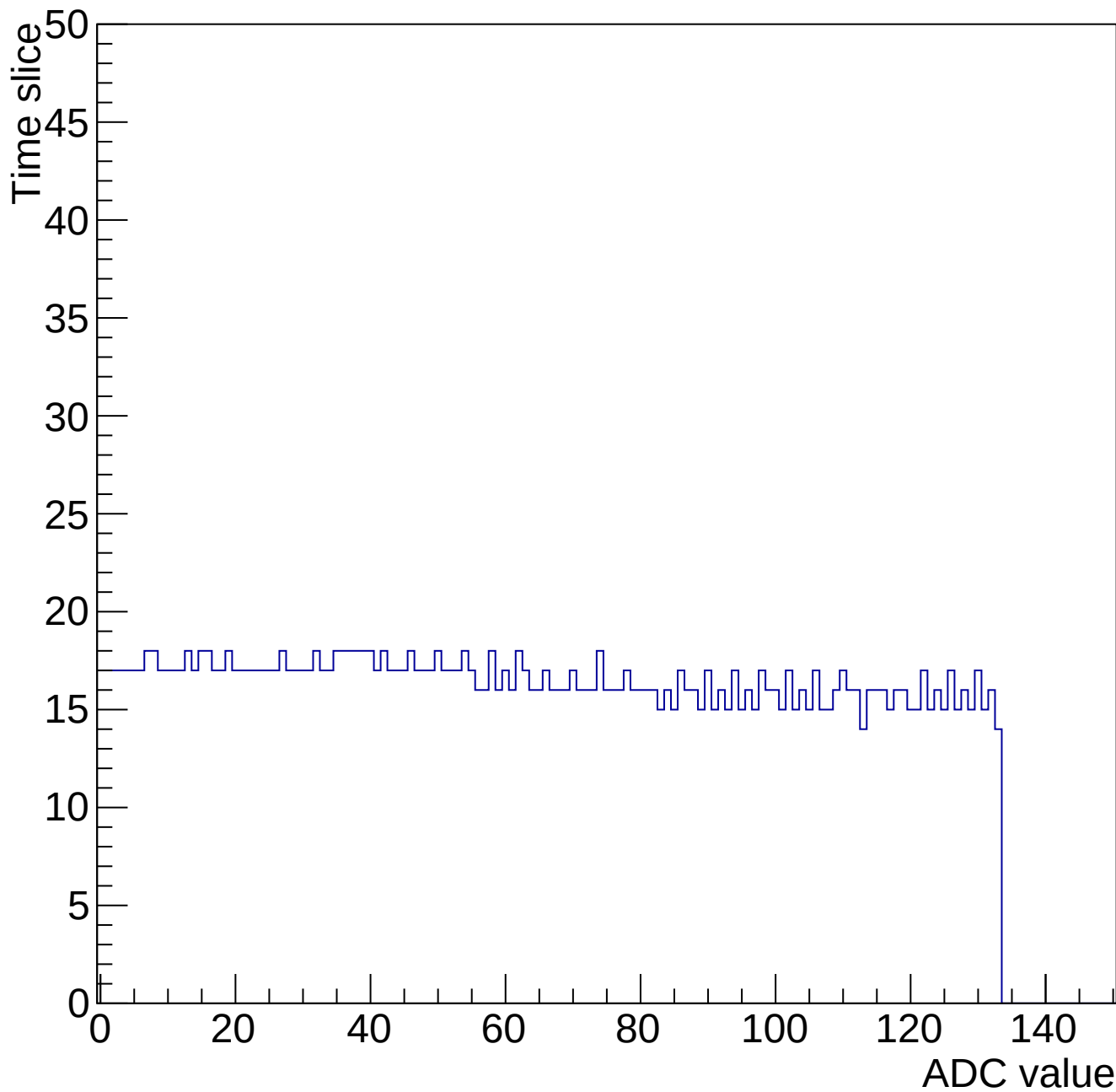
Evt 10 Time mcm 2001 chip 1 chan 10 34668-gain0-shaper6



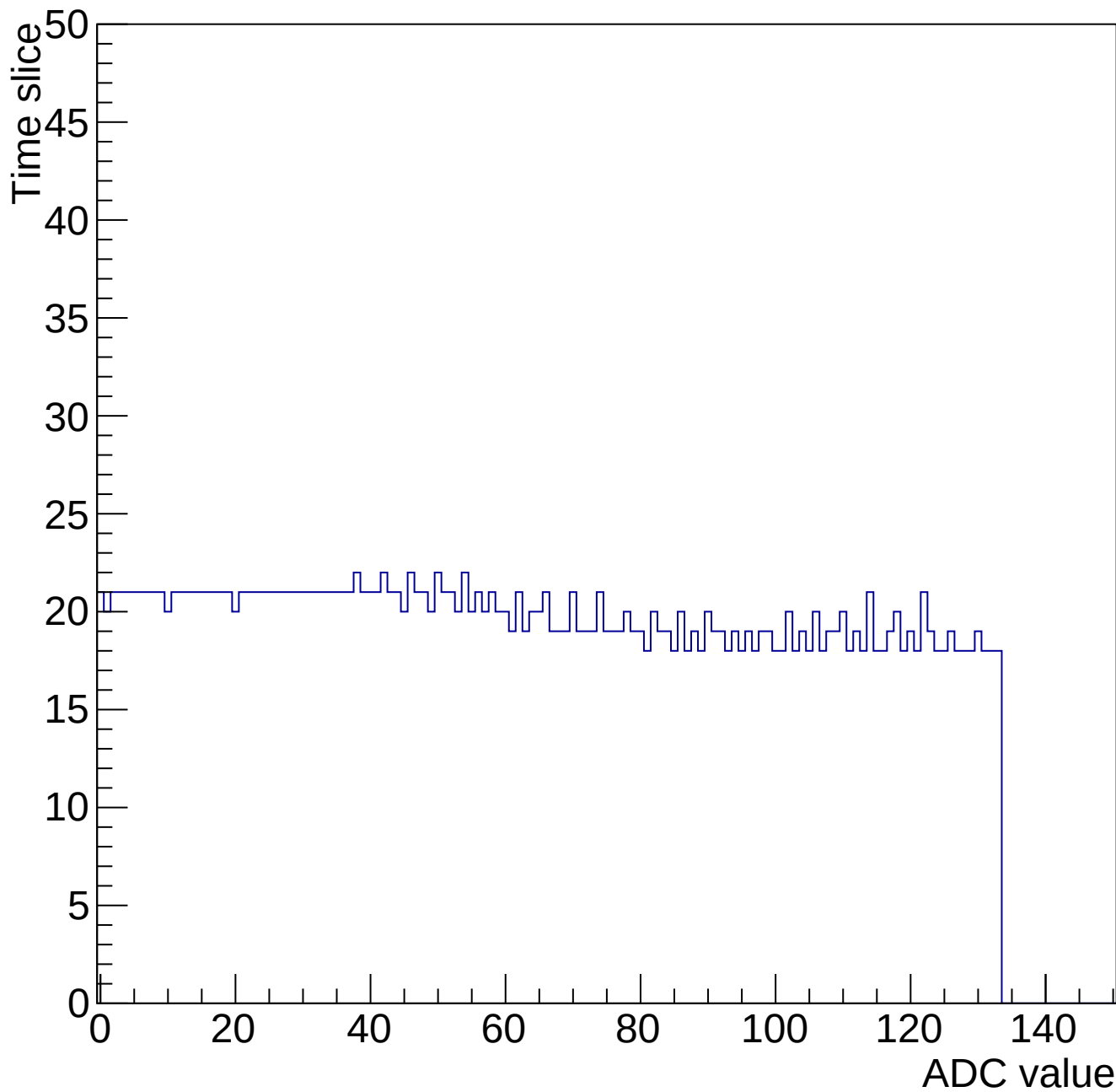
Evt 10 Time mcm 2001 chip 1 chan 11 34668-gain0-shaper6



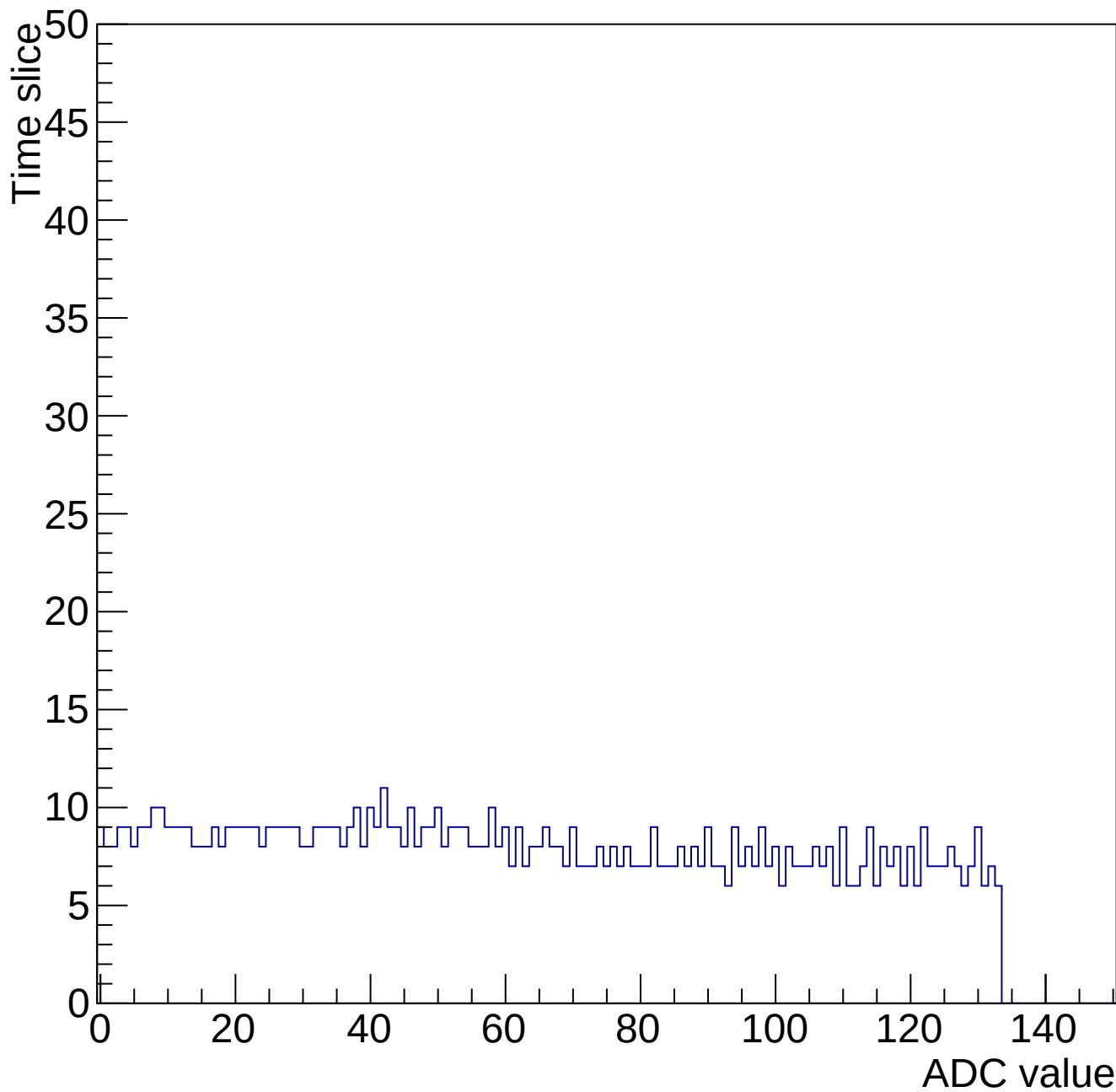
Evt 10 Time mcm 2001 chip 1 chan 12 34668-gain0-shaper6



Evt 10 Time mcm 2001 chip 1 chan 13 34668-gain0-shaper6



Evt 10 Time mcm 2001 chip 1 chan 14 34668-gain0-shaper6



Evt 10 Time mcm 2001 chip 1 chan 15 34668-gain0-shaper6

