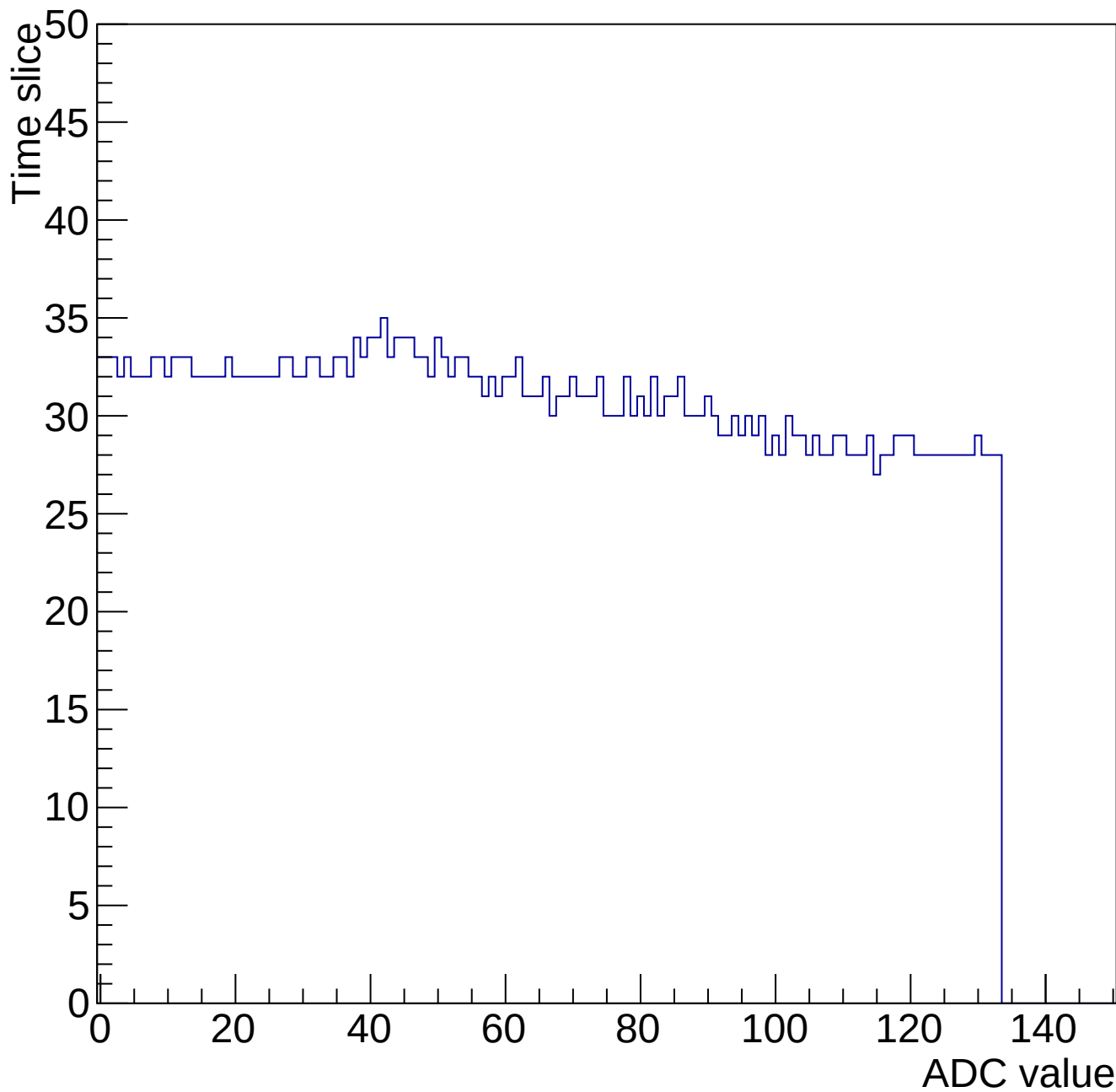
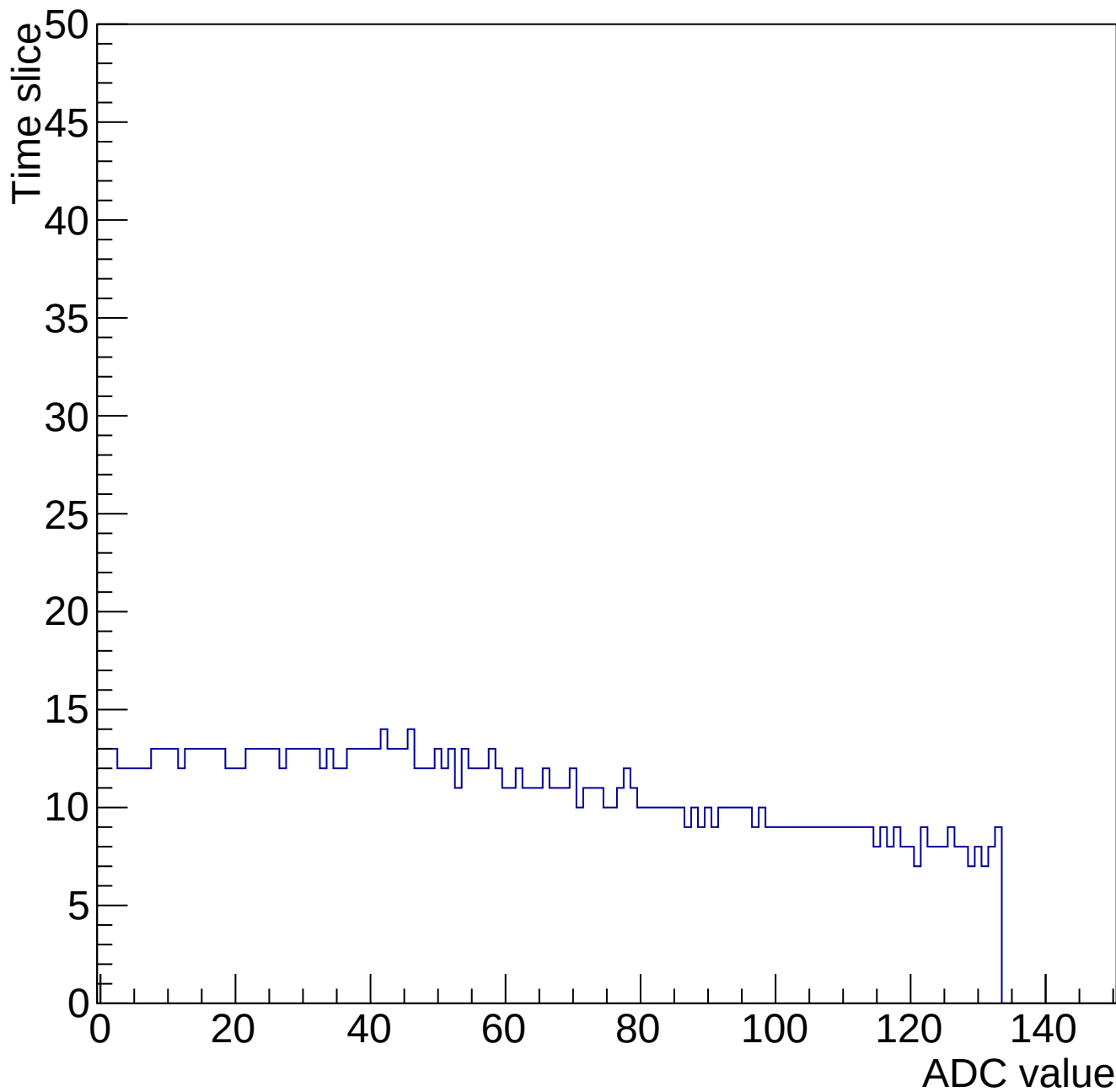


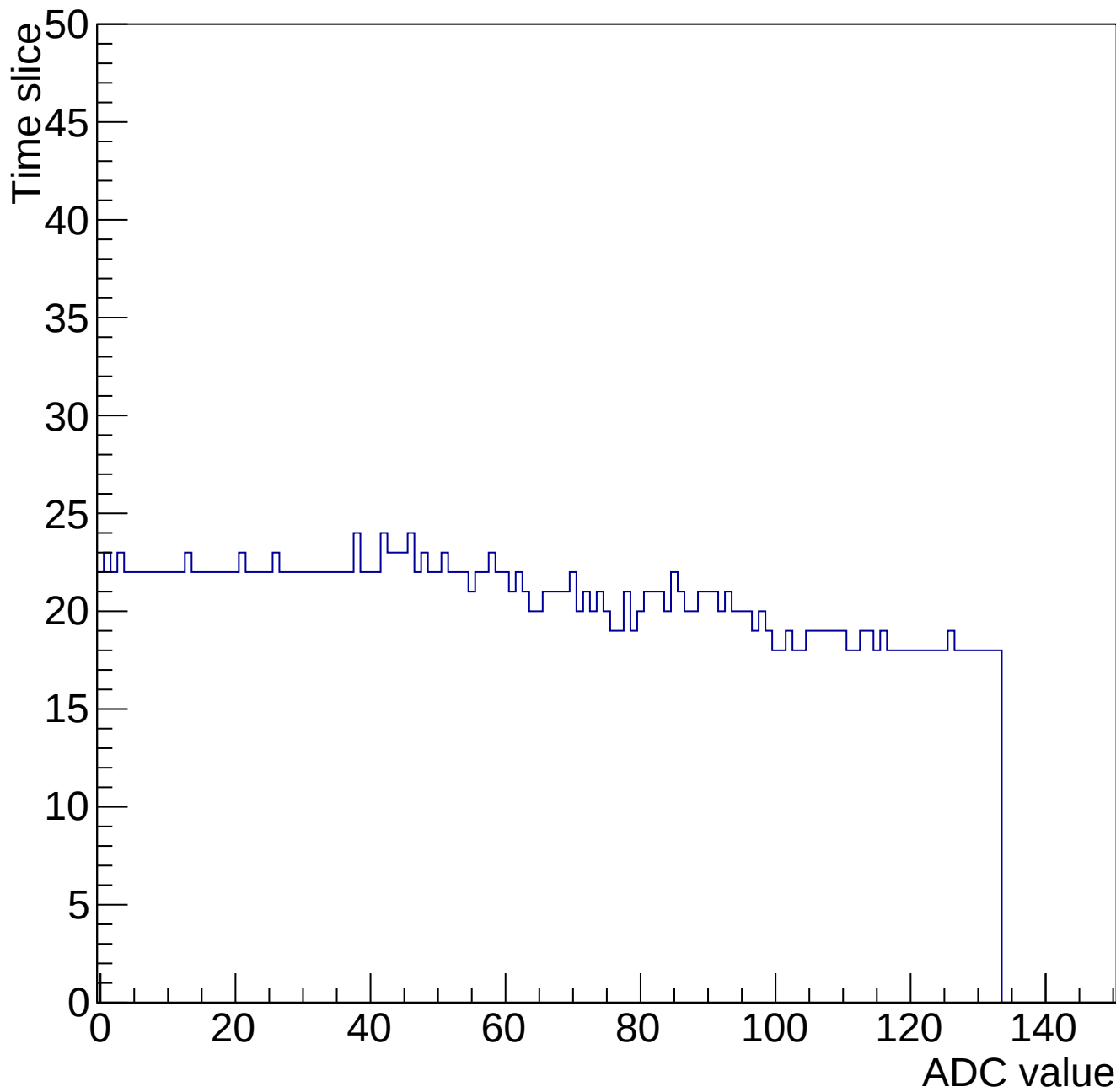
Evt 10 Time mcm 2004 chip 4 chan 0 34651-gain0-shaper0



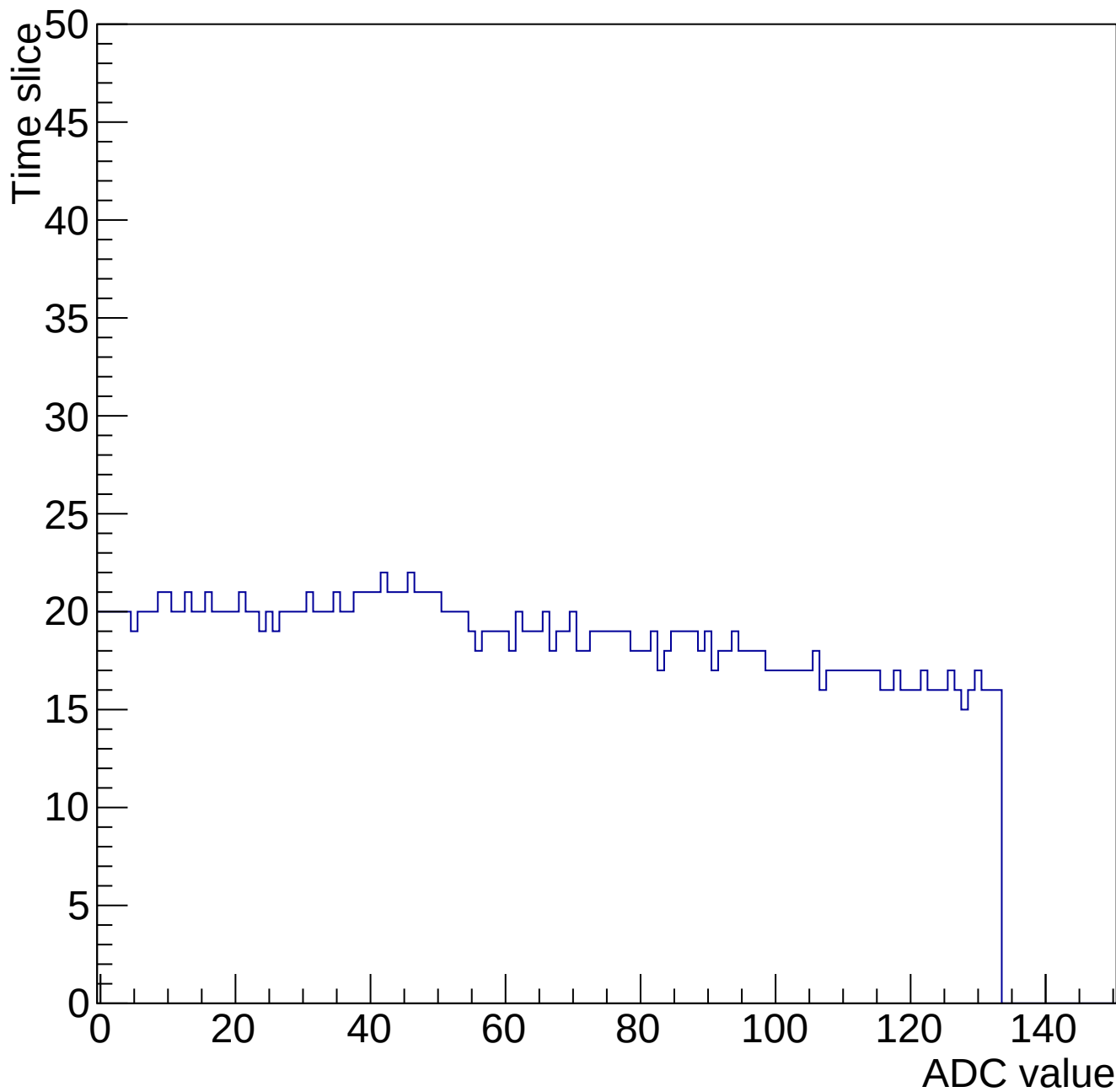
Evt 10 Time mcm 2004 chip 4 chan 1 34651-gain0-shaper0



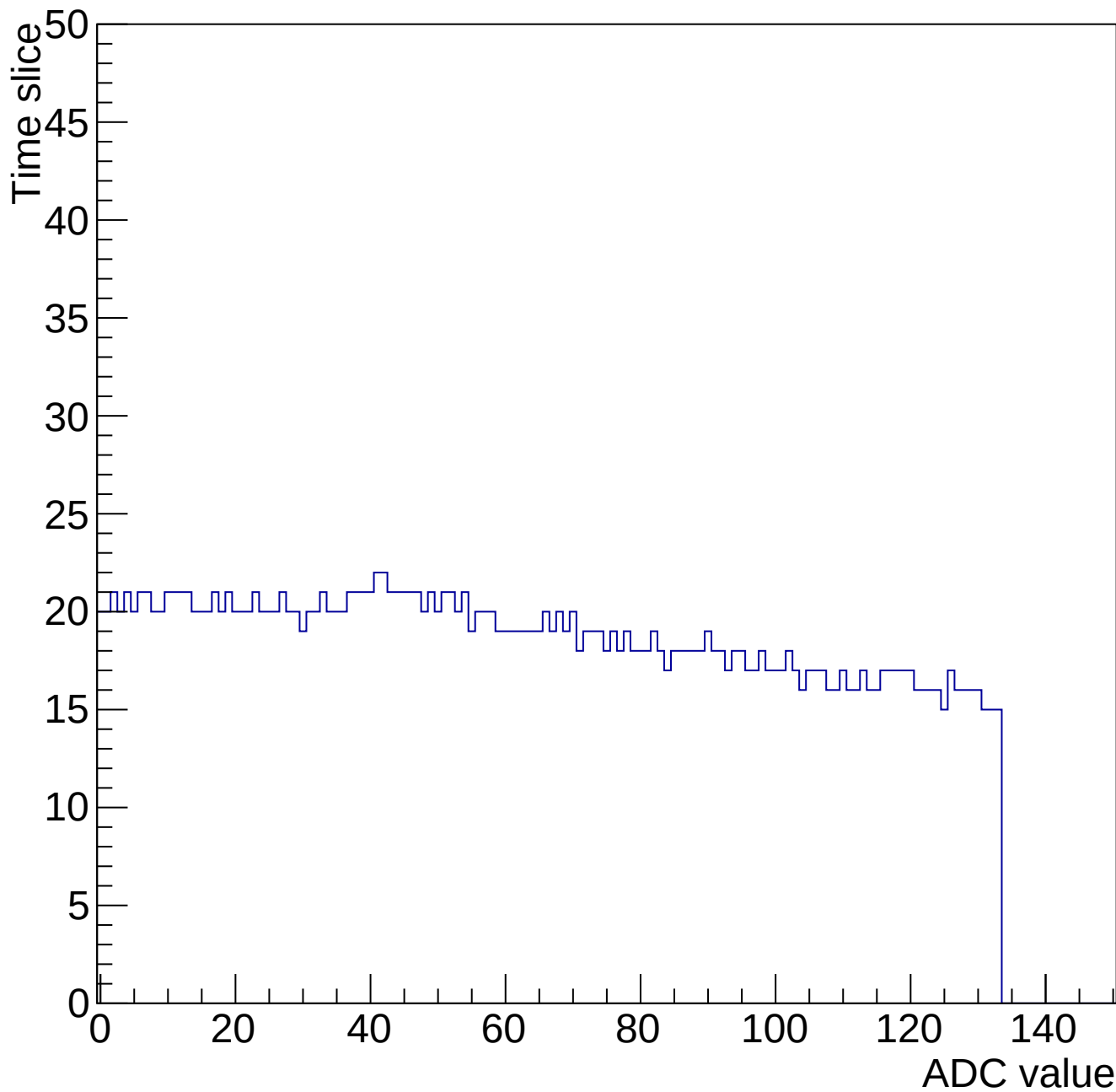
Evt 10 Time mcm 2004 chip 4 chan 2 34651-gain0-shaper0



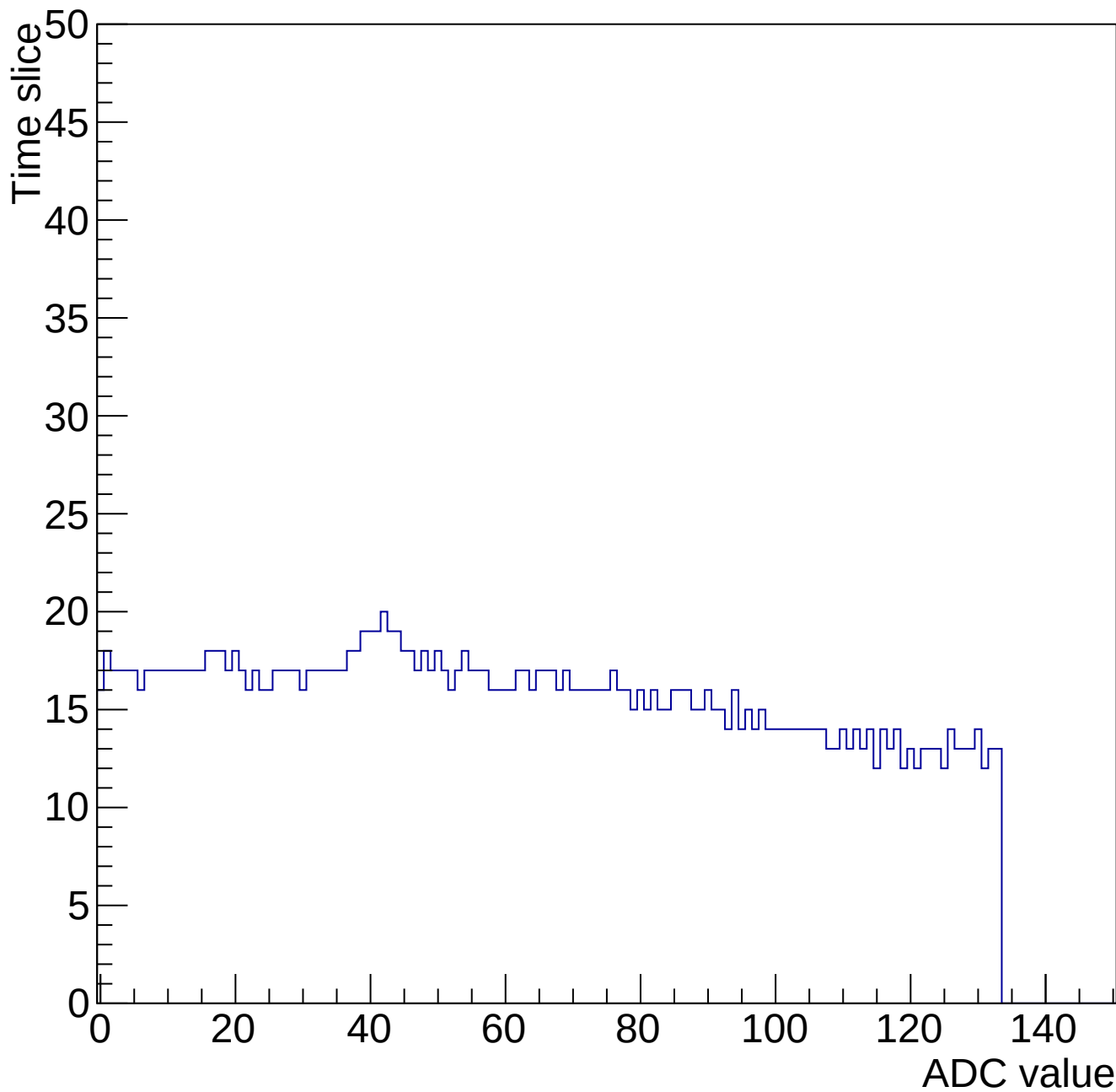
Evt 10 Time mcm 2004 chip 4 chan 3 34651-gain0-shaper0



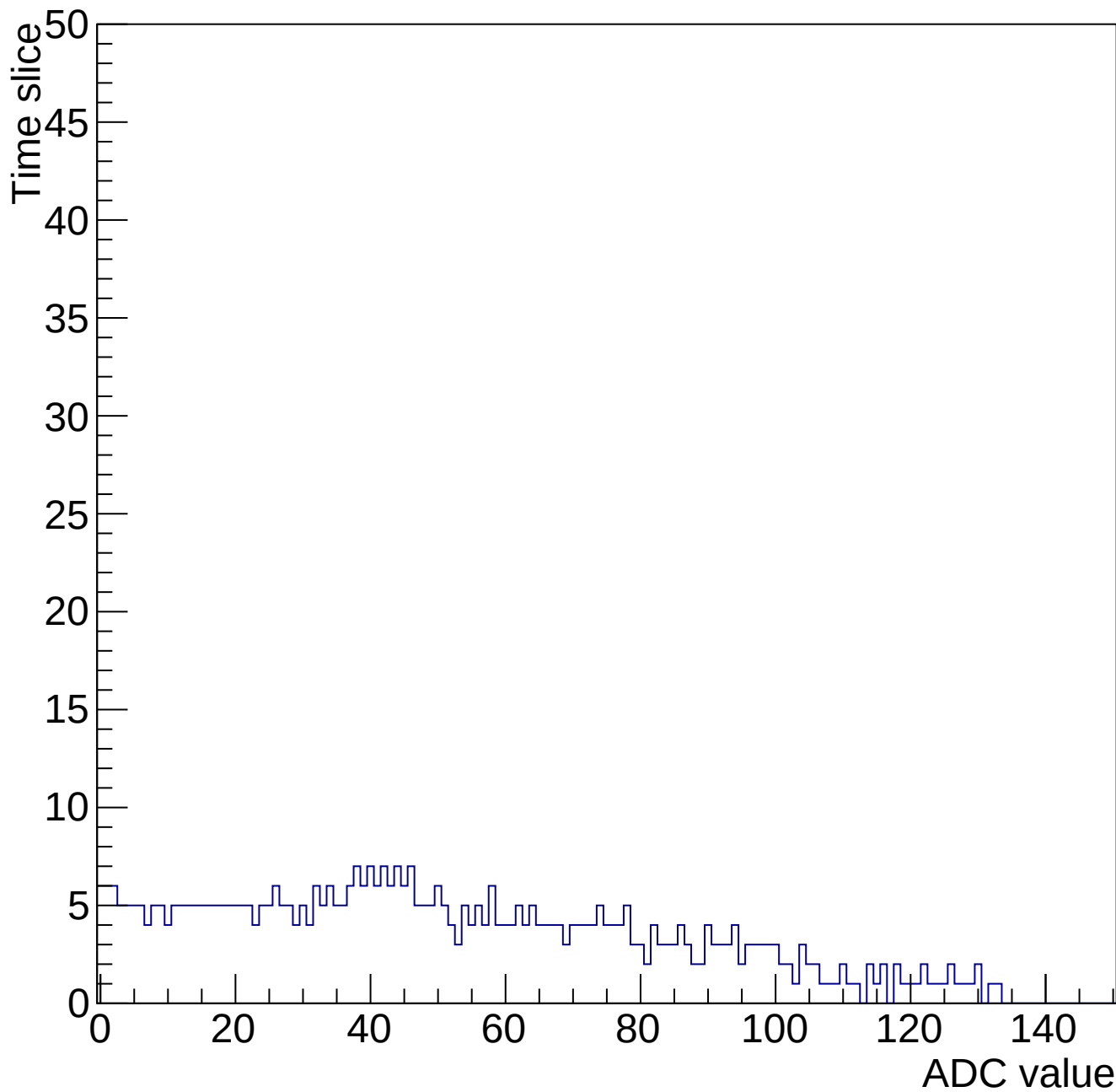
Evt 10 Time mcm 2004 chip 4 chan 4 34651-gain0-shaper0



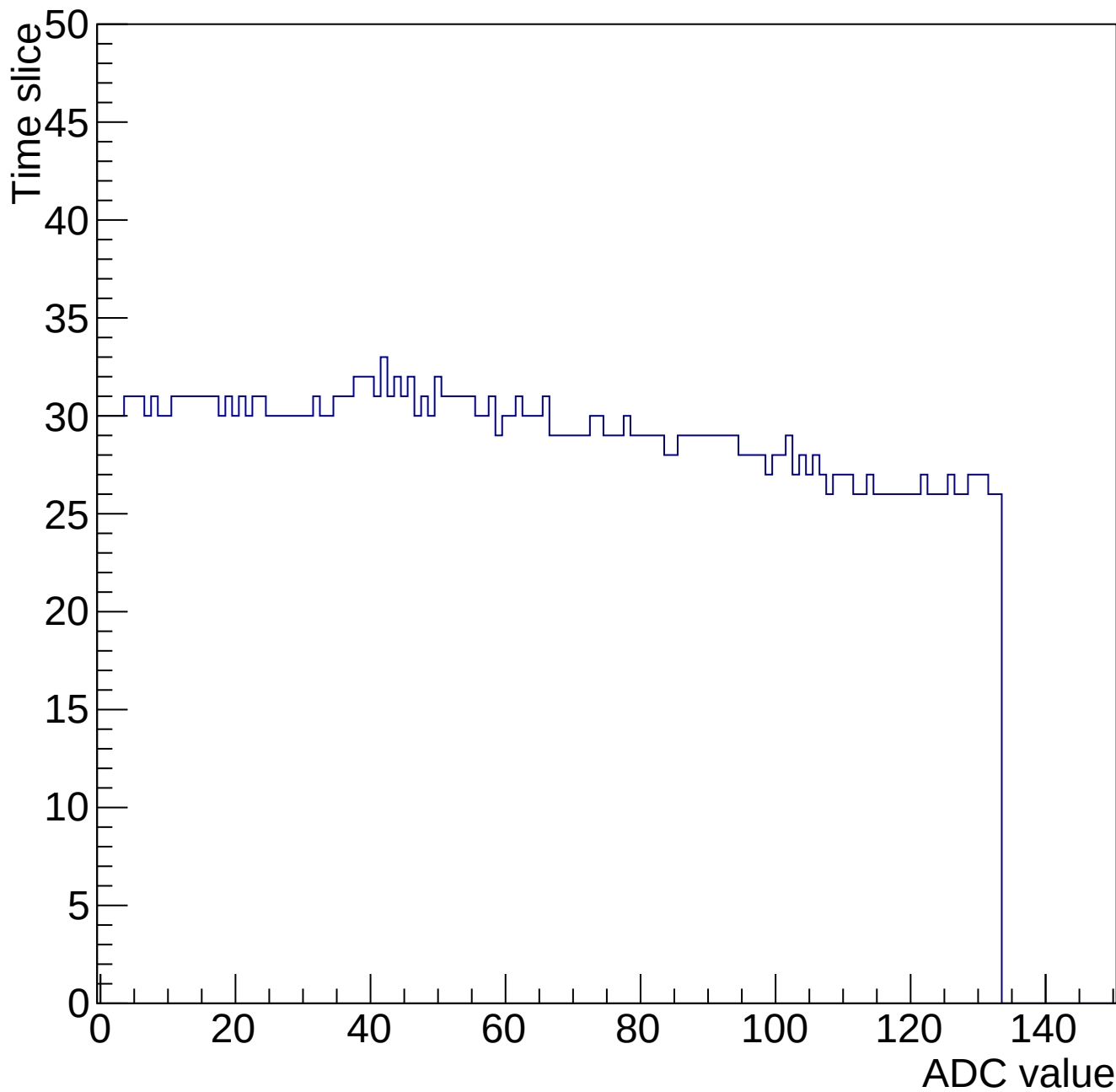
Evt 10 Time mcm 2004 chip 4 chan 5 34651-gain0-shaper0



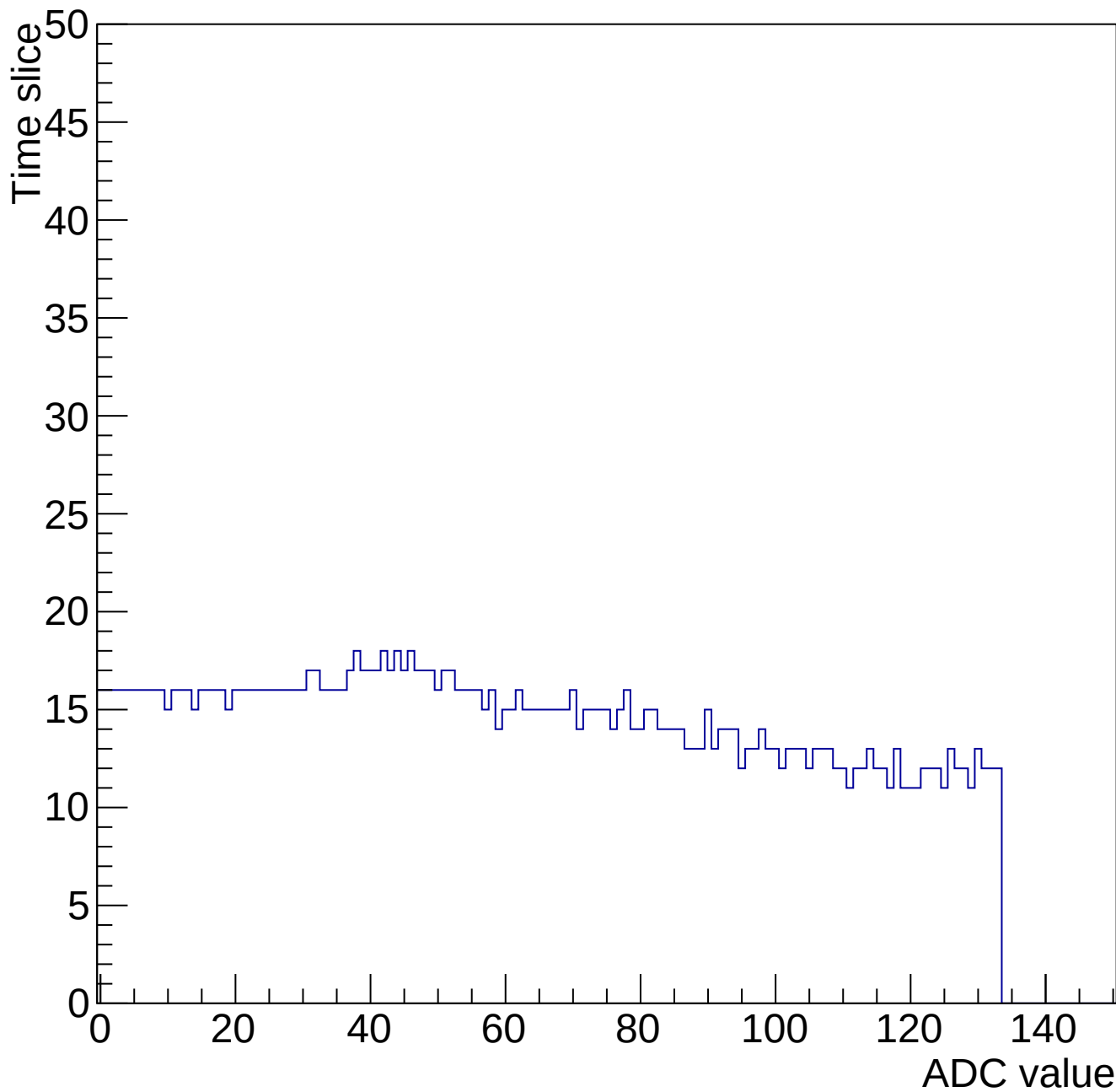
Evt 10 Time mcm 2004 chip 4 chan 6 34651-gain0-shaper0



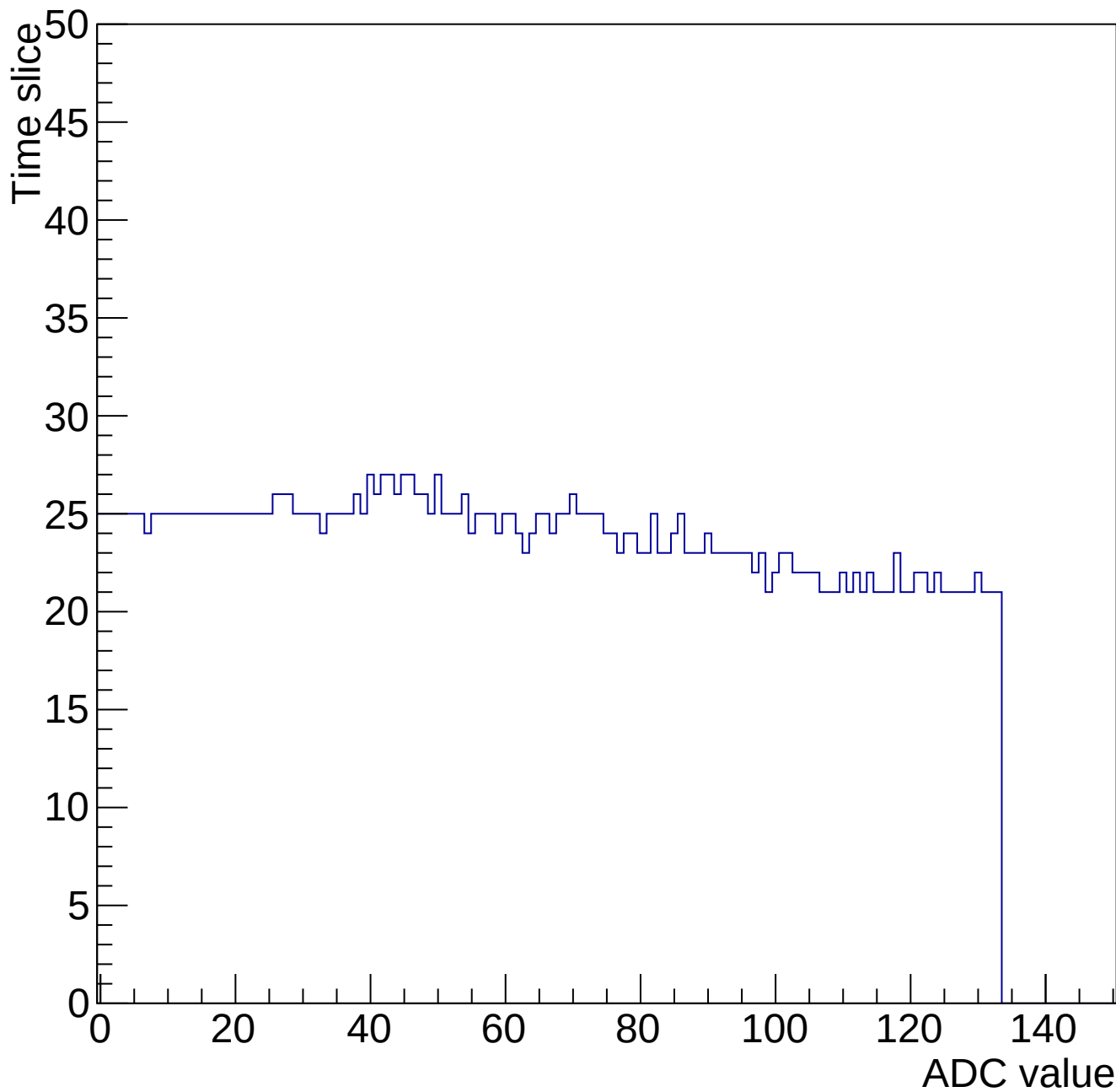
Evt 10 Time mcm 2004 chip 4 chan 7 34651-gain0-shaper0



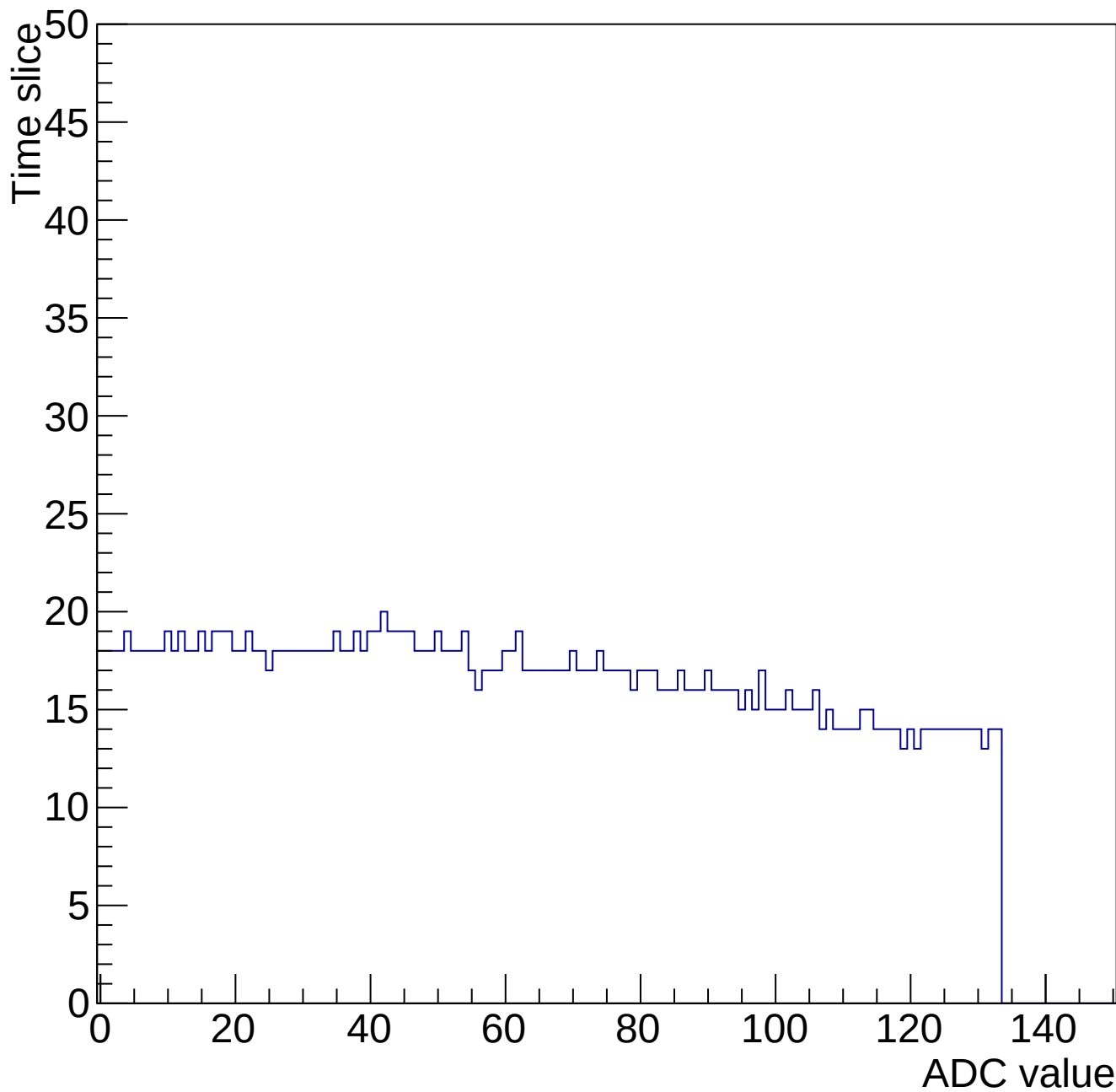
Evt 10 Time mcm 2004 chip 4 chan 8 34651-gain0-shaper0



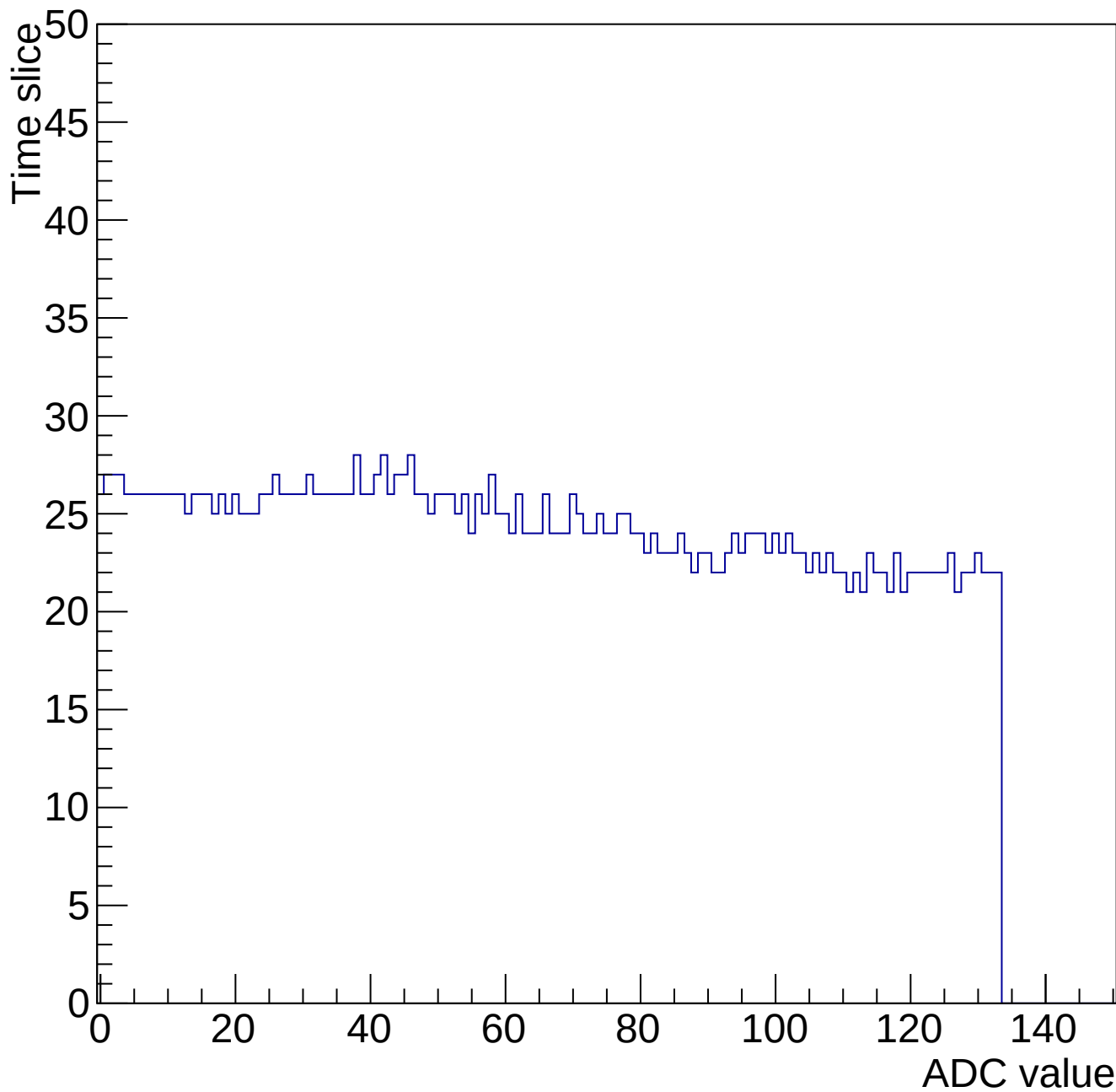
Evt 10 Time mcm 2004 chip 4 chan 9 34651-gain0-shaper0



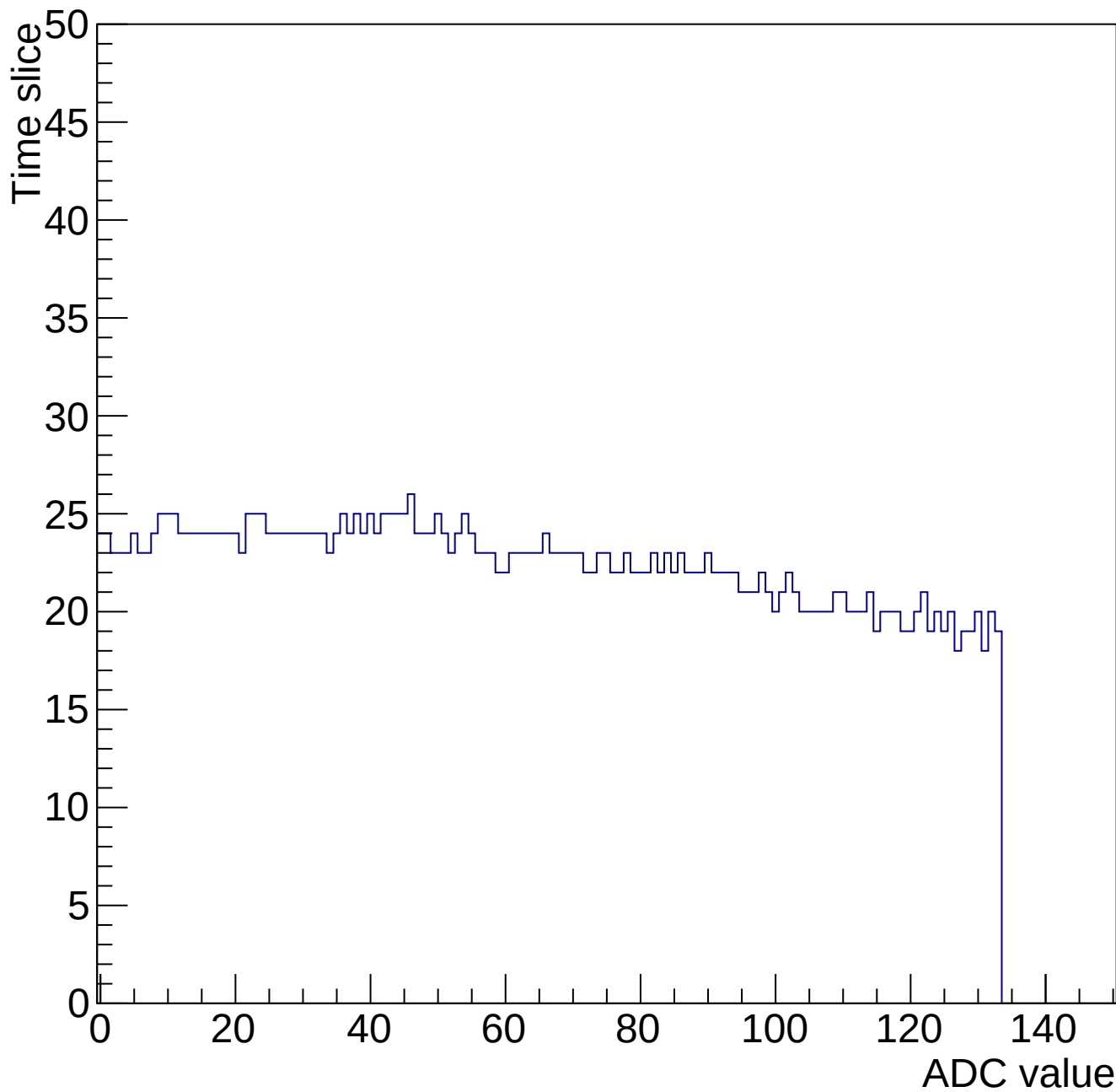
Evt 10 Time mcm 2004 chip 4 chan 10 34651-gain0-shaper0



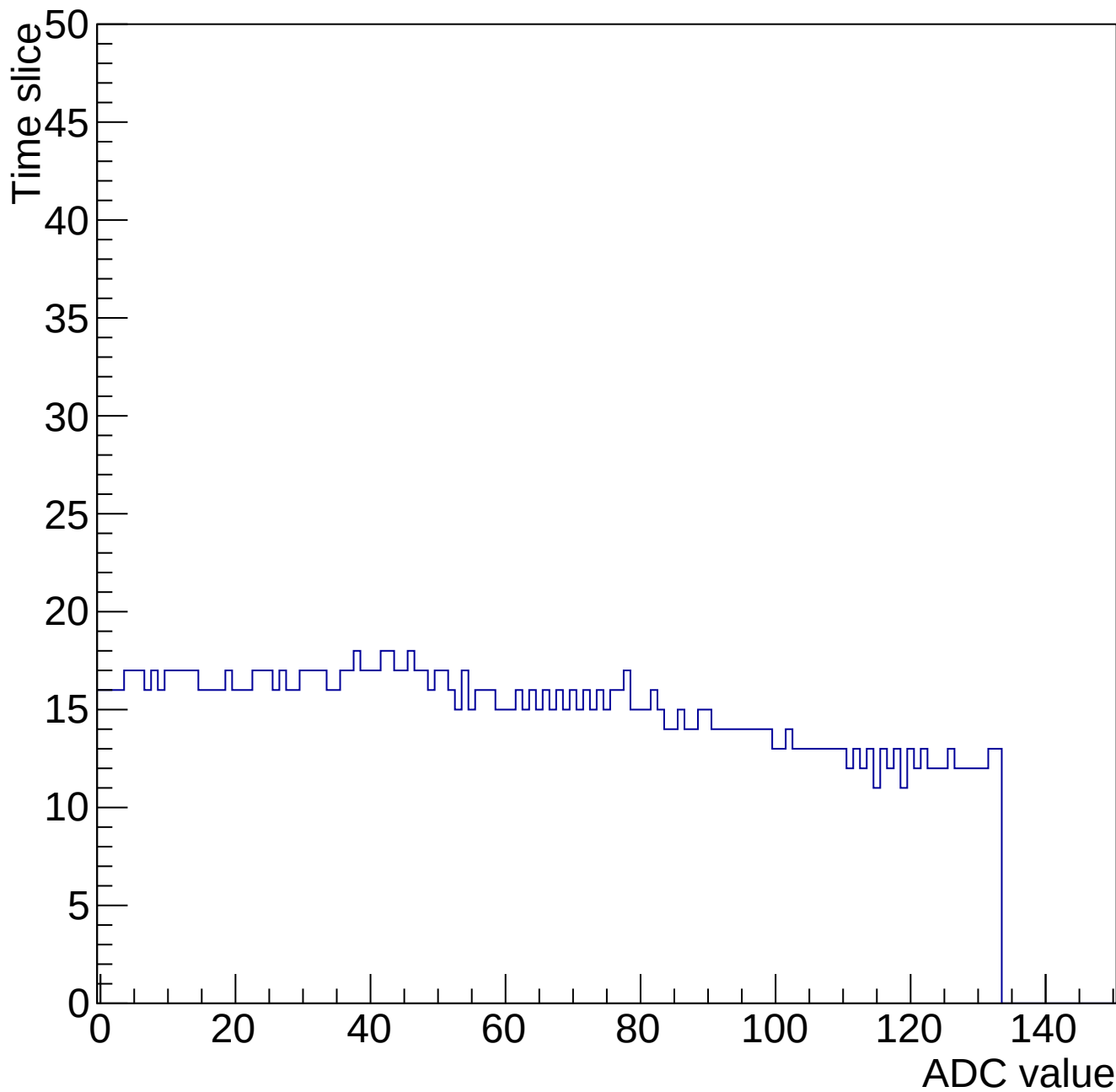
Evt 10 Time mcm 2004 chip 4 chan 11 34651-gain0-shaper0



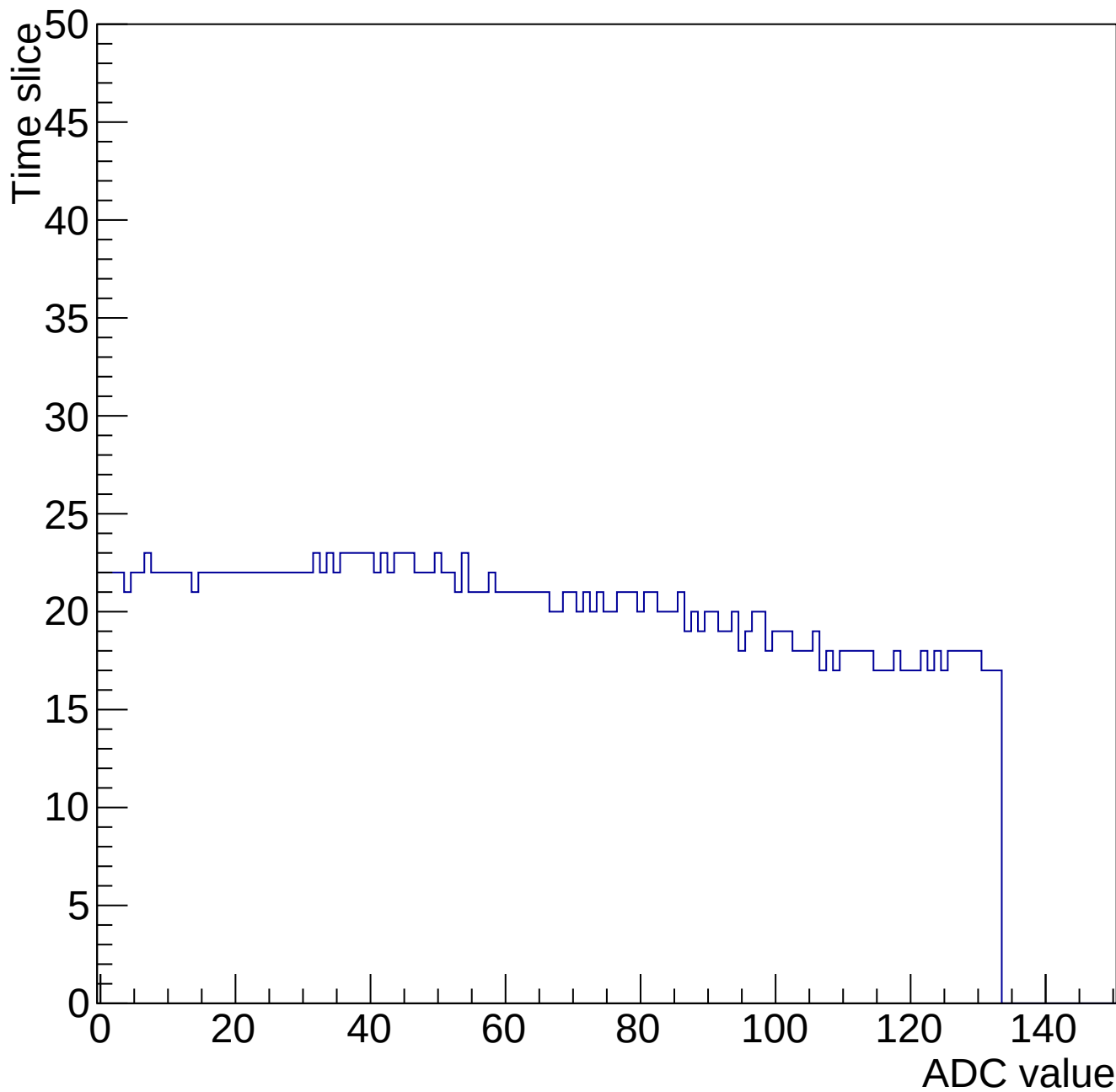
Evt 10 Time mcm 2004 chip 4 chan 12 34651-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 13 34651-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 14 34651-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 15 34651-gain0-shaper0

