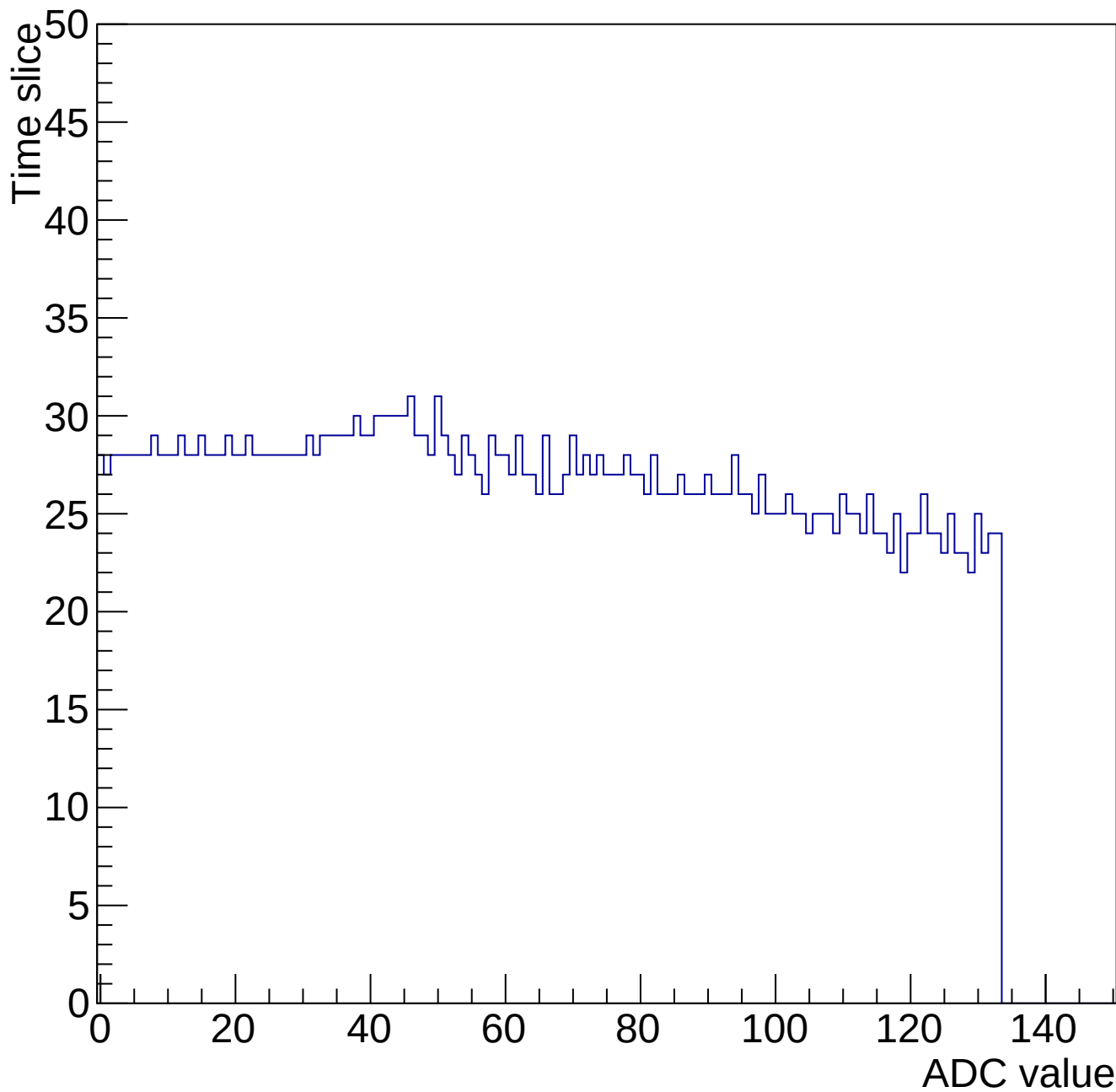
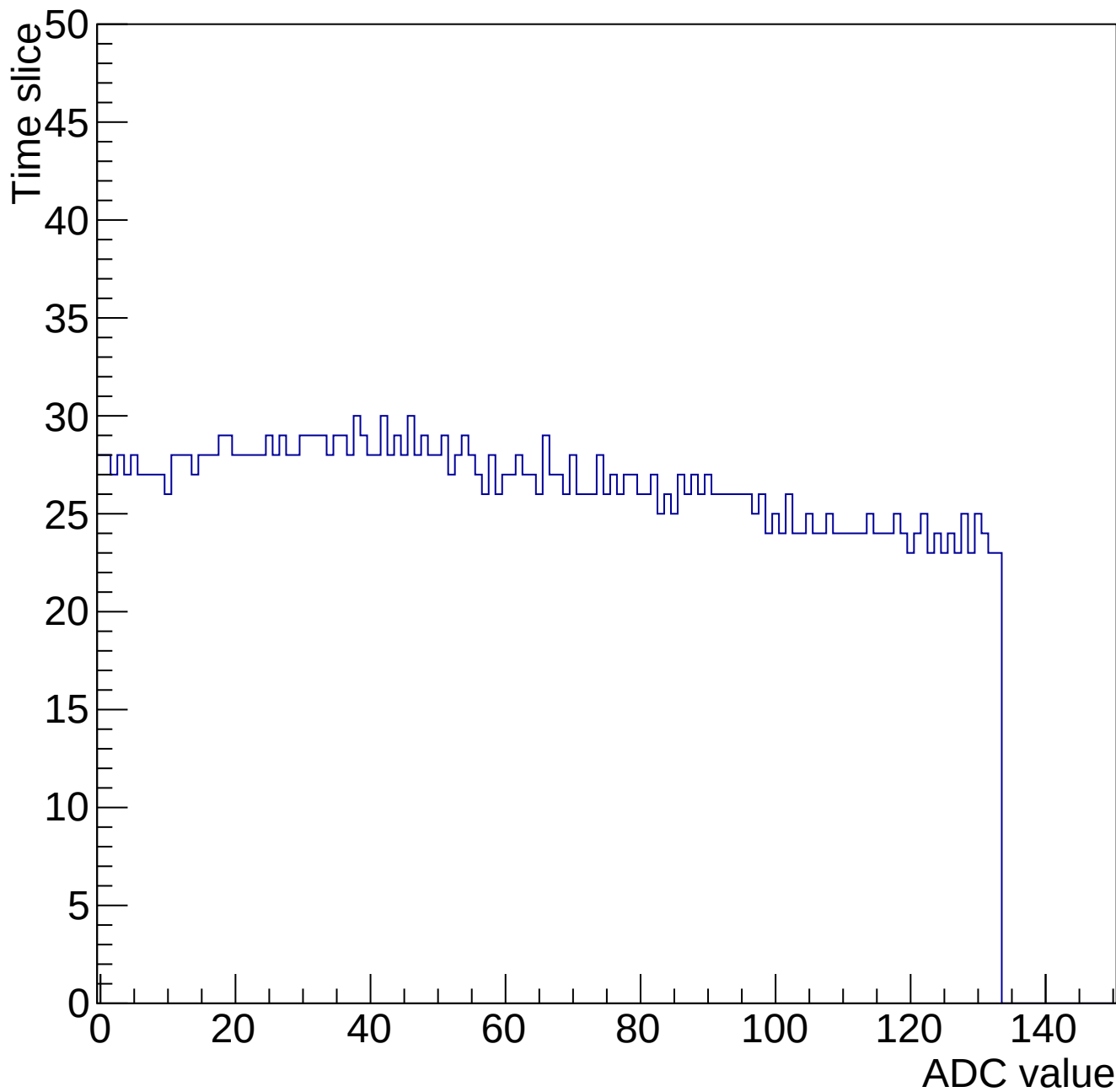


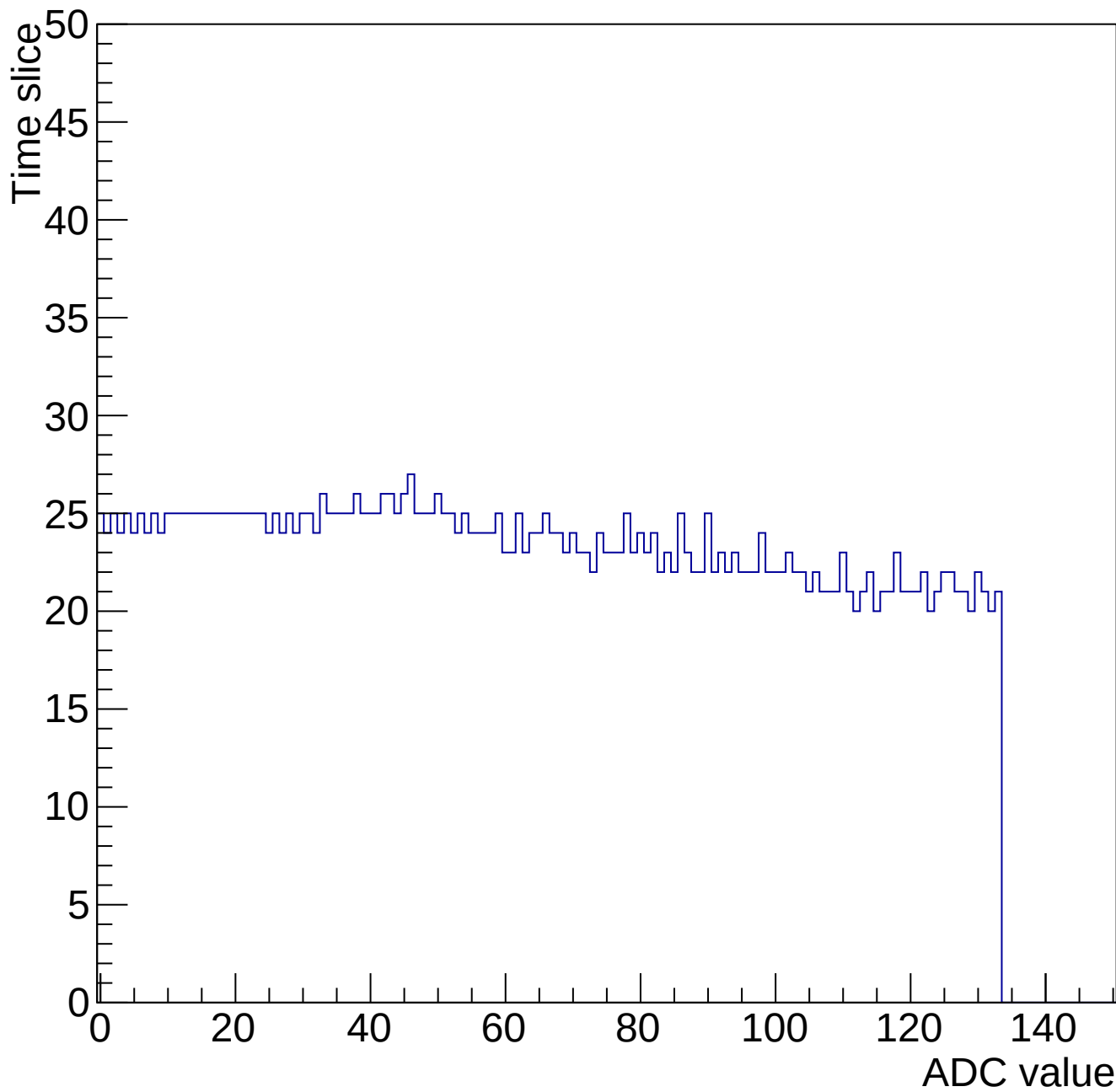
Evt 10 Time mcm 2005 chip 5 chan 0 34653-gain0-shaper0



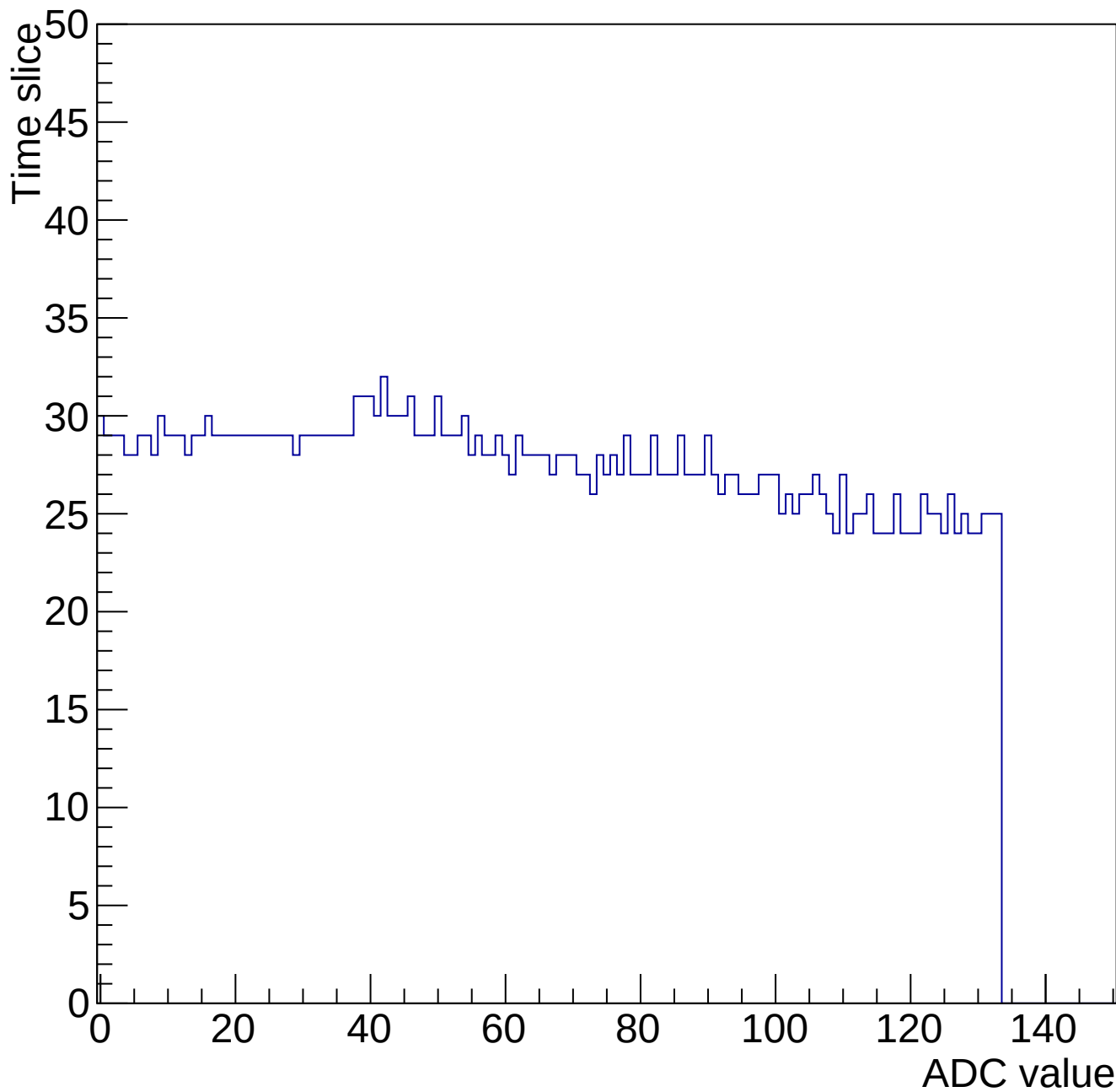
Evt 10 Time mcm 2005 chip 5 chan 1 34653-gain0-shaper0



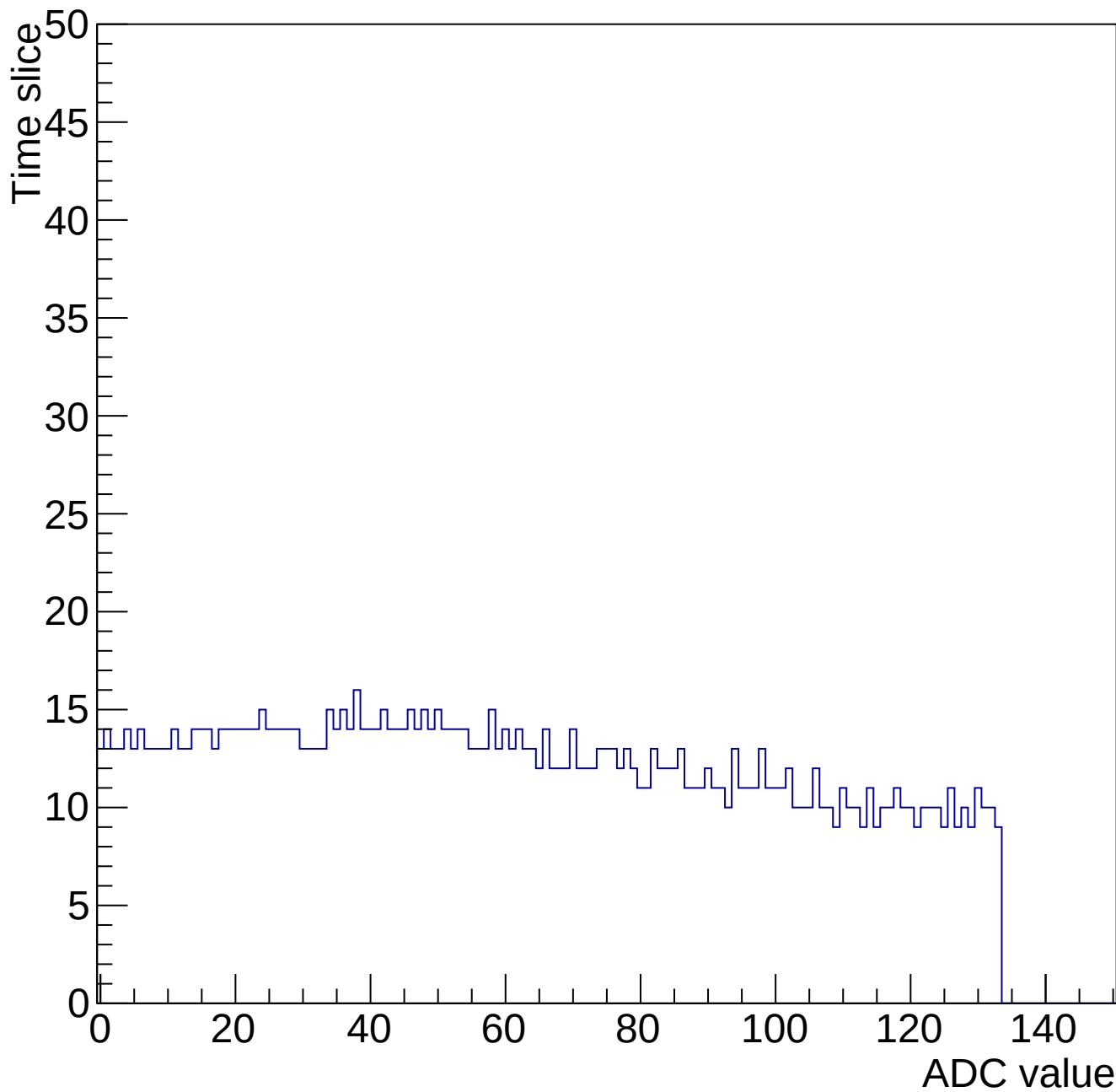
Evt 10 Time mcm 2005 chip 5 chan 2 34653-gain0-shaper0



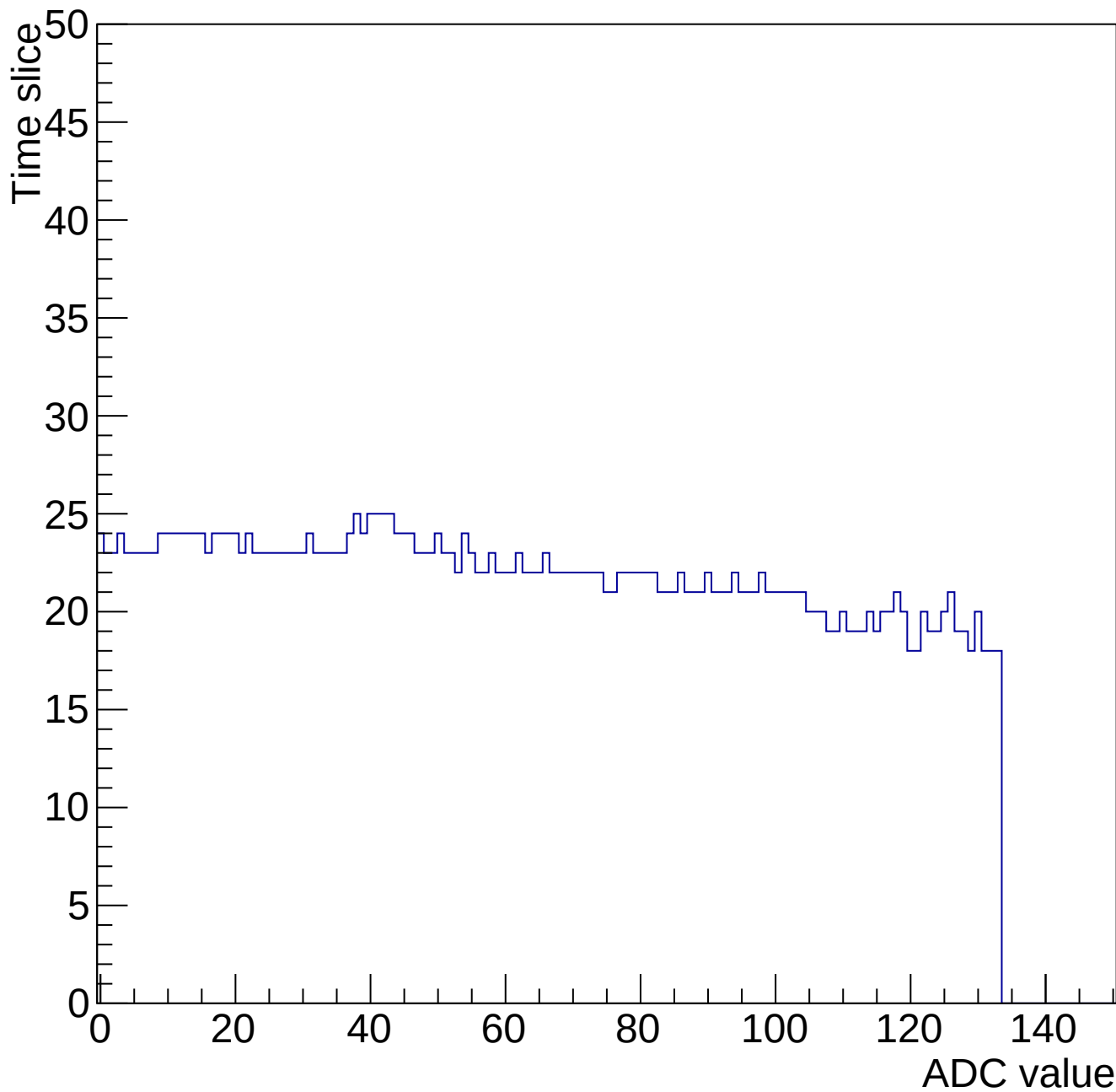
Evt 10 Time mcm 2005 chip 5 chan 3 34653-gain0-shaper0



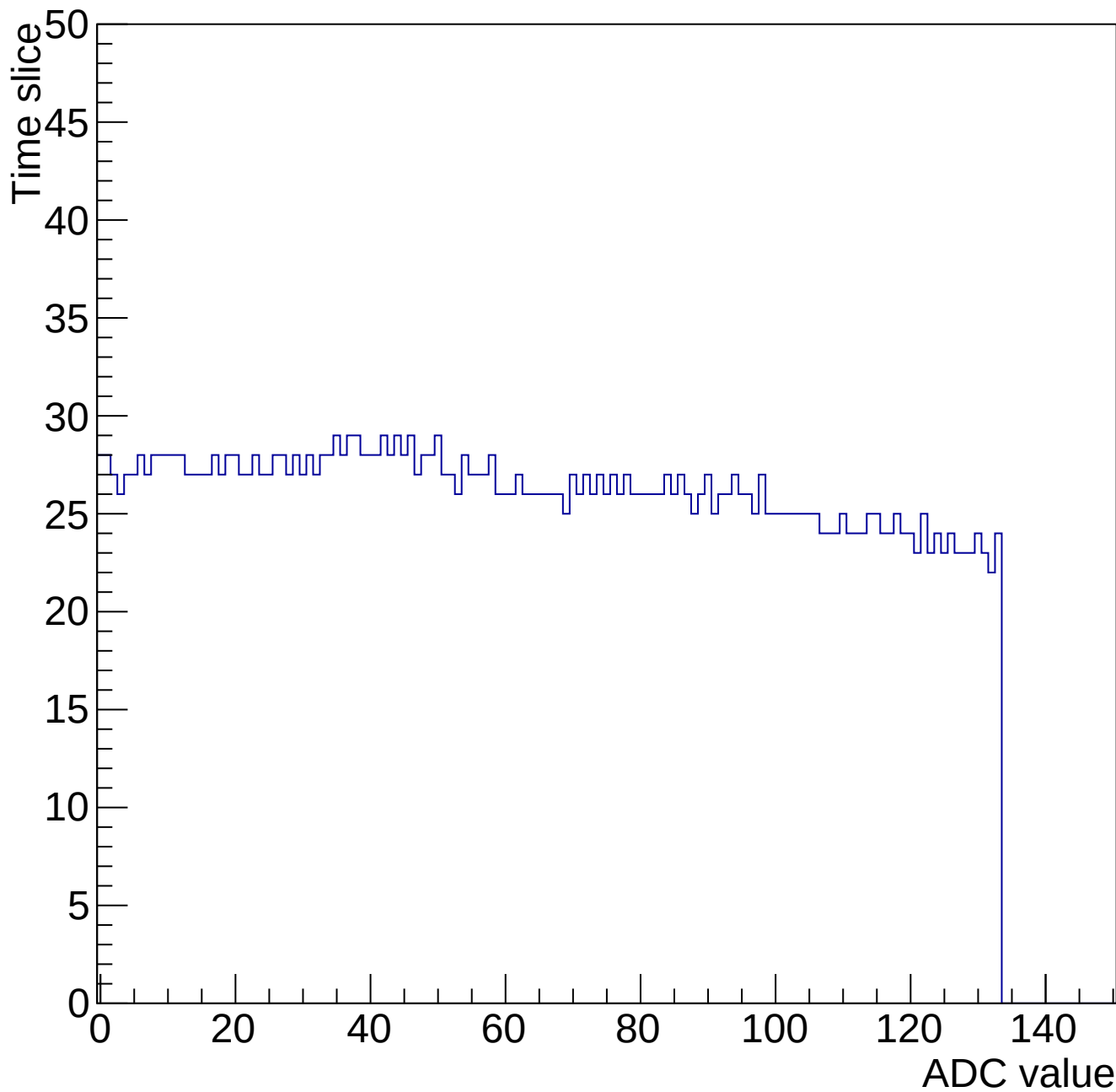
Evt 10 Time mcm 2005 chip 5 chan 4 34653-gain0-shaper0



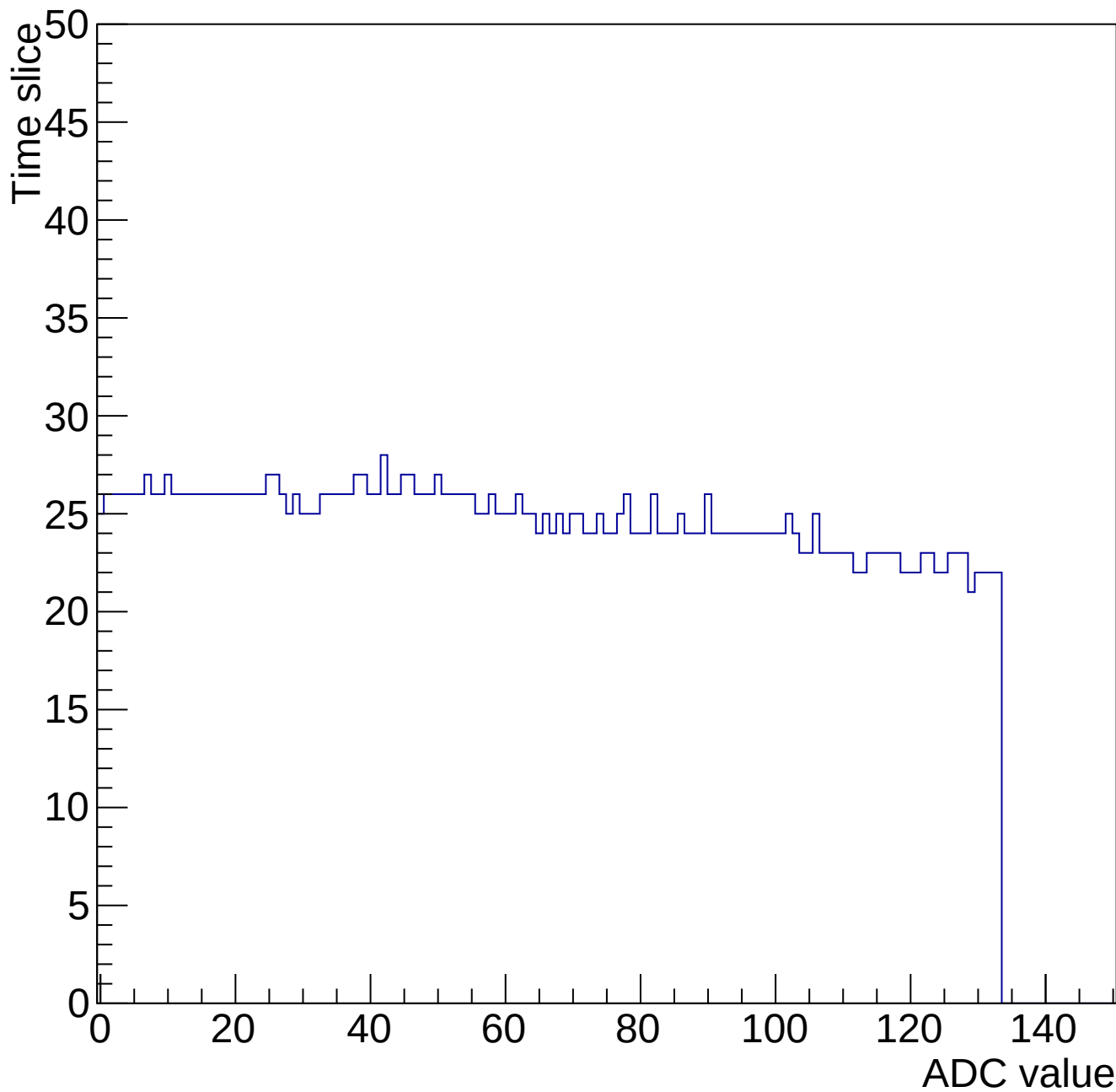
Evt 10 Time mcm 2005 chip 5 chan 5 34653-gain0-shaper0



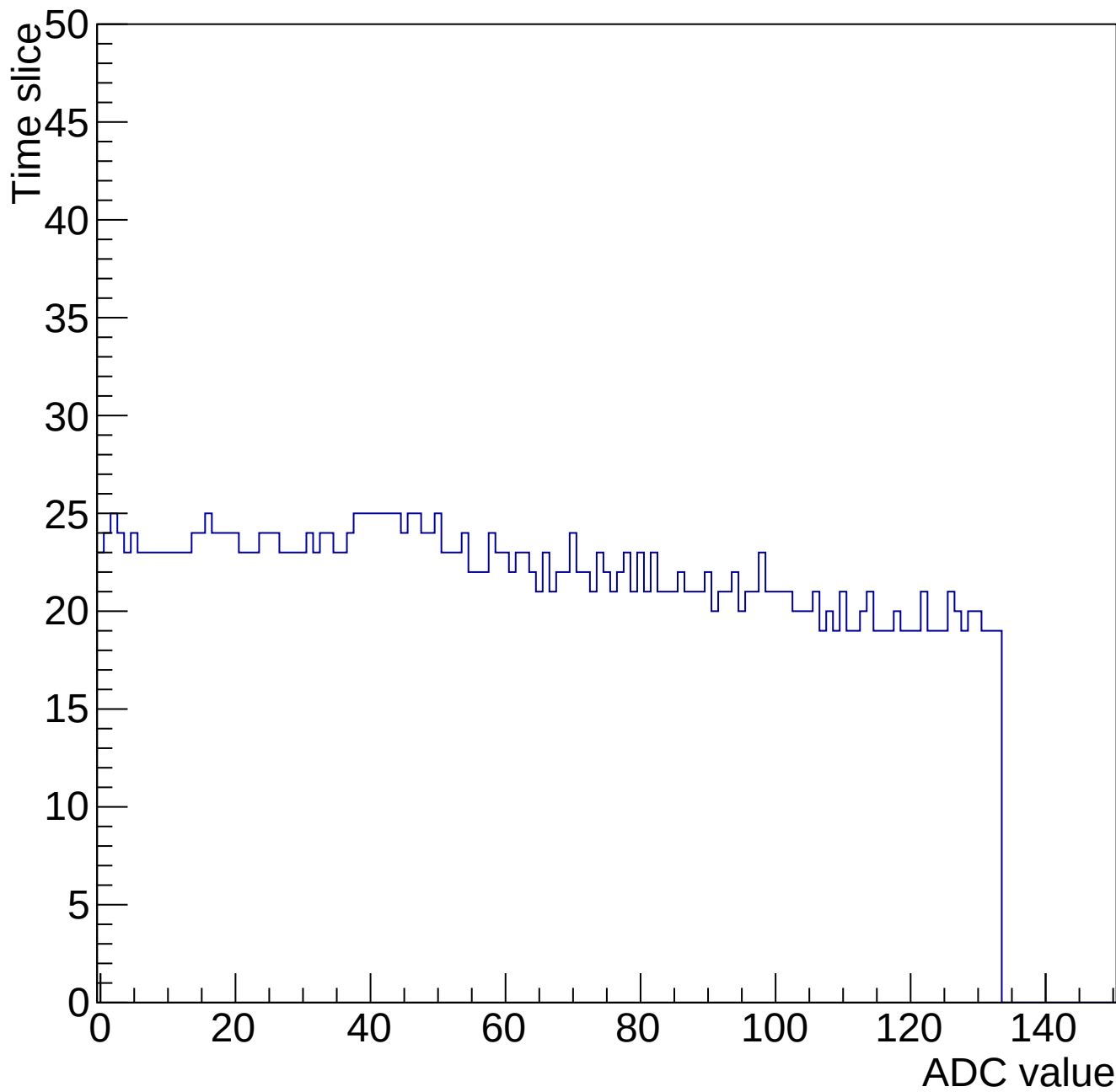
Evt 10 Time mcm 2005 chip 5 chan 6 34653-gain0-shaper0



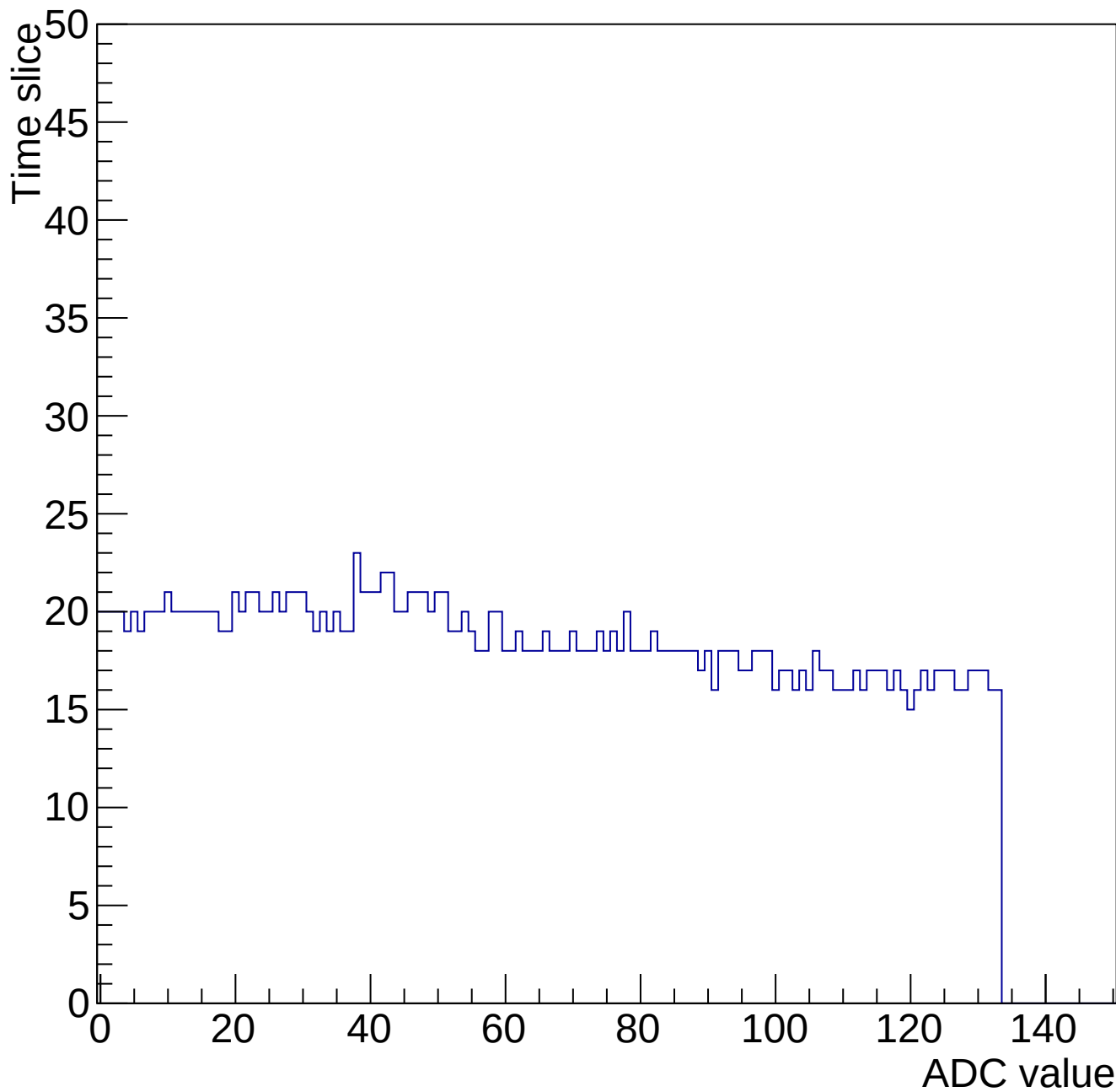
Evt 10 Time mcm 2005 chip 5 chan 7 34653-gain0-shaper0



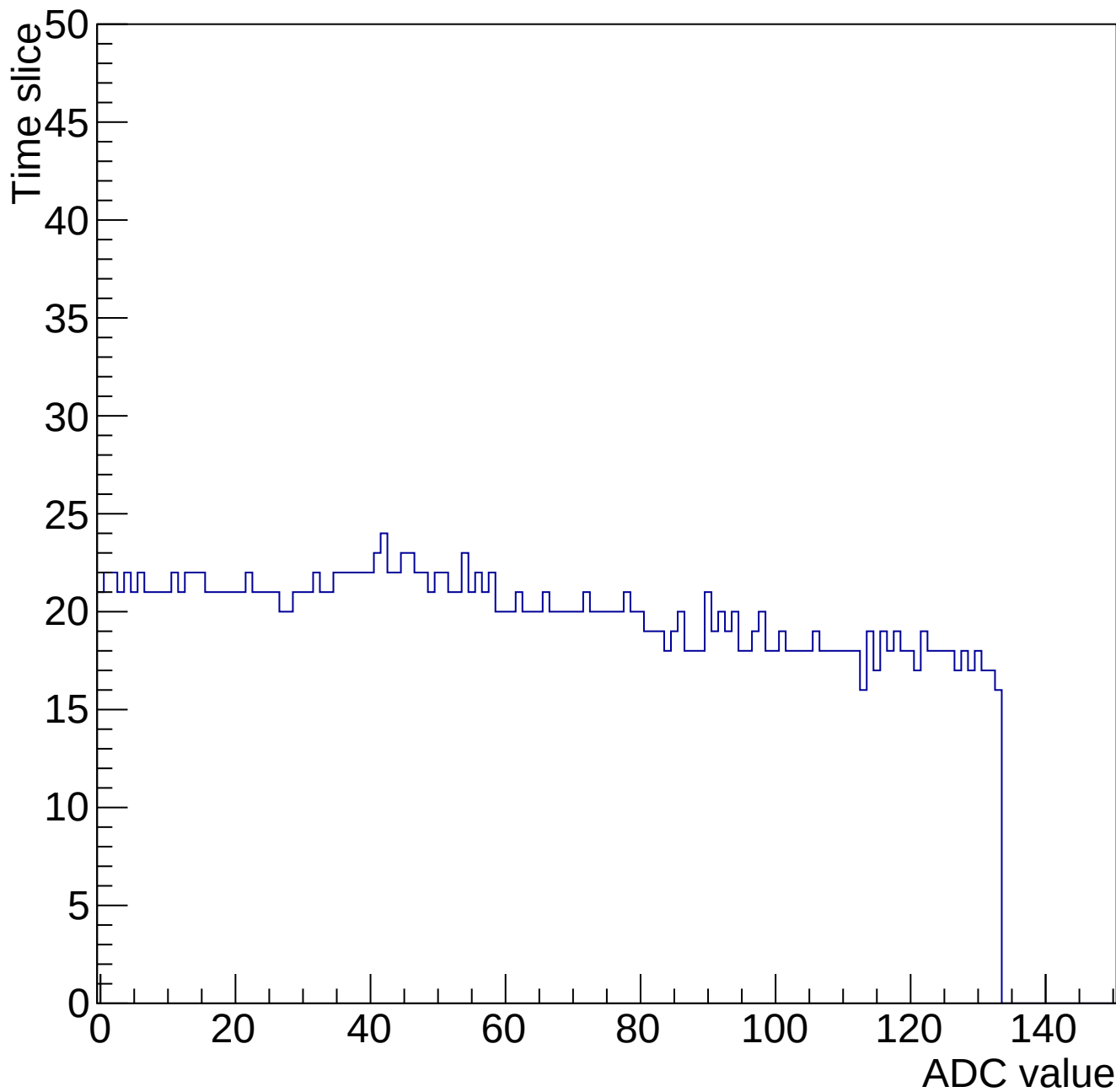
Evt 10 Time mcm 2005 chip 5 chan 8 34653-gain0-shaper0



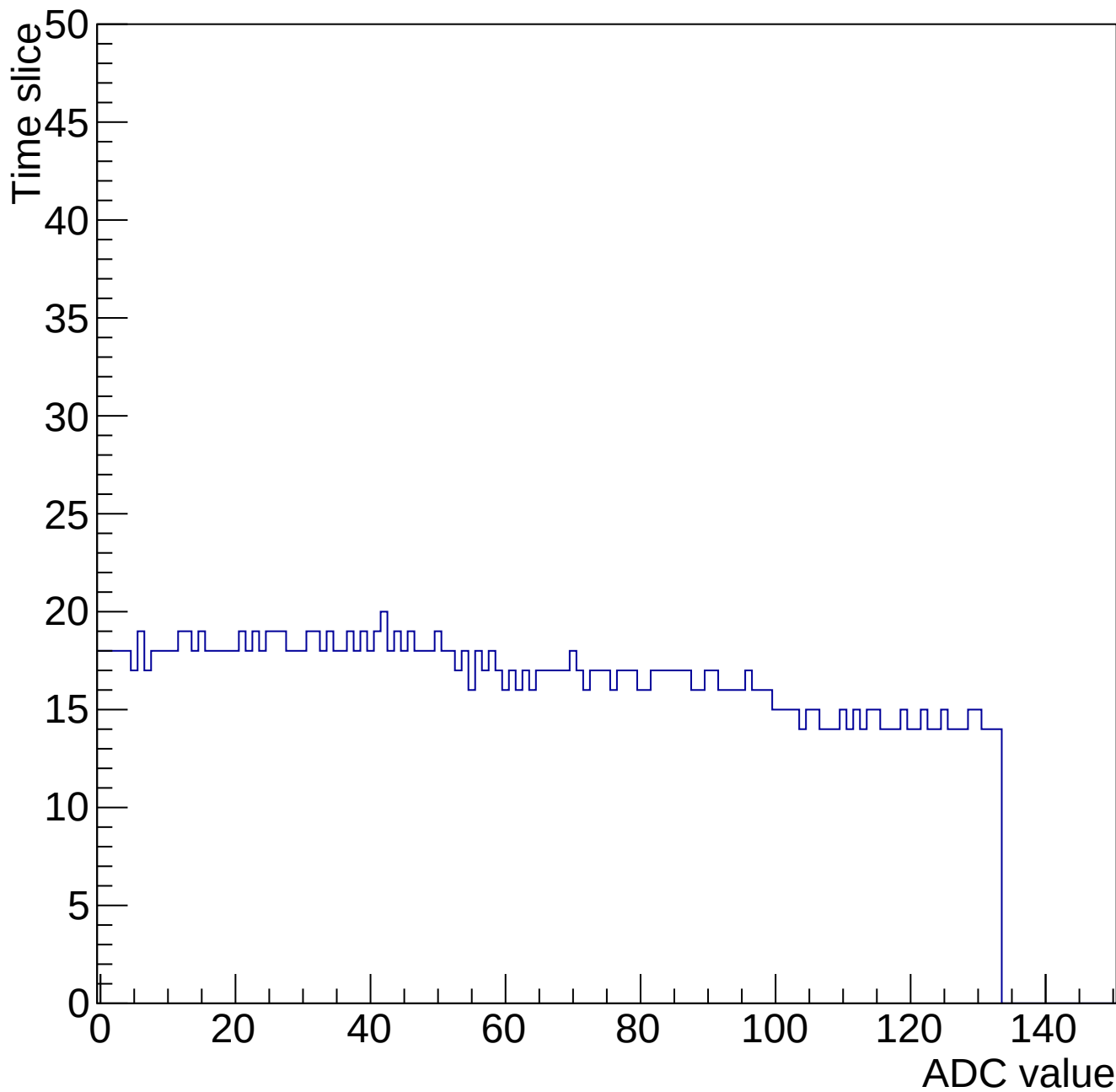
Evt 10 Time mcm 2005 chip 5 chan 9 34653-gain0-shaper0



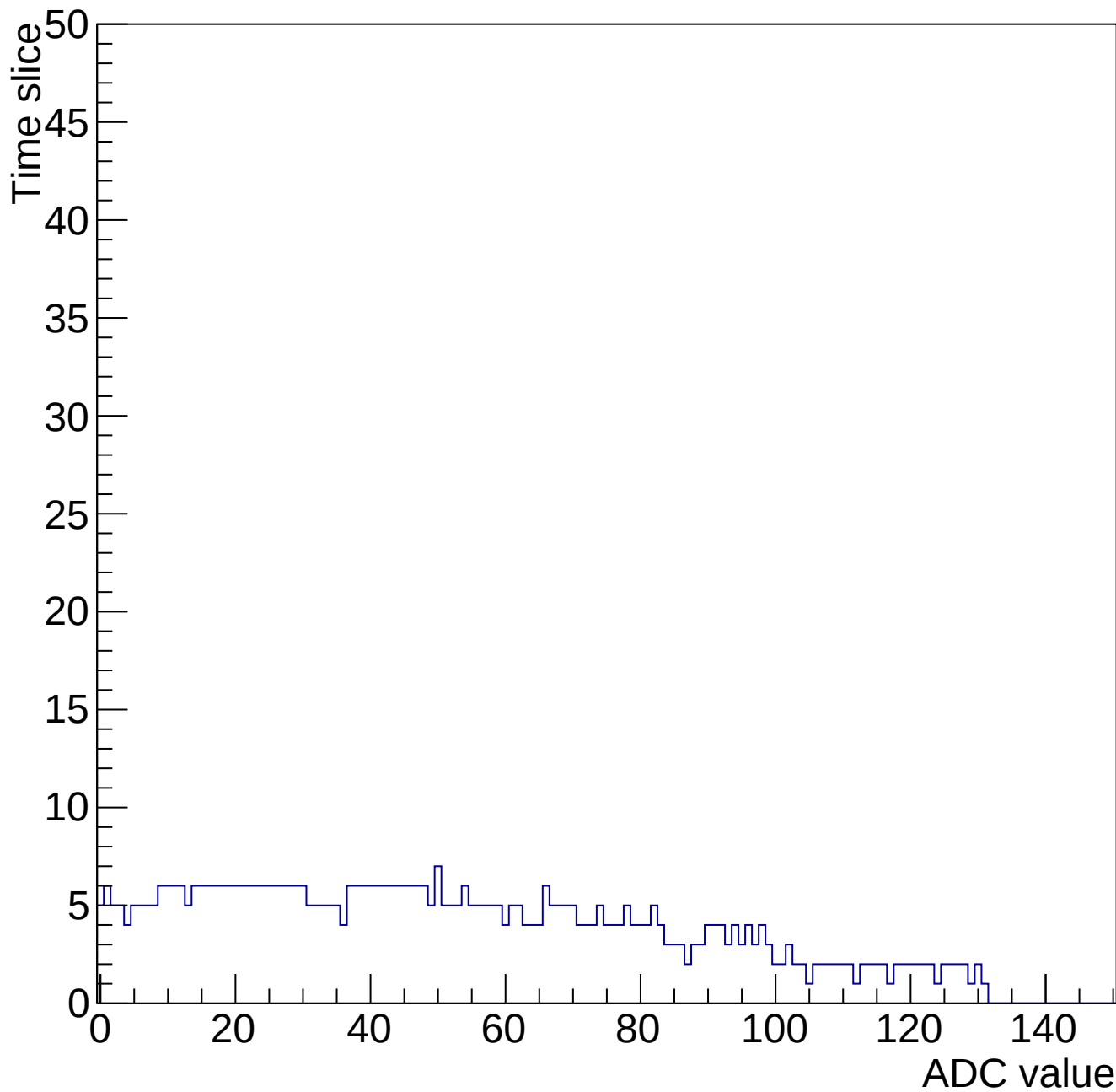
Evt 10 Time mcm 2005 chip 5 chan 10 34653-gain0-shaper0



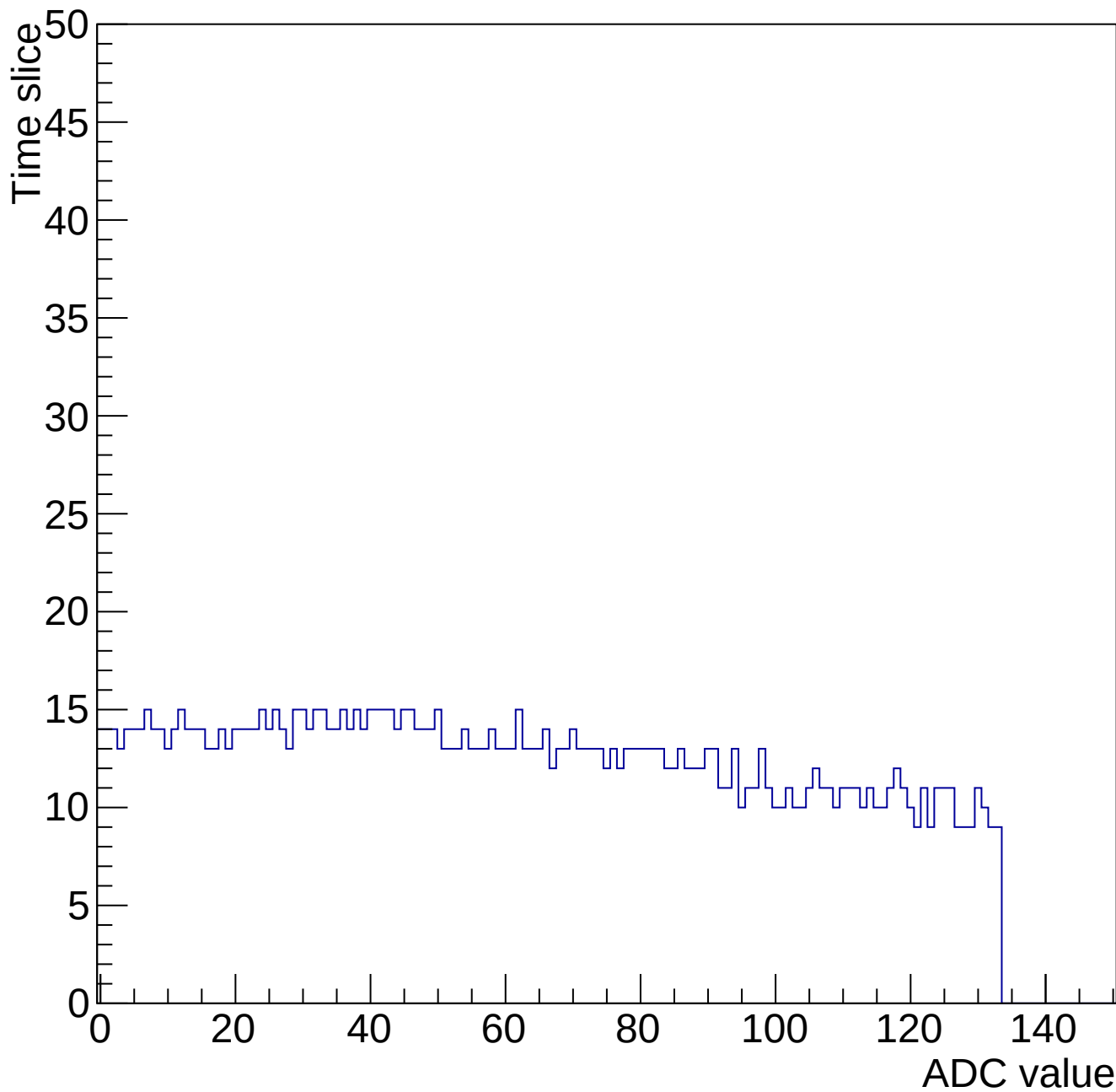
Evt 10 Time mcm 2005 chip 5 chan 11 34653-gain0-shaper0



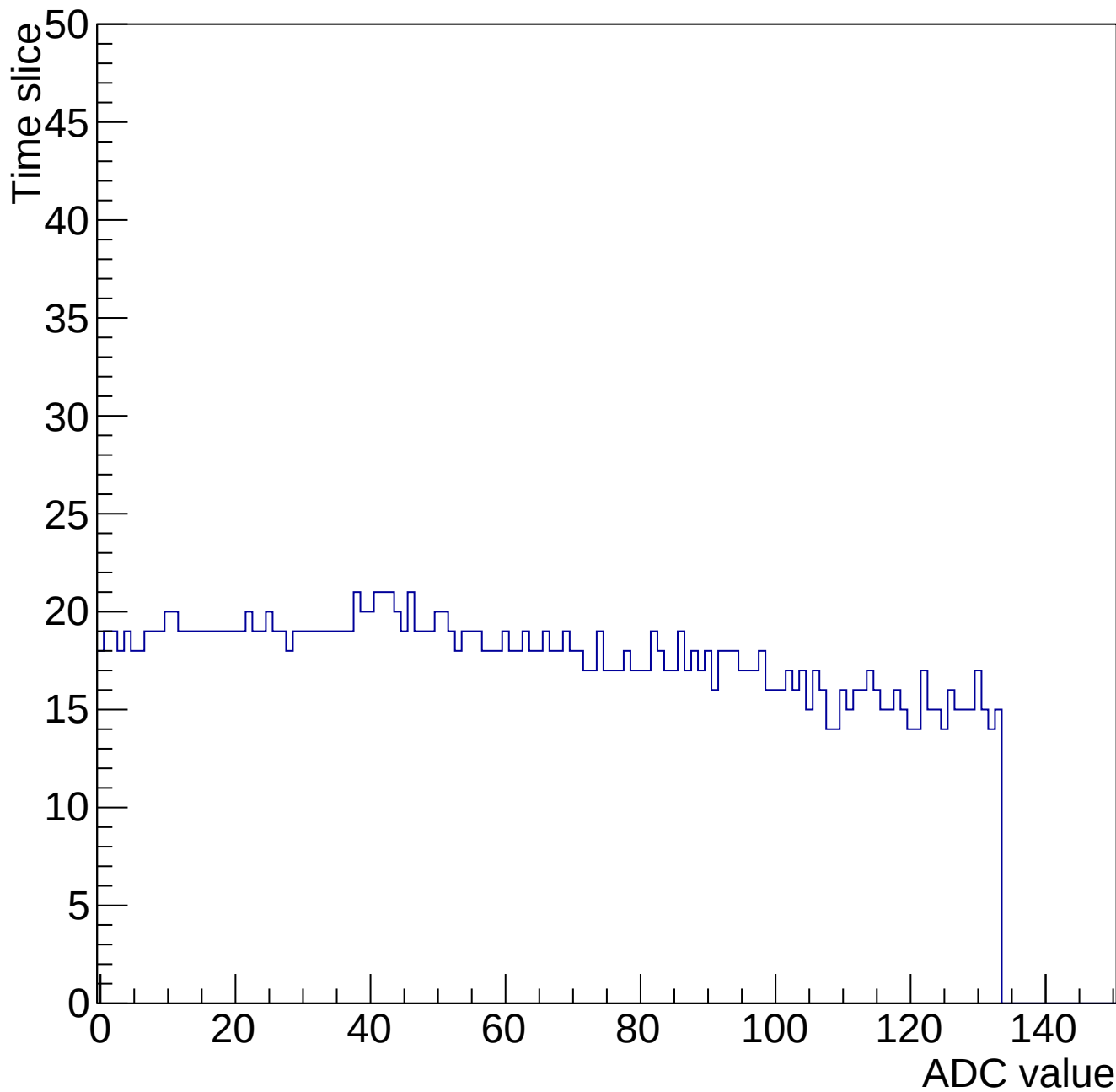
Evt 10 Time mcm 2005 chip 5 chan 12 34653-gain0-shaper0



Evt 10 Time mcm 2005 chip 5 chan 13 34653-gain0-shaper0



Evt 10 Time mcm 2005 chip 5 chan 14 34653-gain0-shaper0



Evt 10 Time mcm 2005 chip 5 chan 15 34653-gain0-shaper0

