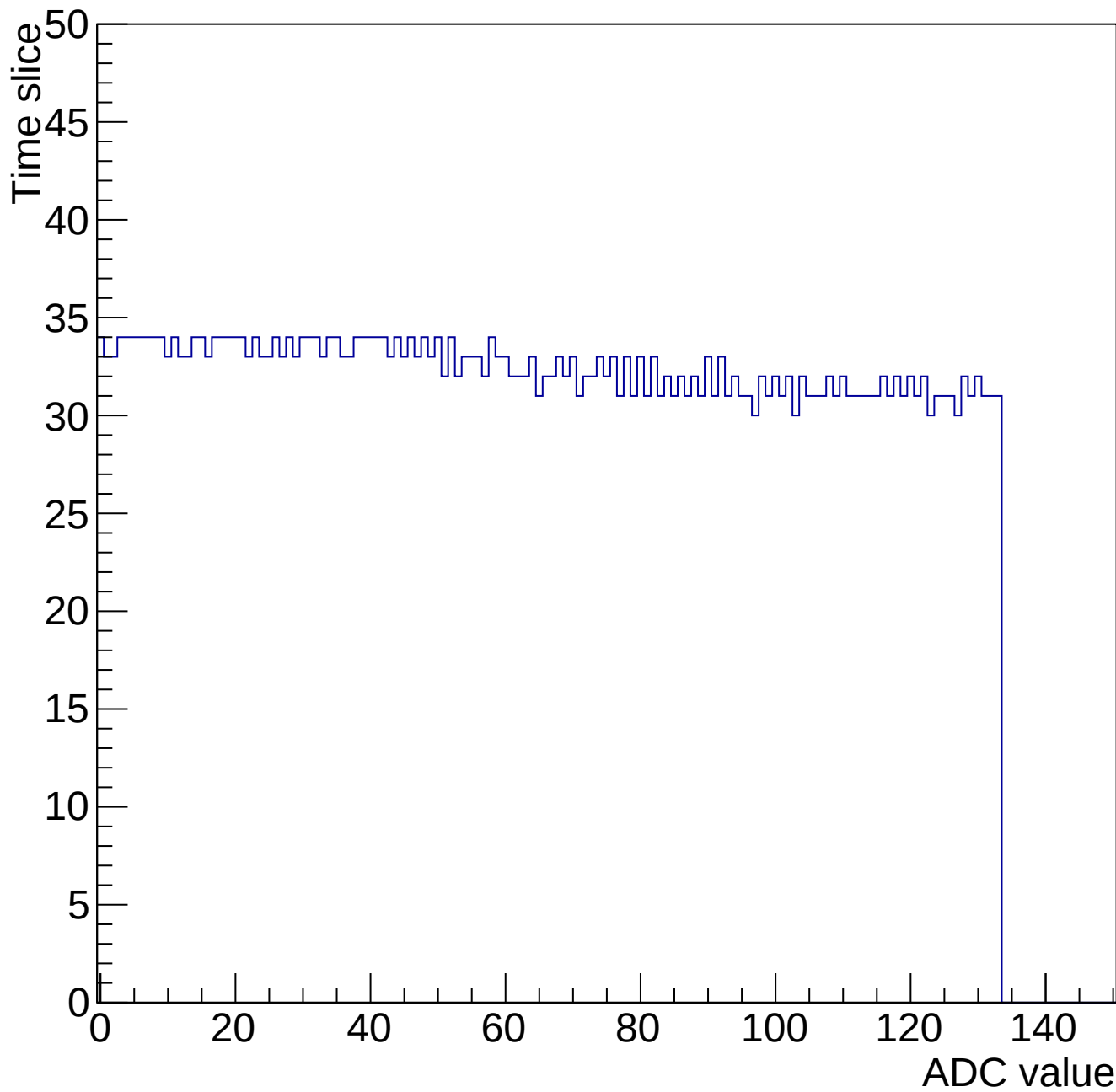
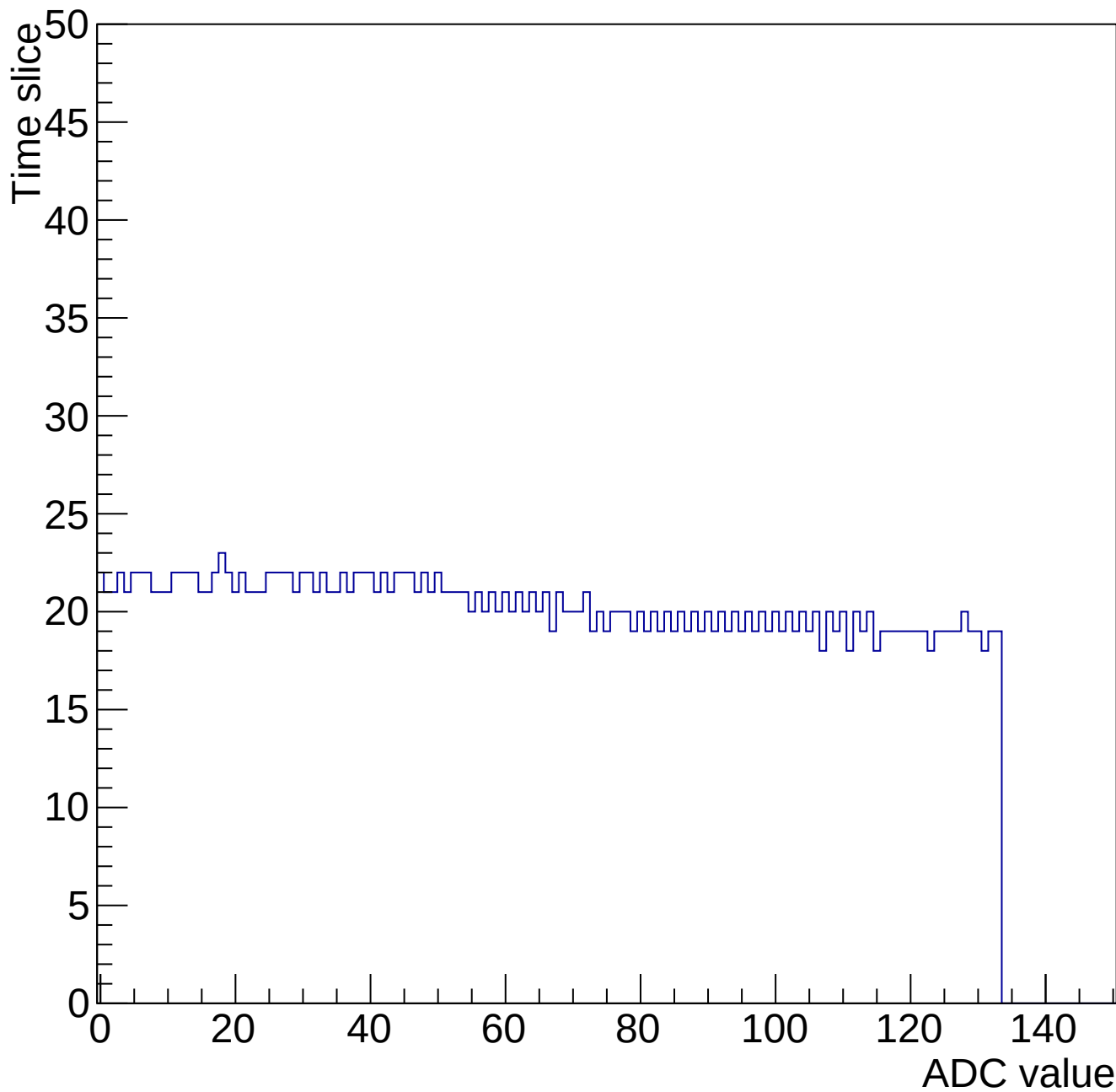


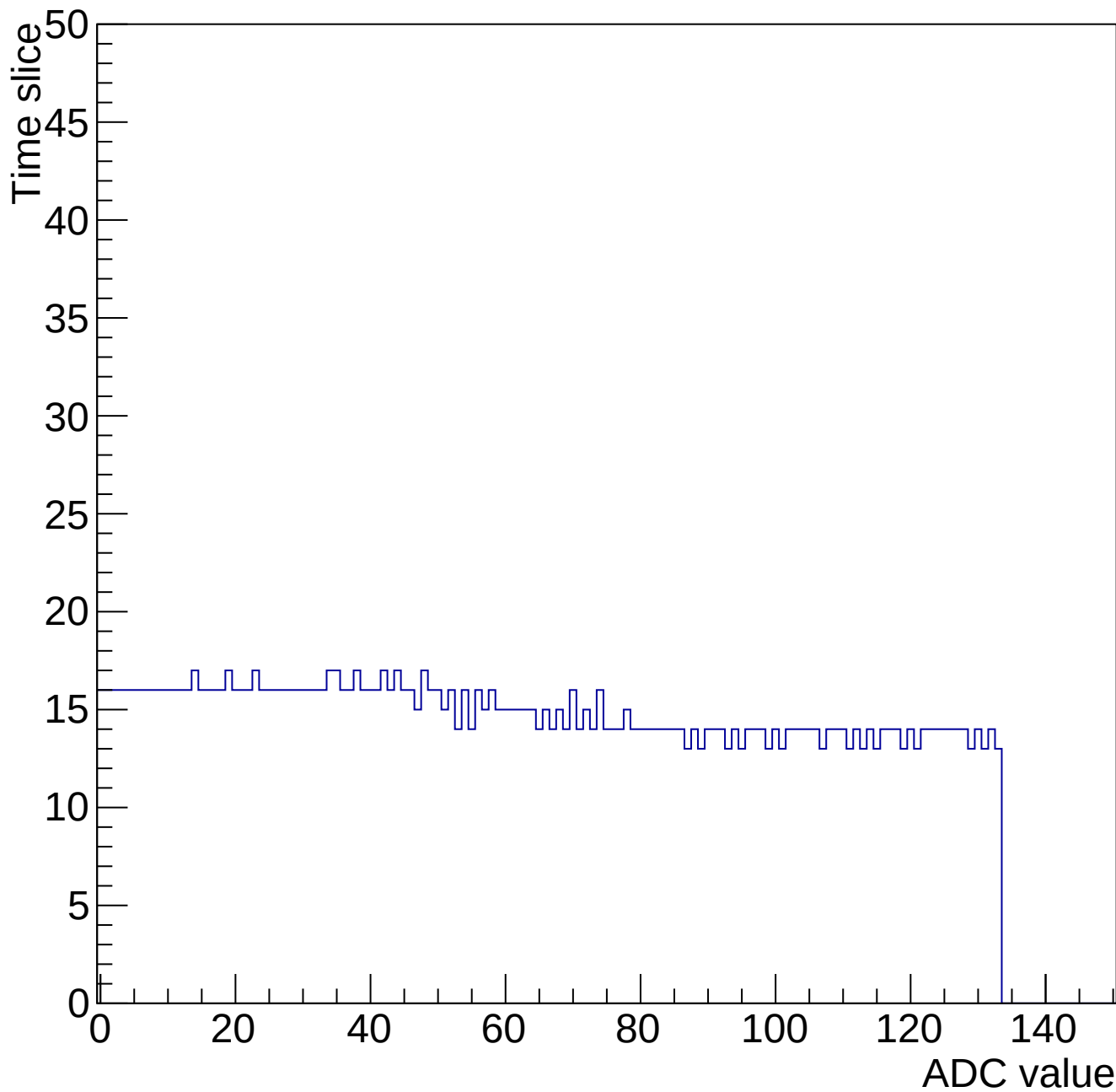
Evt 10 Time mcm 2006 chip 6 chan 0 34665-gain0-shaper6



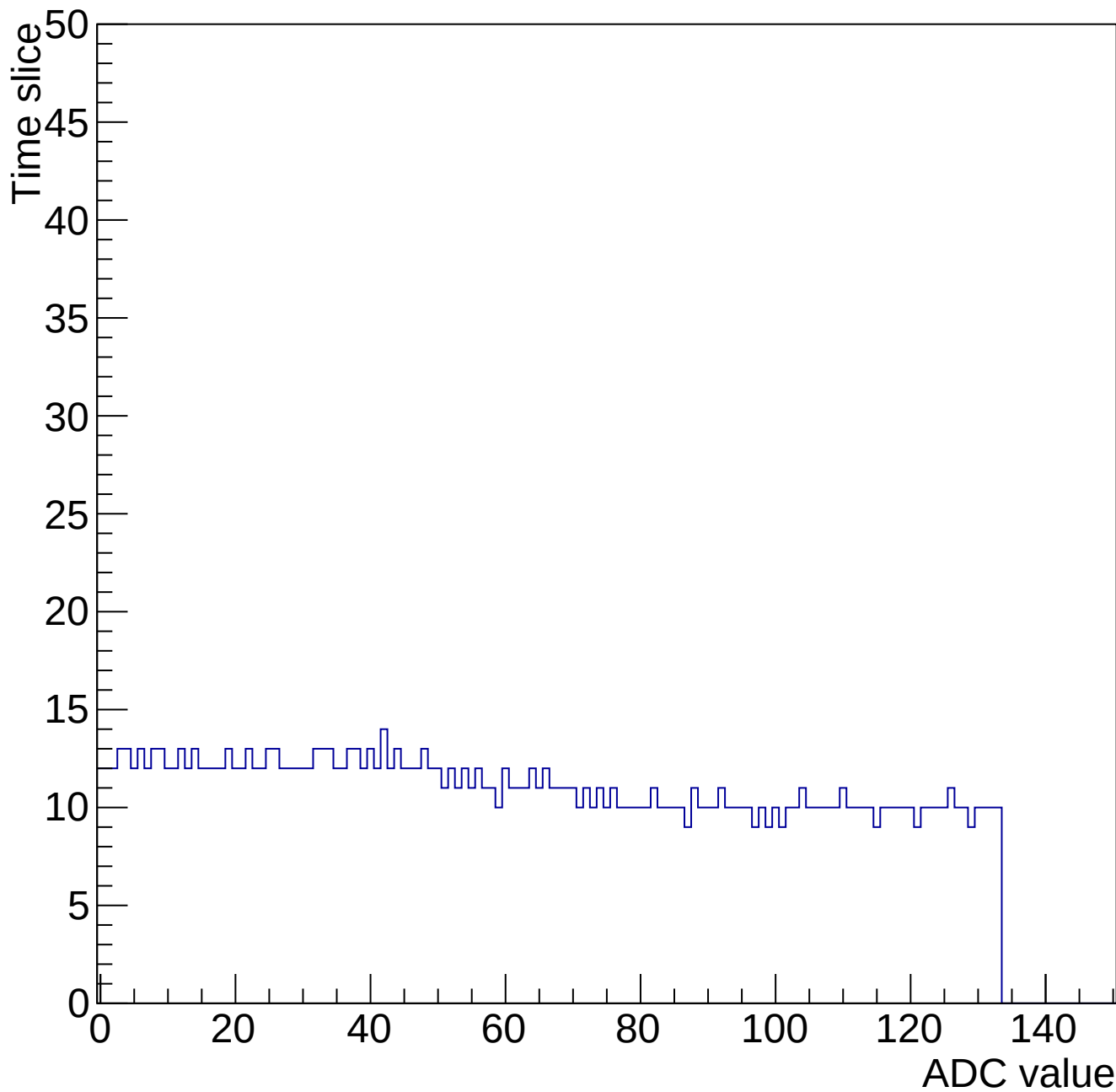
Evt 10 Time mcm 2006 chip 6 chan 1 34665-gain0-shaper6



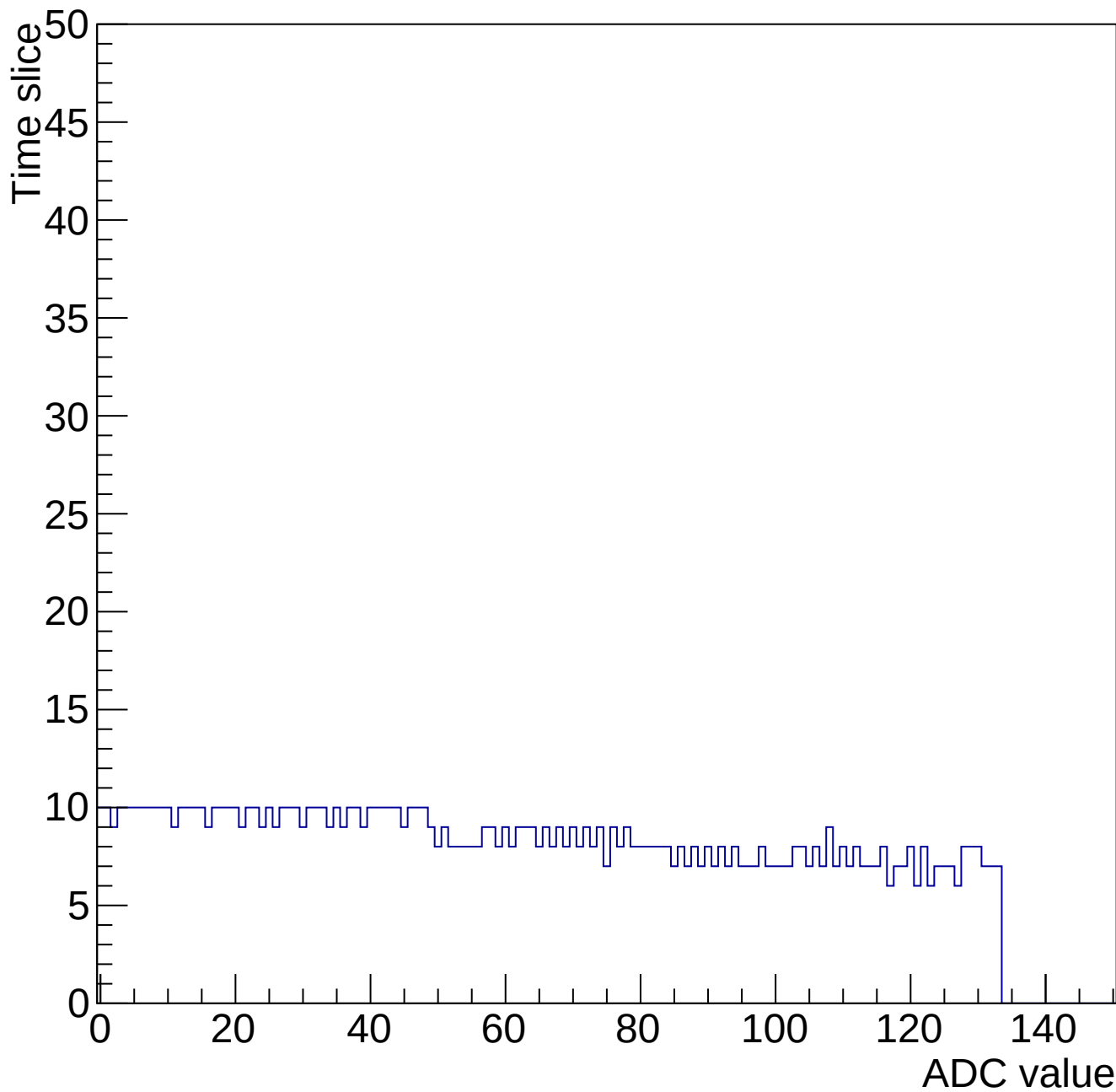
Evt 10 Time mcm 2006 chip 6 chan 2 34665-gain0-shaper6



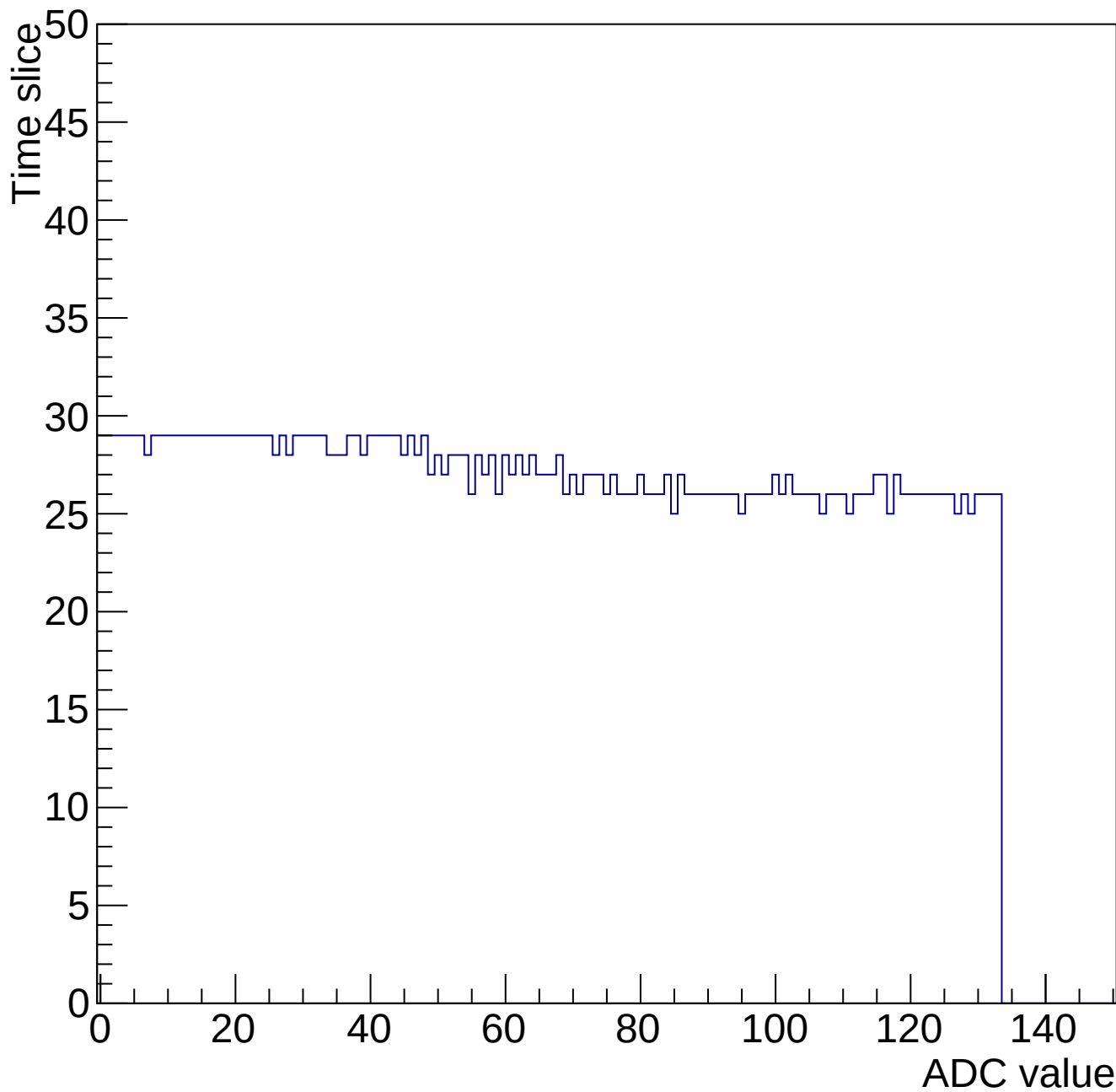
Evt 10 Time mcm 2006 chip 6 chan 3 34665-gain0-shaper6



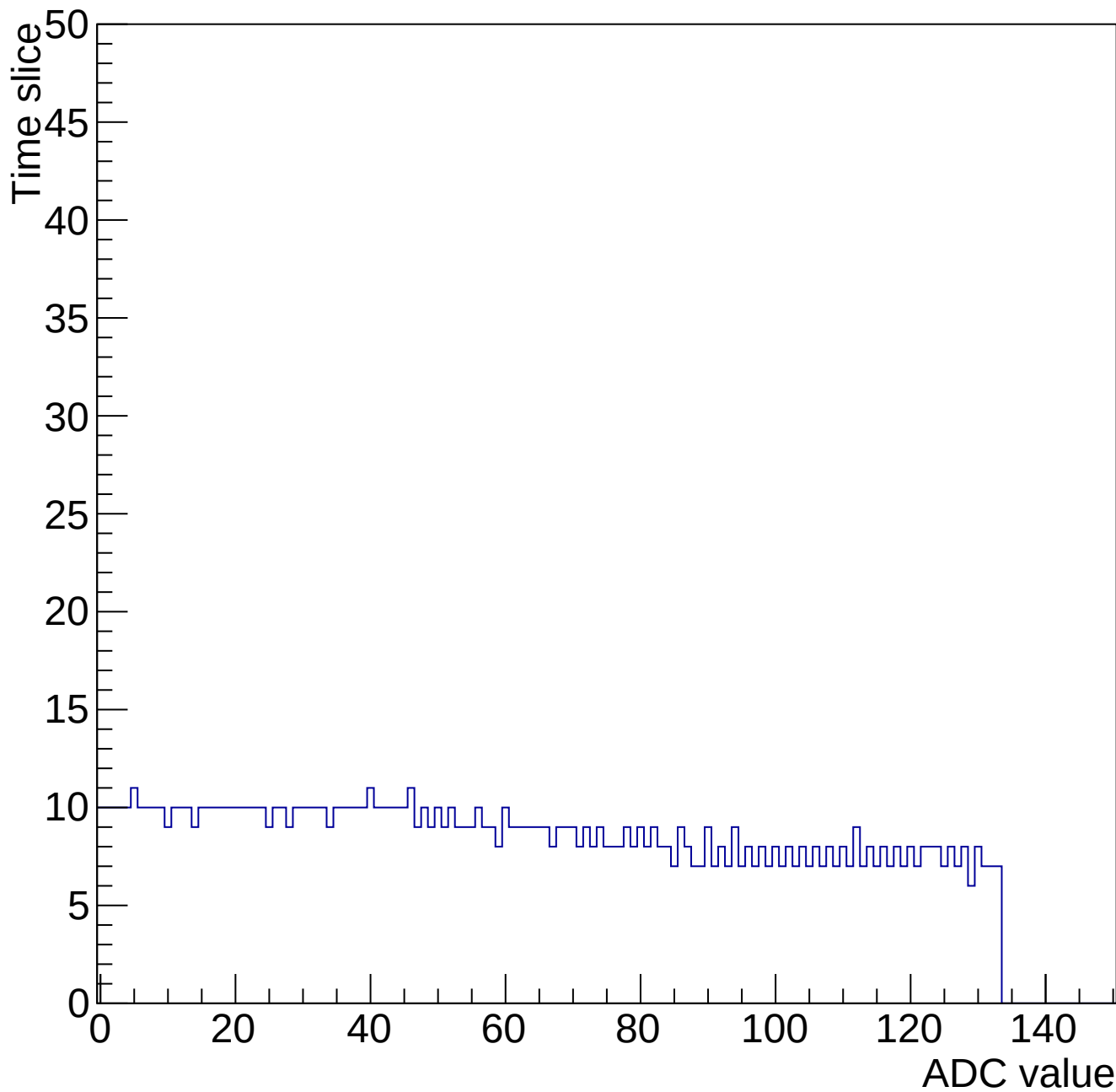
Evt 10 Time mcm 2006 chip 6 chan 4 34665-gain0-shaper6



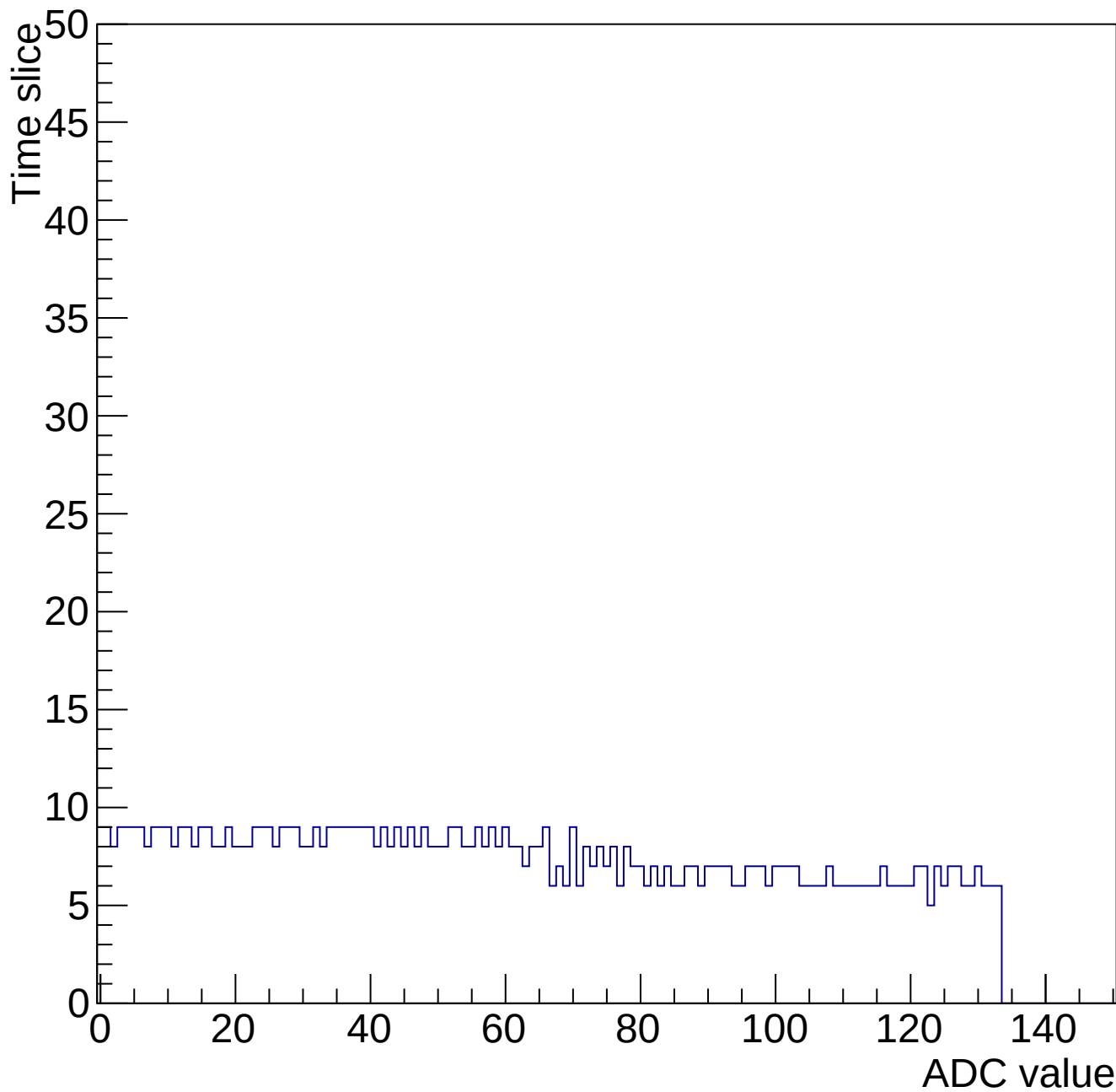
Evt 10 Time mcm 2006 chip 6 chan 5 34665-gain0-shaper6



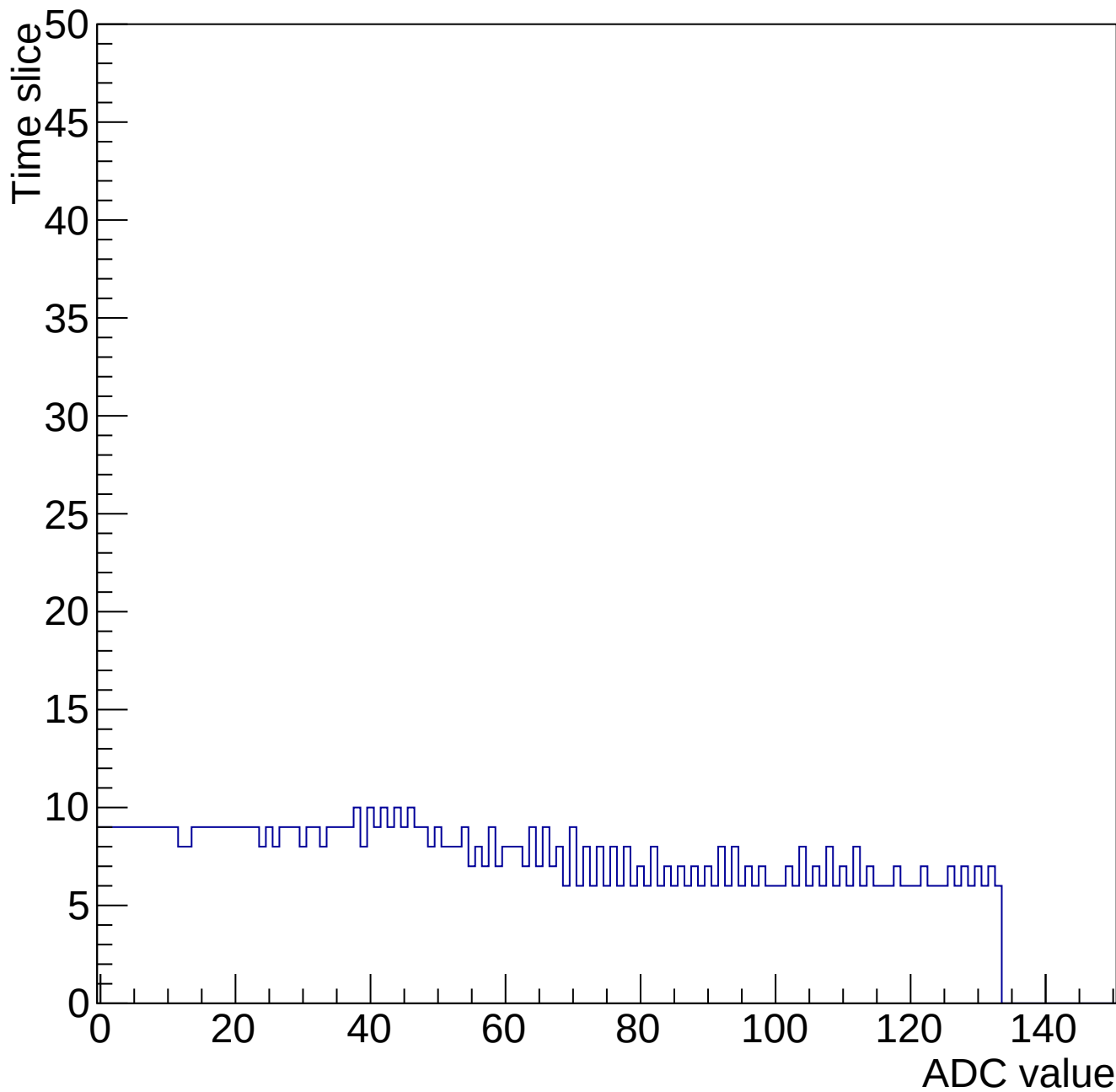
Evt 10 Time mcm 2006 chip 6 chan 6 34665-gain0-shaper6



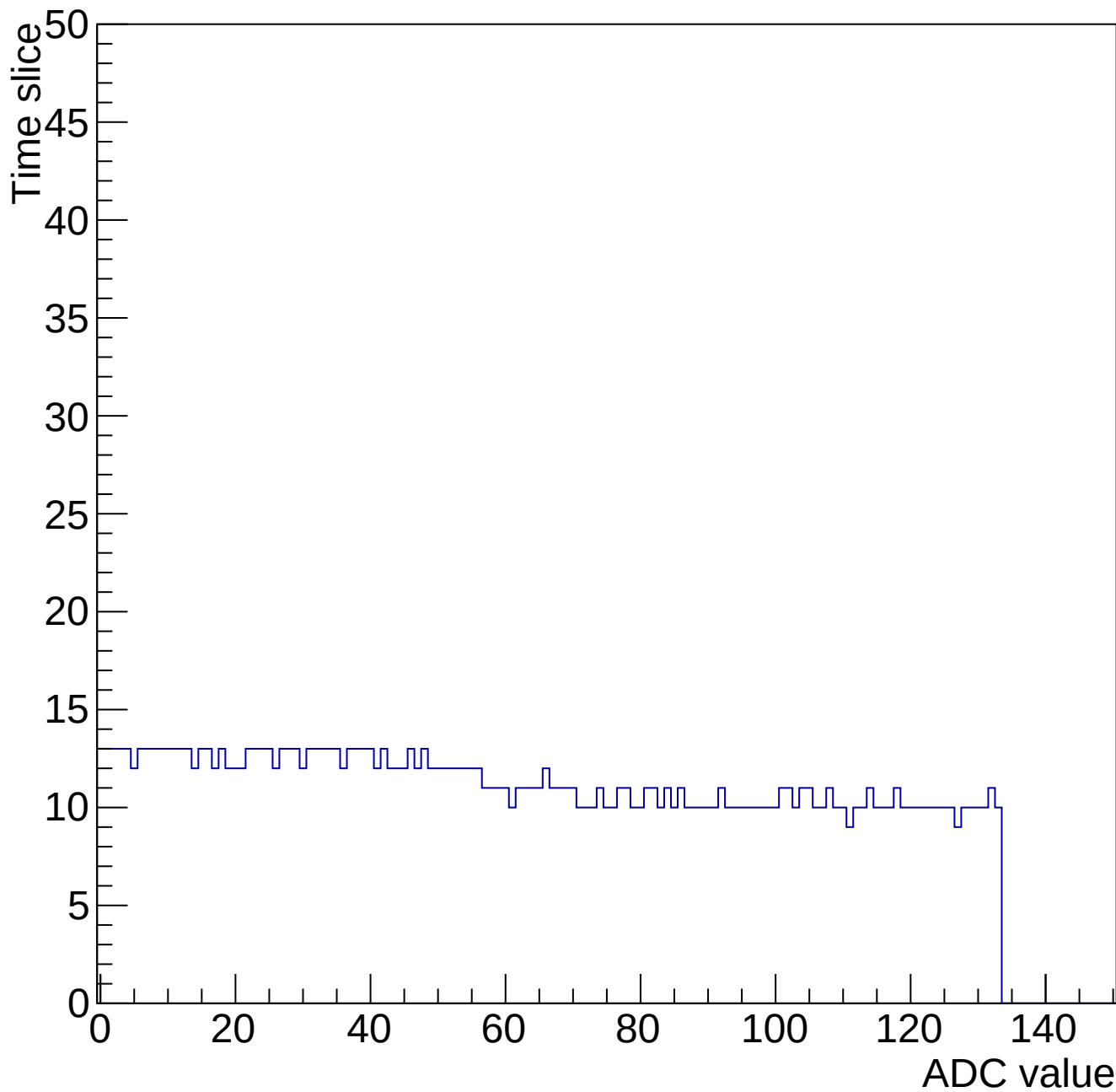
Evt 10 Time mcm 2006 chip 6 chan 7 34665-gain0-shaper6



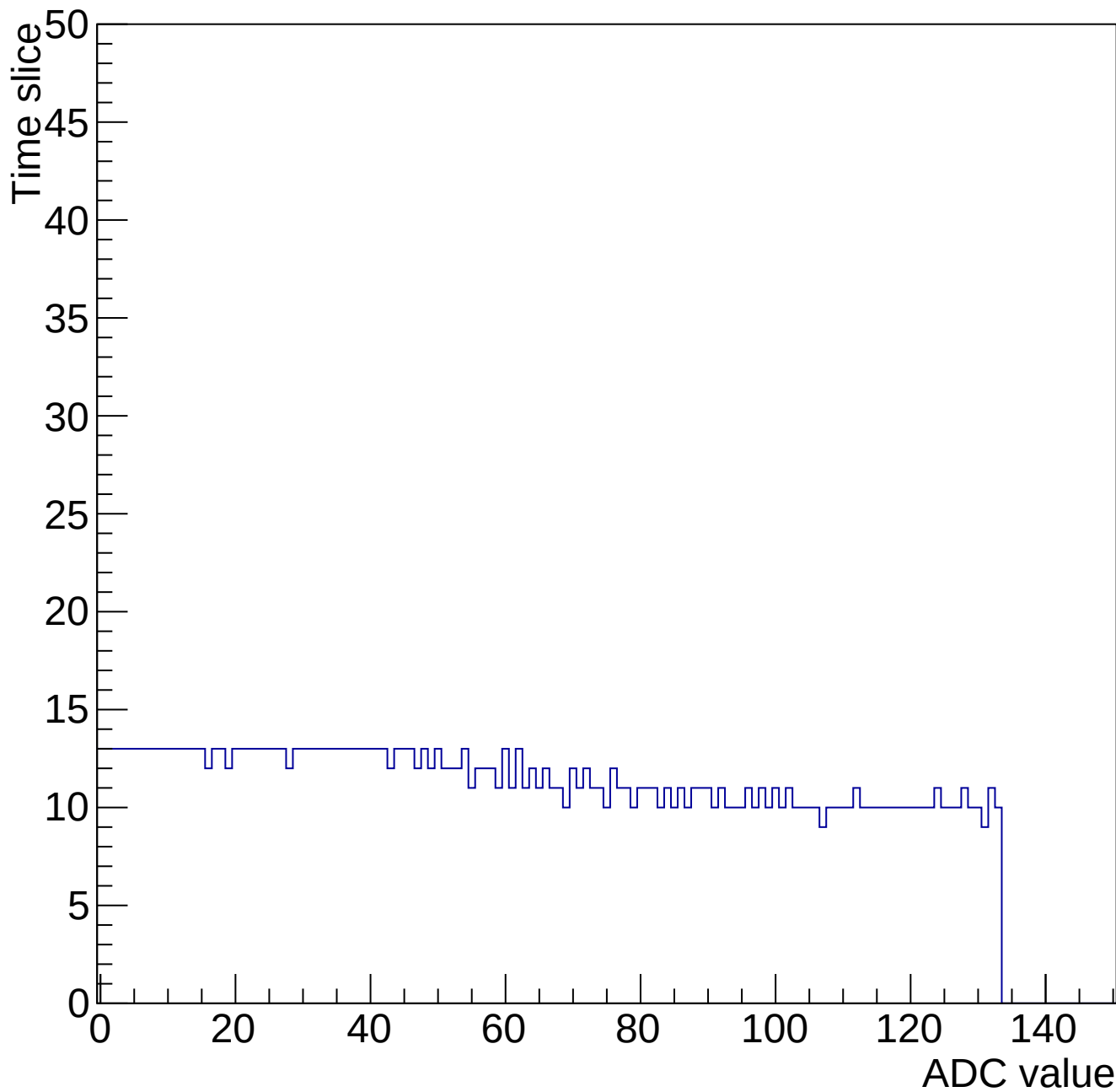
Evt 10 Time mcm 2006 chip 6 chan 8 34665-gain0-shaper6



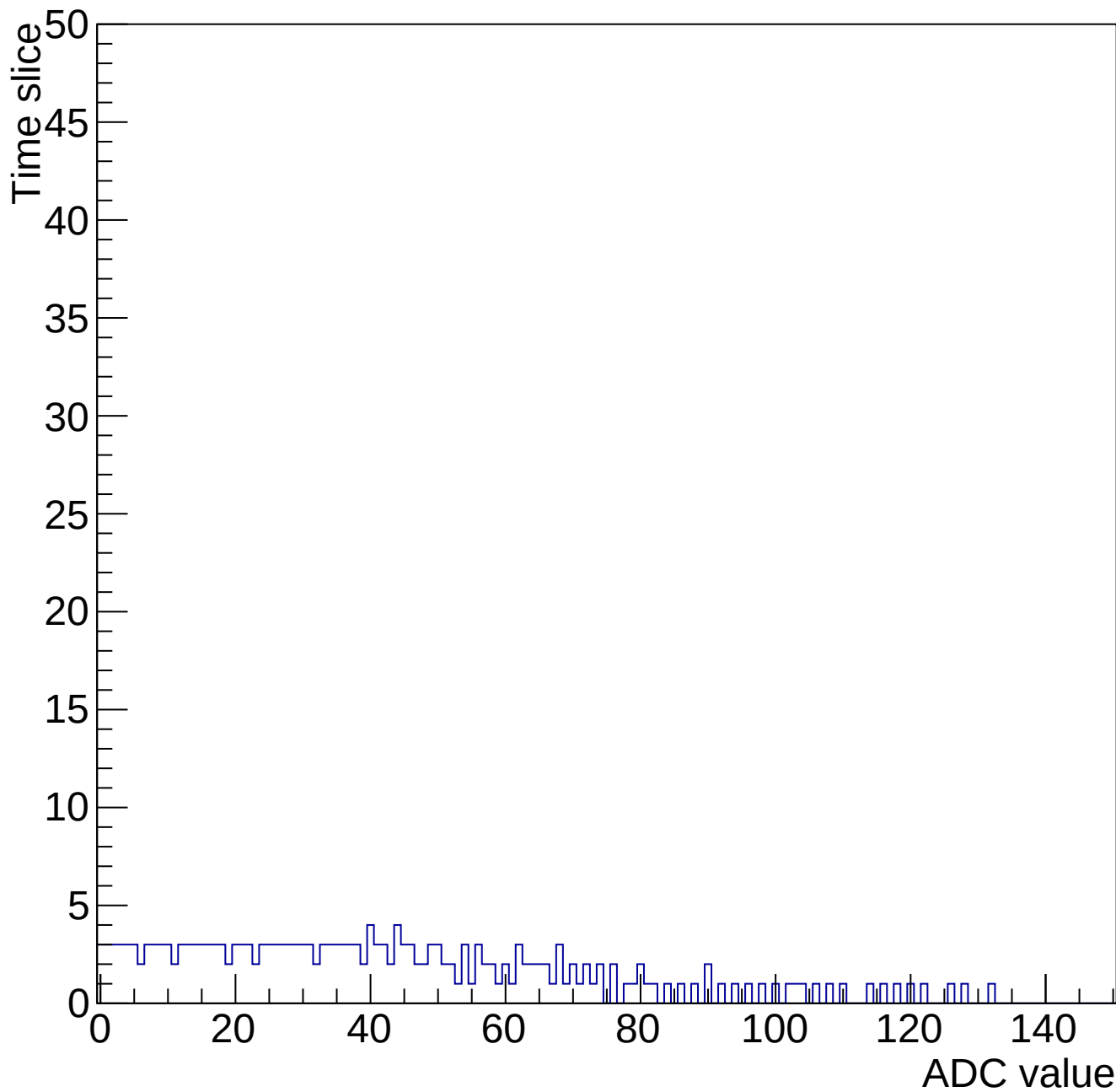
Evt 10 Time mcm 2006 chip 6 chan 9 34665-gain0-shaper6



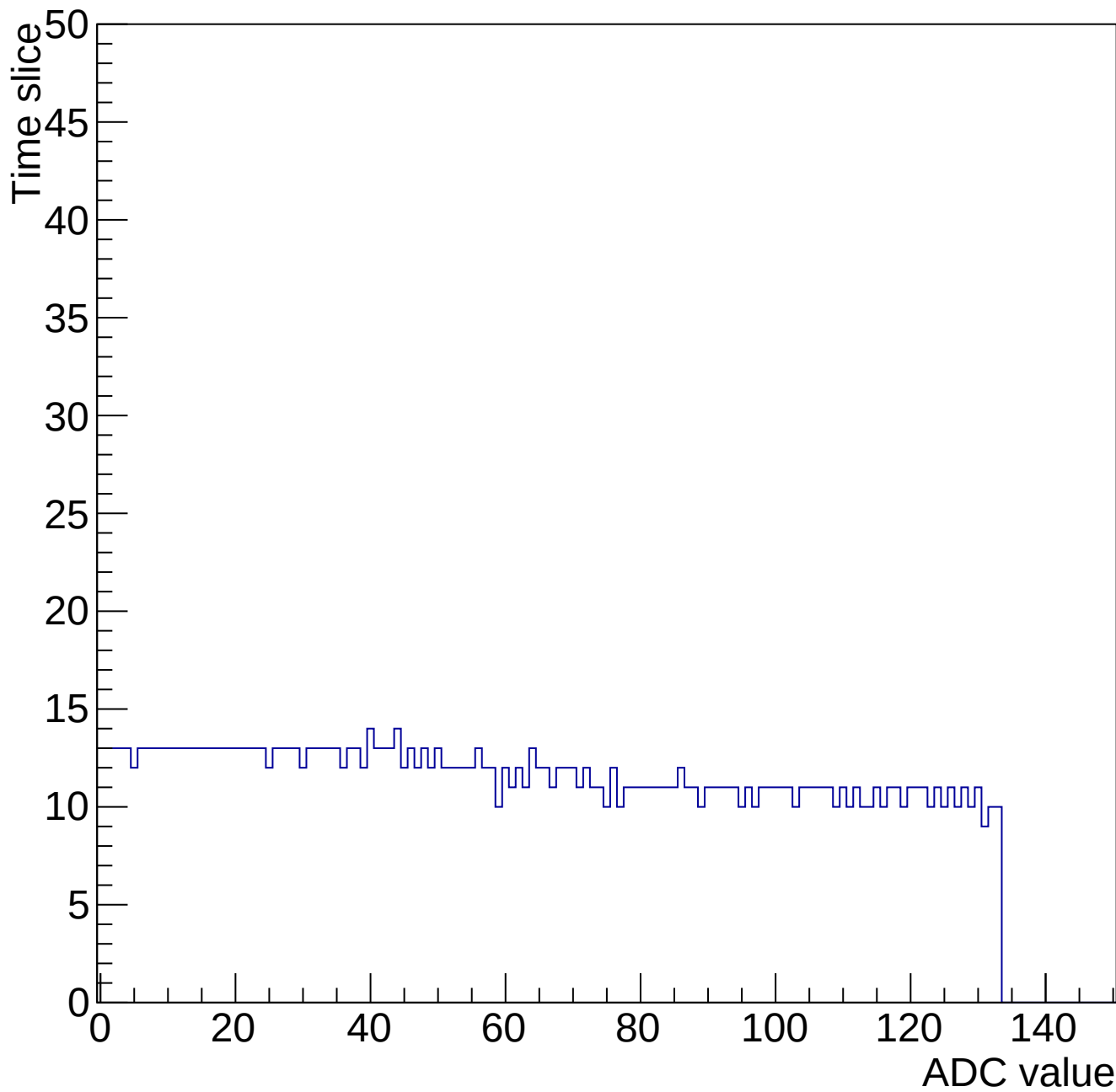
Evt 10 Time mcm 2006 chip 6 chan 10 34665-gain0-shaper6



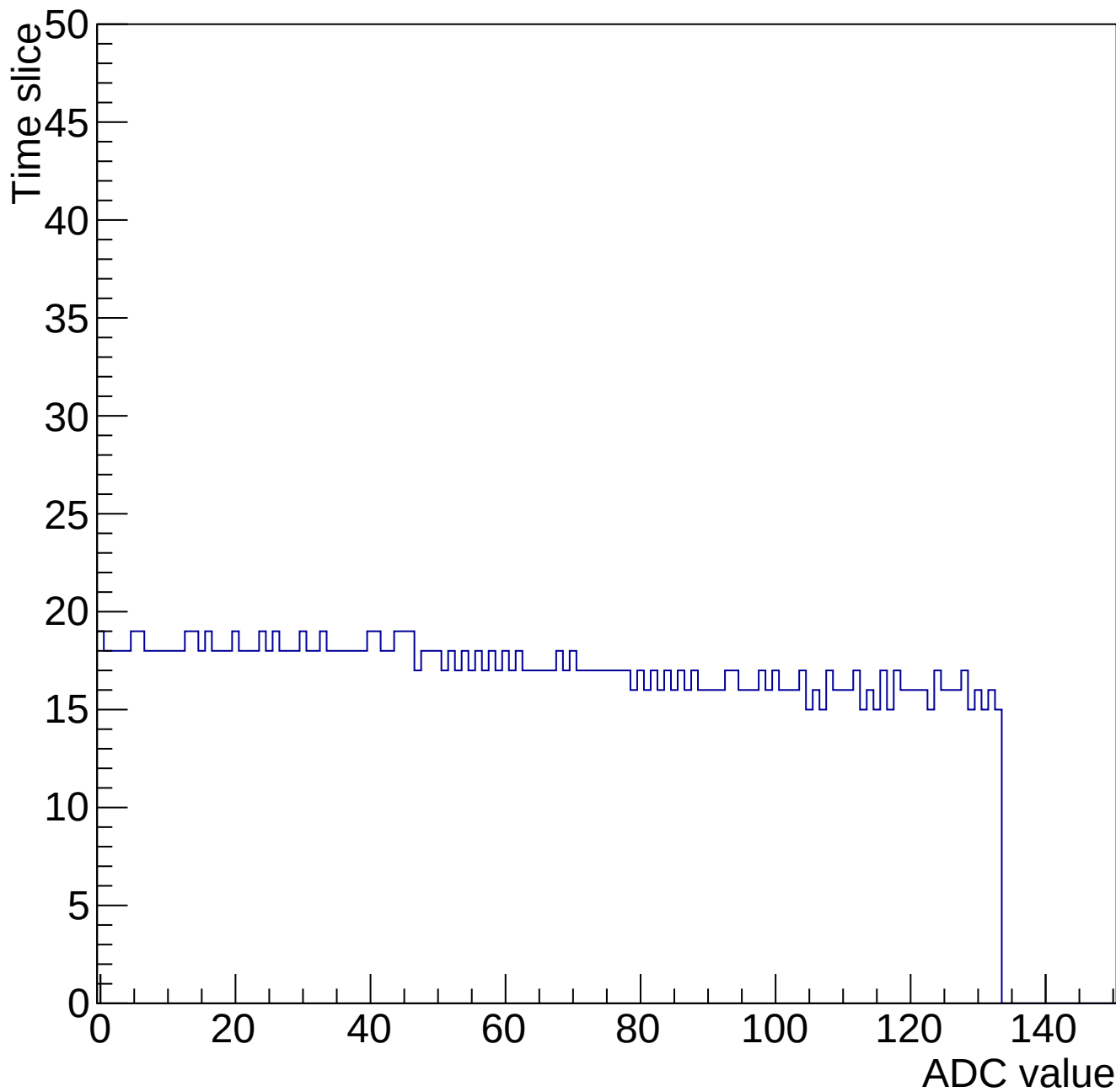
Evt 10 Time mcm 2006 chip 6 chan 11 34665-gain0-shaper6



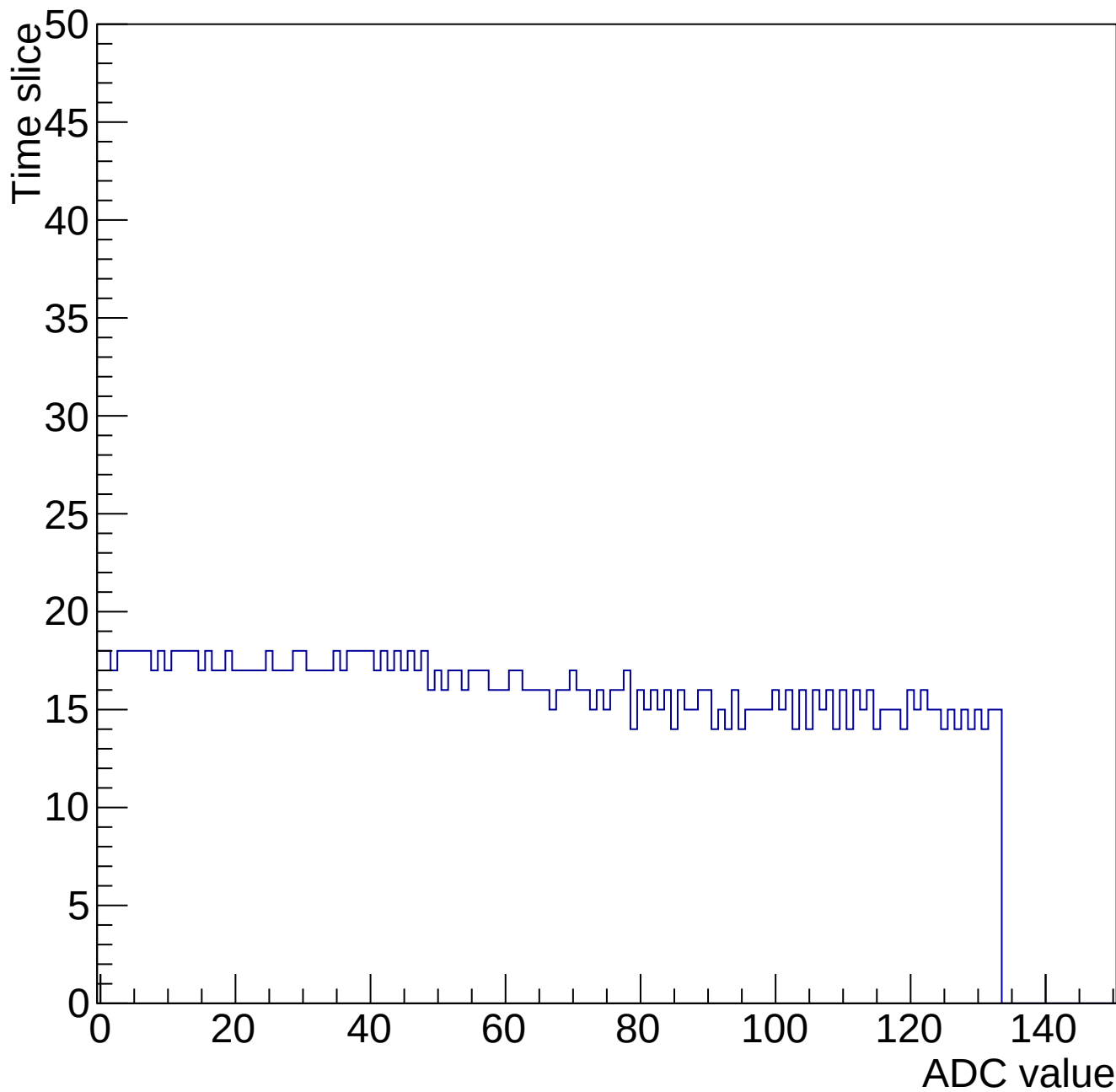
Evt 10 Time mcm 2006 chip 6 chan 12 34665-gain0-shaper6



Evt 10 Time mcm 2006 chip 6 chan 13 34665-gain0-shaper6



Evt 10 Time mcm 2006 chip 6 chan 14 34665-gain0-shaper6



Evt 10 Time mcm 2006 chip 6 chan 15 34665-gain0-shaper6

