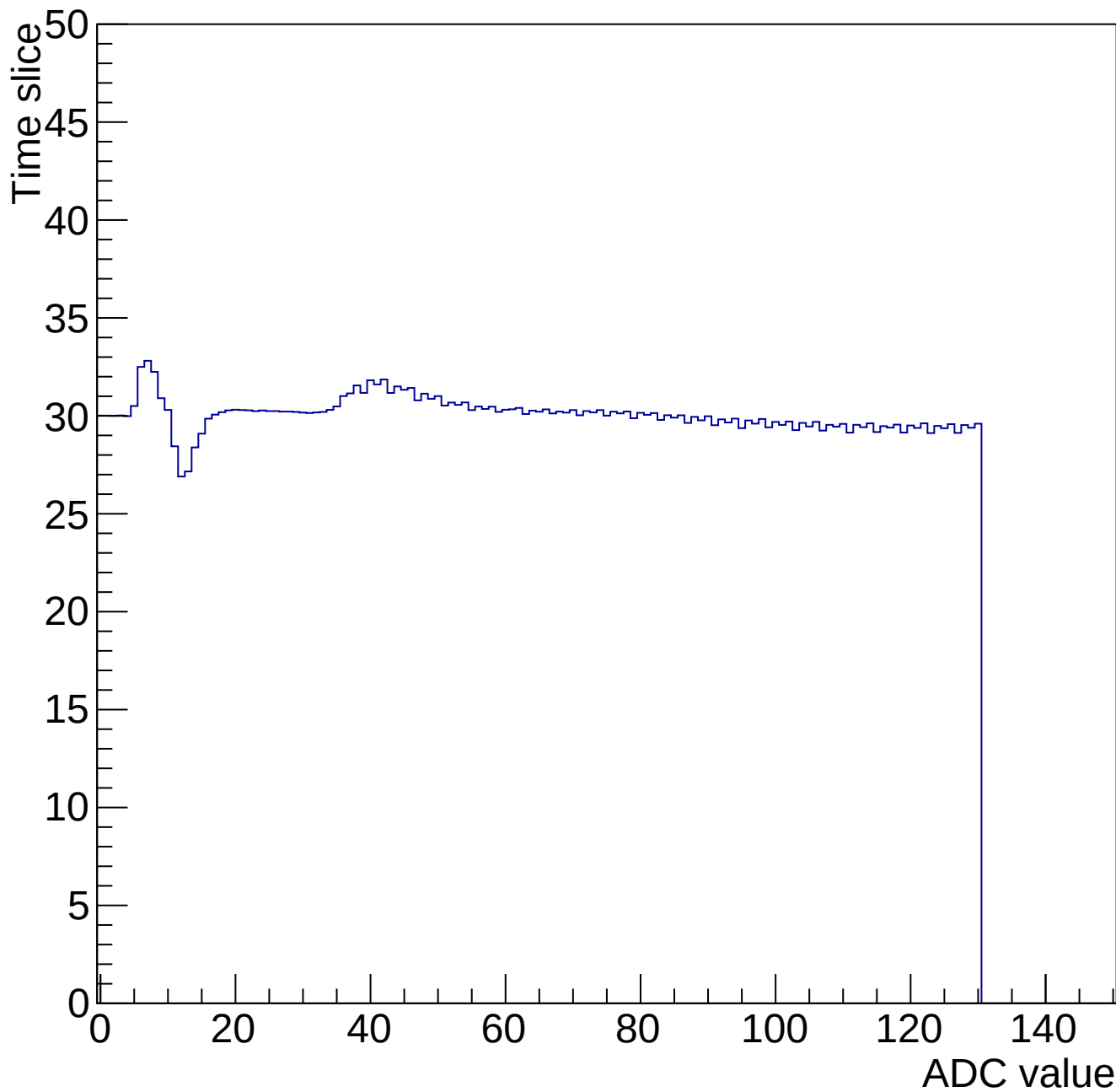
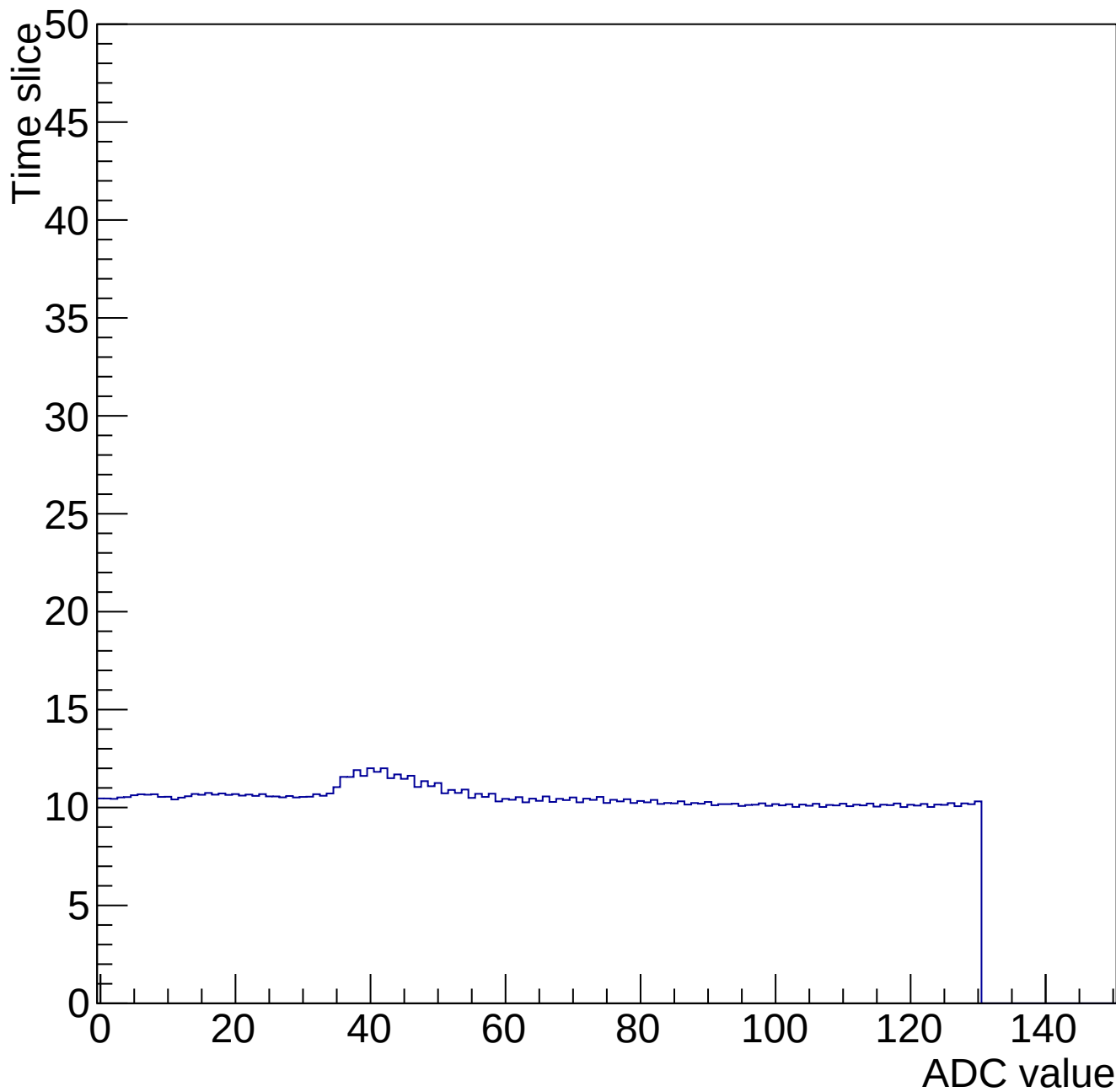


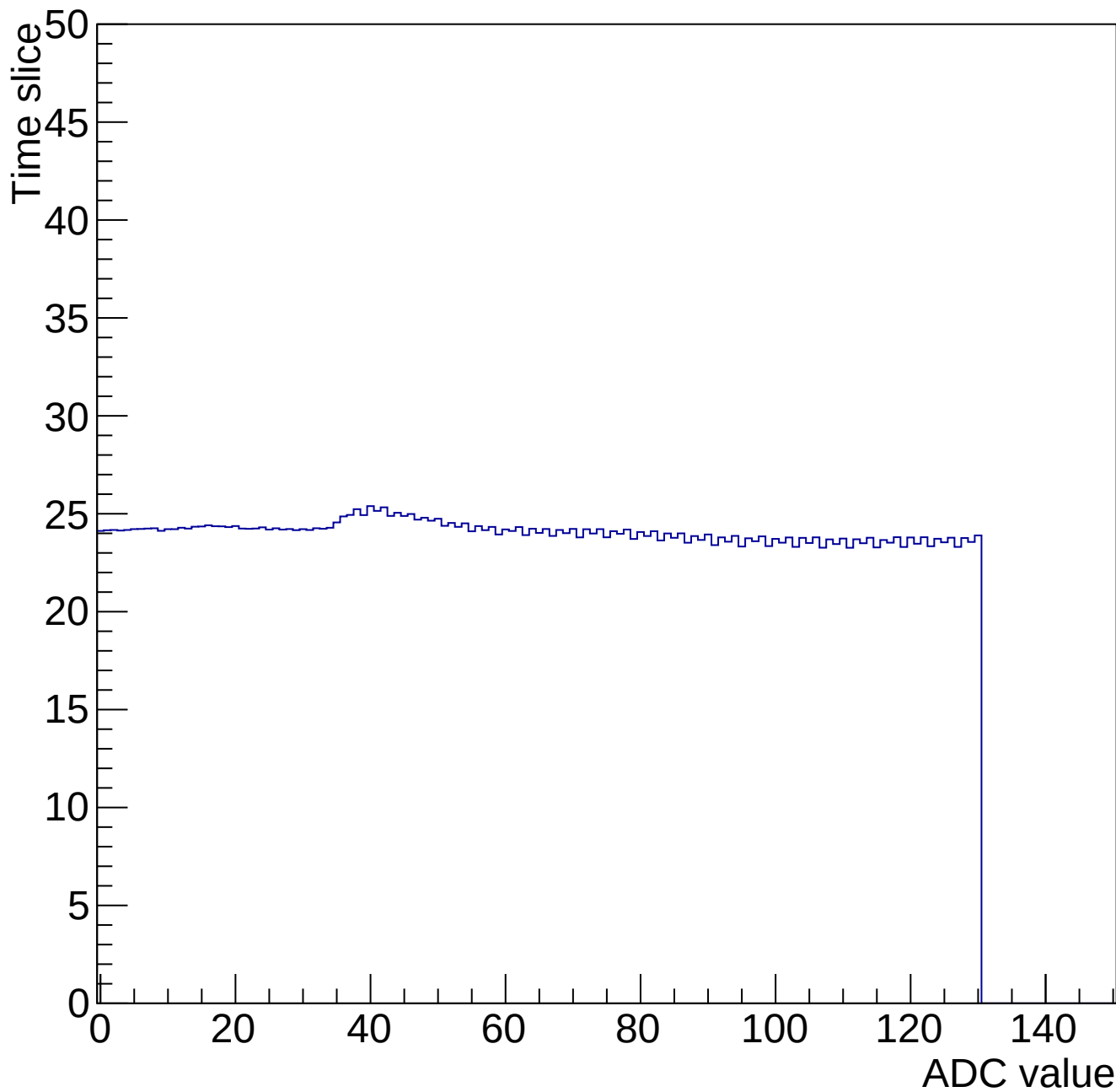
Time mcm 2003 chip 3 chan 0 34697-gain0-shaper0



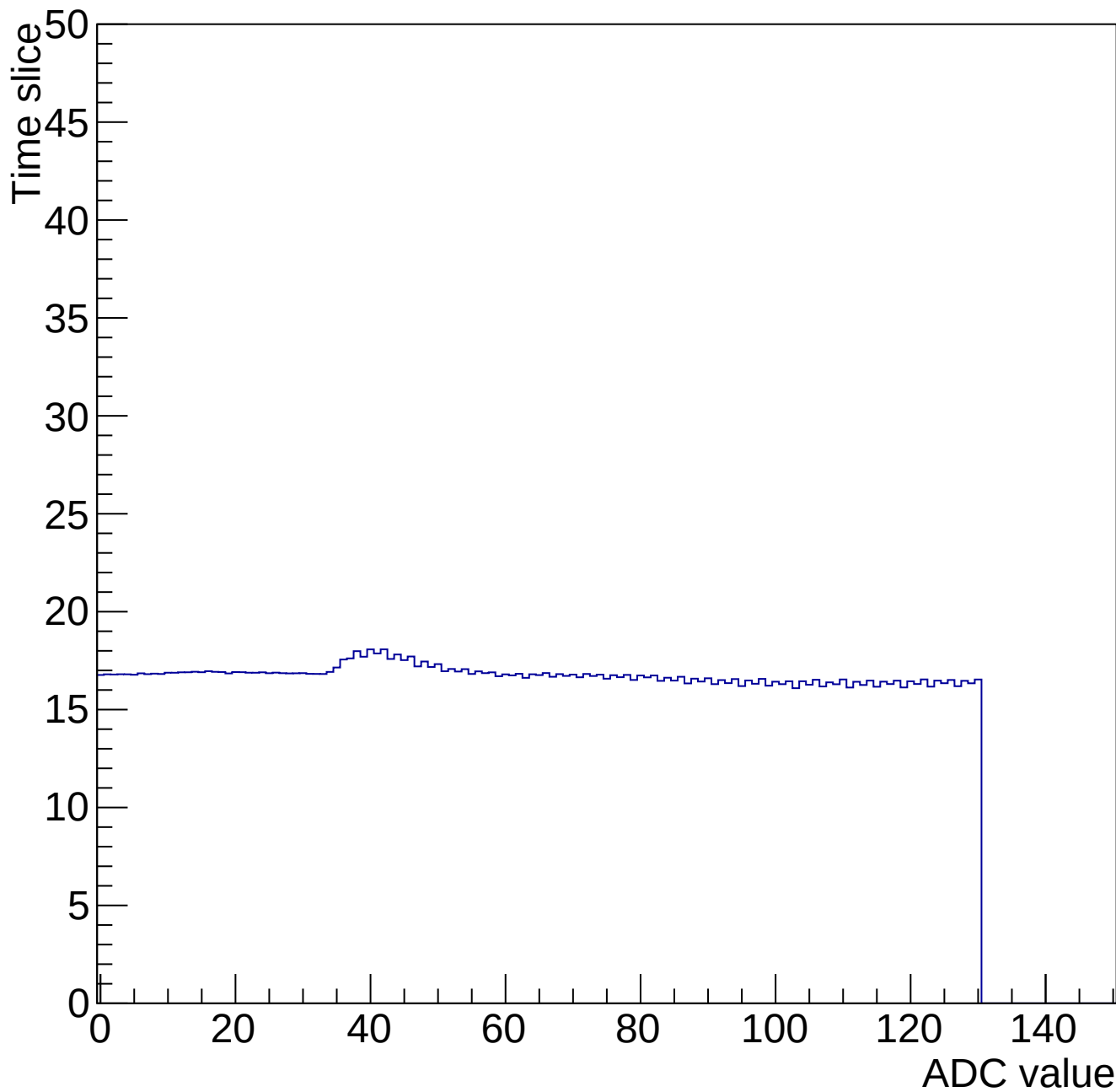
Time mcm 2003 chip 3 chan 1 34697-gain0-shaper0



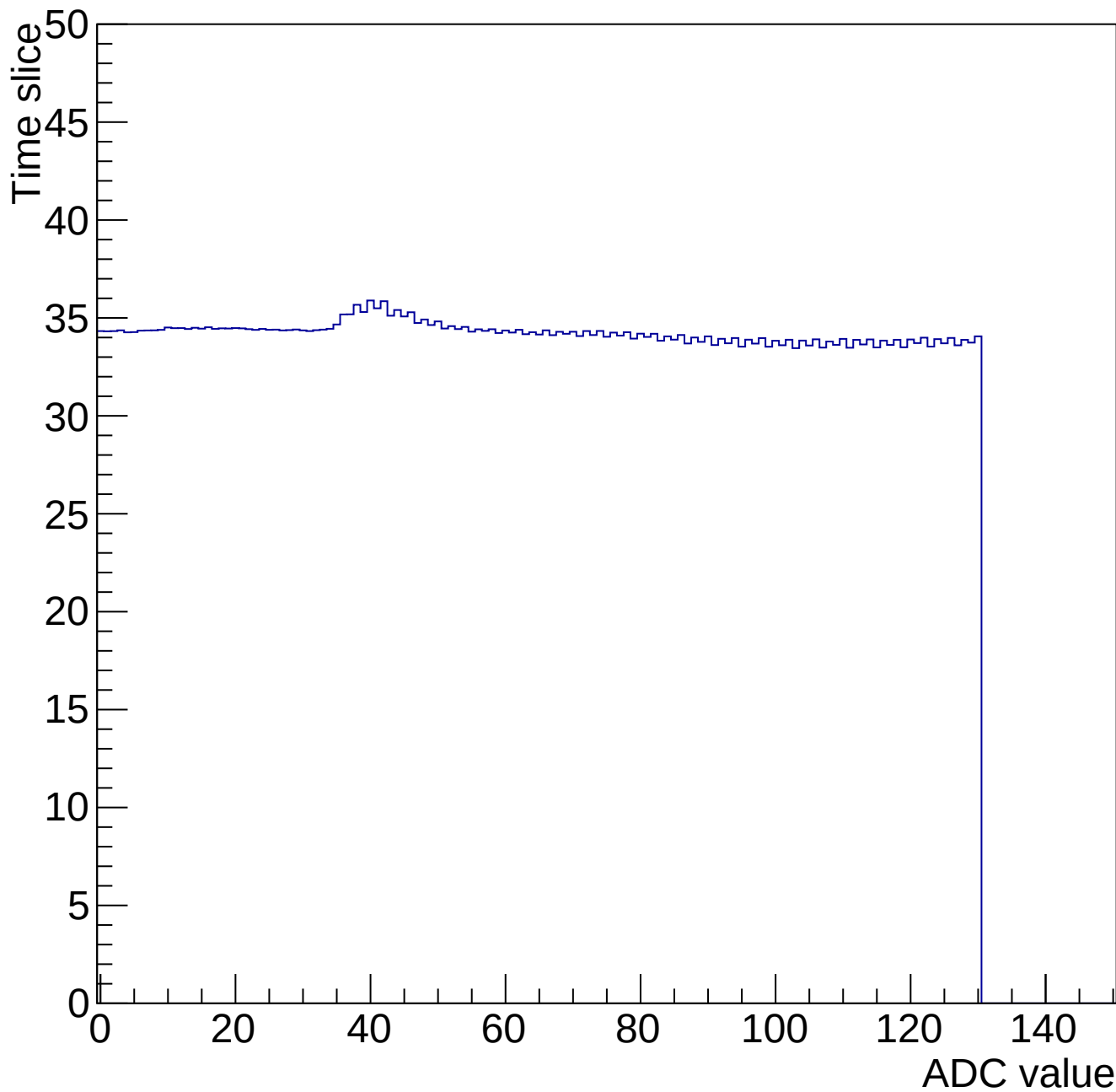
Time mcm 2003 chip 3 chan 2 34697-gain0-shaper0



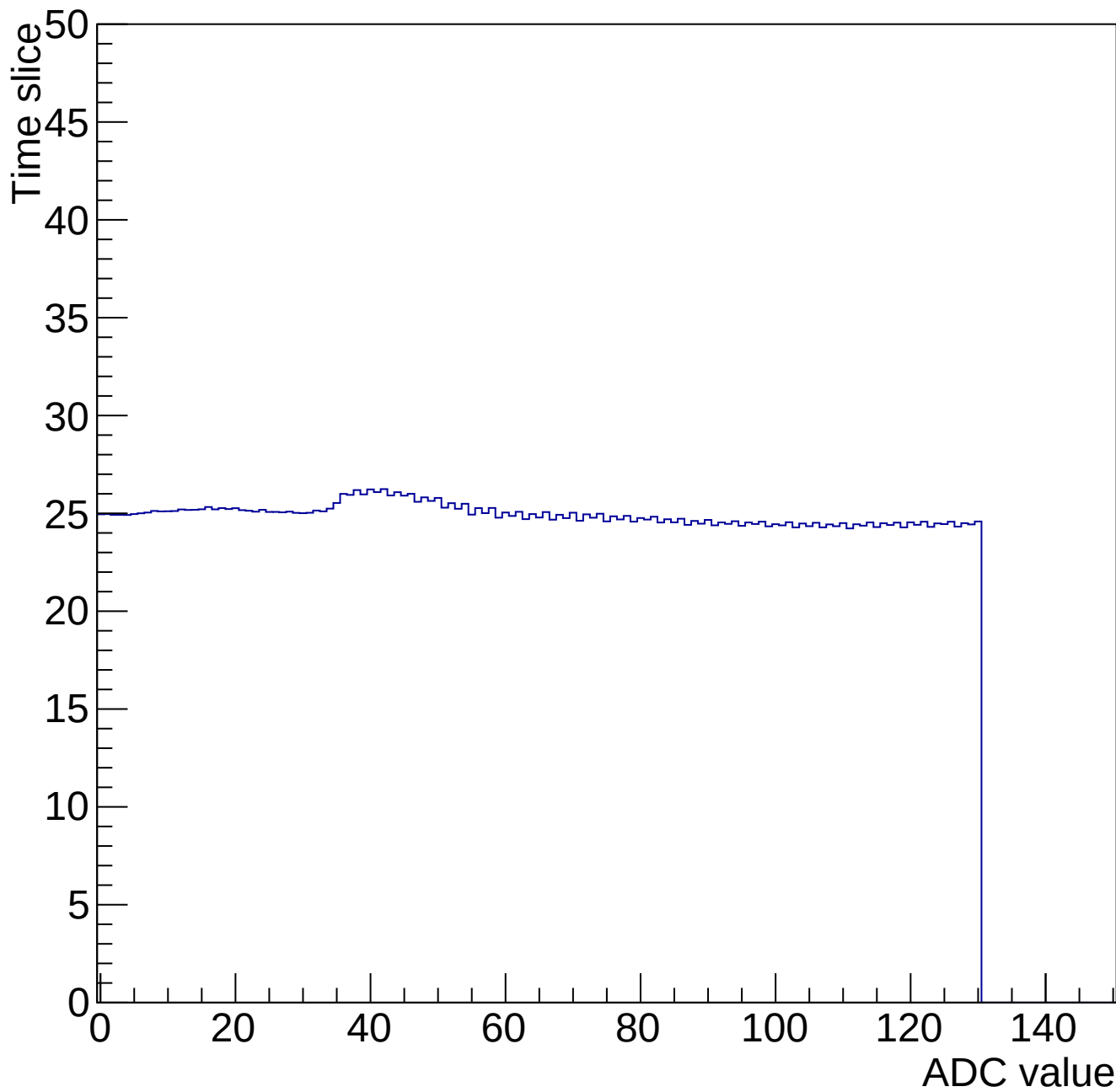
Time mcm 2003 chip 3 chan 3 34697-gain0-shaper0



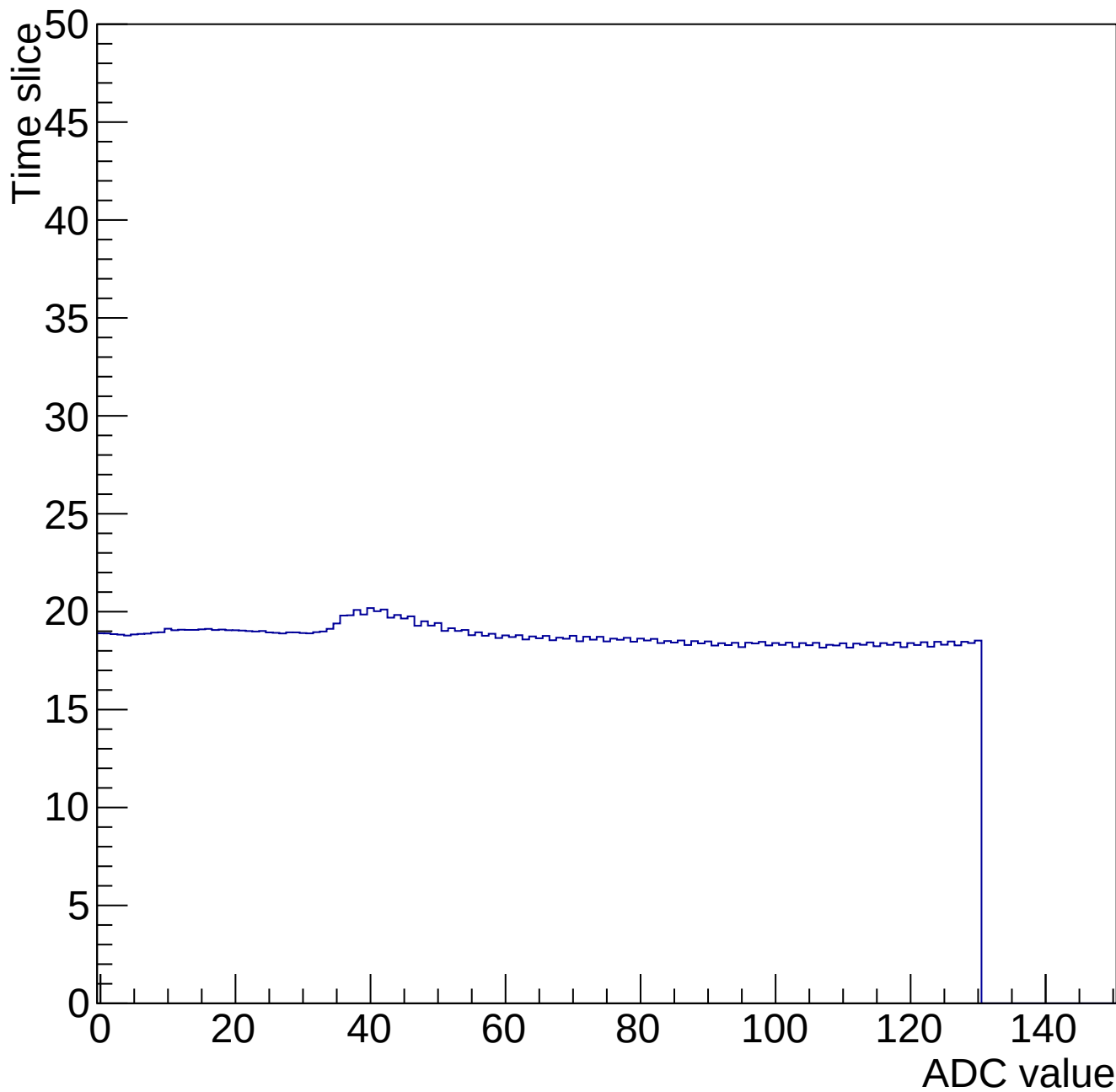
Time mcm 2003 chip 3 chan 4 34697-gain0-shaper0



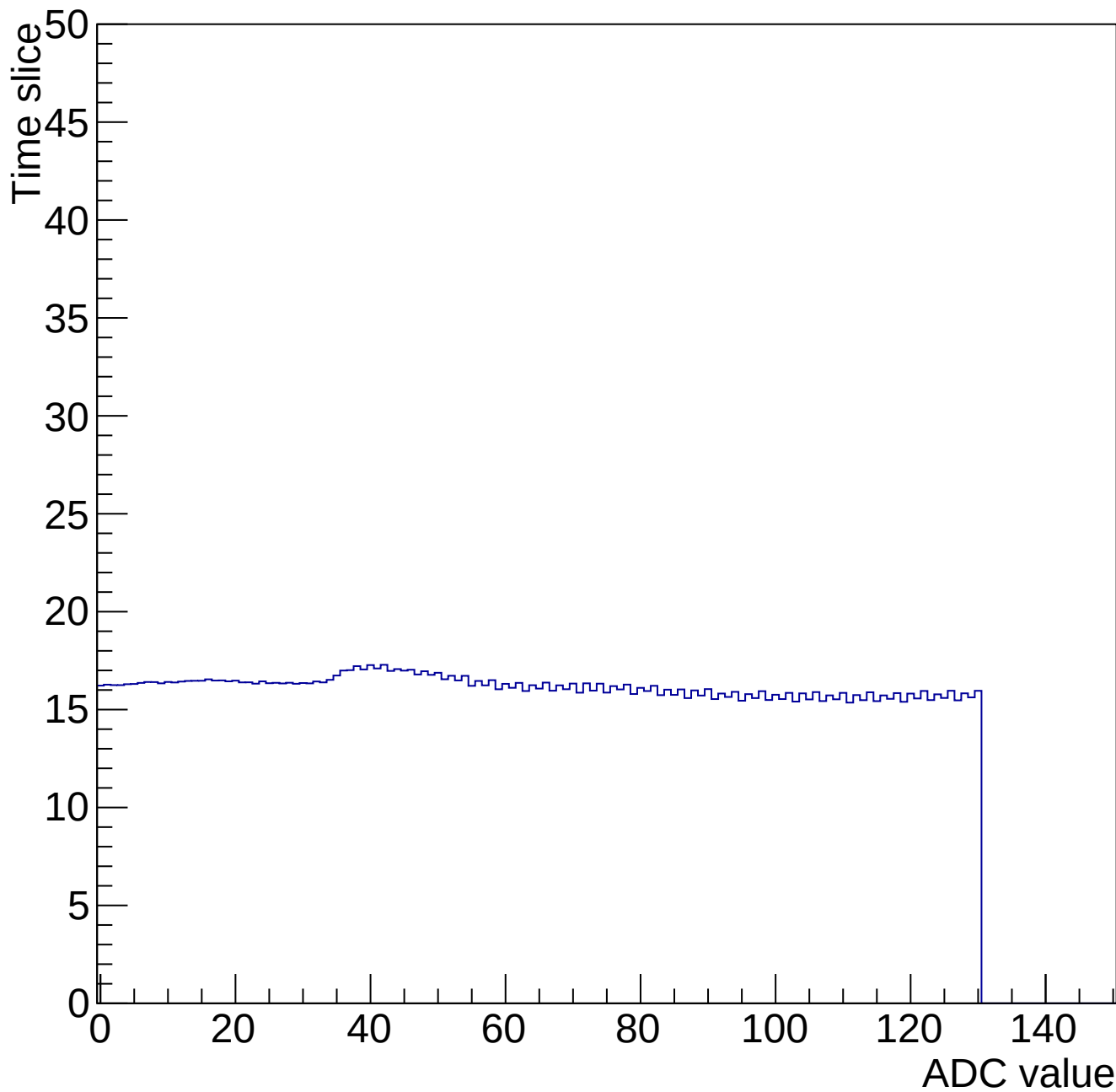
Time mcm 2003 chip 3 chan 5 34697-gain0-shaper0



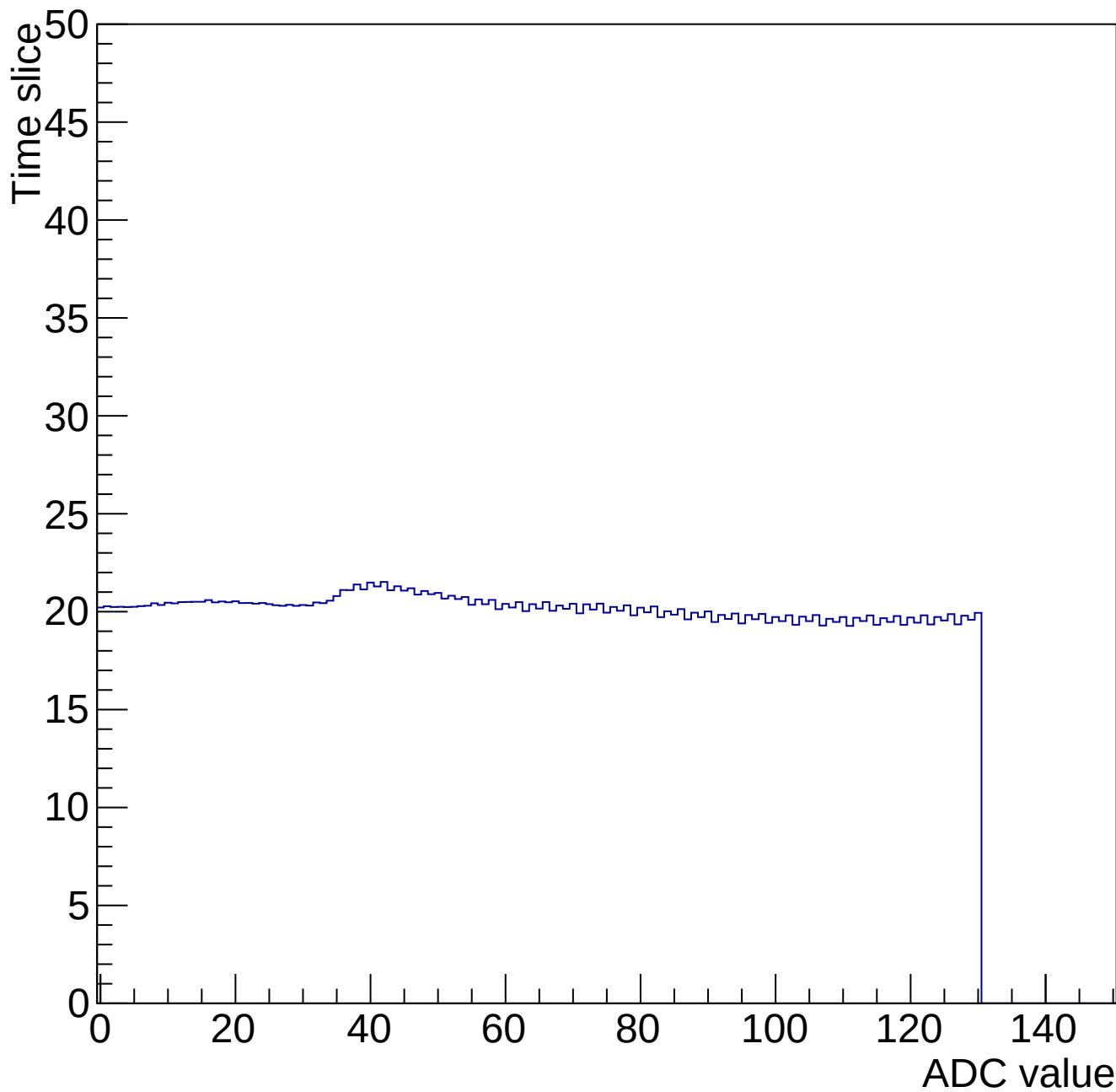
Time mcm 2003 chip 3 chan 6 34697-gain0-shaper0



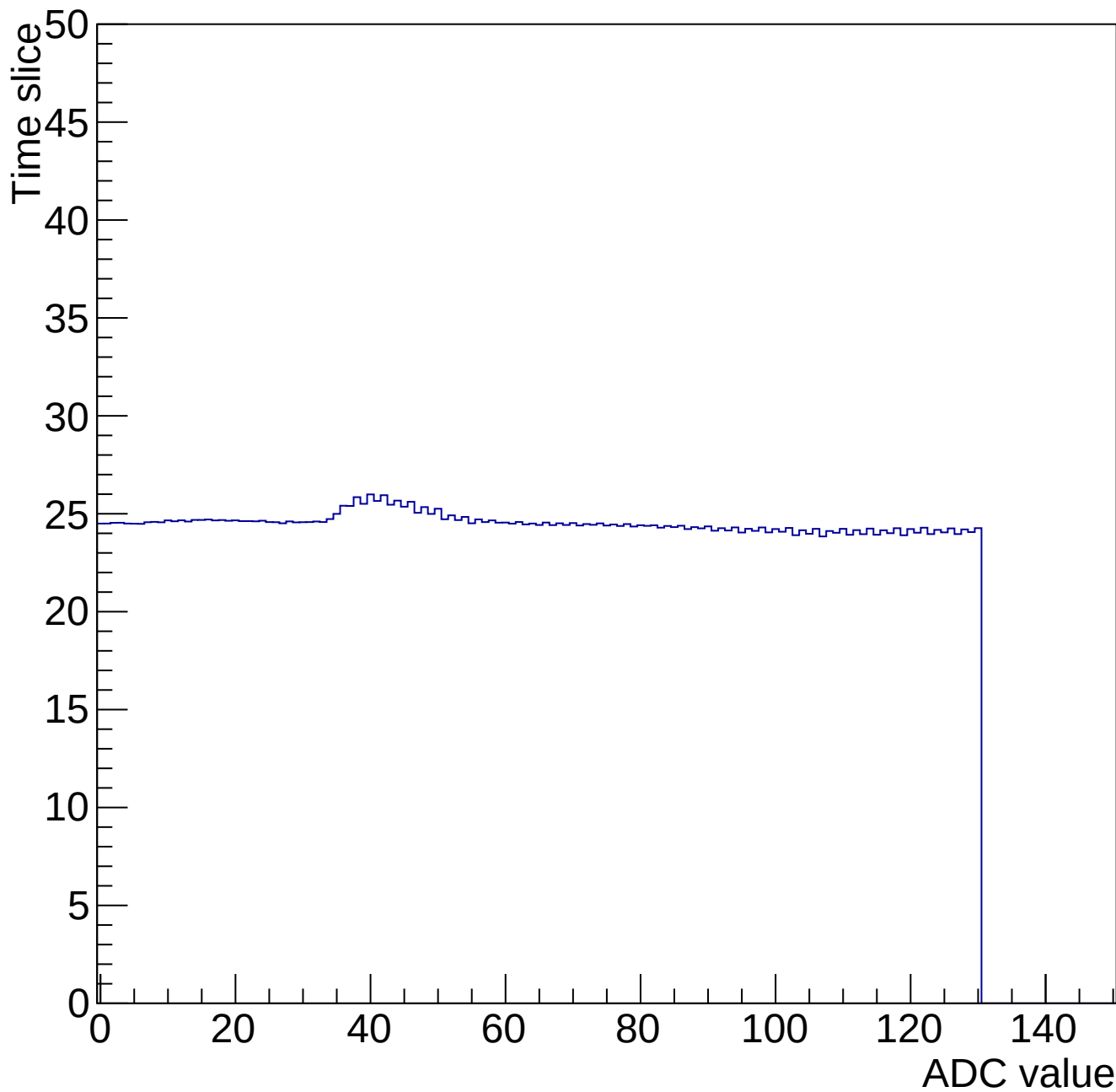
Time mcm 2003 chip 3 chan 7 34697-gain0-shaper0



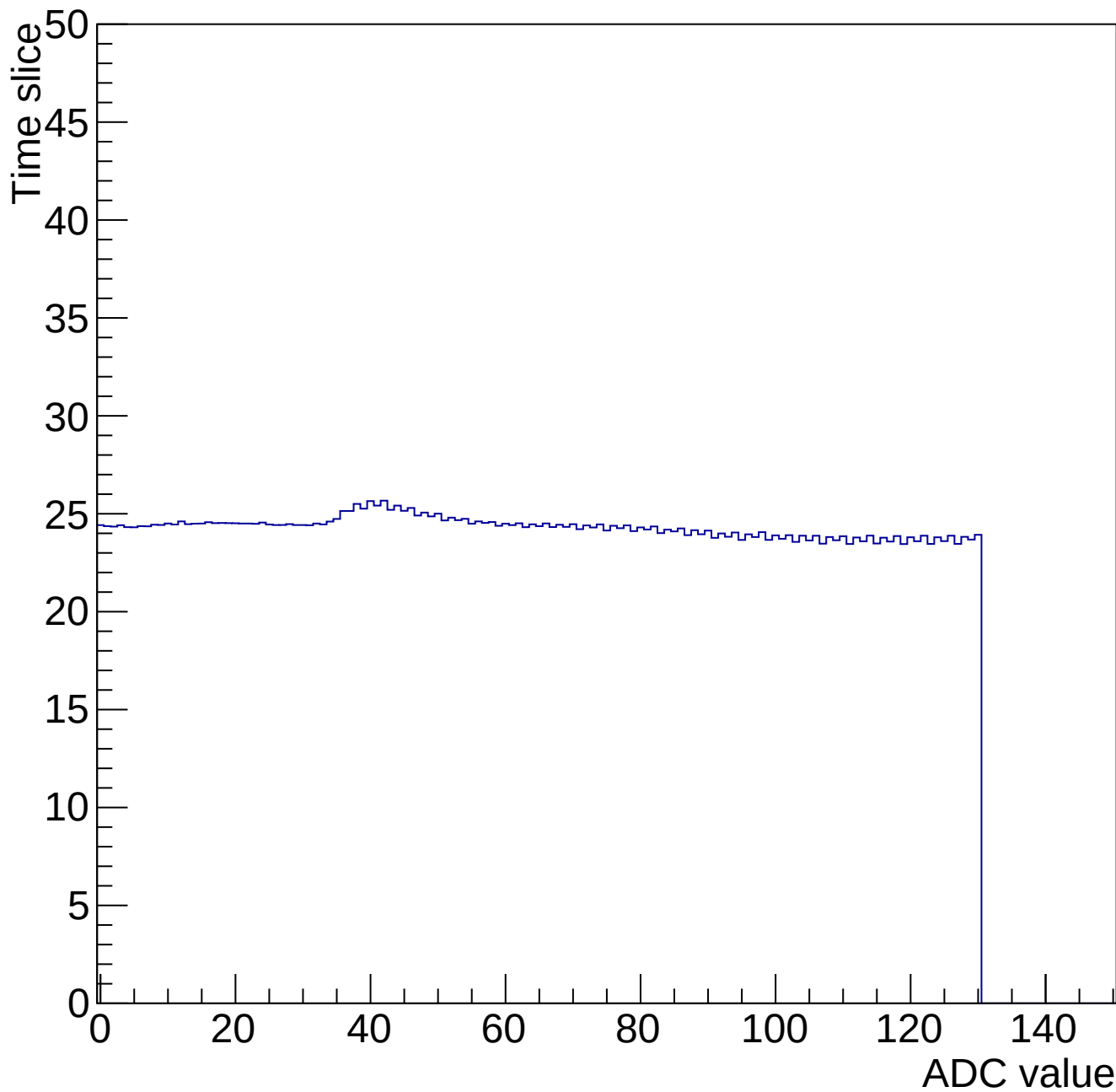
Time mcm 2003 chip 3 chan 8 34697-gain0-shaper0



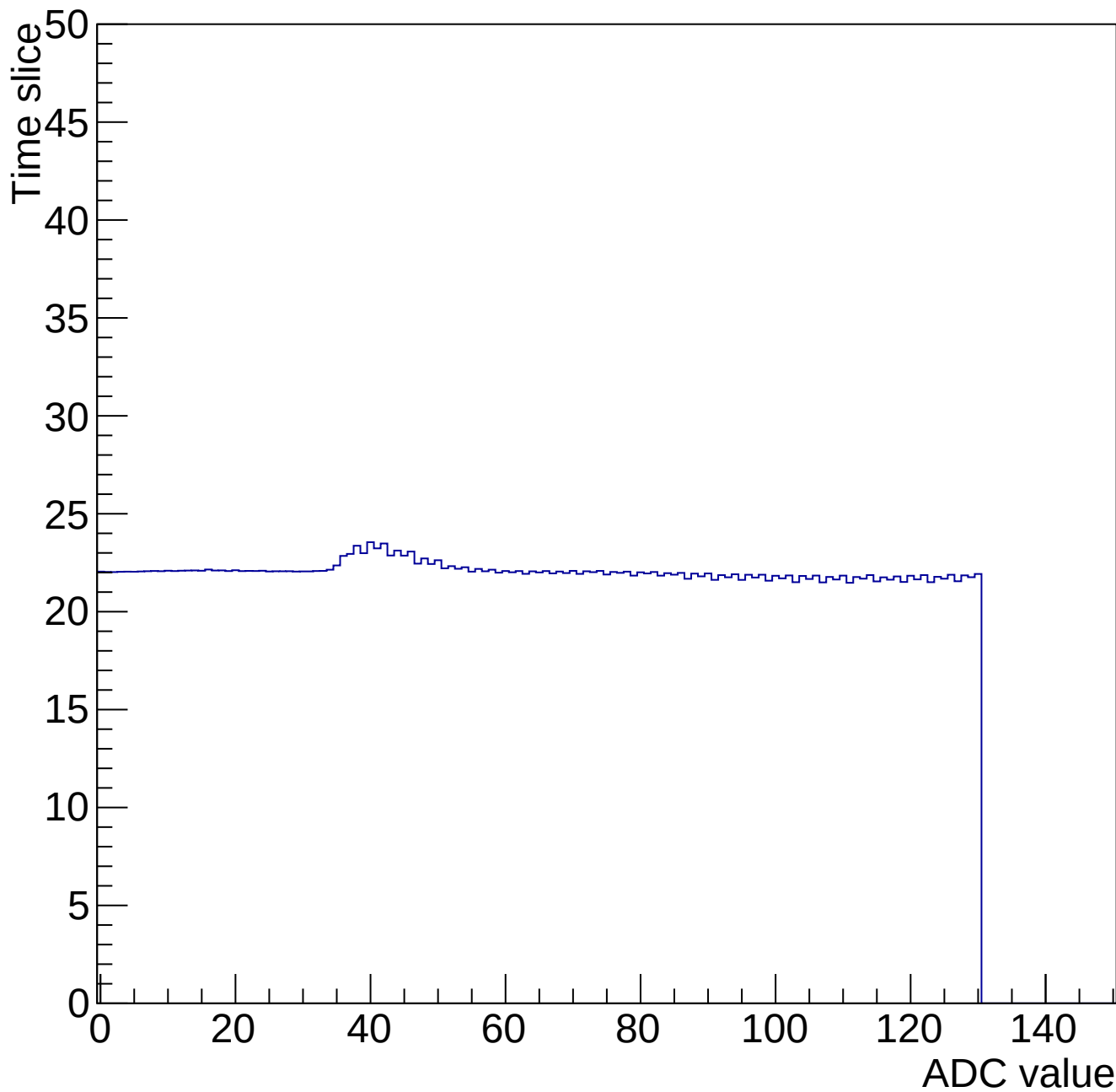
Time mcm 2003 chip 3 chan 9 34697-gain0-shaper0



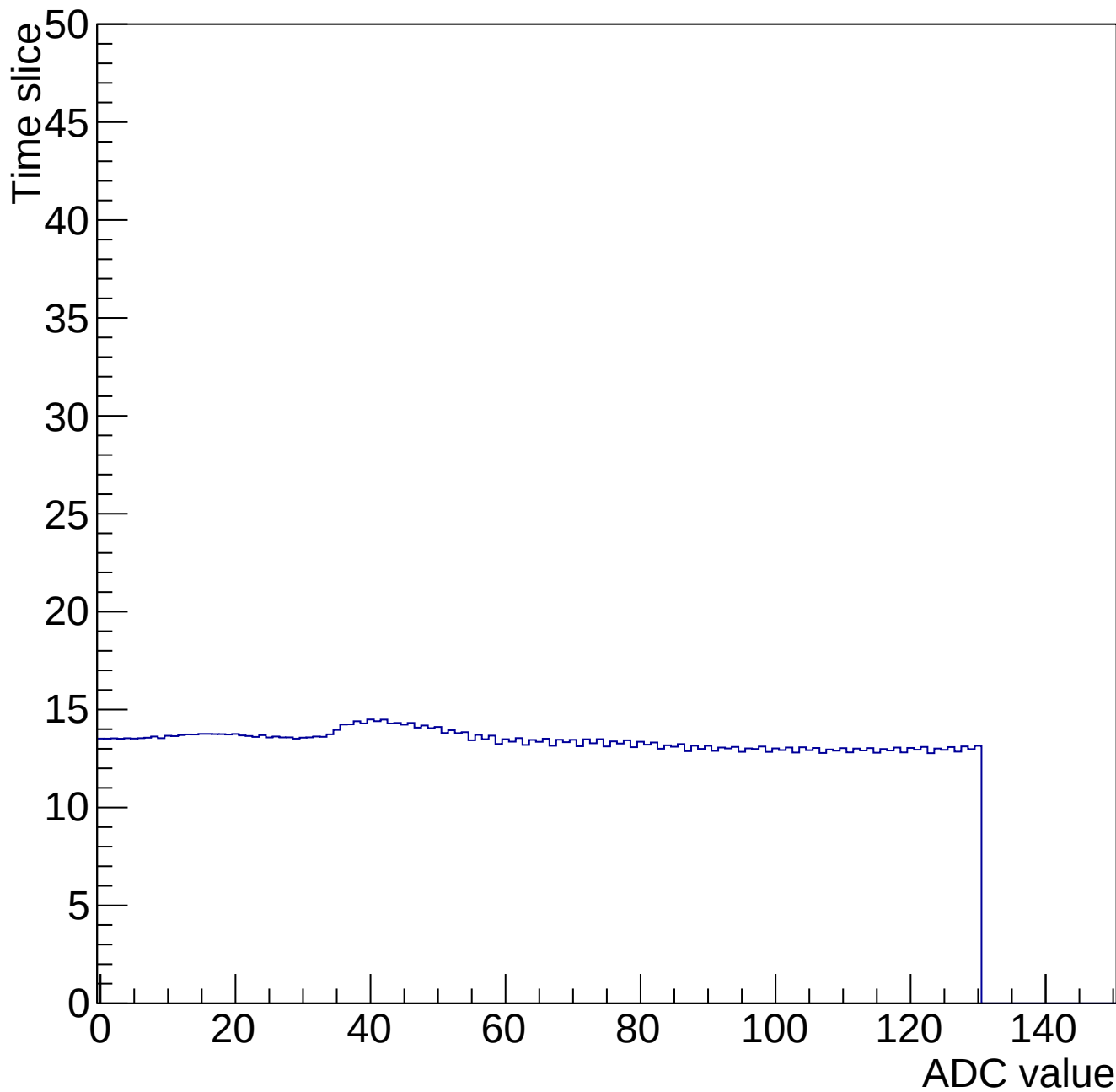
Time mcm 2003 chip 3 chan 10 34697-gain0-shaper0



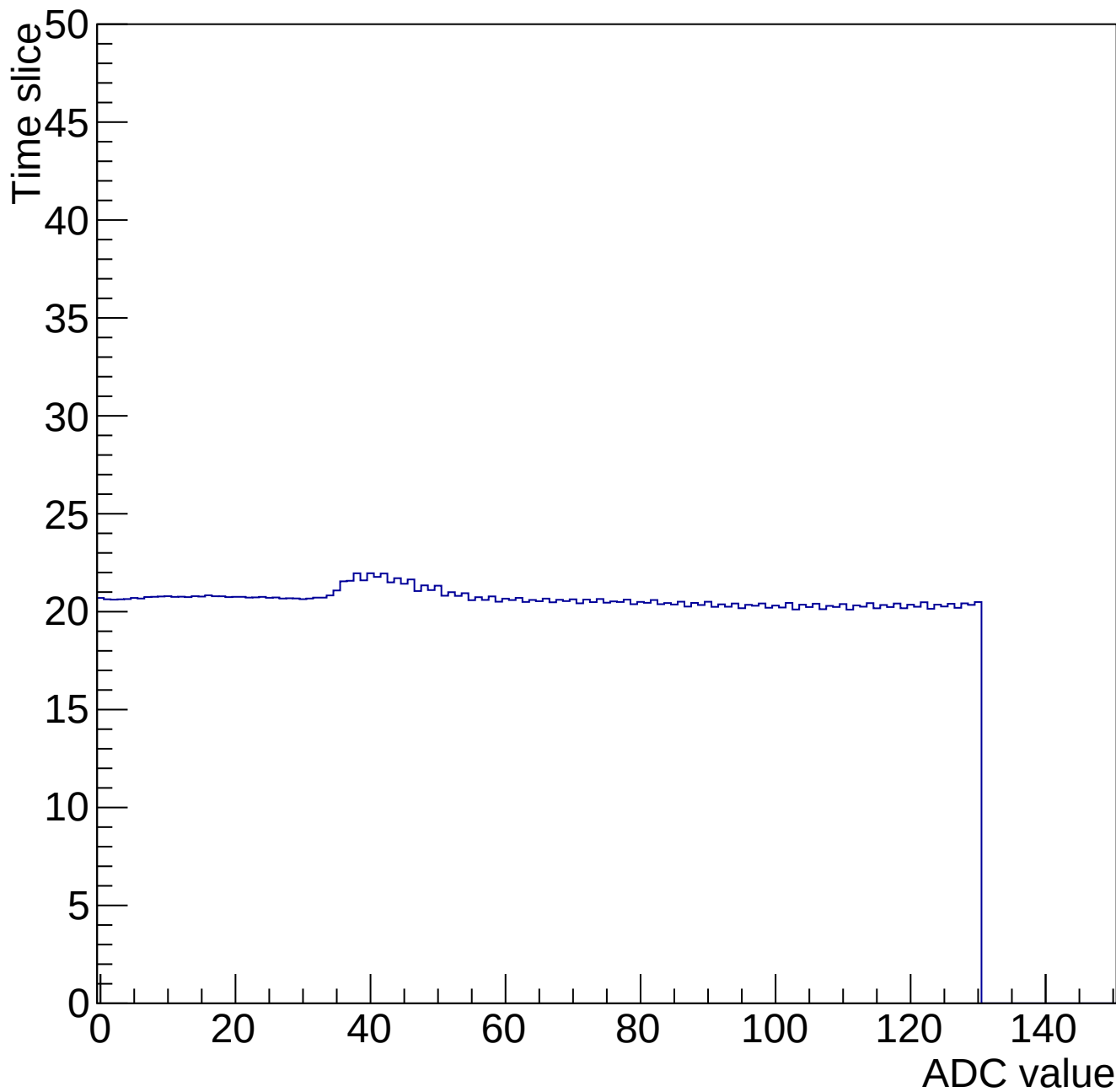
Time mcm 2003 chip 3 chan 11 34697-gain0-shaper0



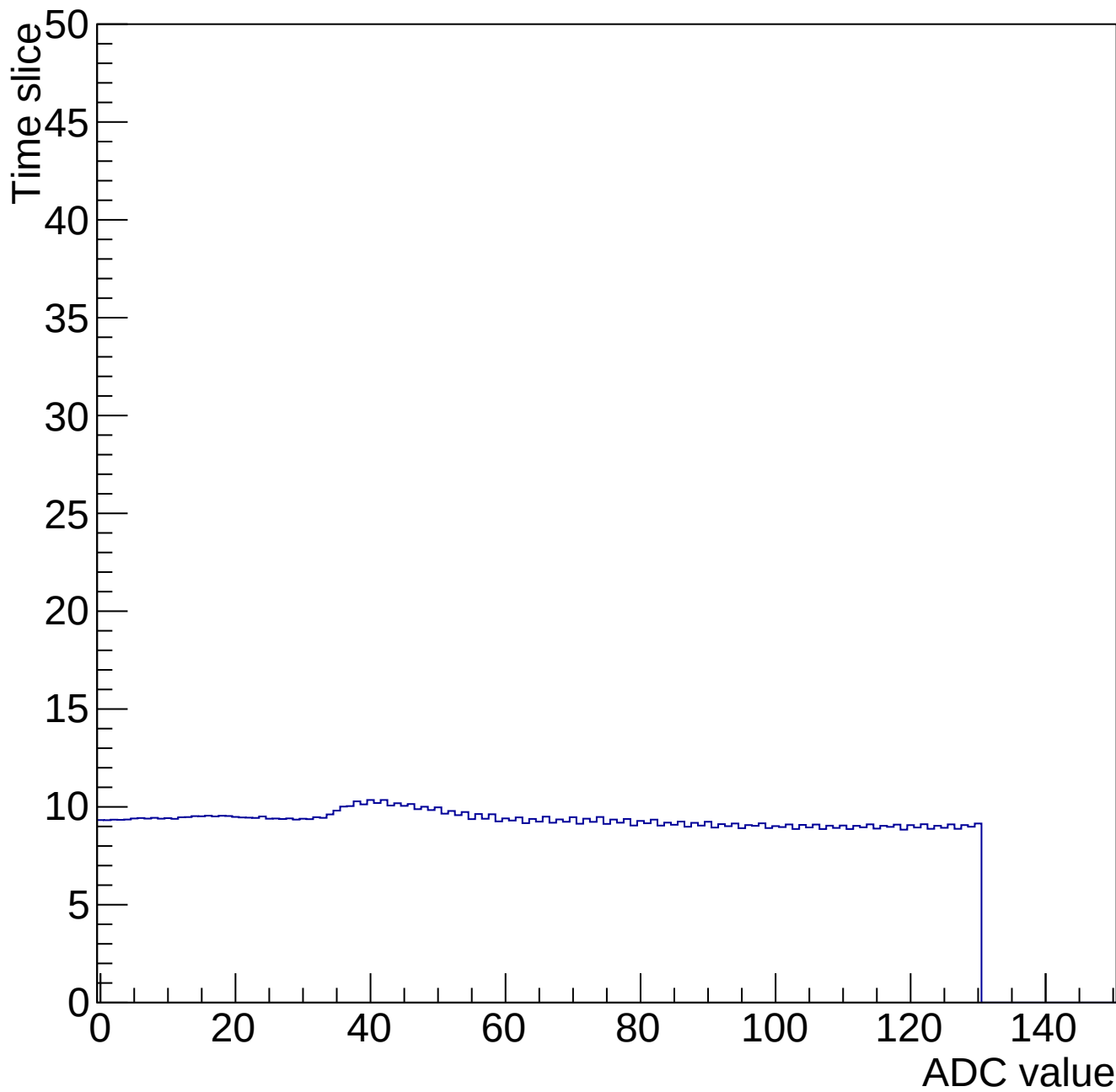
Time mcm 2003 chip 3 chan 12 34697-gain0-shaper0



Time mcm 2003 chip 3 chan 13 34697-gain0-shaper0



Time mcm 2003 chip 3 chan 14 34697-gain0-shaper0



Time mcm 2003 chip 3 chan 15 34697-gain0-shaper0

