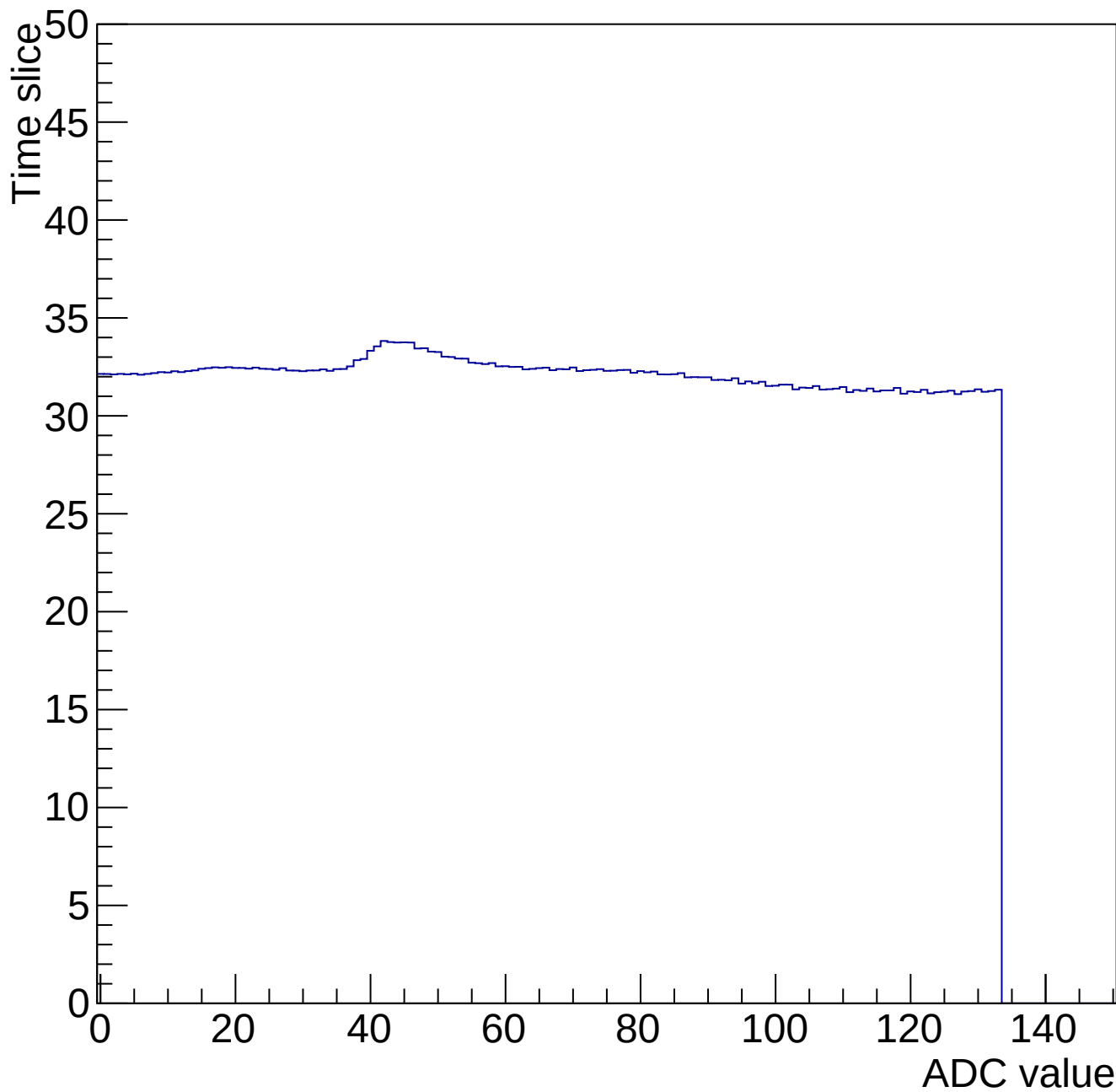
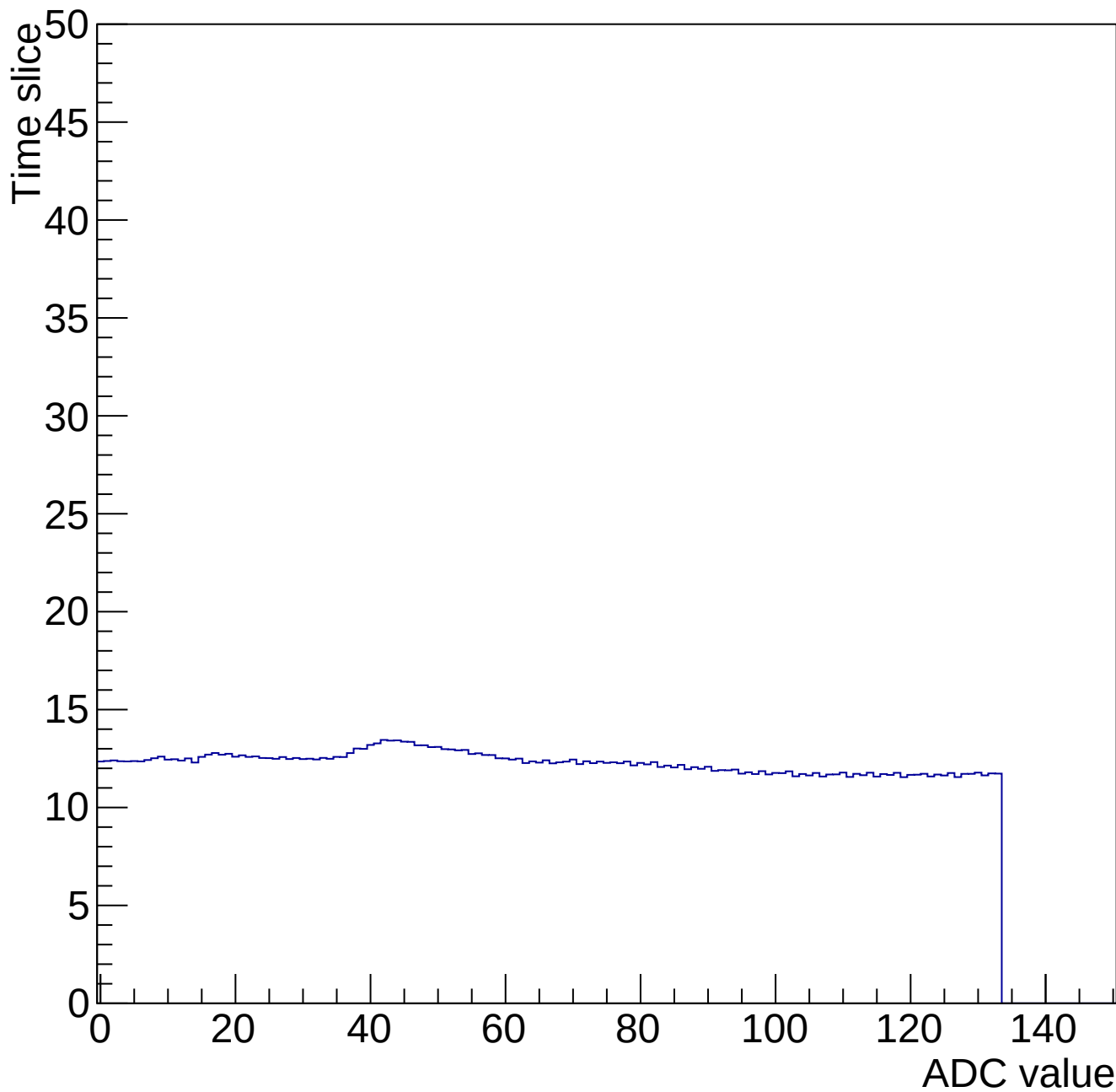


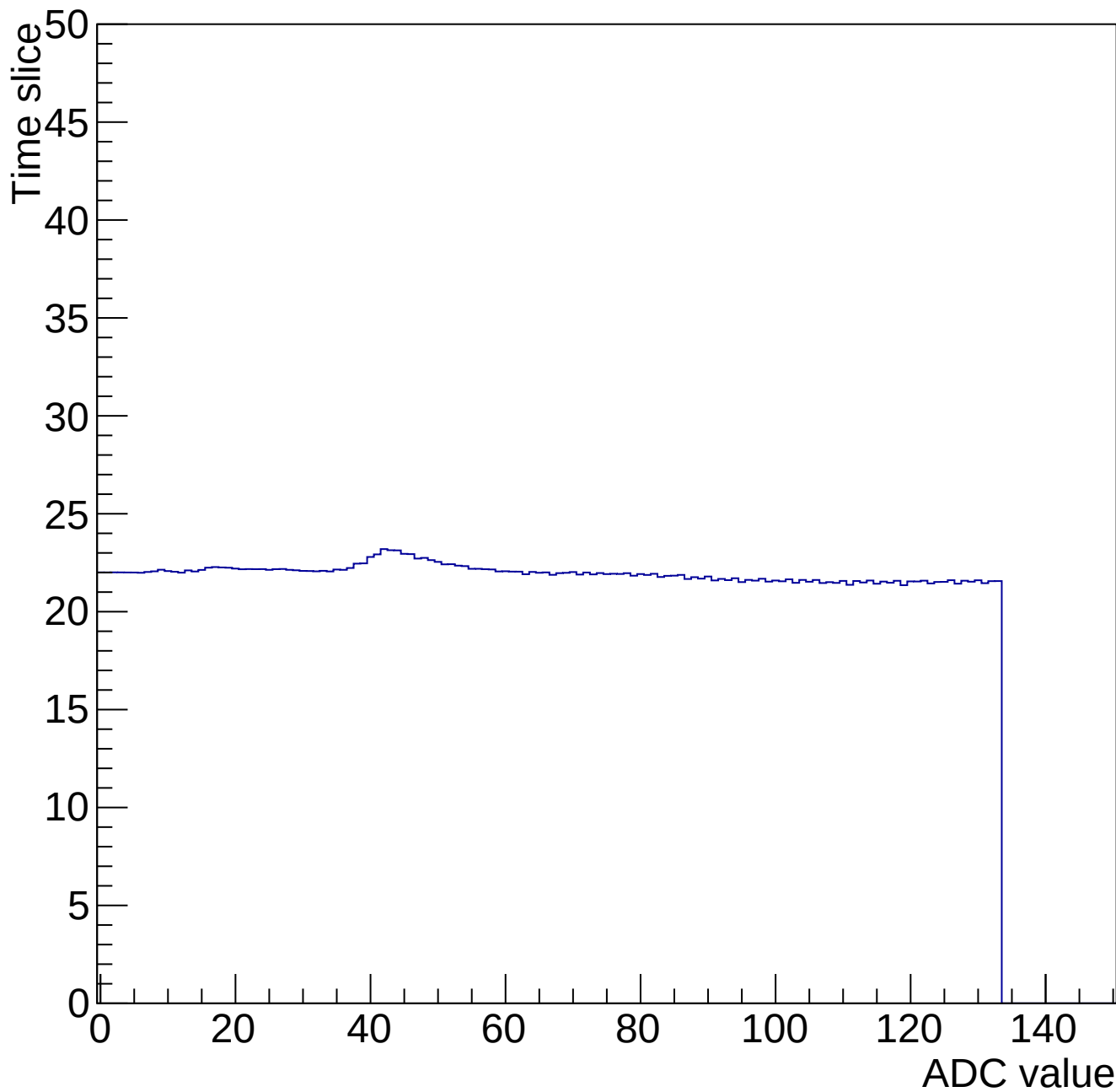
Time mcm 2004 chip 4 chan 0 34698-gain0-shaper0



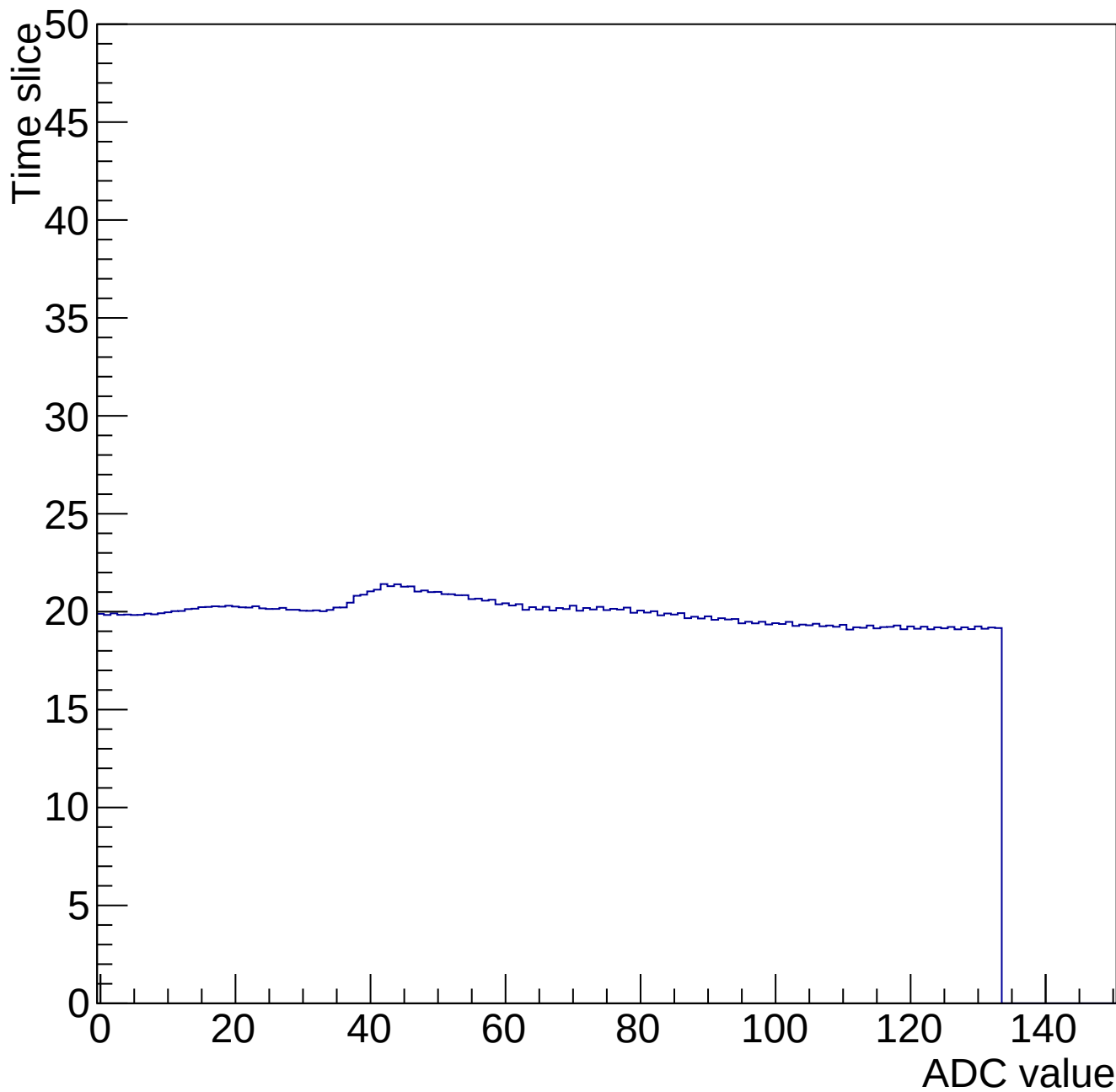
Time mcm 2004 chip 4 chan 1 34698-gain0-shaper0



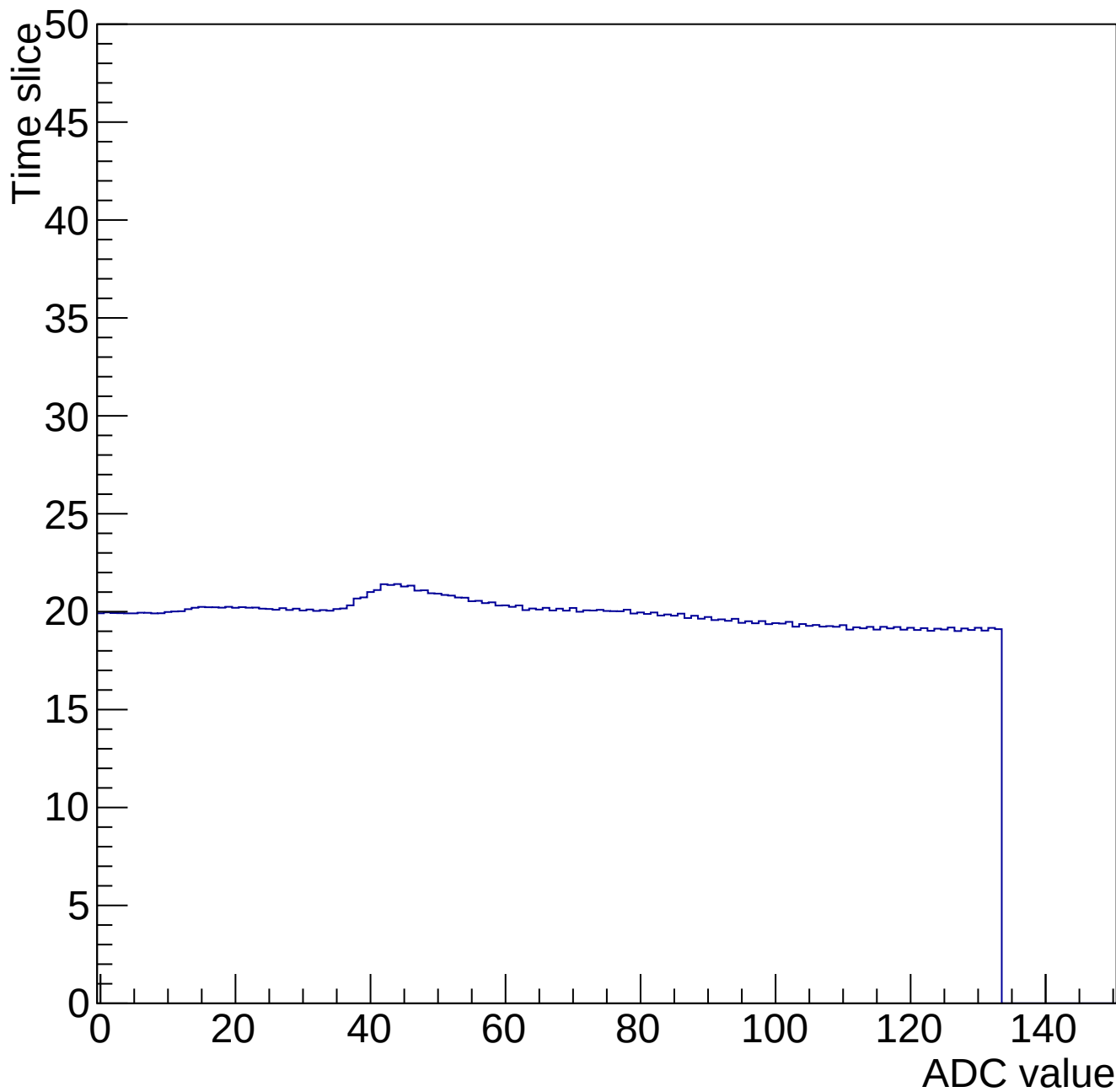
Time mcm 2004 chip 4 chan 2 34698-gain0-shaper0



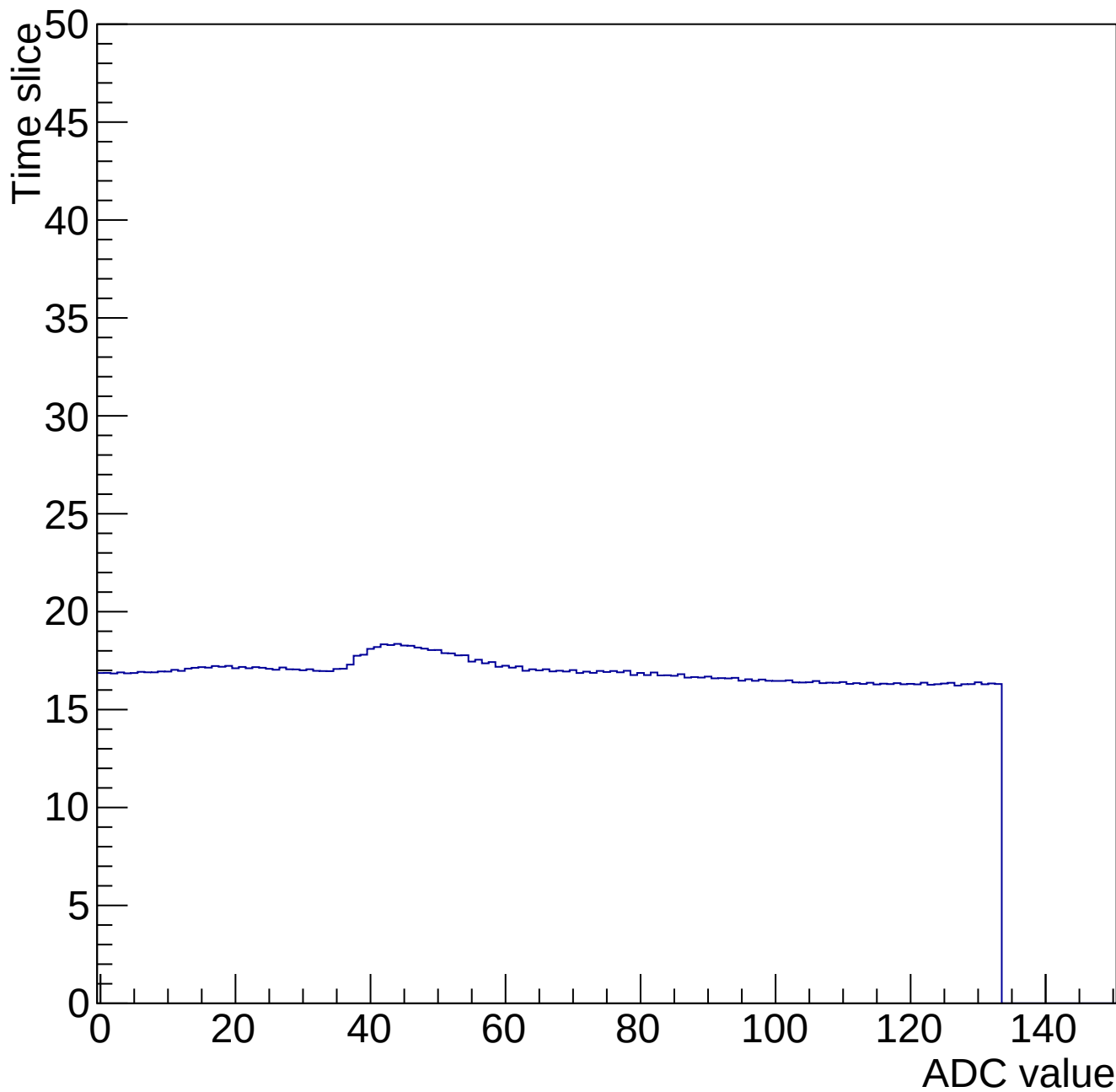
Time mcm 2004 chip 4 chan 3 34698-gain0-shaper0



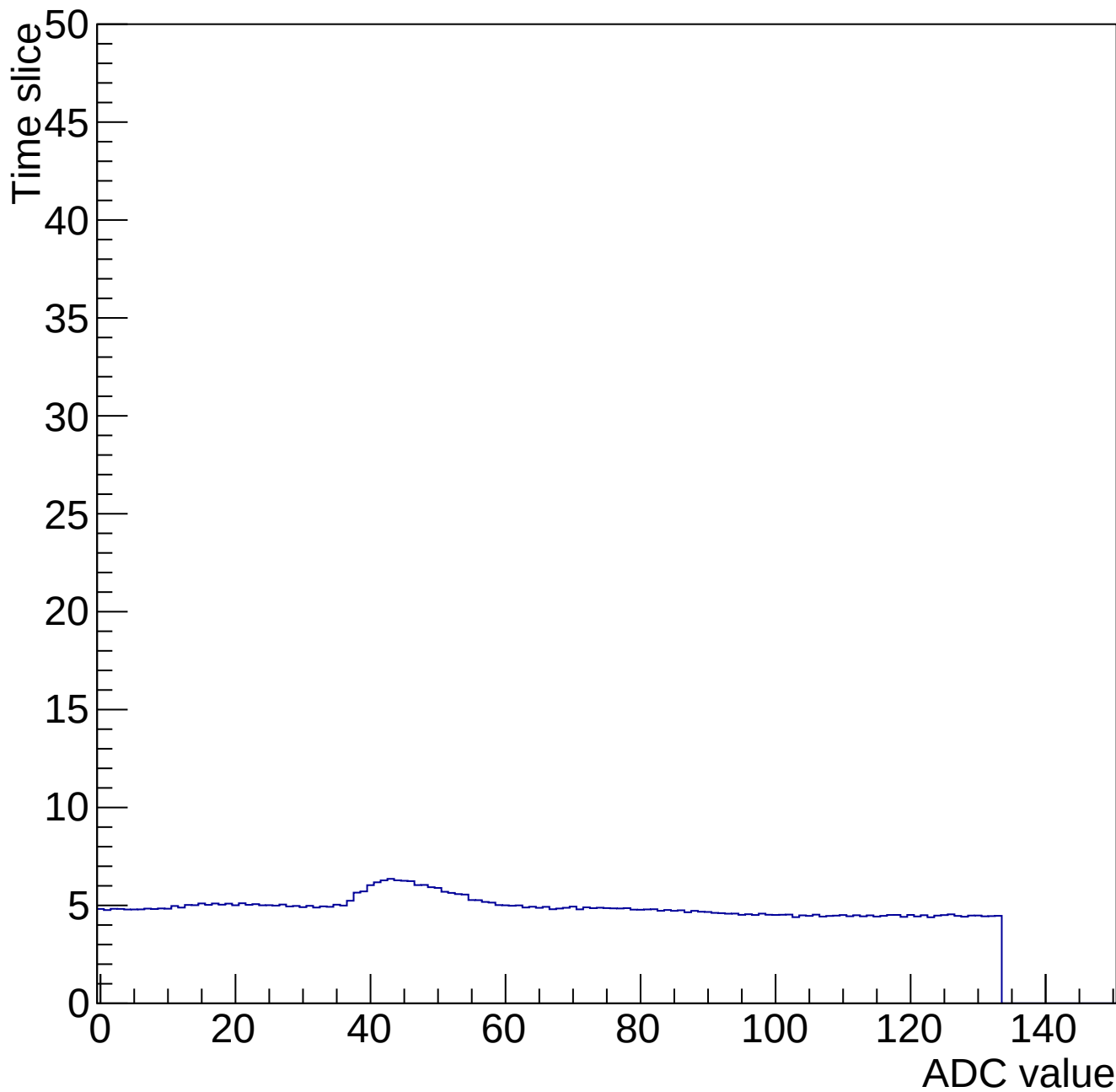
Time mcm 2004 chip 4 chan 4 34698-gain0-shaper0



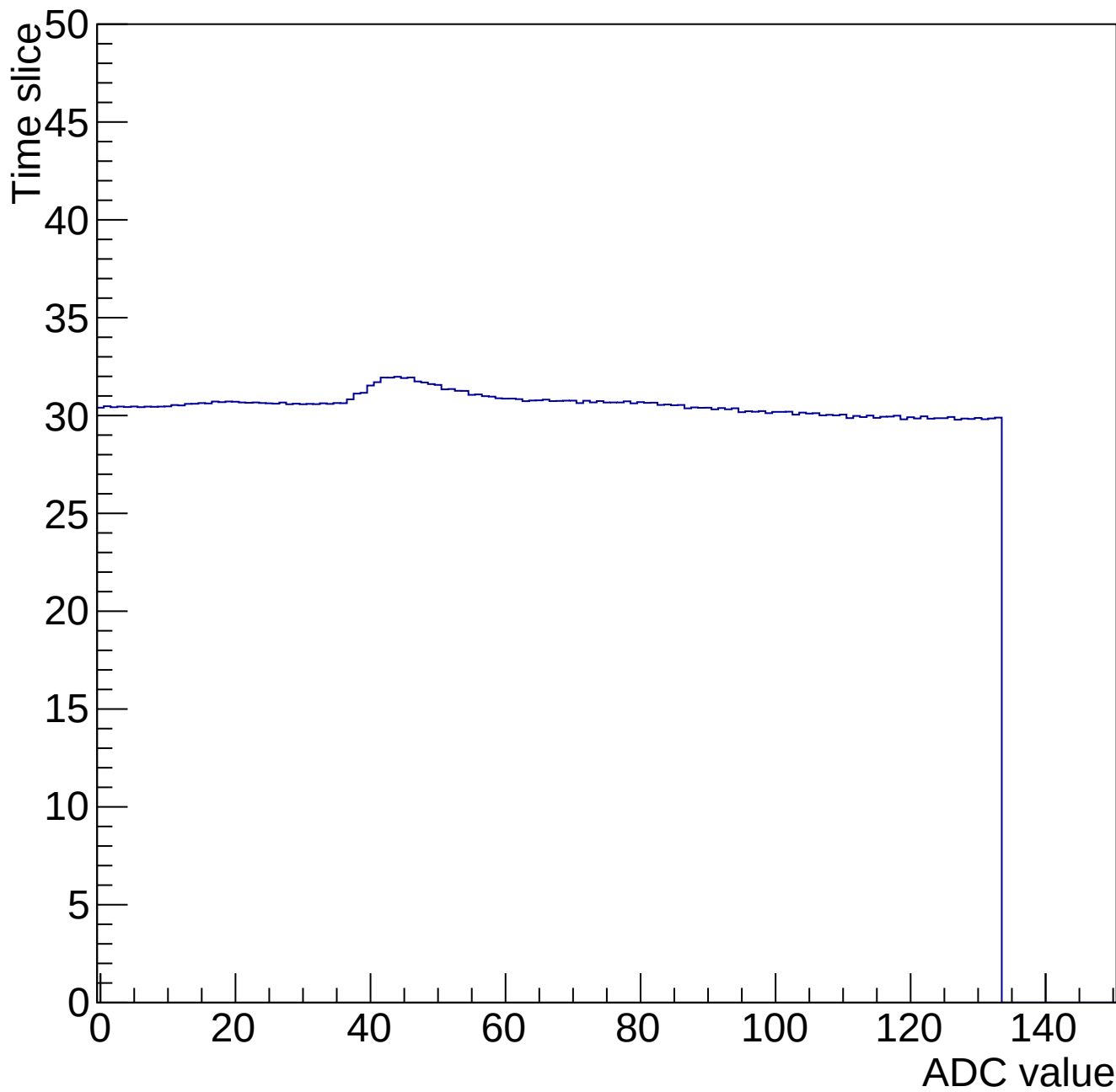
Time mcm 2004 chip 4 chan 5 34698-gain0-shaper0



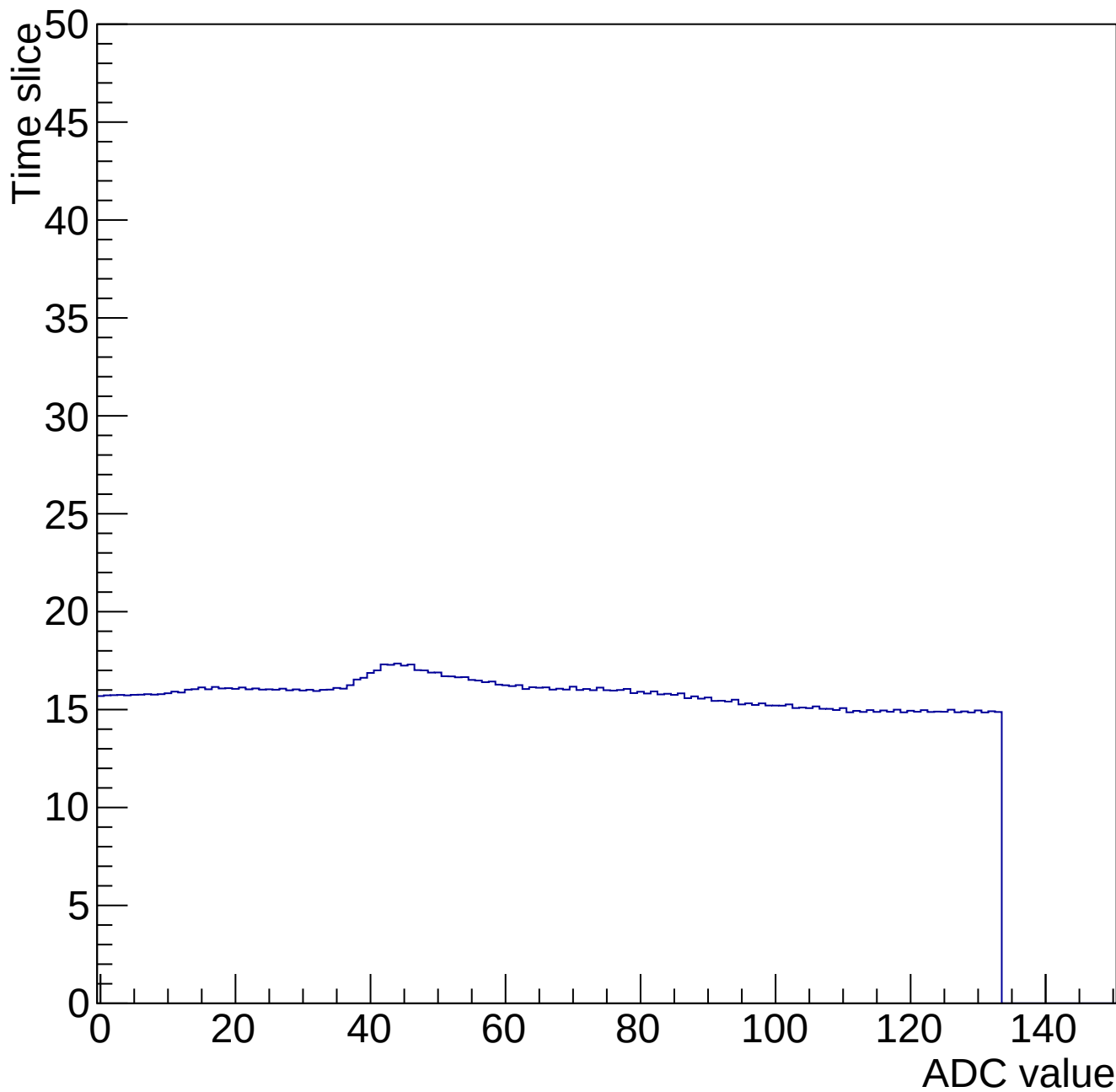
Time mcm 2004 chip 4 chan 6 34698-gain0-shaper0



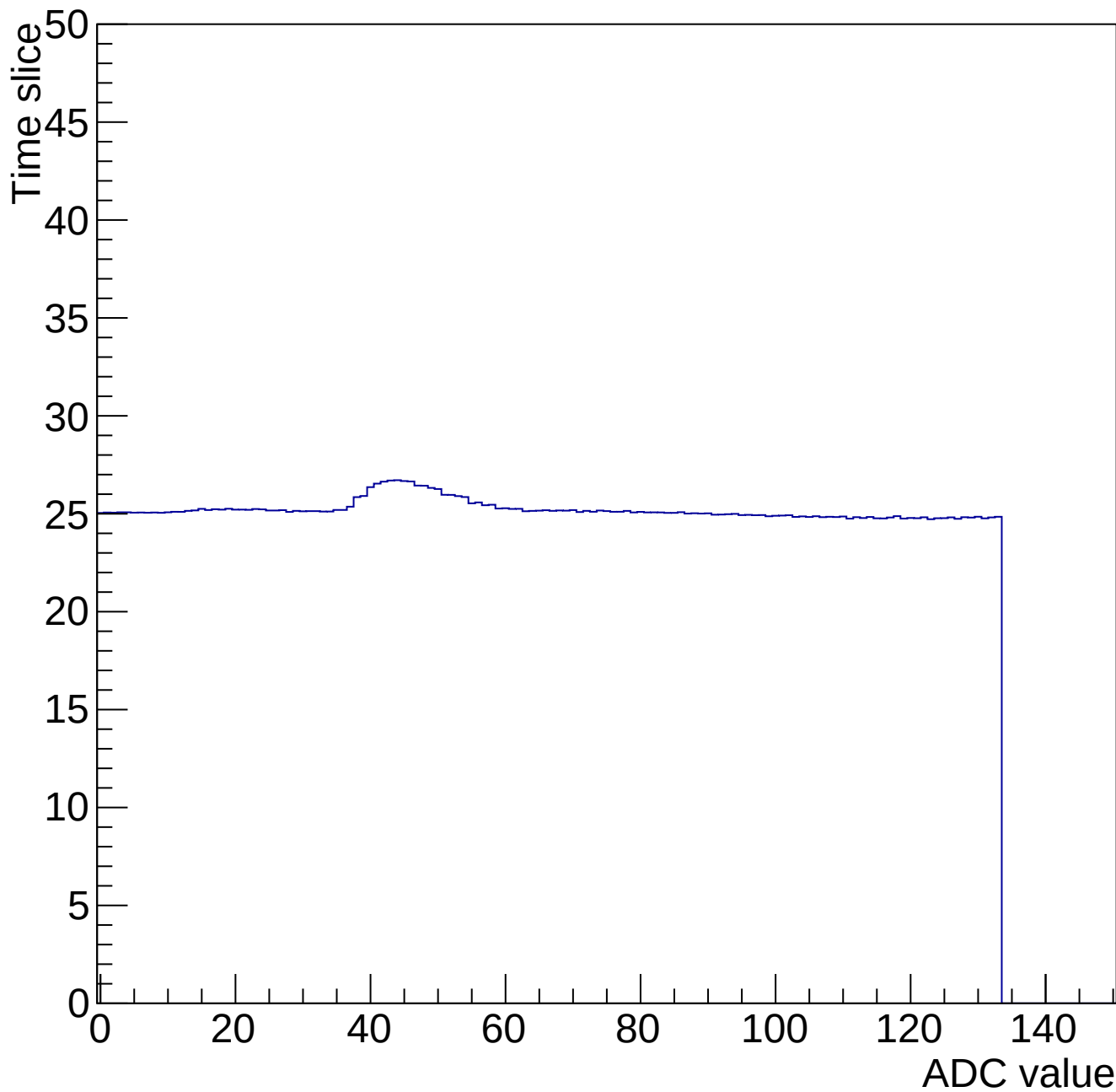
Time mcm 2004 chip 4 chan 7 34698-gain0-shaper0



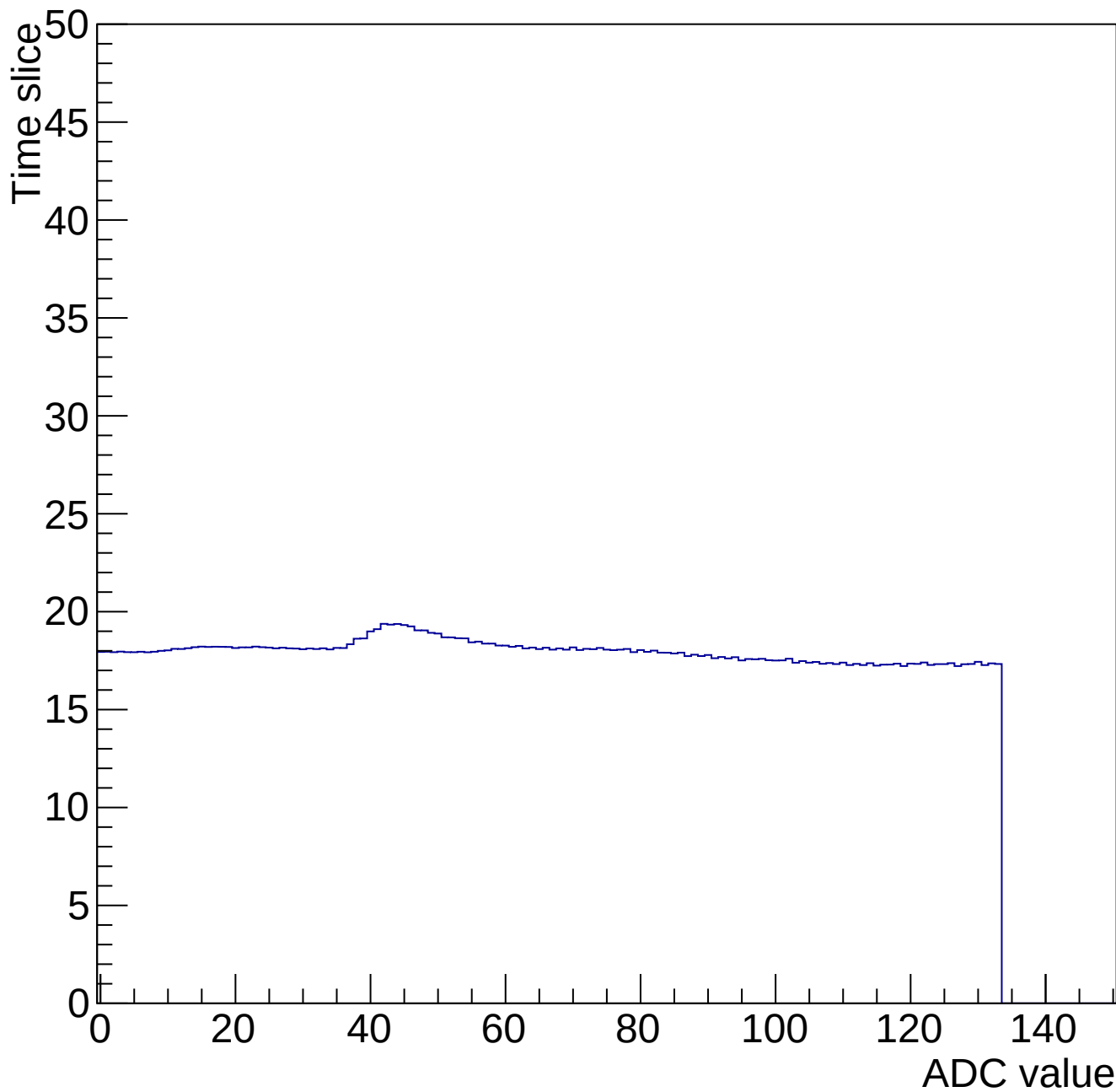
Time mcm 2004 chip 4 chan 8 34698-gain0-shaper0



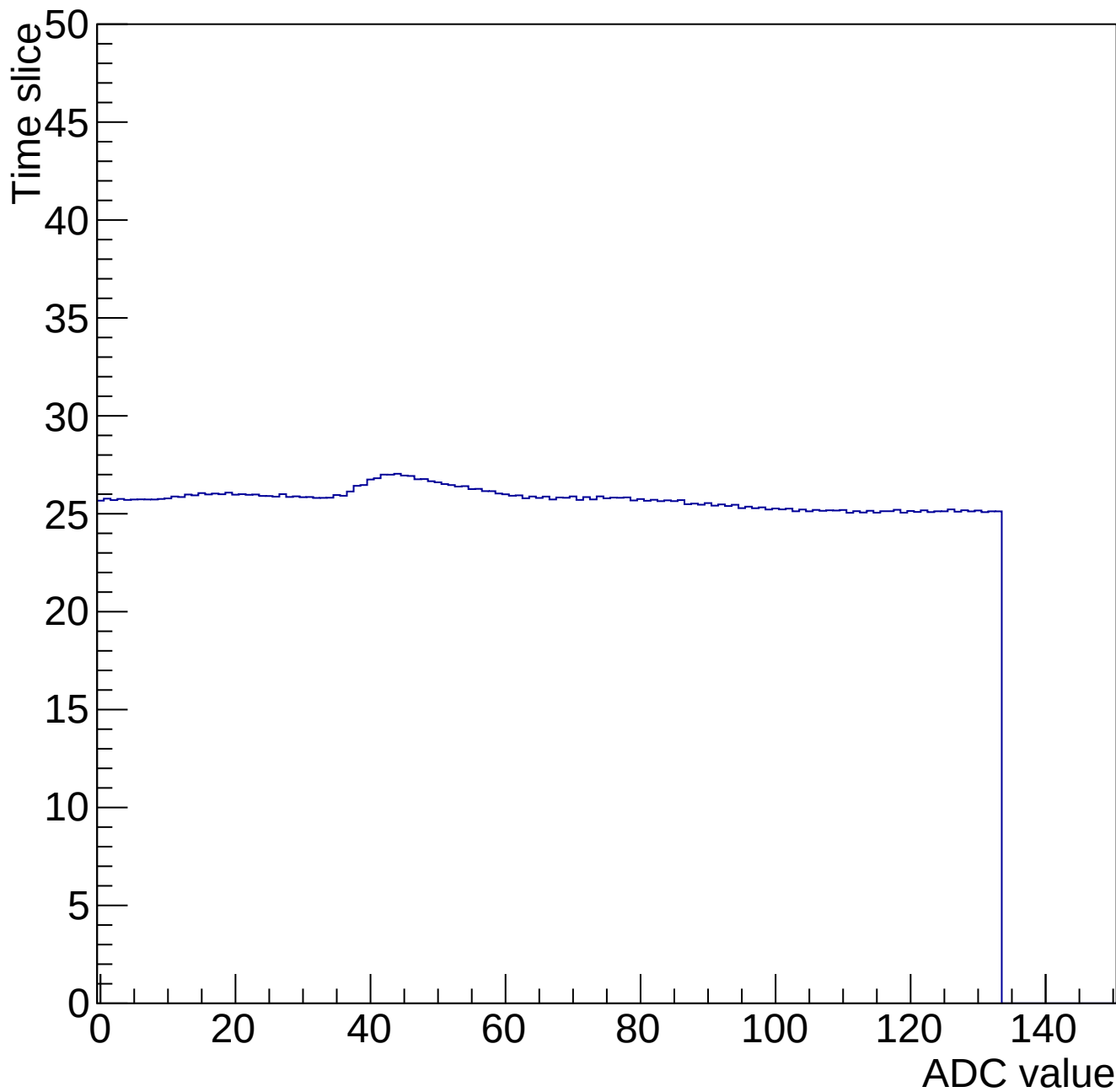
Time mcm 2004 chip 4 chan 9 34698-gain0-shaper0



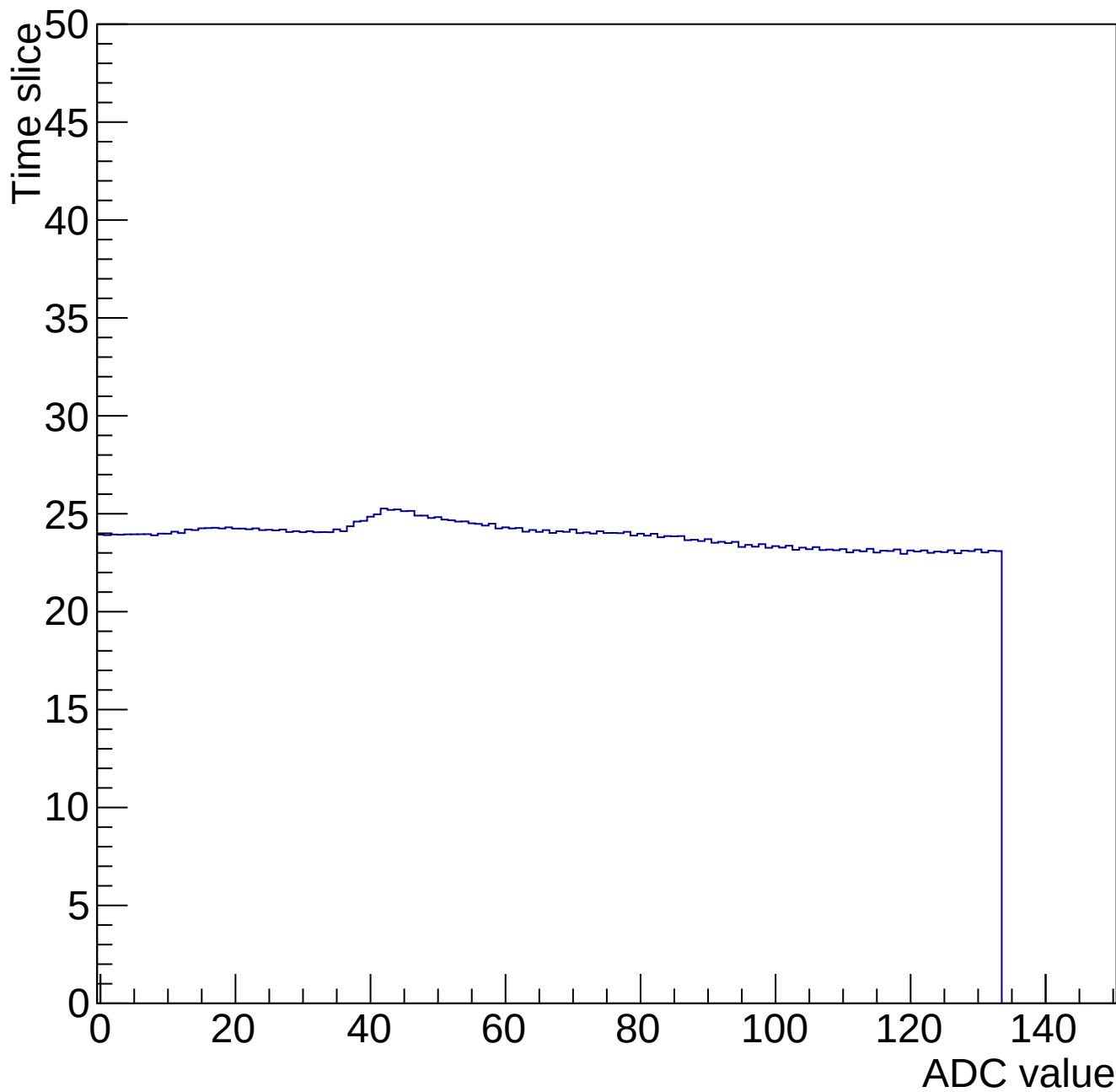
Time mcm 2004 chip 4 chan 10 34698-gain0-shaper0



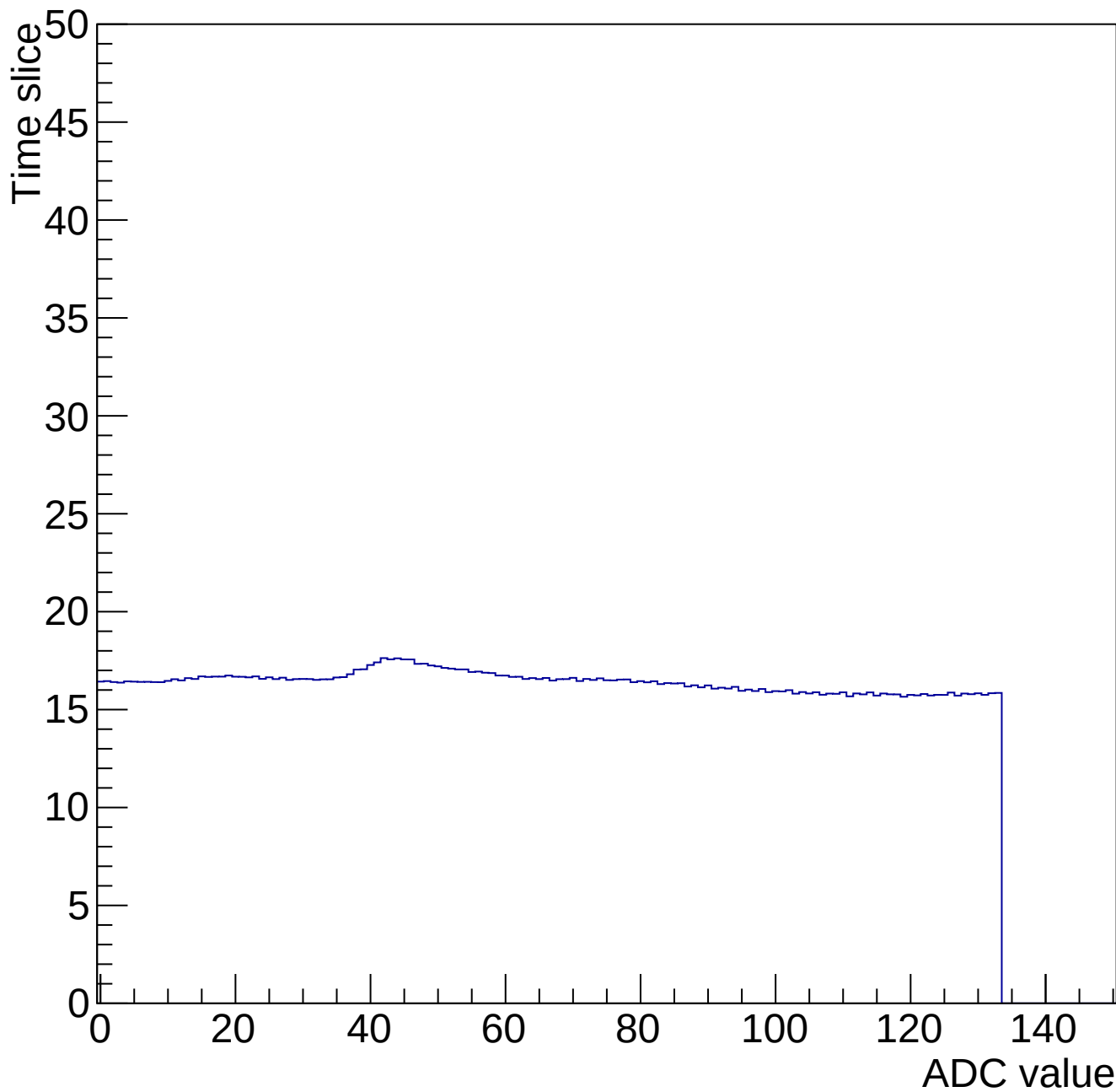
Time mcm 2004 chip 4 chan 11 34698-gain0-shaper0



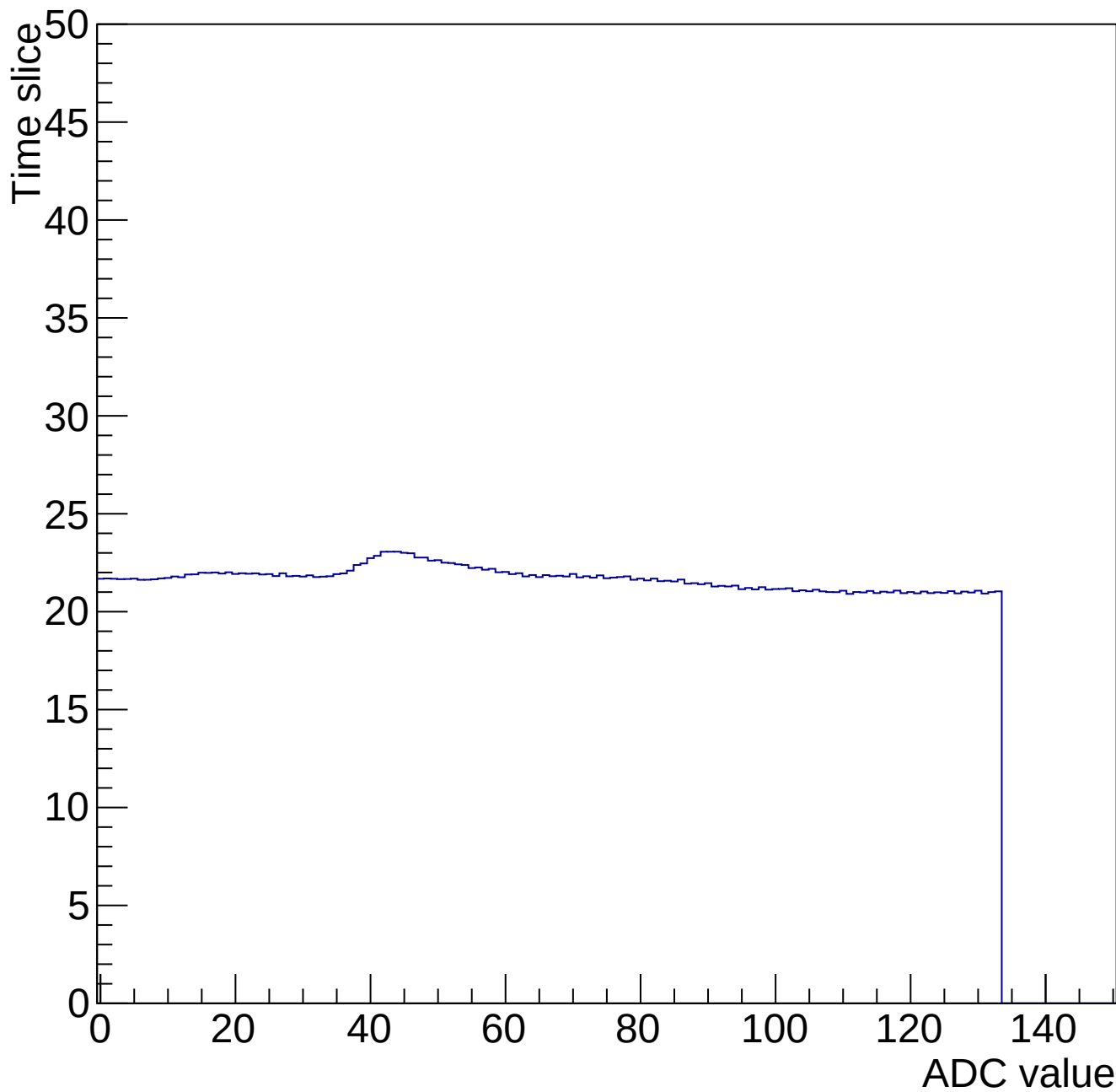
Time mcm 2004 chip 4 chan 12 34698-gain0-shaper0



Time mcm 2004 chip 4 chan 13 34698-gain0-shaper0



Time mcm 2004 chip 4 chan 14 34698-gain0-shaper0



Time mcm 2004 chip 4 chan 15 34698-gain0-shaper0

