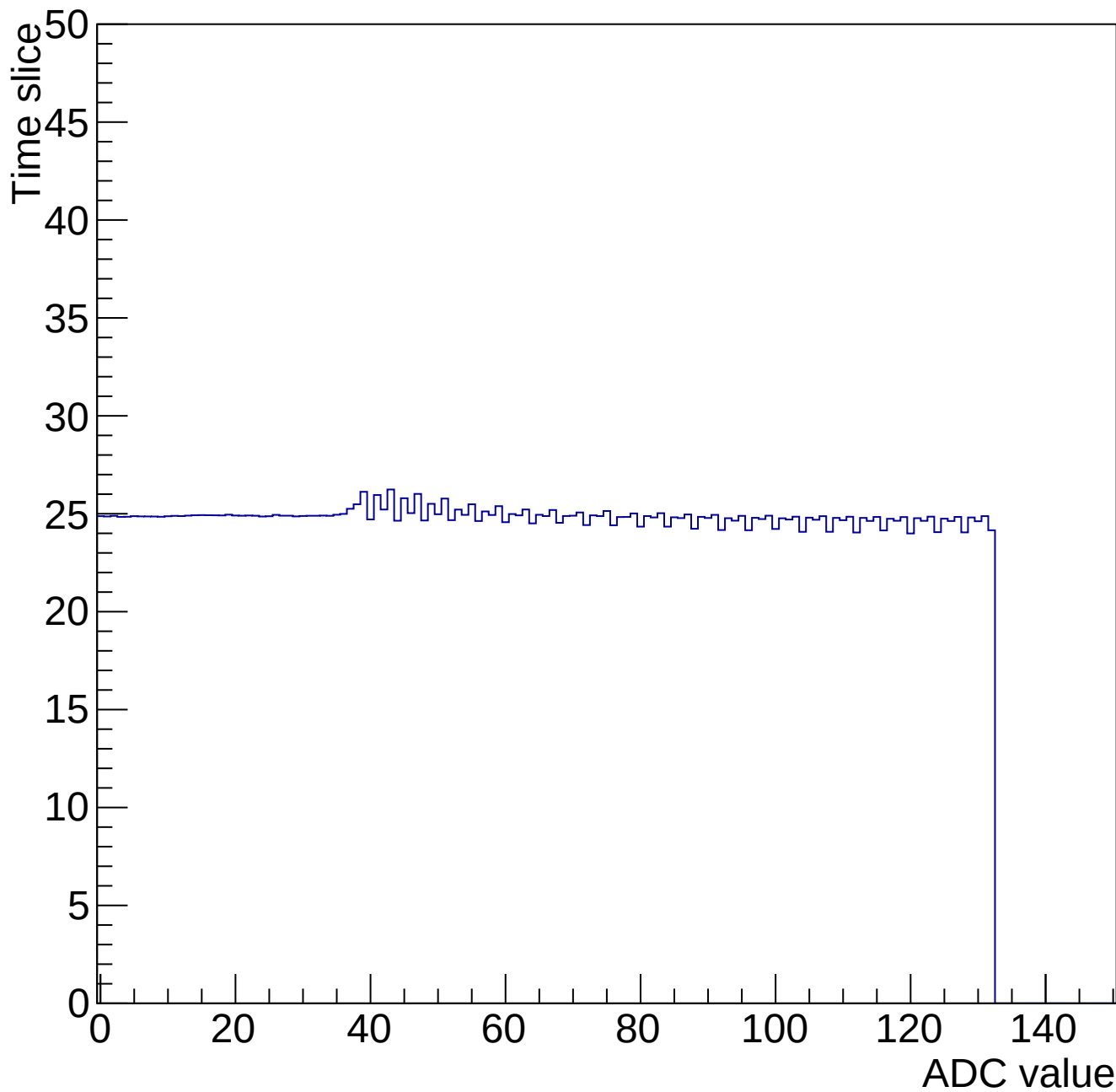
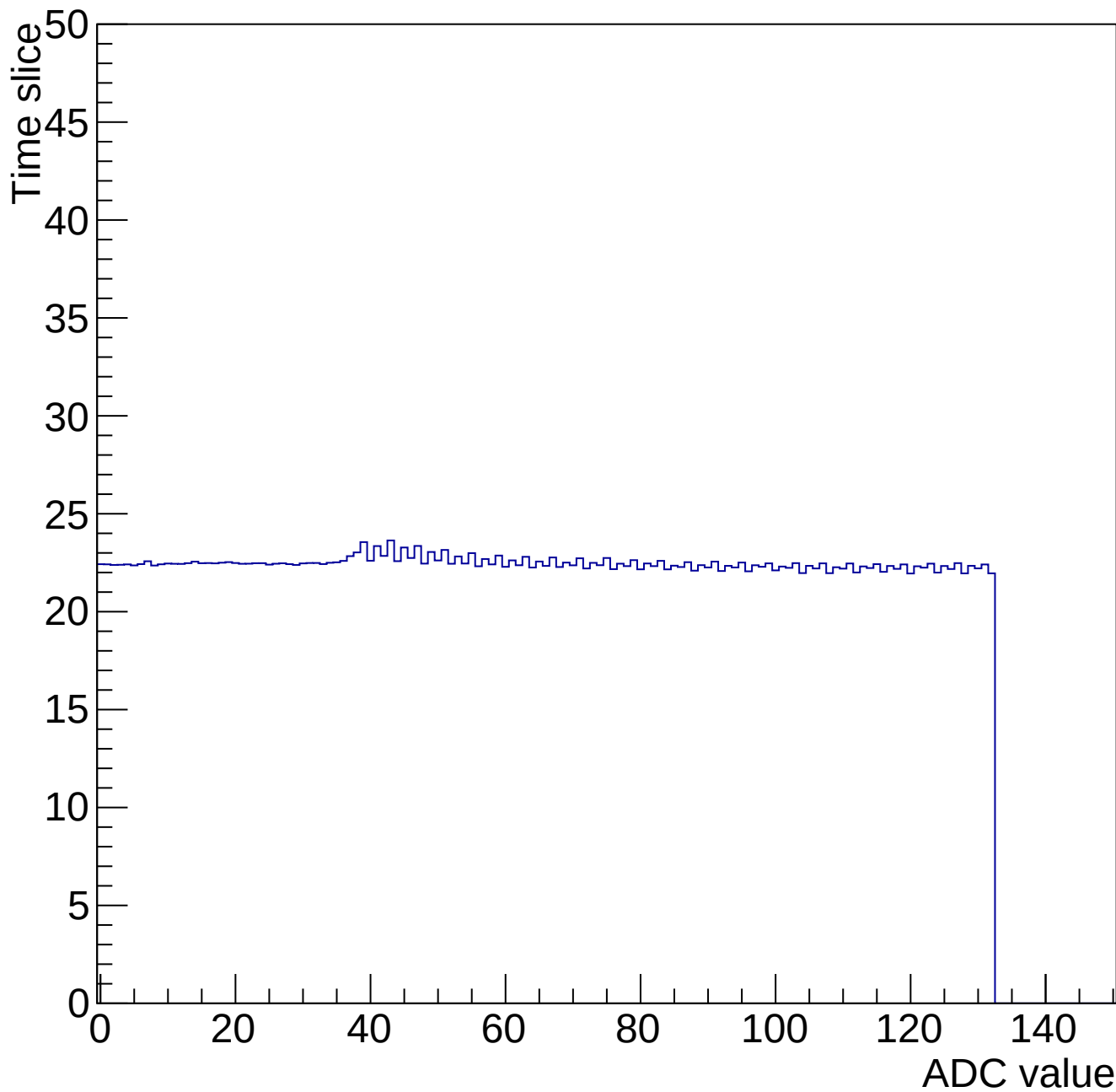


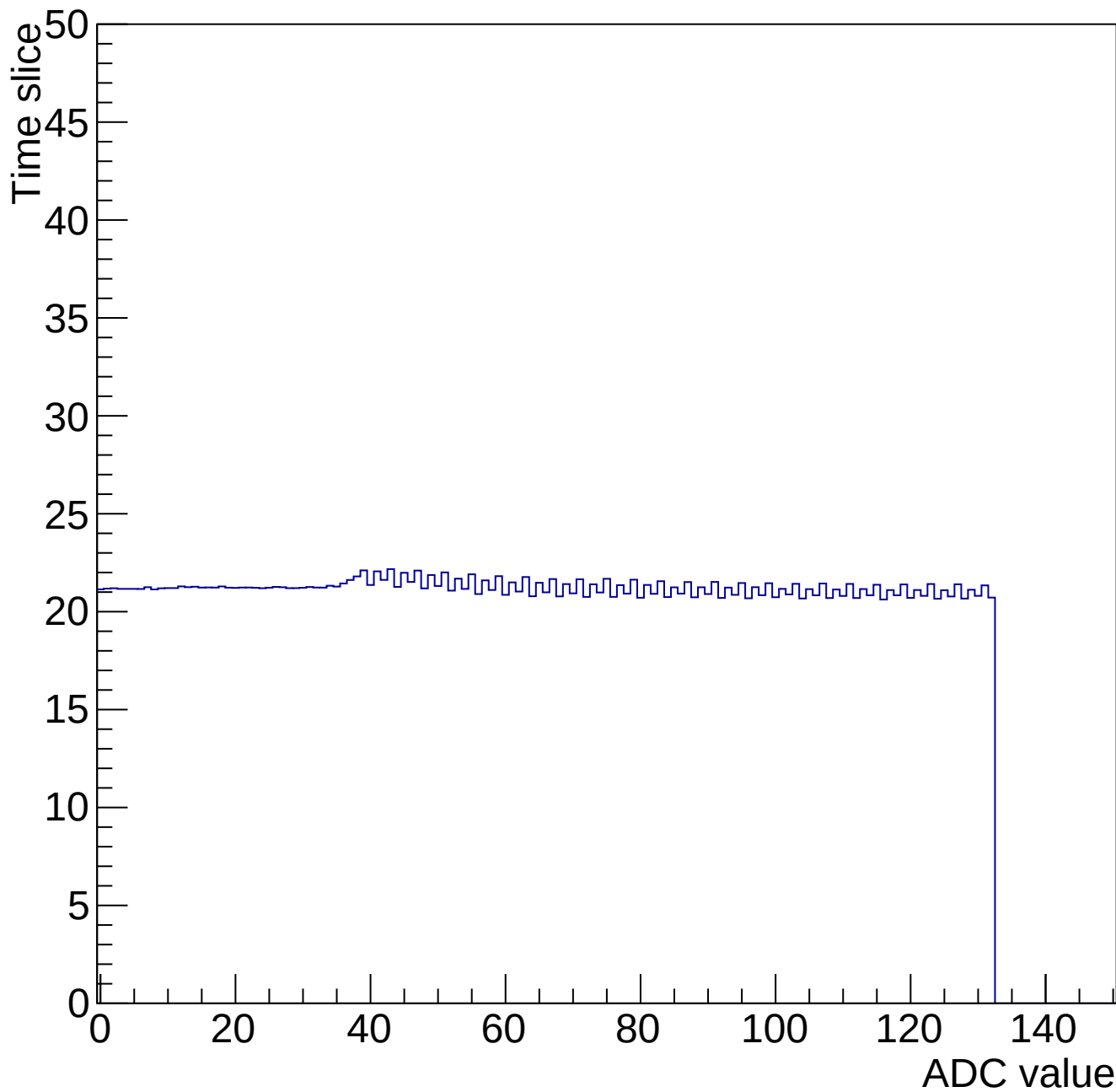
Time mcm 2005 chip 5 chan 0 34692-gain0-shaper0



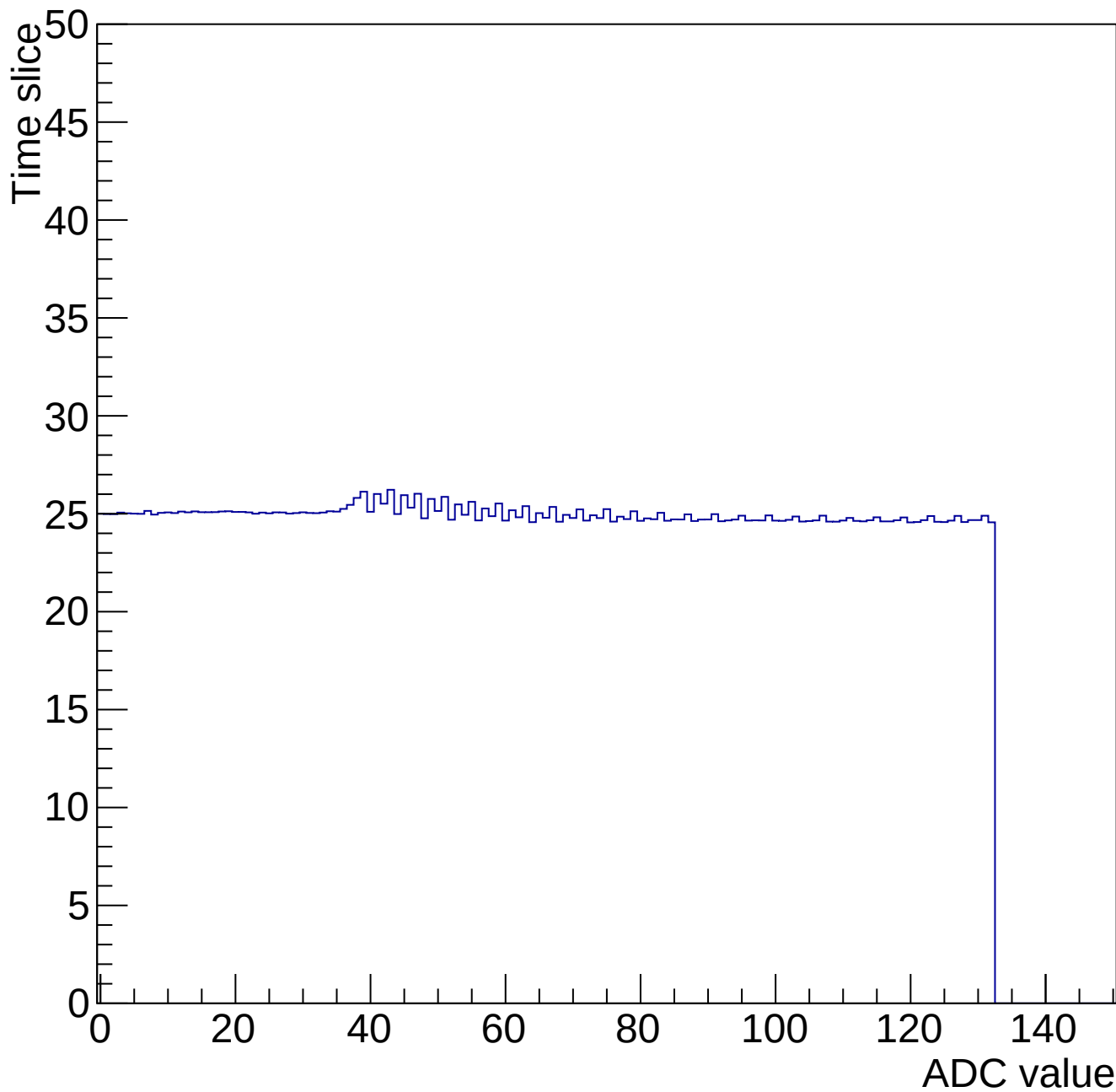
Time mcm 2005 chip 5 chan 1 34692-gain0-shaper0



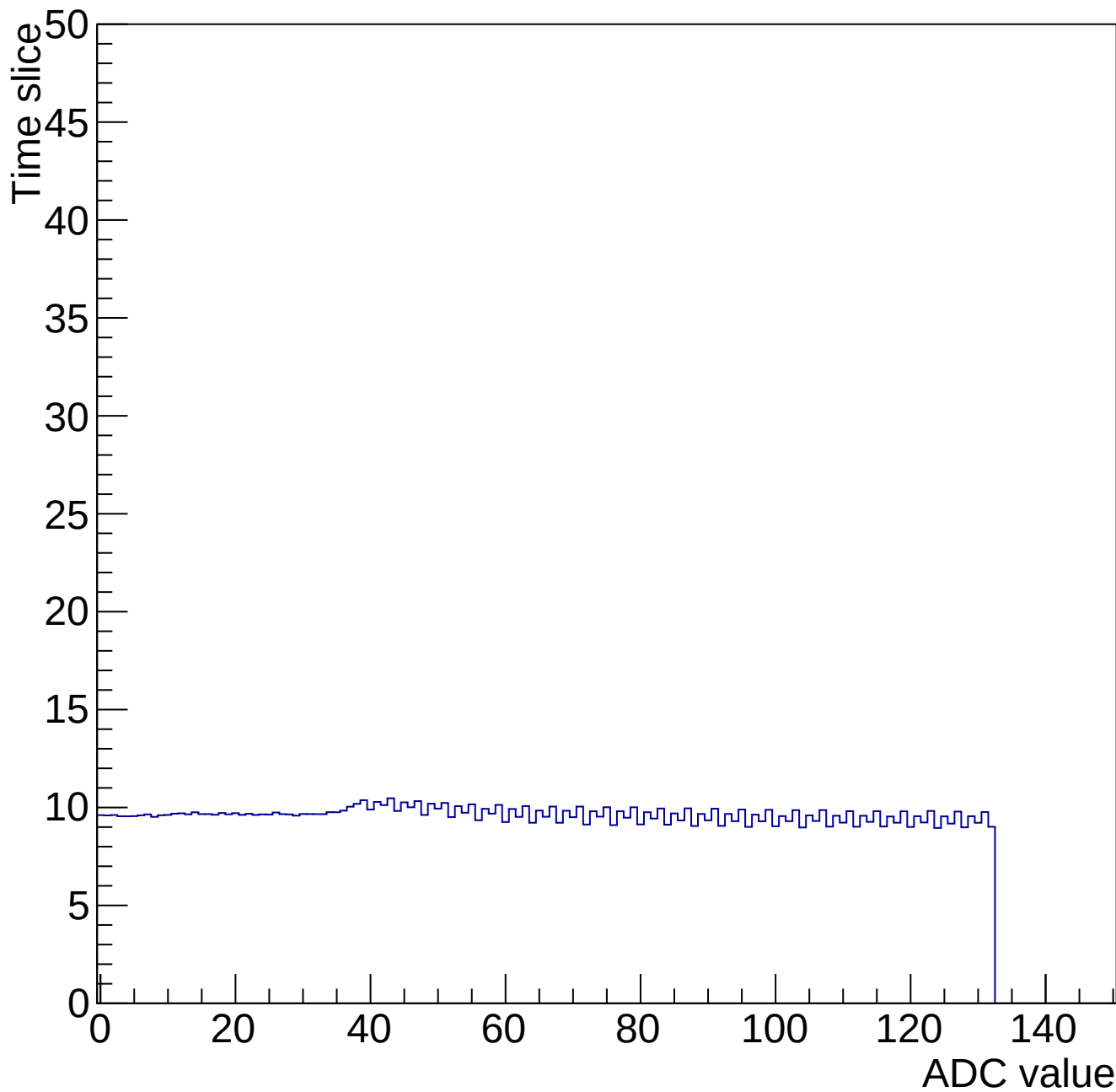
Time mcm 2005 chip 5 chan 2 34692-gain0-shaper0



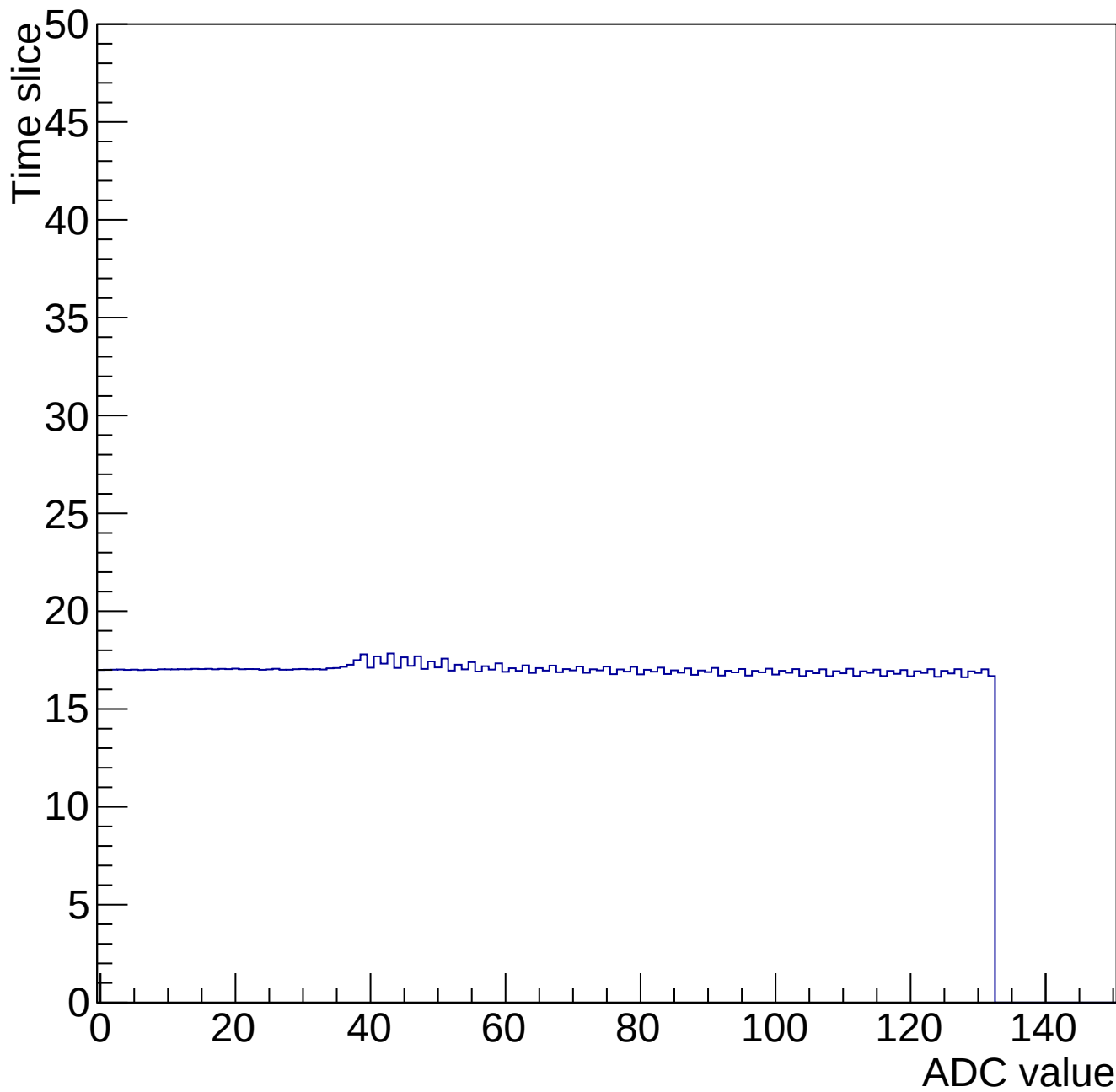
Time mcm 2005 chip 5 chan 3 34692-gain0-shaper0



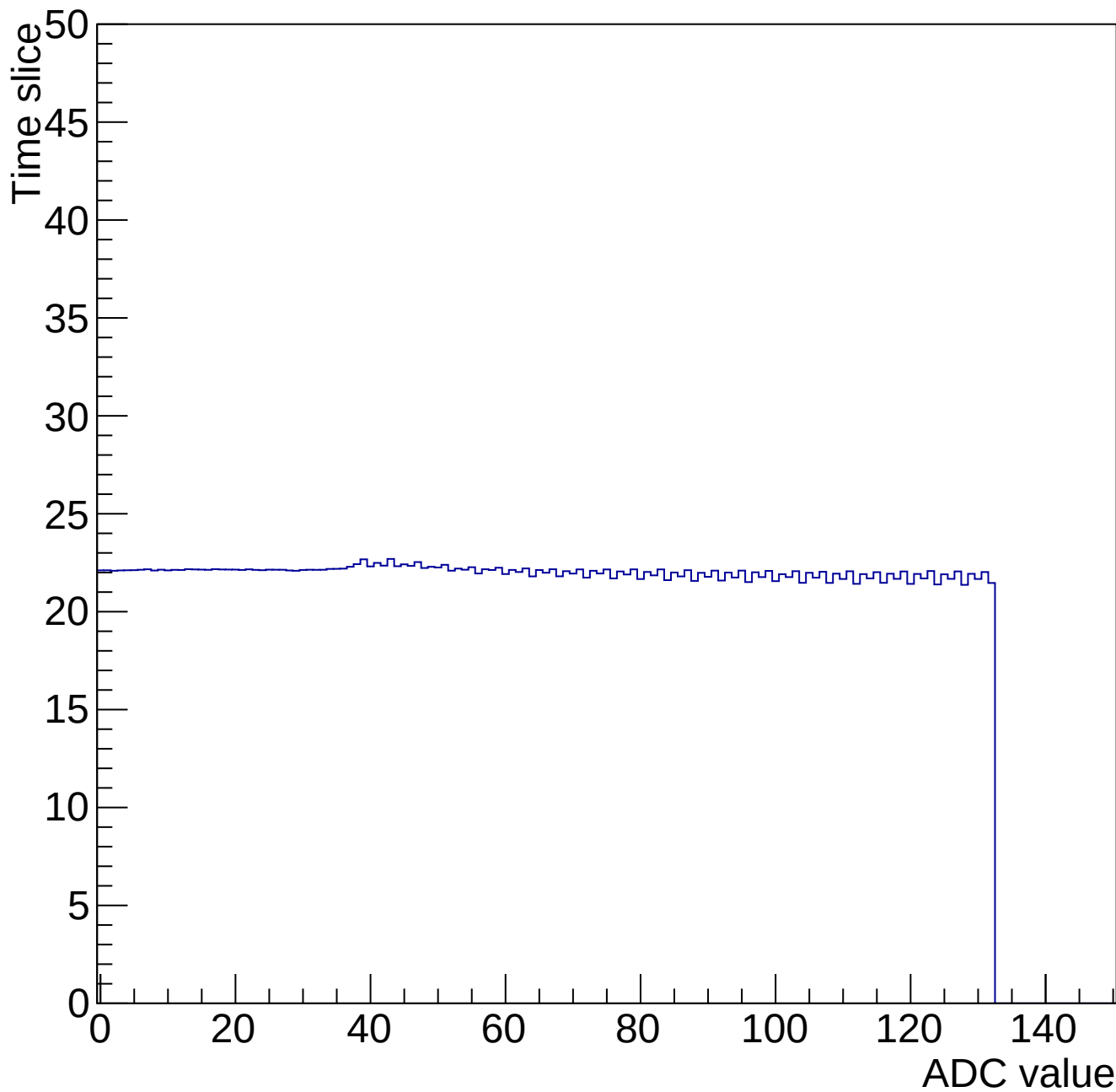
Time mcm 2005 chip 5 chan 4 34692-gain0-shaper0



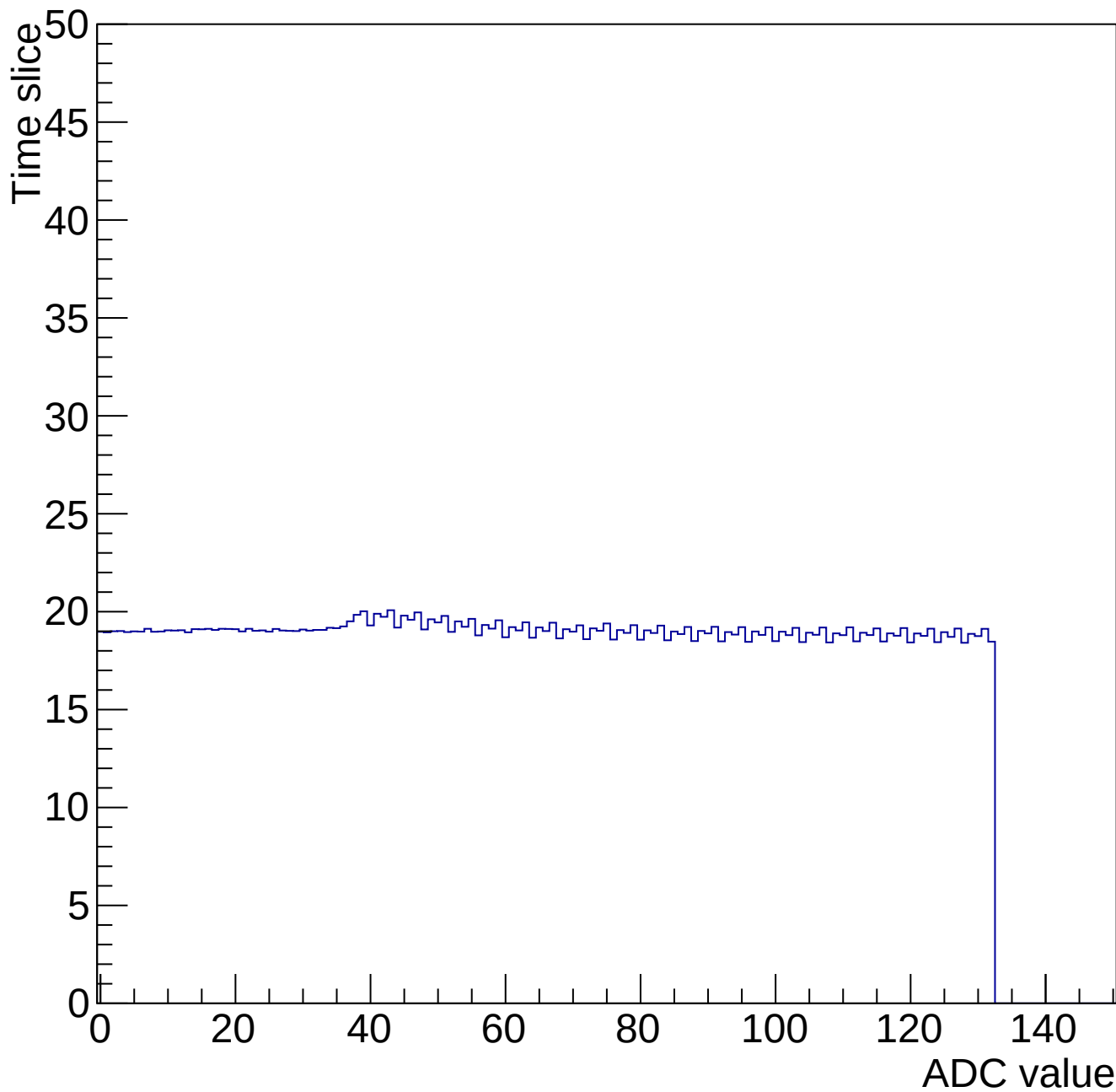
Time mcm 2005 chip 5 chan 5 34692-gain0-shaper0



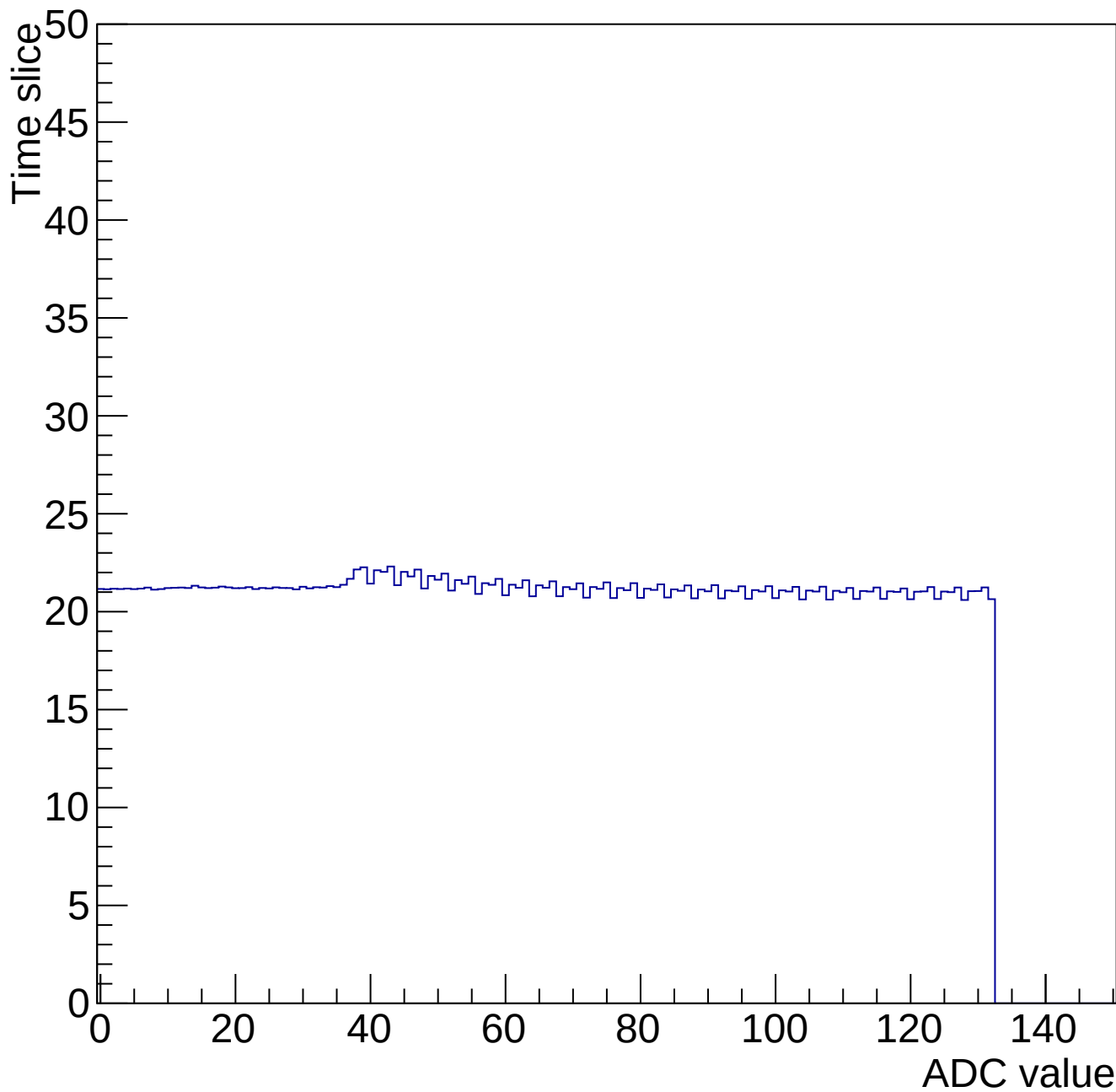
Time mcm 2005 chip 5 chan 6 34692-gain0-shaper0



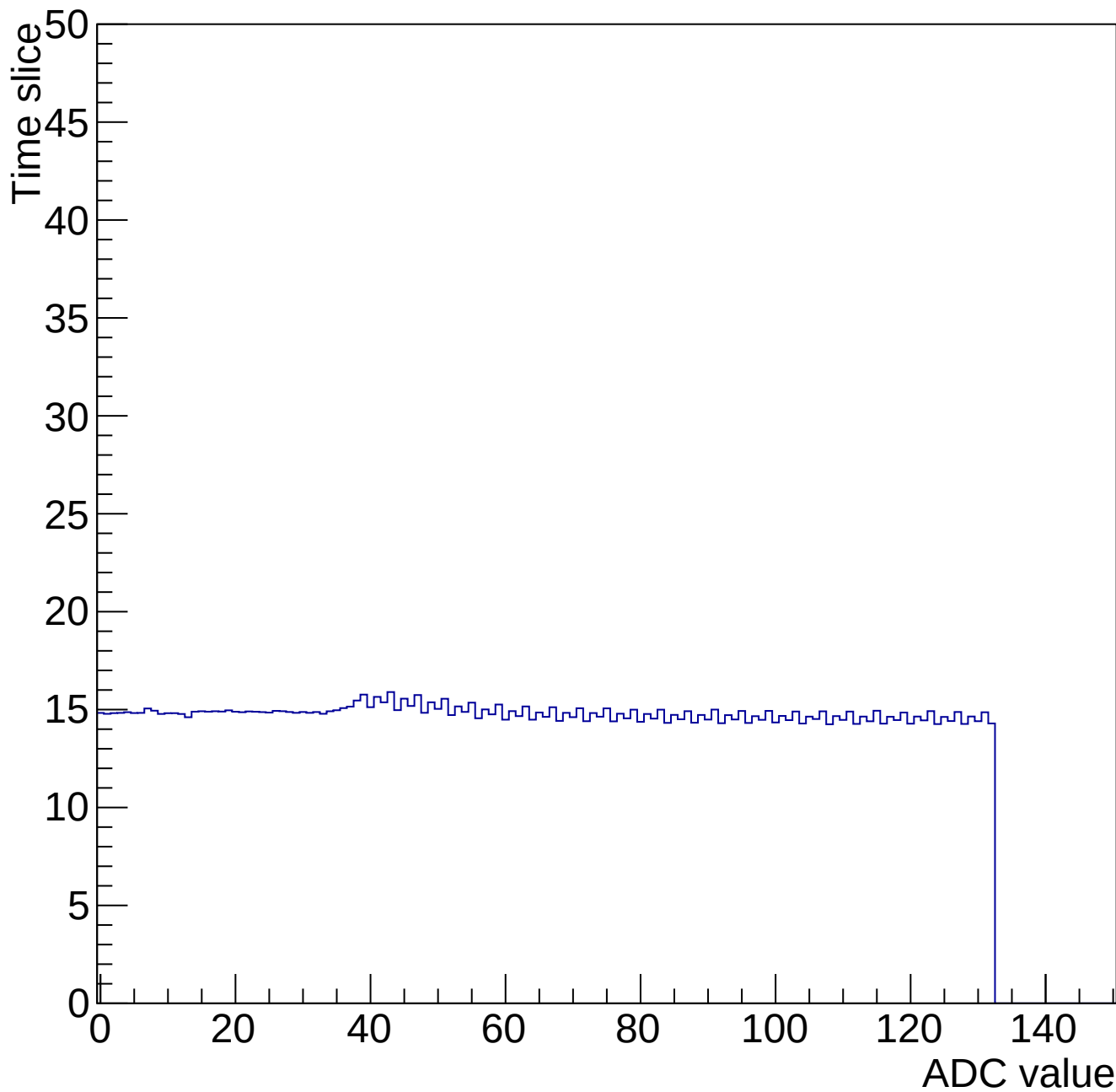
Time mcm 2005 chip 5 chan 7 34692-gain0-shaper0



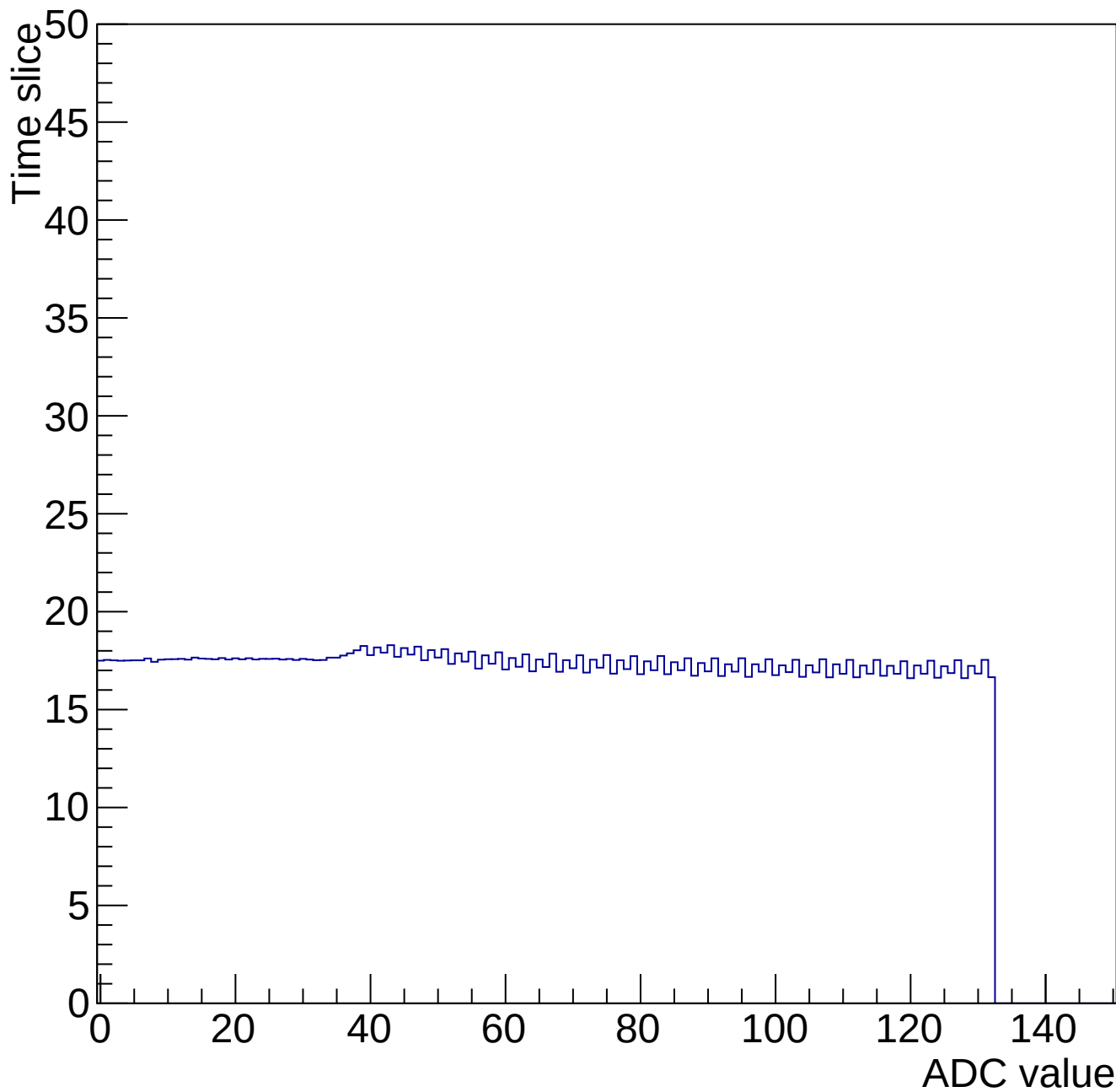
Time mcm 2005 chip 5 chan 8 34692-gain0-shaper0



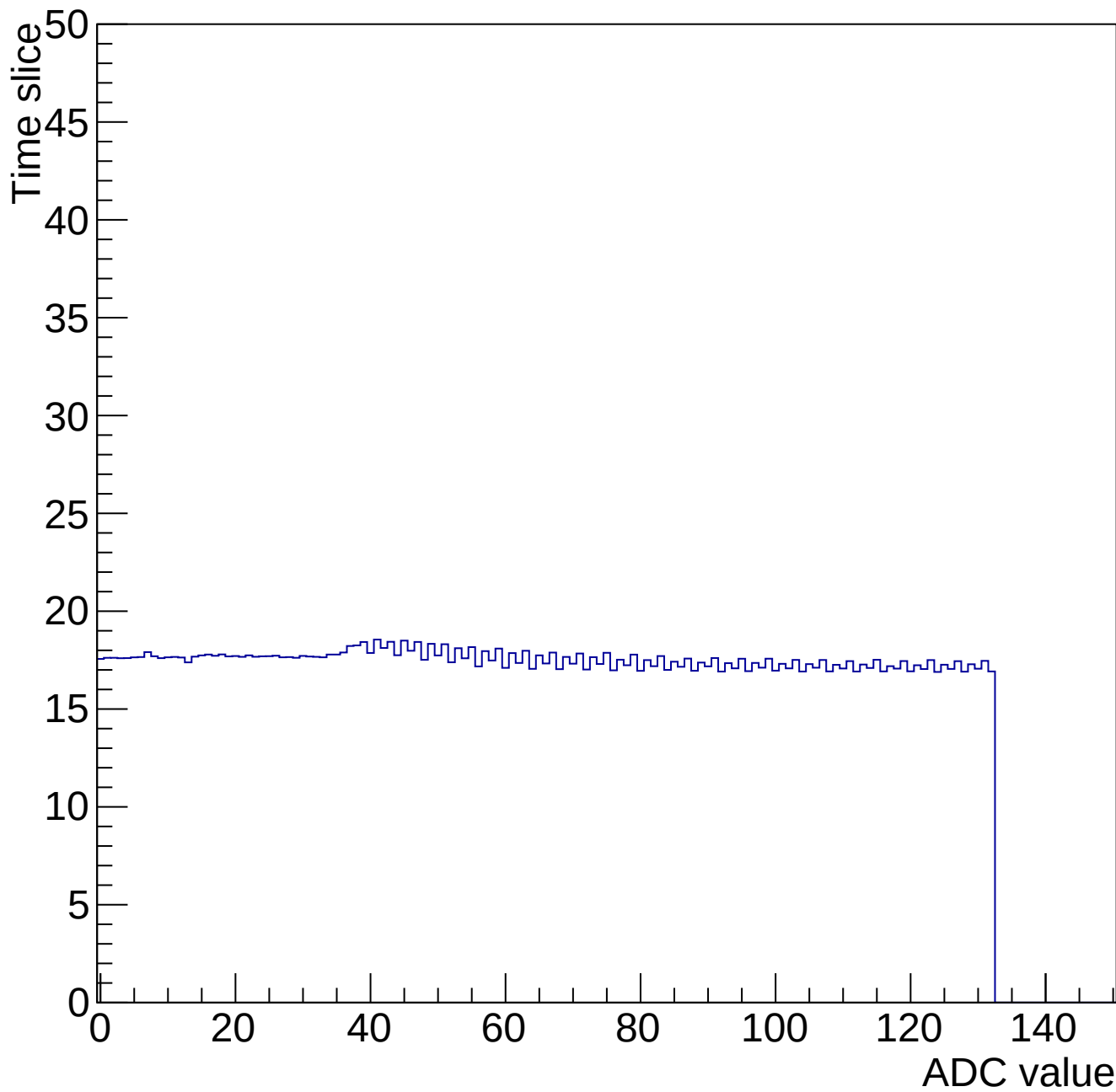
Time mcm 2005 chip 5 chan 9 34692-gain0-shaper0



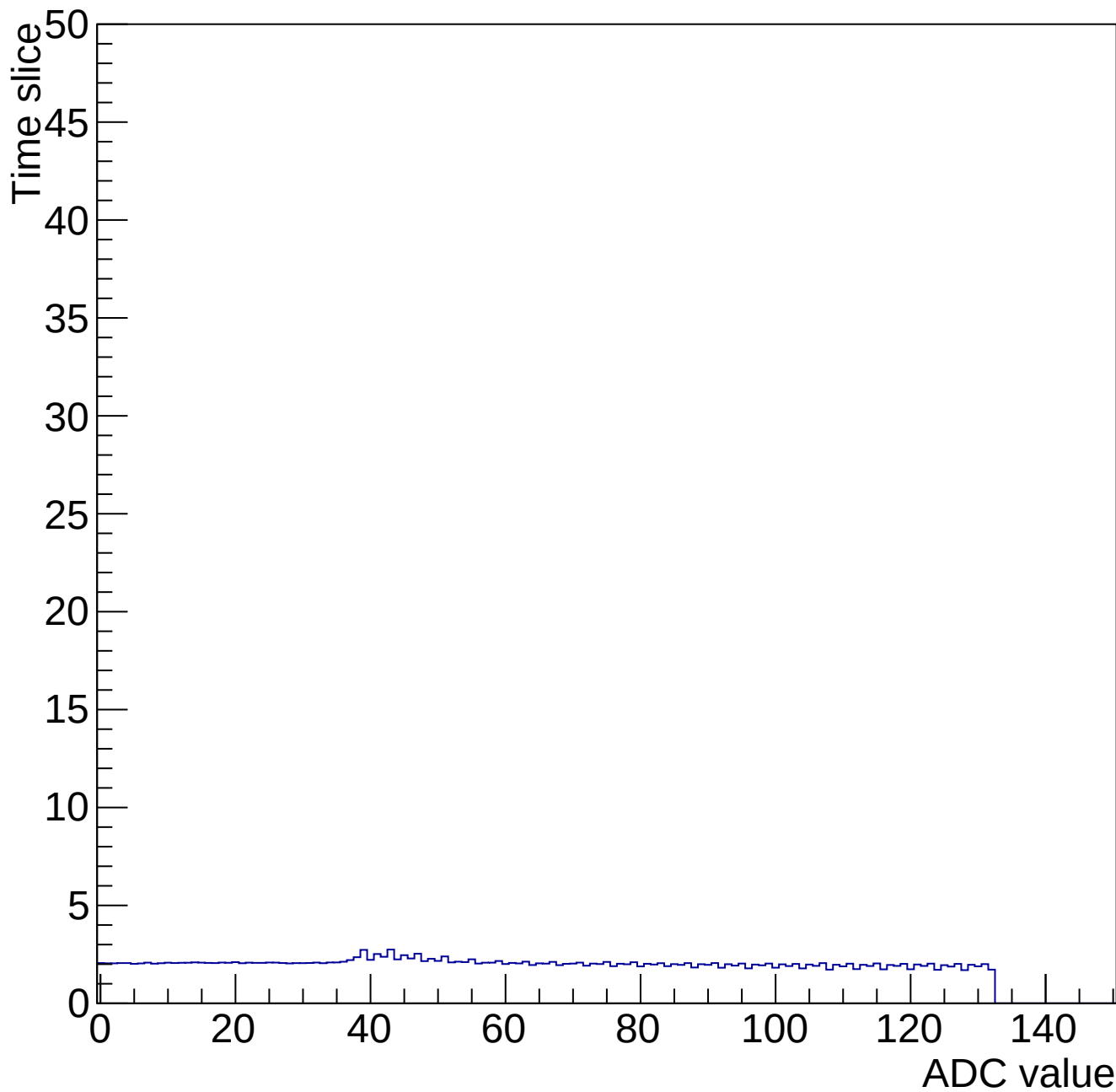
Time mcm 2005 chip 5 chan 10 34692-gain0-shaper0



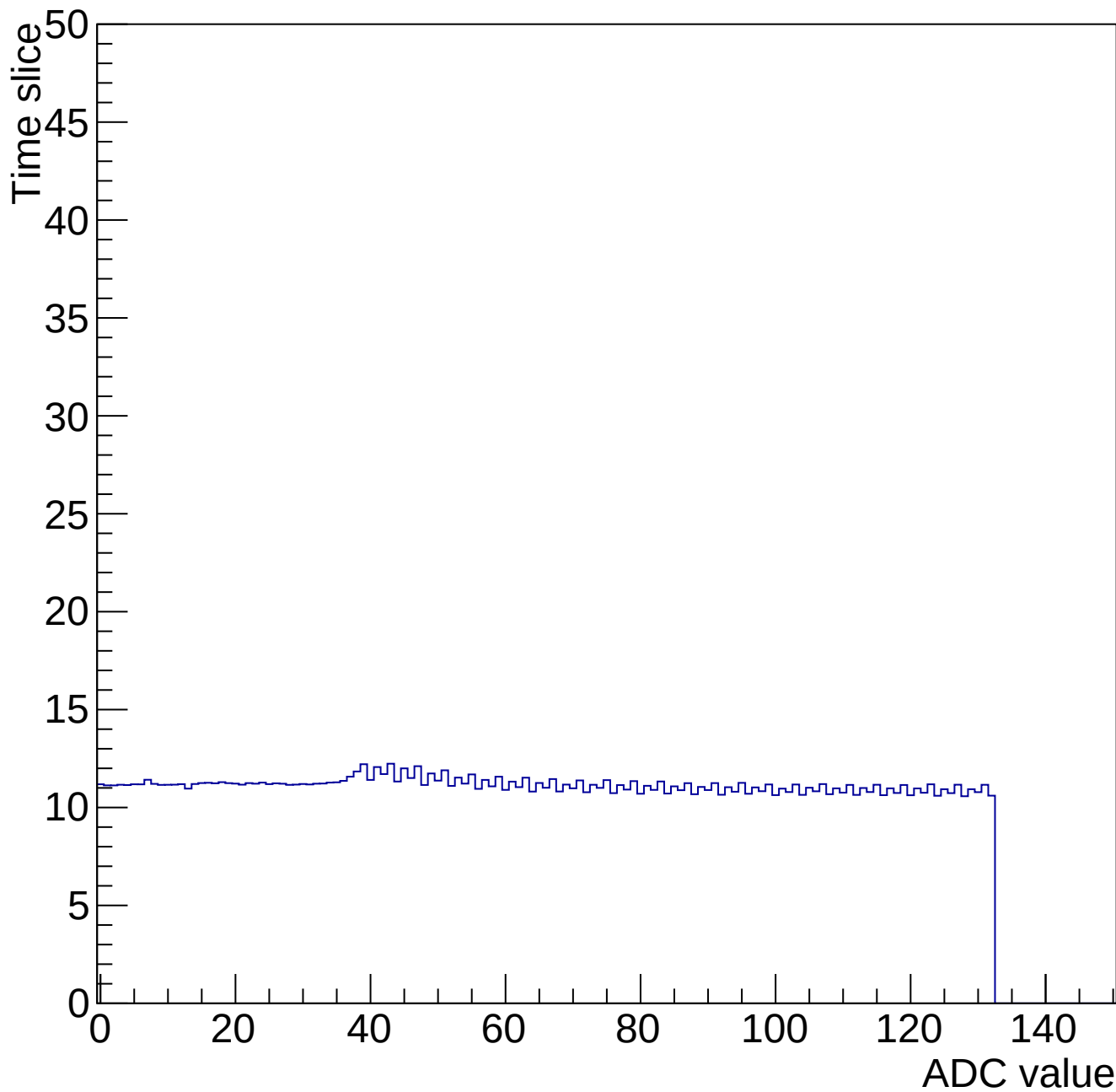
Time mcm 2005 chip 5 chan 11 34692-gain0-shaper0



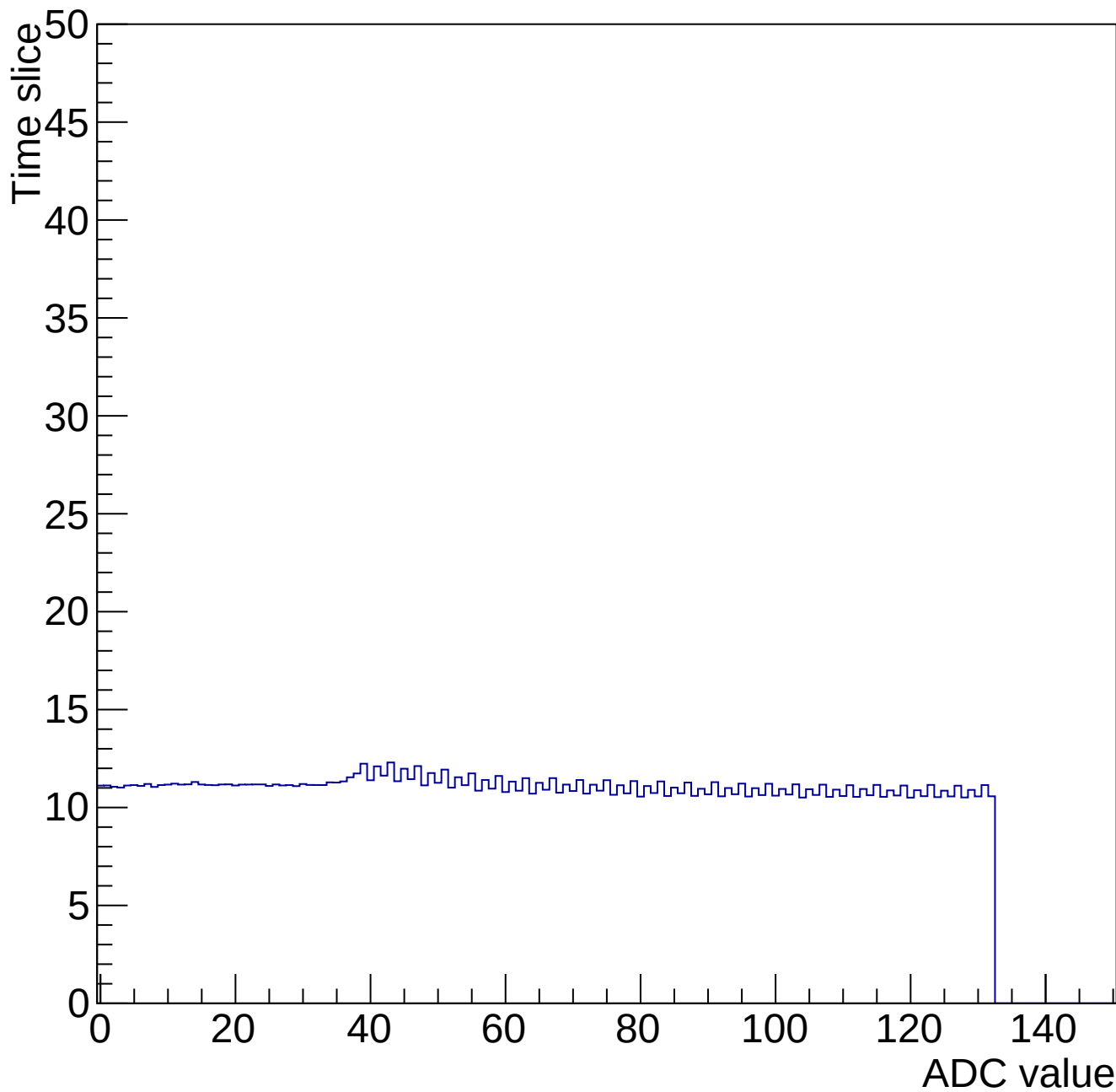
Time mcm 2005 chip 5 chan 12 34692-gain0-shaper0



Time mcm 2005 chip 5 chan 13 34692-gain0-shaper0



Time mcm 2005 chip 5 chan 14 34692-gain0-shaper0



Time mcm 2005 chip 5 chan 15 34692-gain0-shaper0

