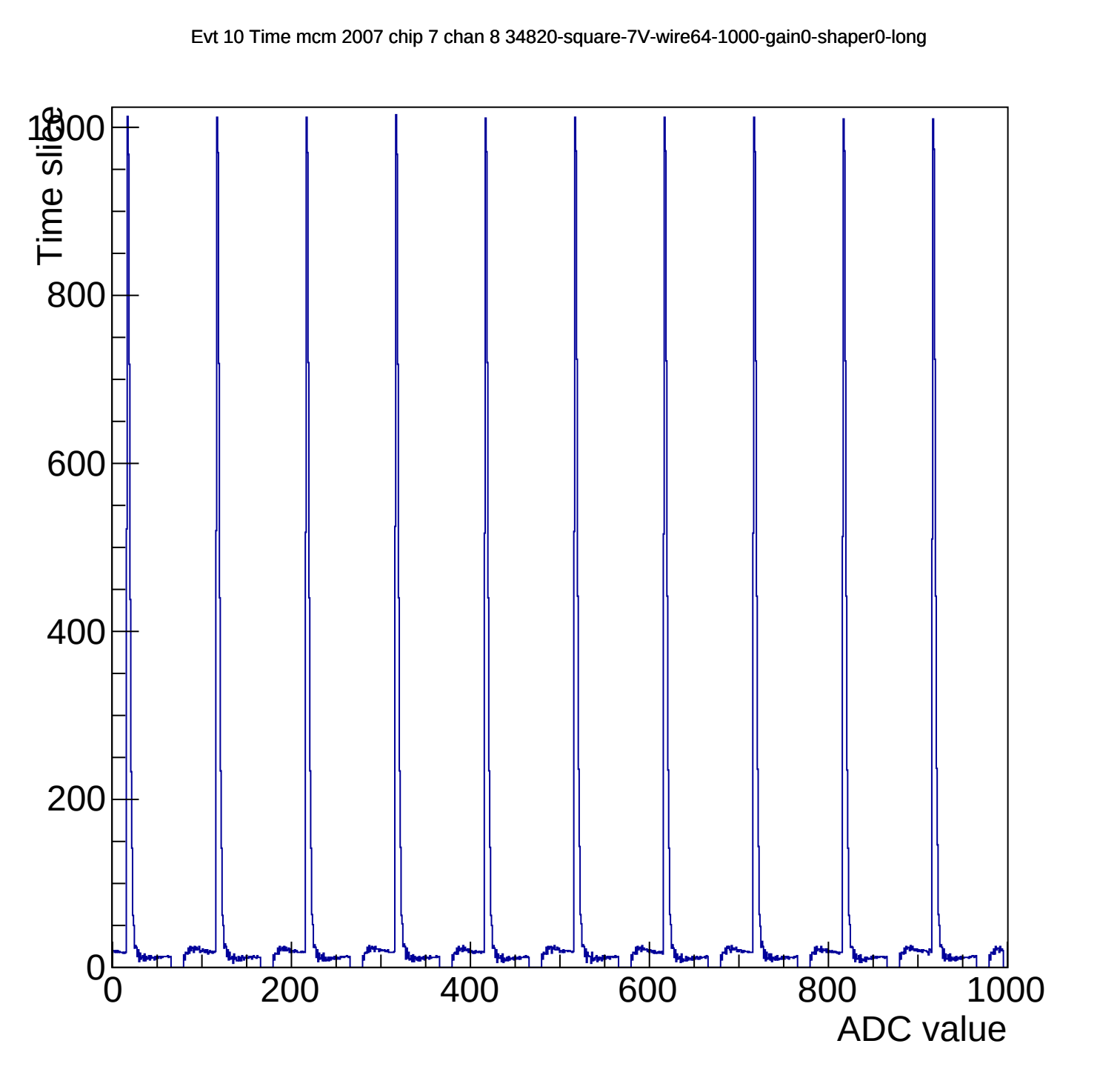
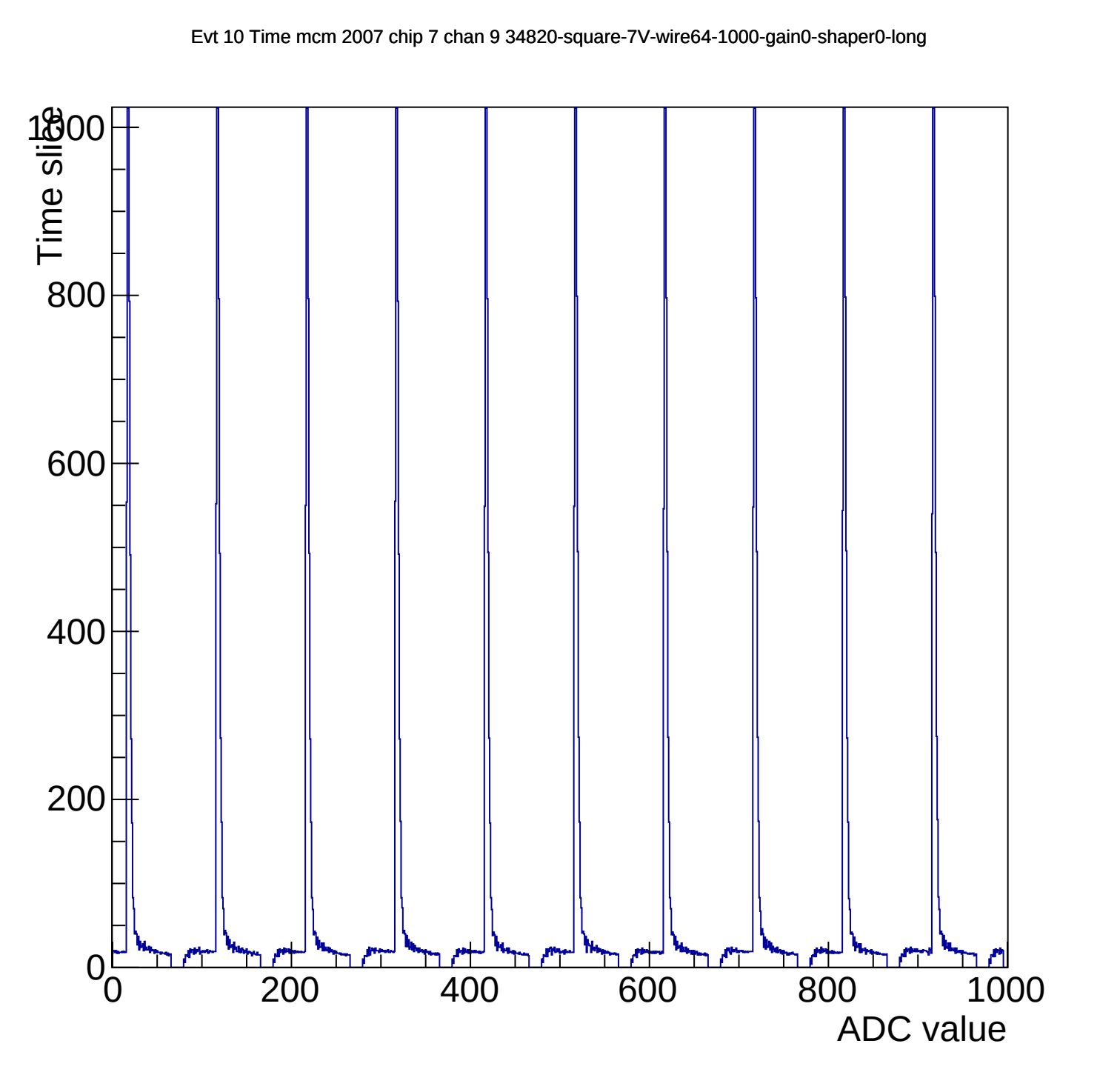
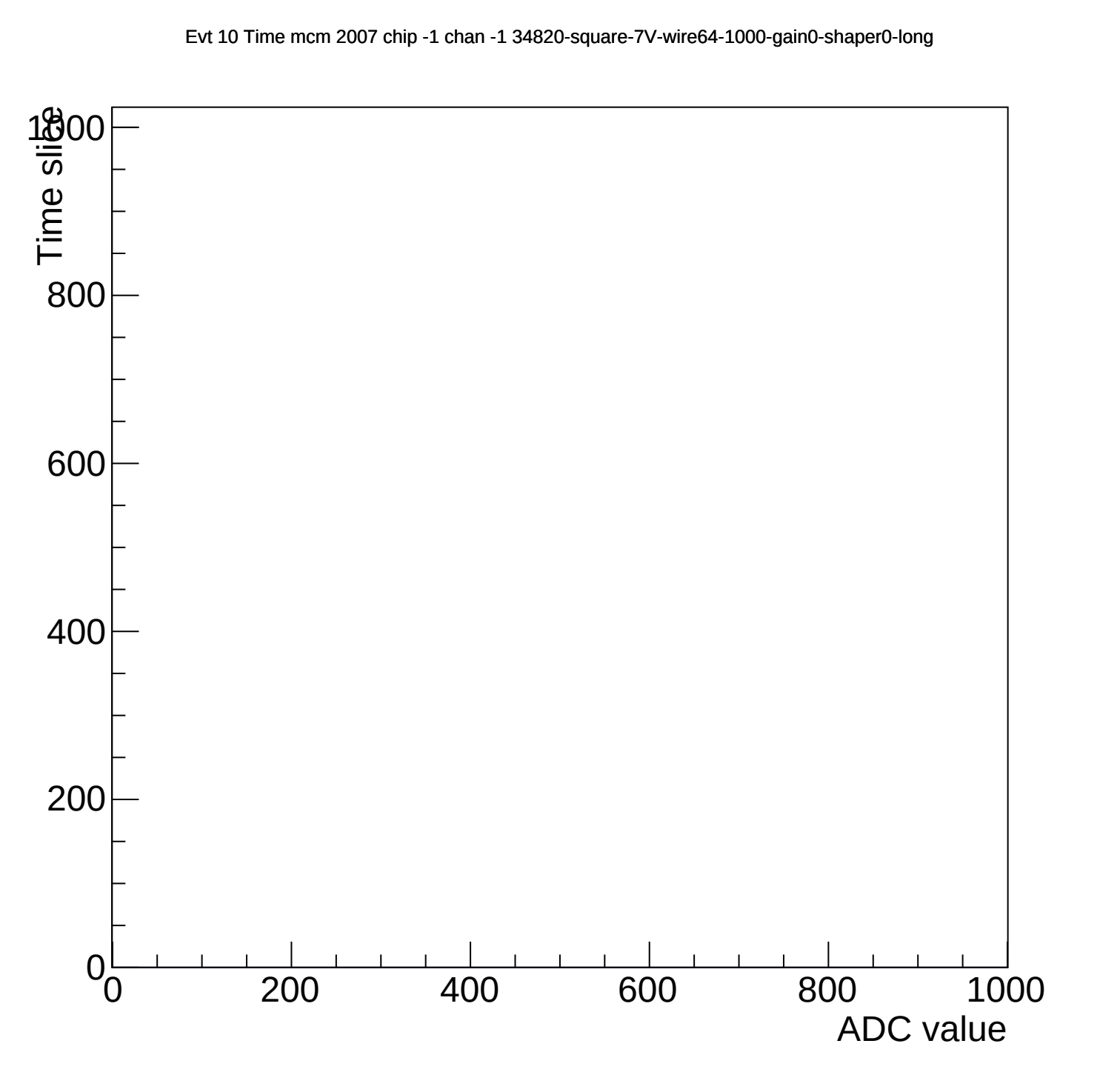
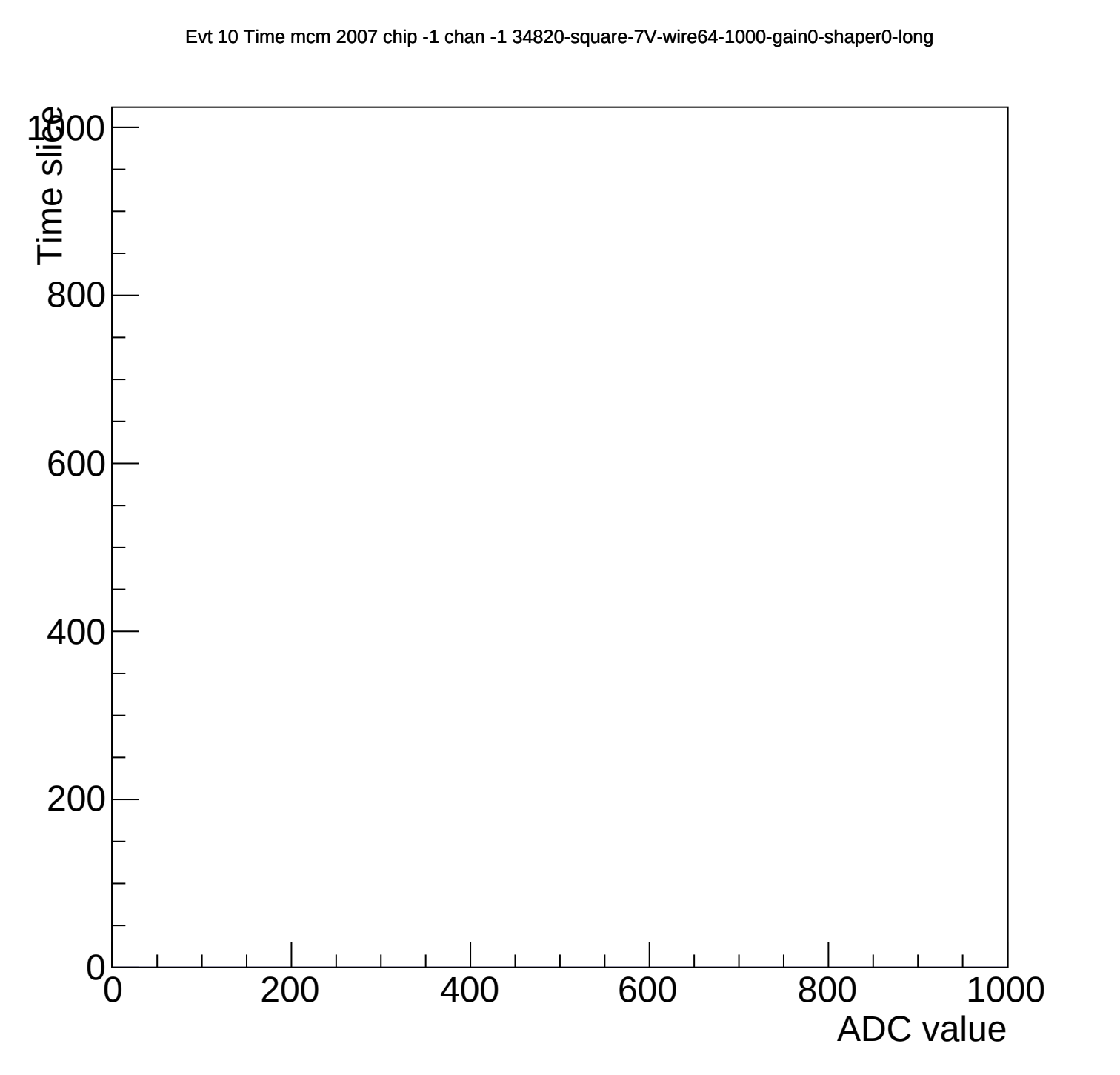
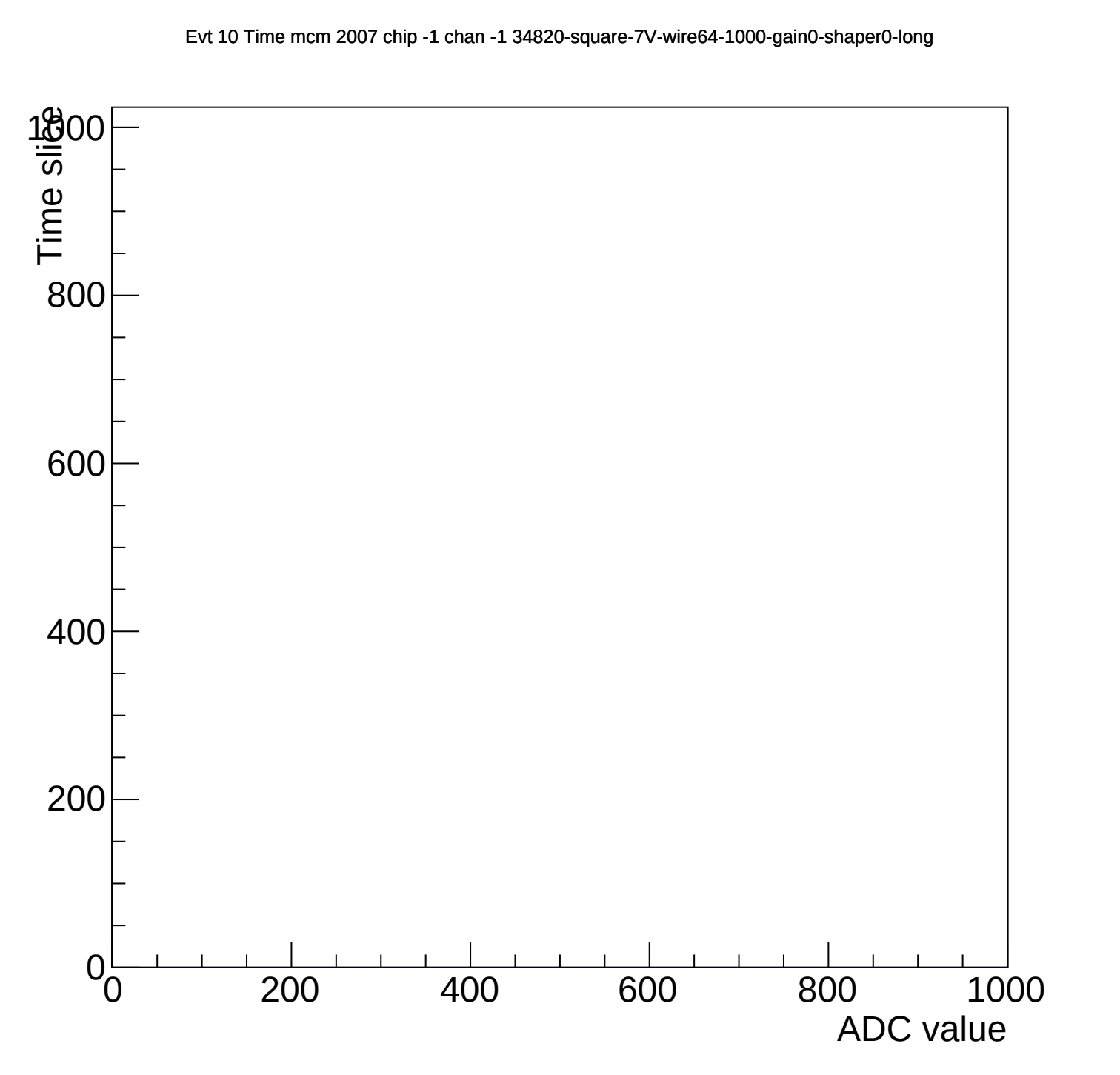
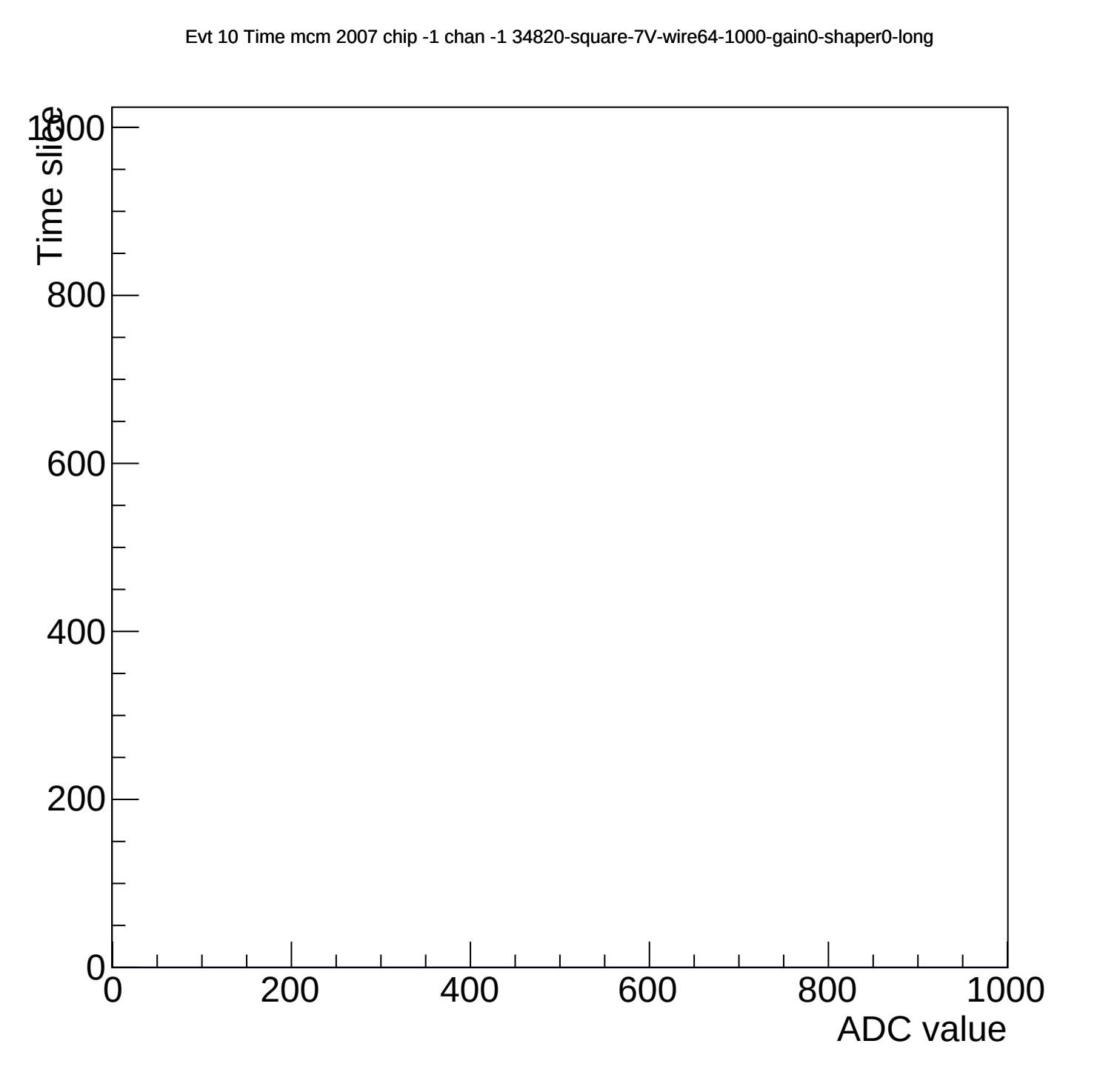

Evt 10 Time mcm 2007 chip 7 chan 8 34820-square-7V-wire64-1000-gain0-shaper0-long
Time slice
1000
800
600
400
200
0
0
200
400
600
800
1000
ADC value

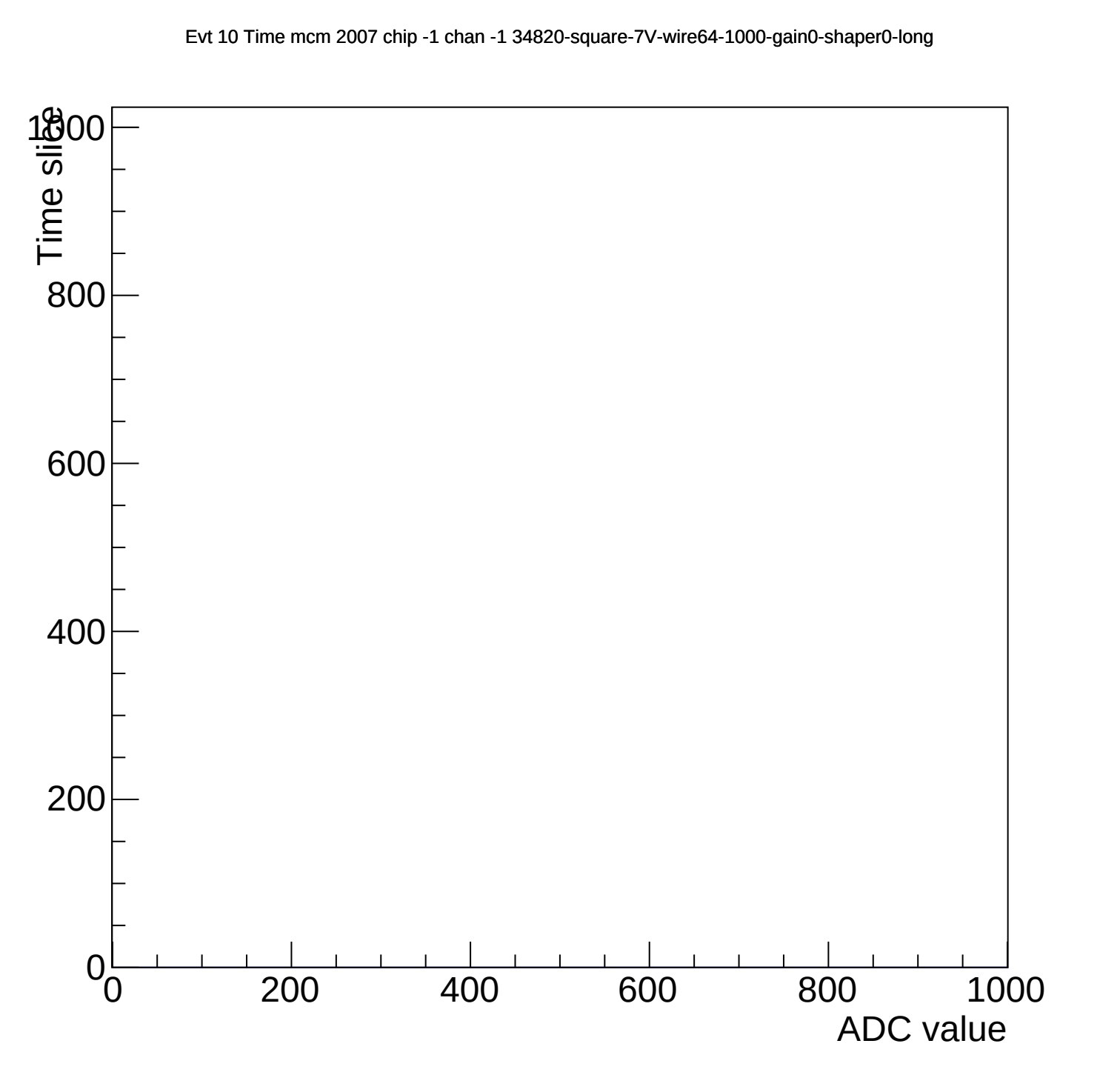

Evt 10 Time mcm 2007 chip 7 chan 9 34820-square-7V-wire64-1000-gain0-shaper0-long
Time slice
0
200
400
600
800
1000
ADC value

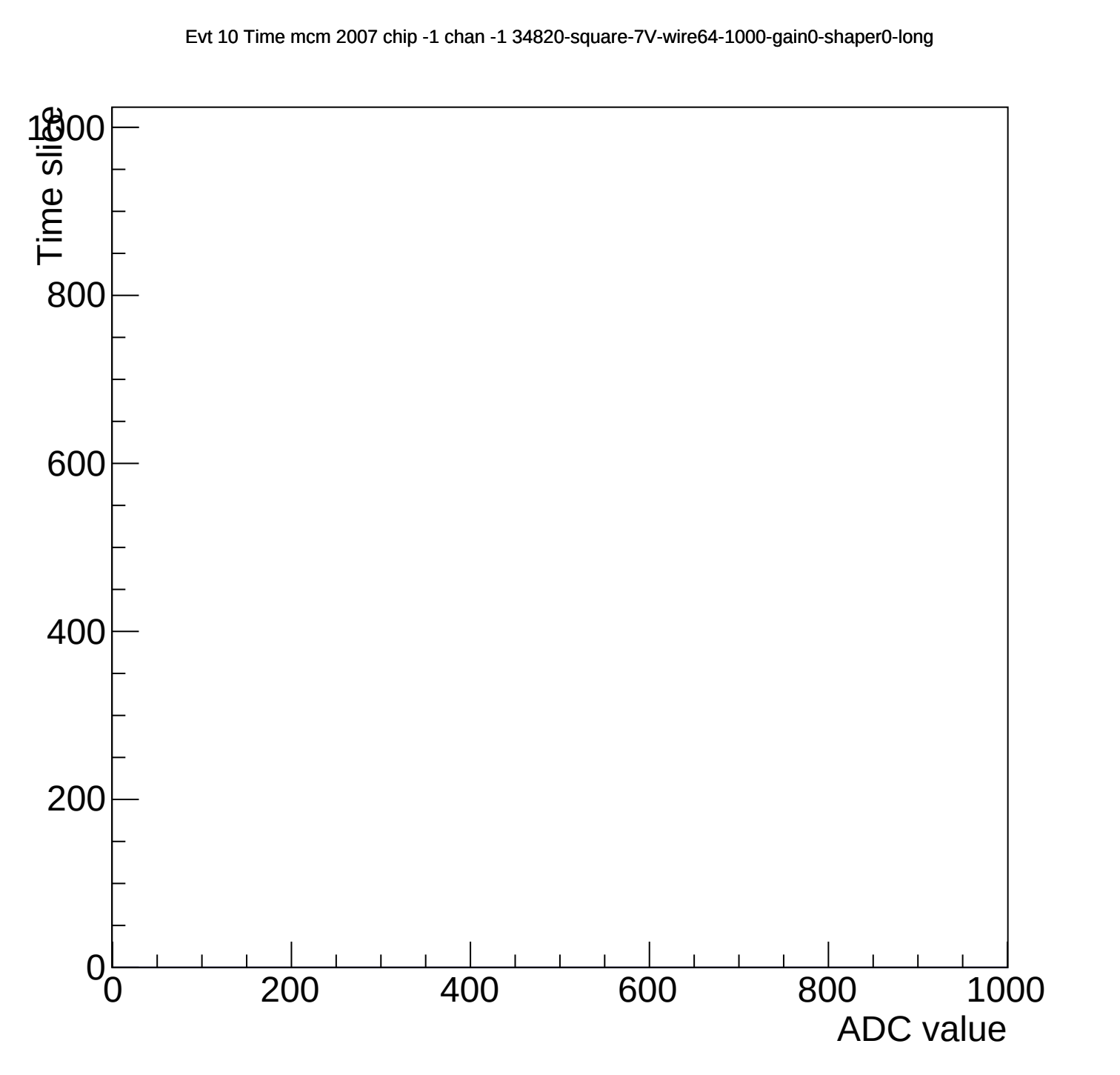

Evt 10 Time mcm 2007 chip -1 chan -1 34820-square-7V-wire64-1000-gain0-shaper0-long
Time slice
1000
800
600
400
200
0
0
200
400
600
800
1000
ADC value

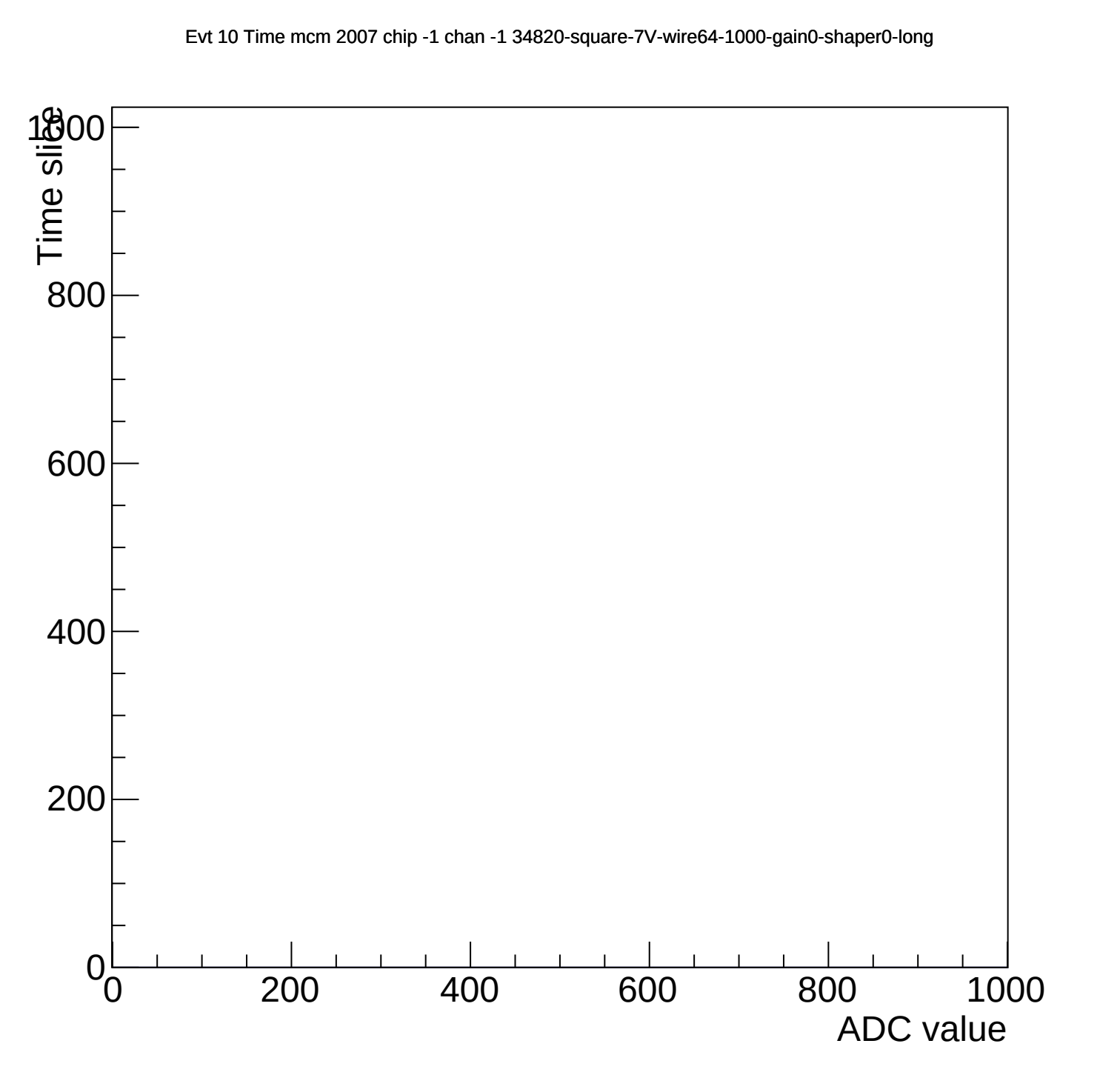

Evt 10 Time mcm 2007 chip -1 chan -1 34820-square-7V-wire64-1000-gain0-shaper0-long
Time slice
1000
800
600
400
200
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200
400
600
800
1000
ADC value

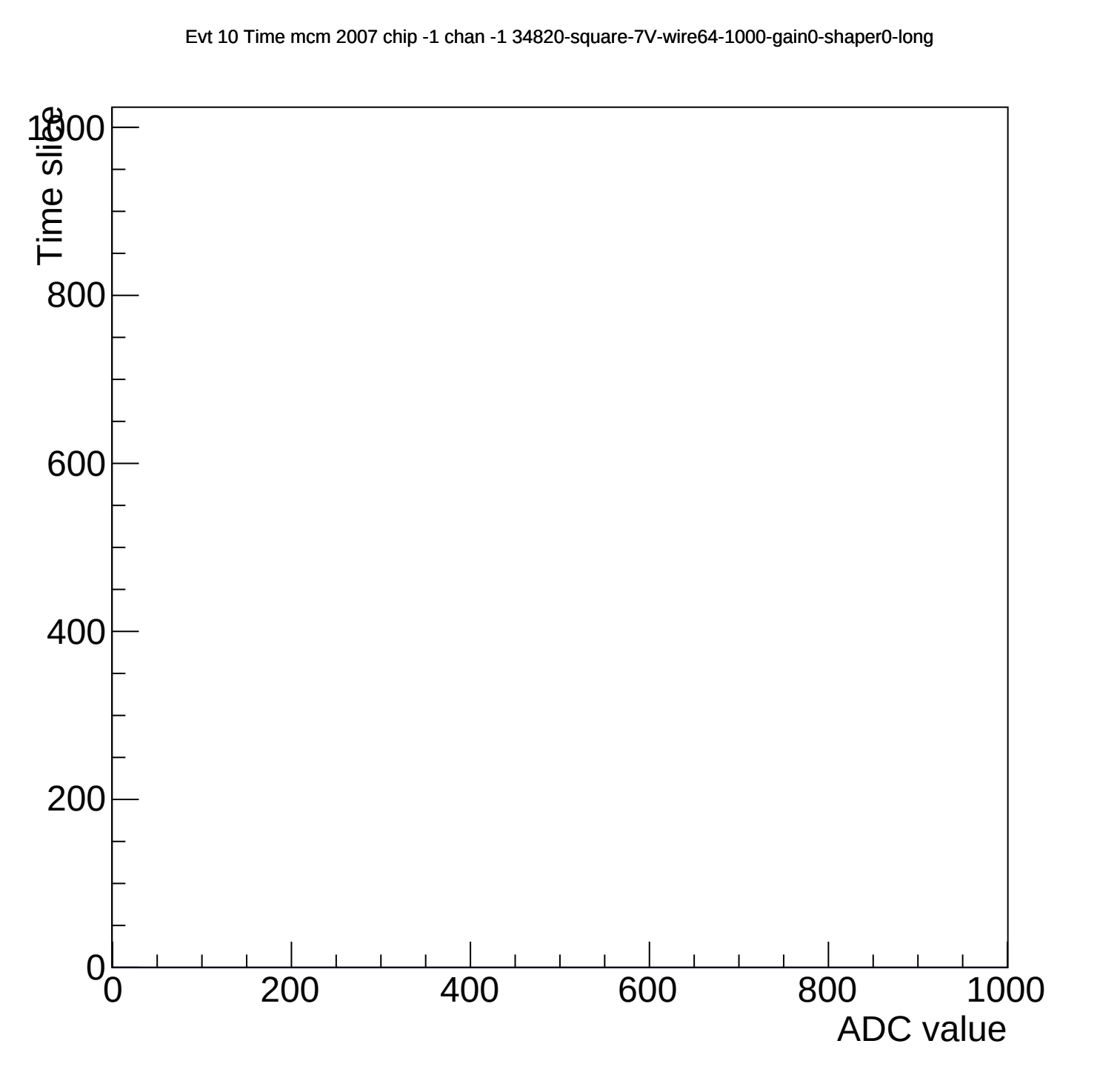

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Time slice
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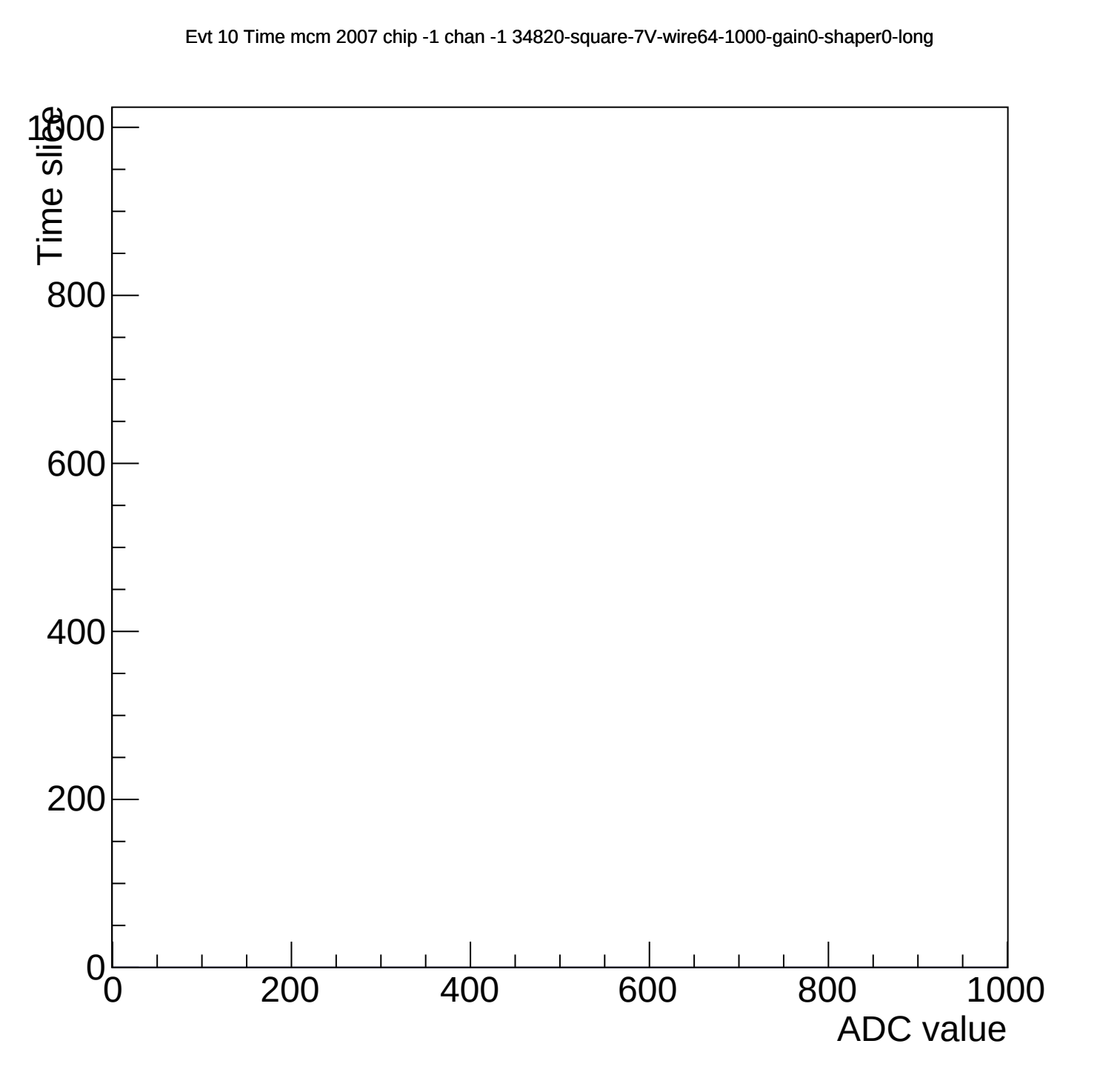

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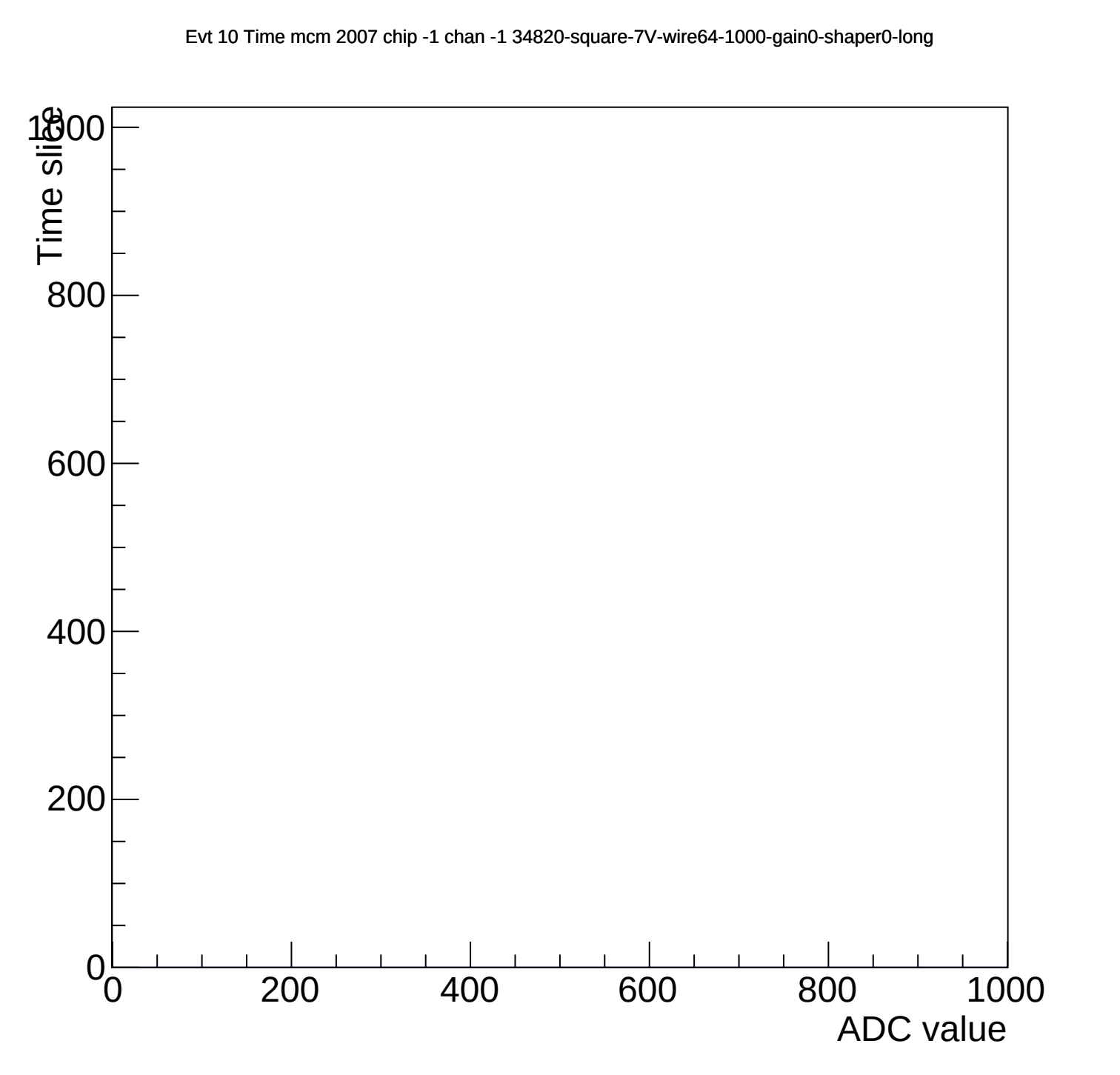

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Time slice
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ADC value

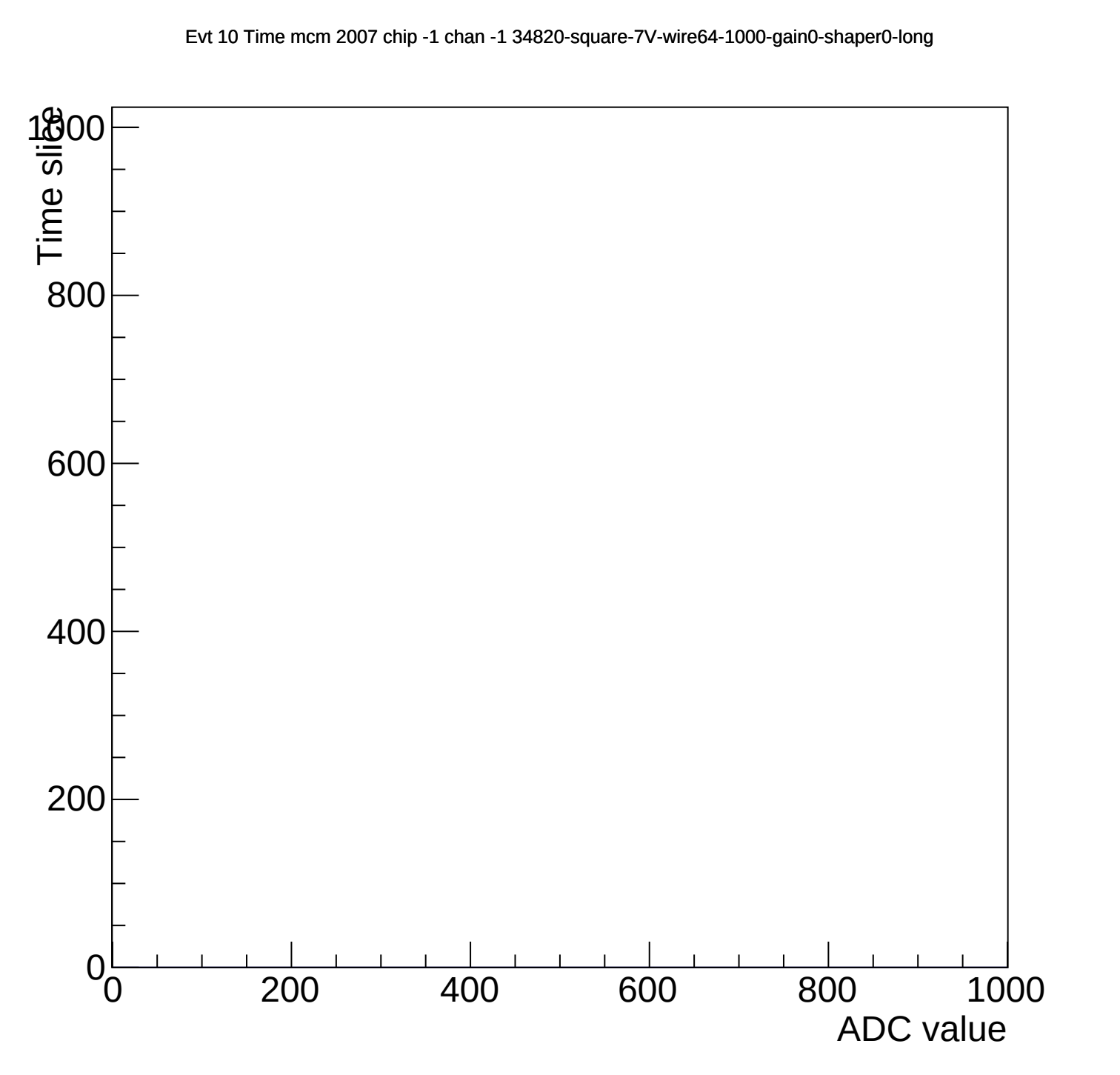

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Time slice
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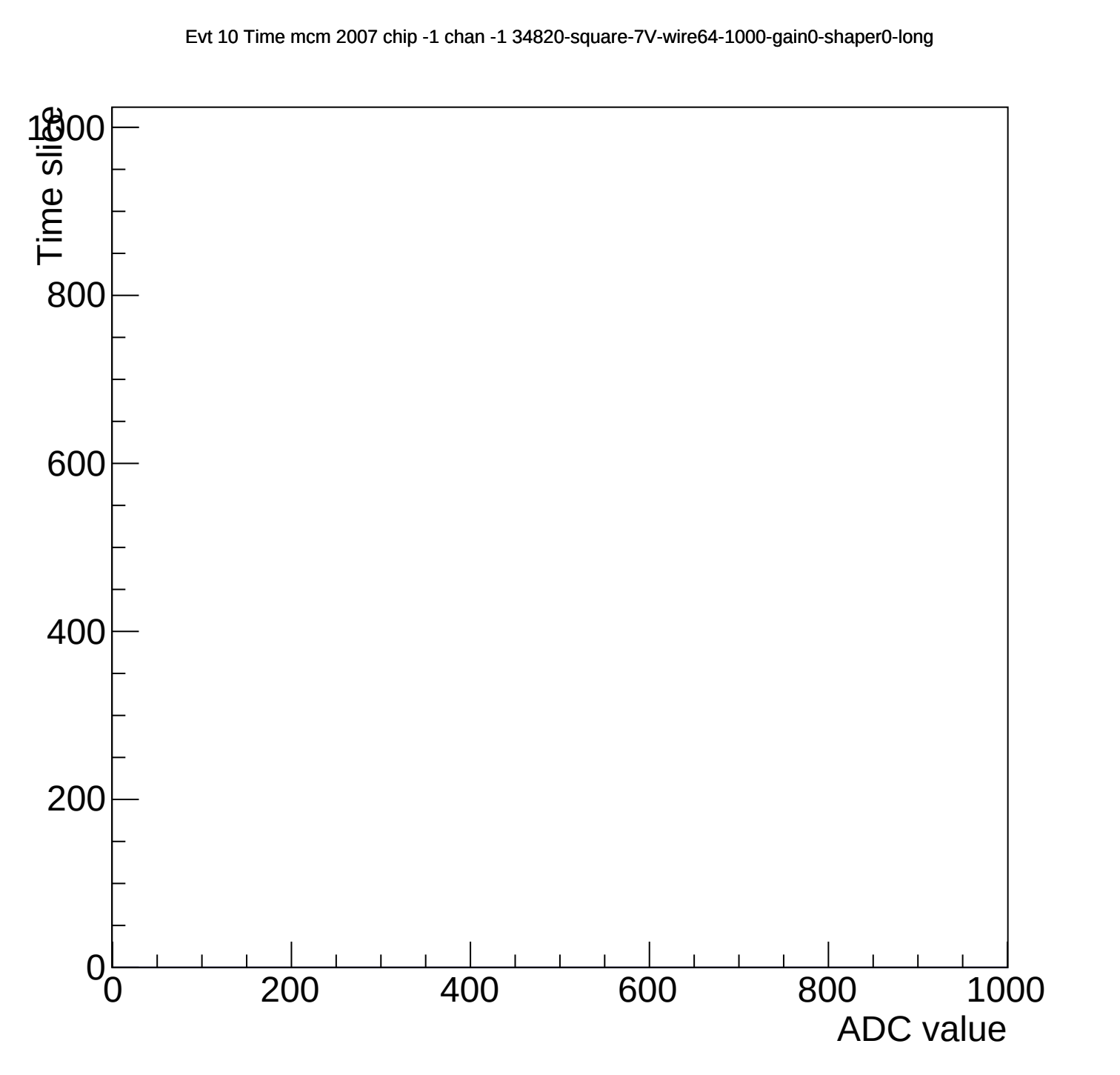

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Time slice
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ADC value

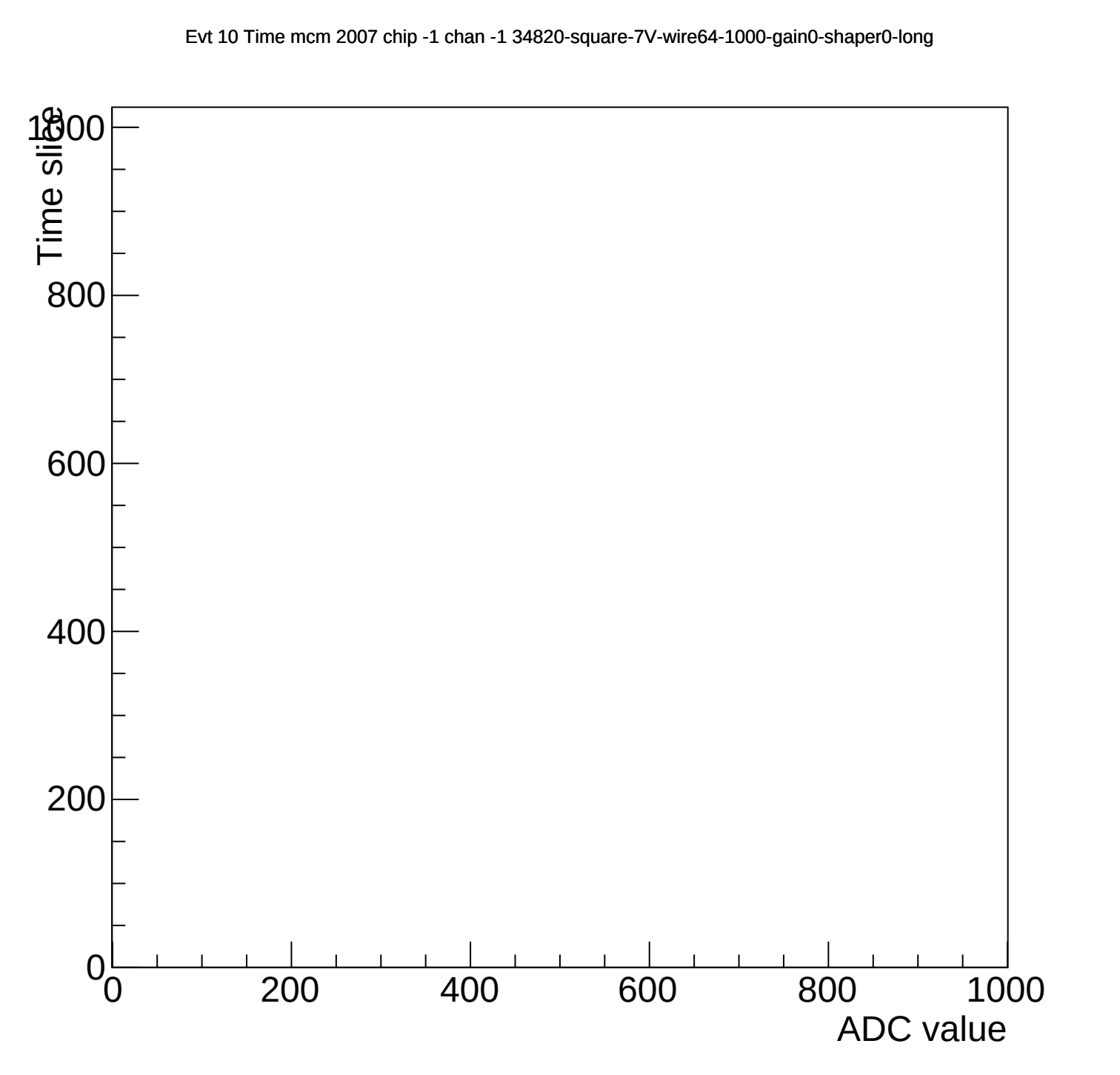

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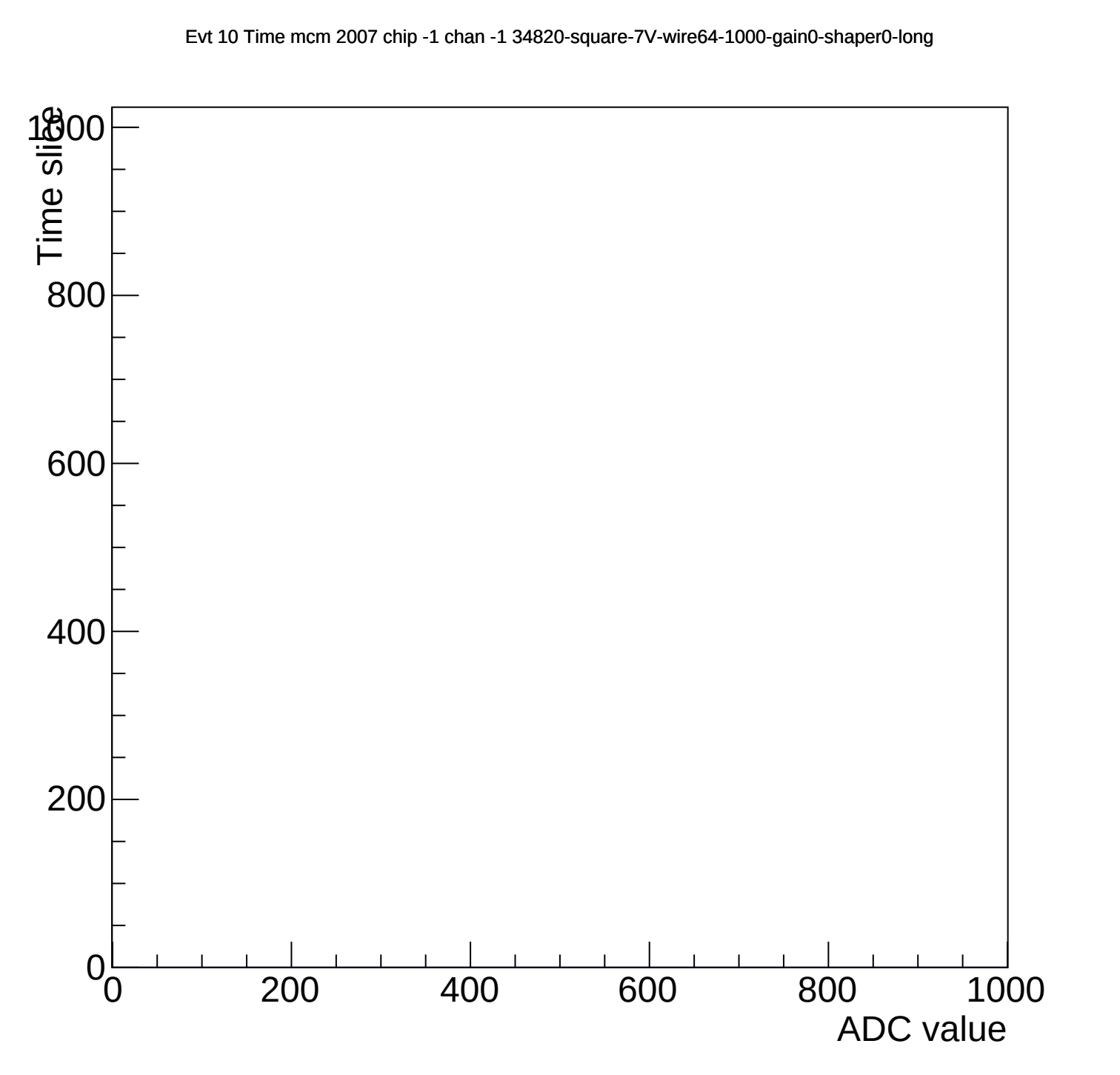

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