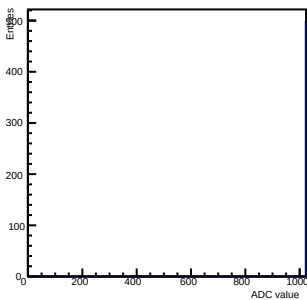
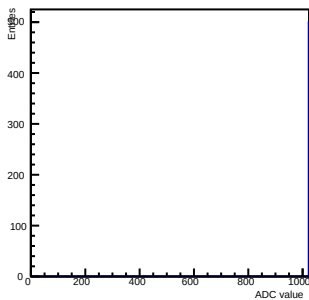


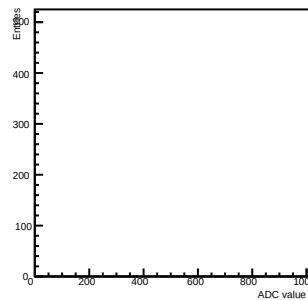
Run 35044 chip 7 Maxadc value ch 0



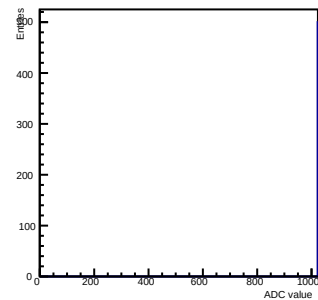
Run 35044 chip 7 Maxadc value ch 1



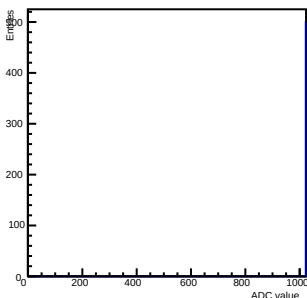
Run 35044 chip 7 Maxadc value ch 2



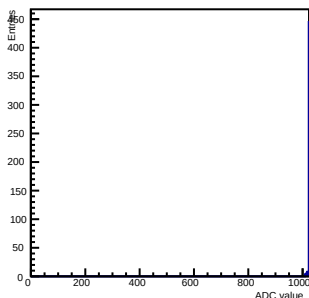
Run 35044 chip 7 Maxadc value ch 3



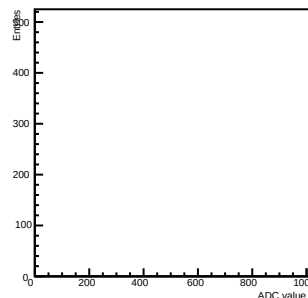
Run 35044 chip 7 Maxadc value ch 4



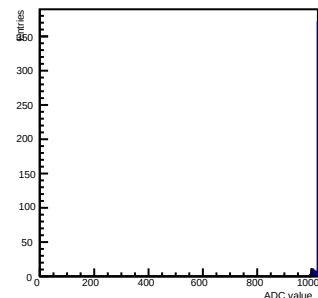
Run 35044 chip 7 Maxadc value ch 5



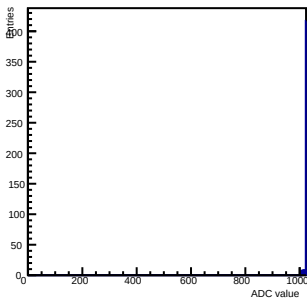
Run 35044 chip 7 Maxadc value ch 6



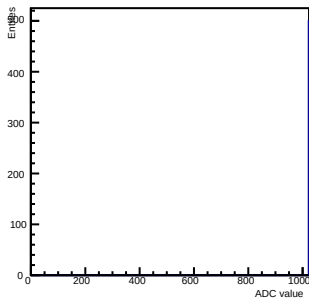
Run 35044 chip 7 Maxadc value ch 7



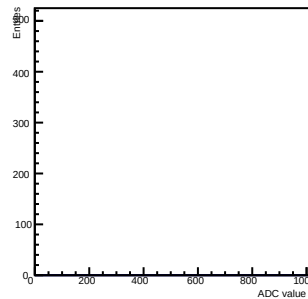
Run 35044 chip 7 Maxadc value ch 8



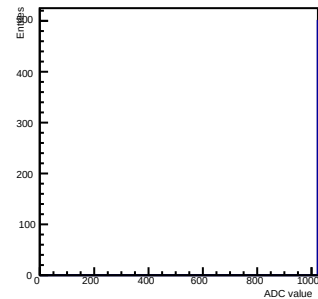
Run 35044 chip 7 Maxadc value ch 9



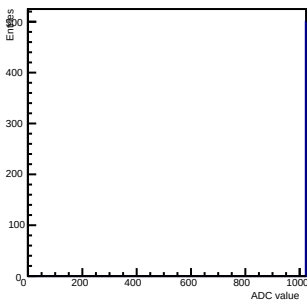
Run 35044 chip 7 Maxadc value ch 10



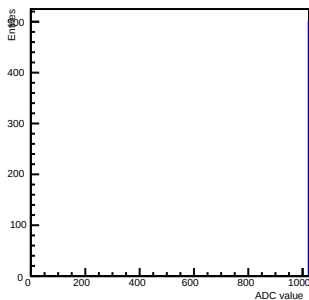
Run 35044 chip 7 Maxadc value ch 11



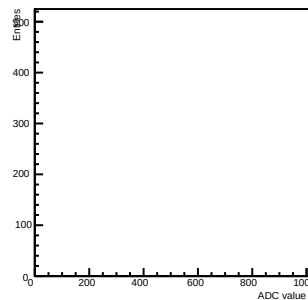
Run 35044 chip 7 Maxadc value ch 12



Run 35044 chip 7 Maxadc value ch 13



Run 35044 chip 7 Maxadc value ch 14



Run 35044 chip 7 Maxadc value ch 15

