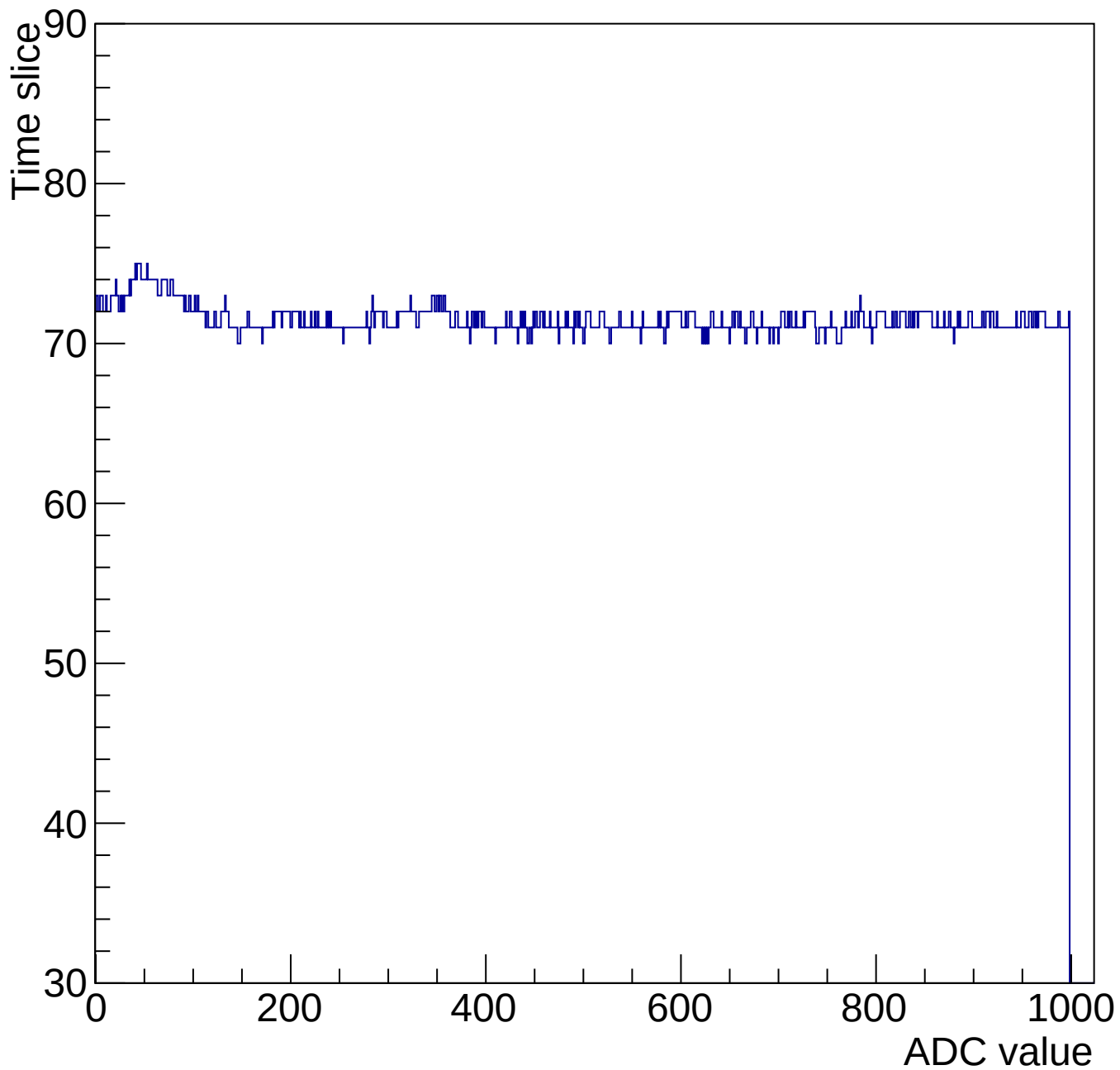
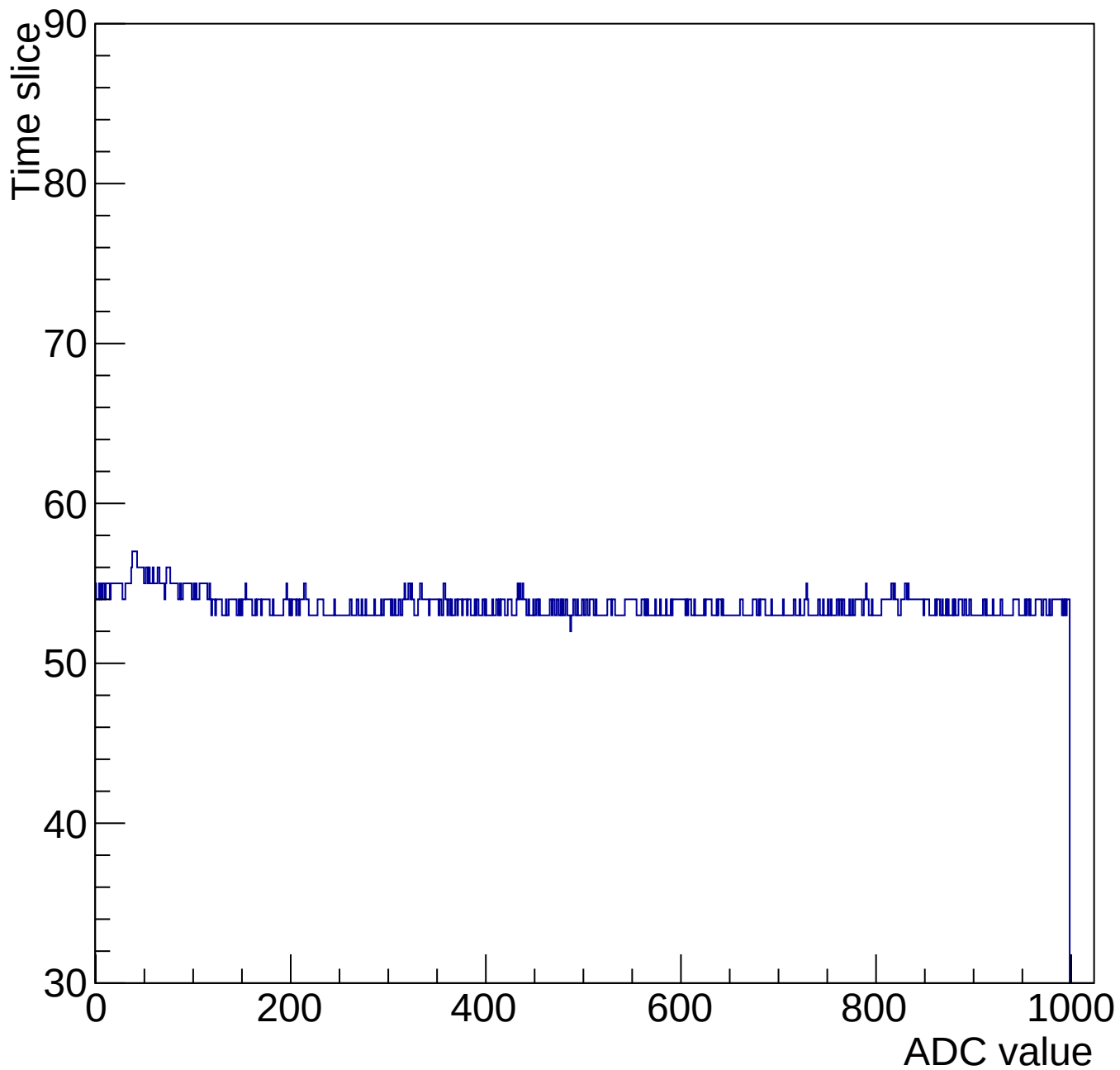


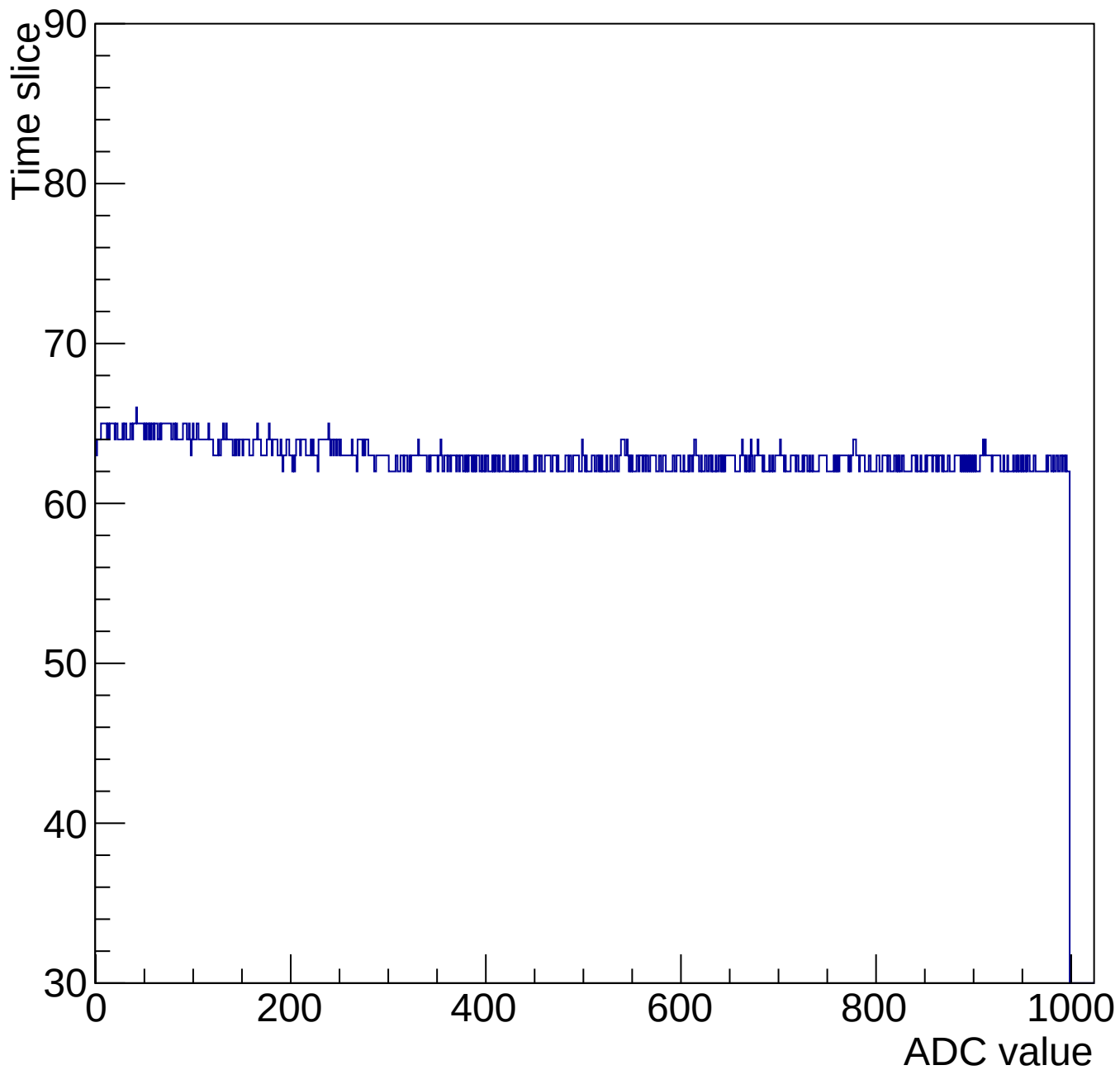
Evt 10 Time mcm 2004 chip 4 chan 0 1000-gain0-shaper0



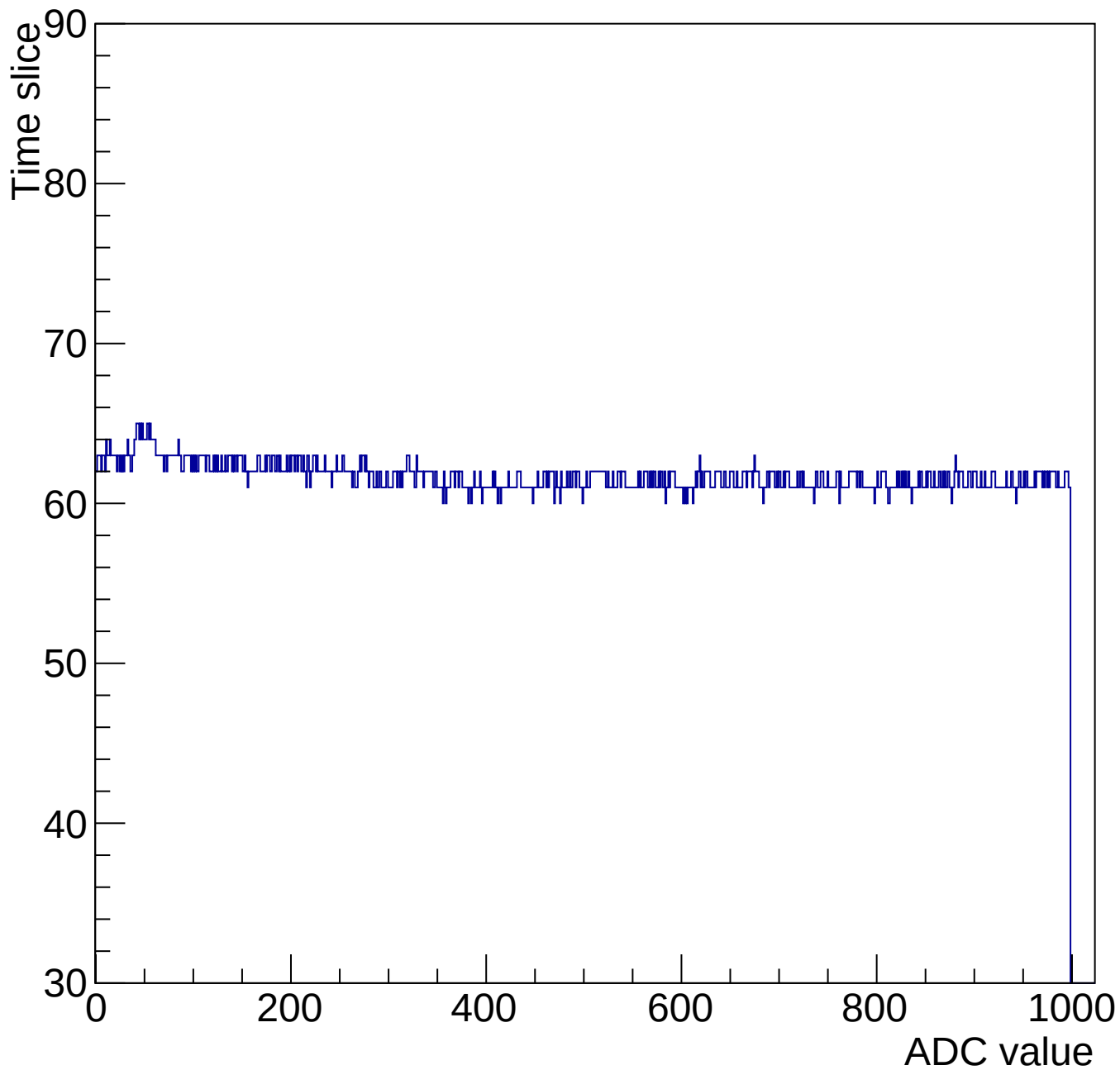
Evt 10 Time mcm 2004 chip 4 chan 1 1000-gain0-shaper0



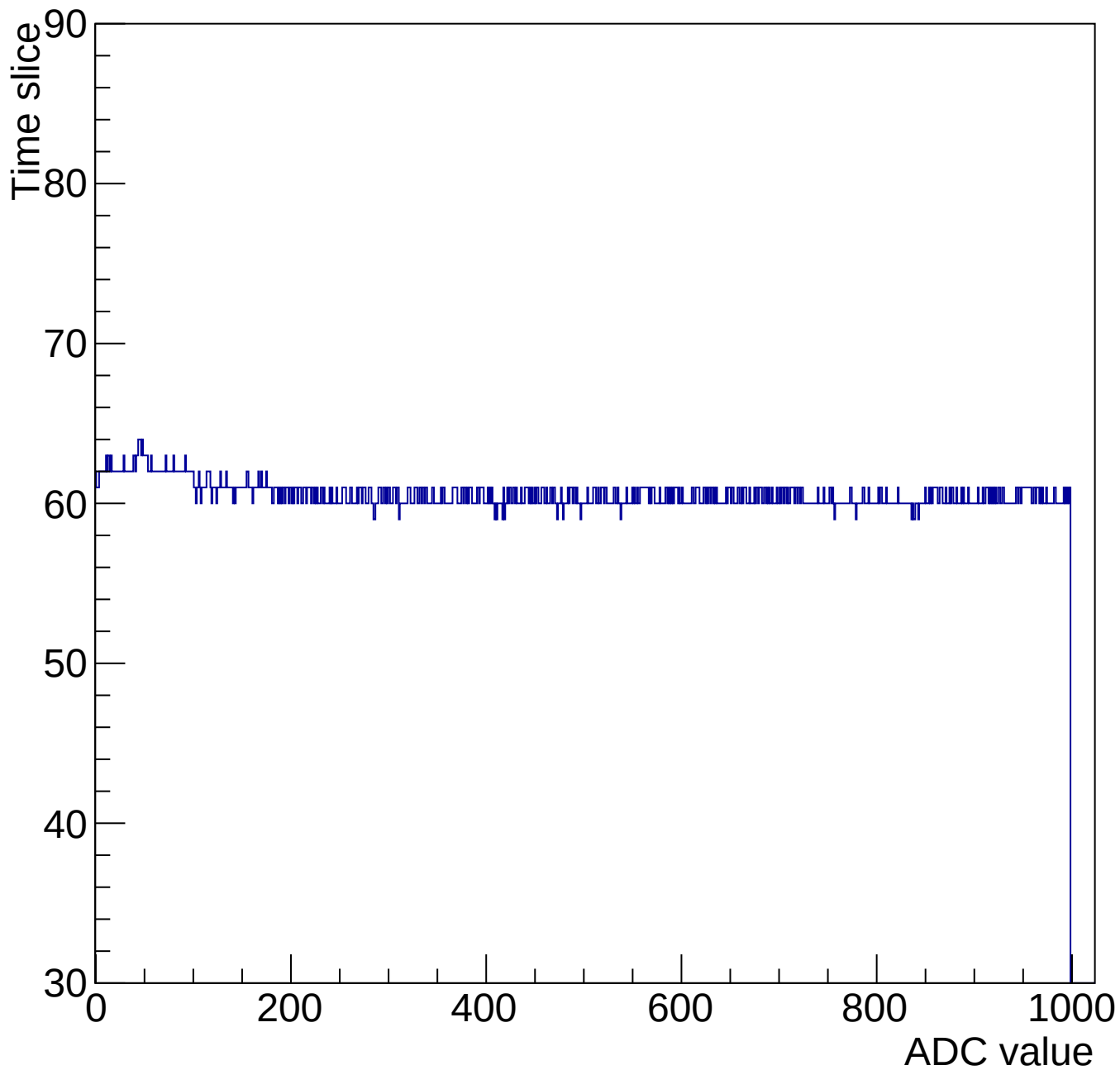
Evt 10 Time mcm 2004 chip 4 chan 2 1000-gain0-shaper0



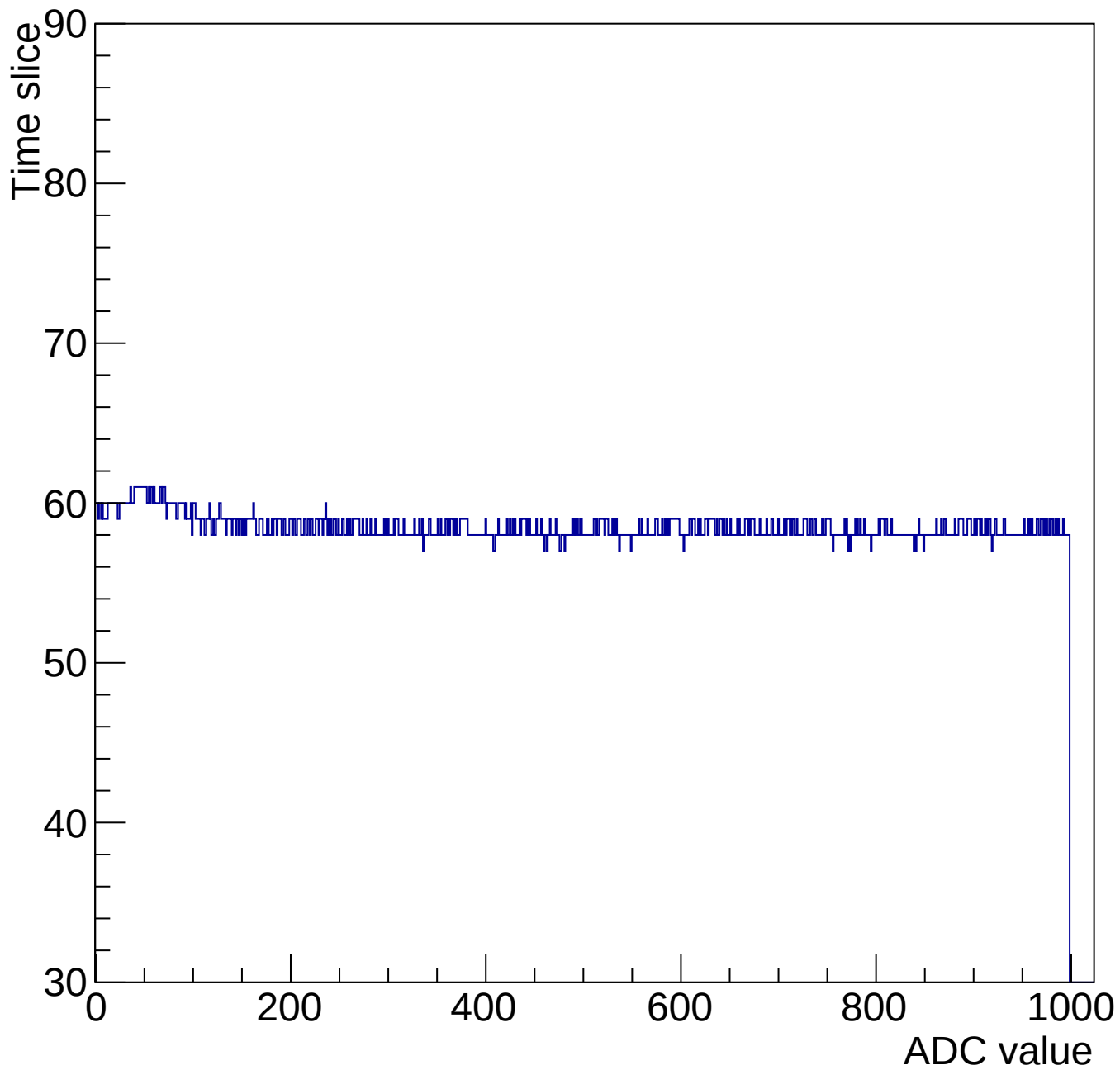
Evt 10 Time mcm 2004 chip 4 chan 3 1000-gain0-shaper0



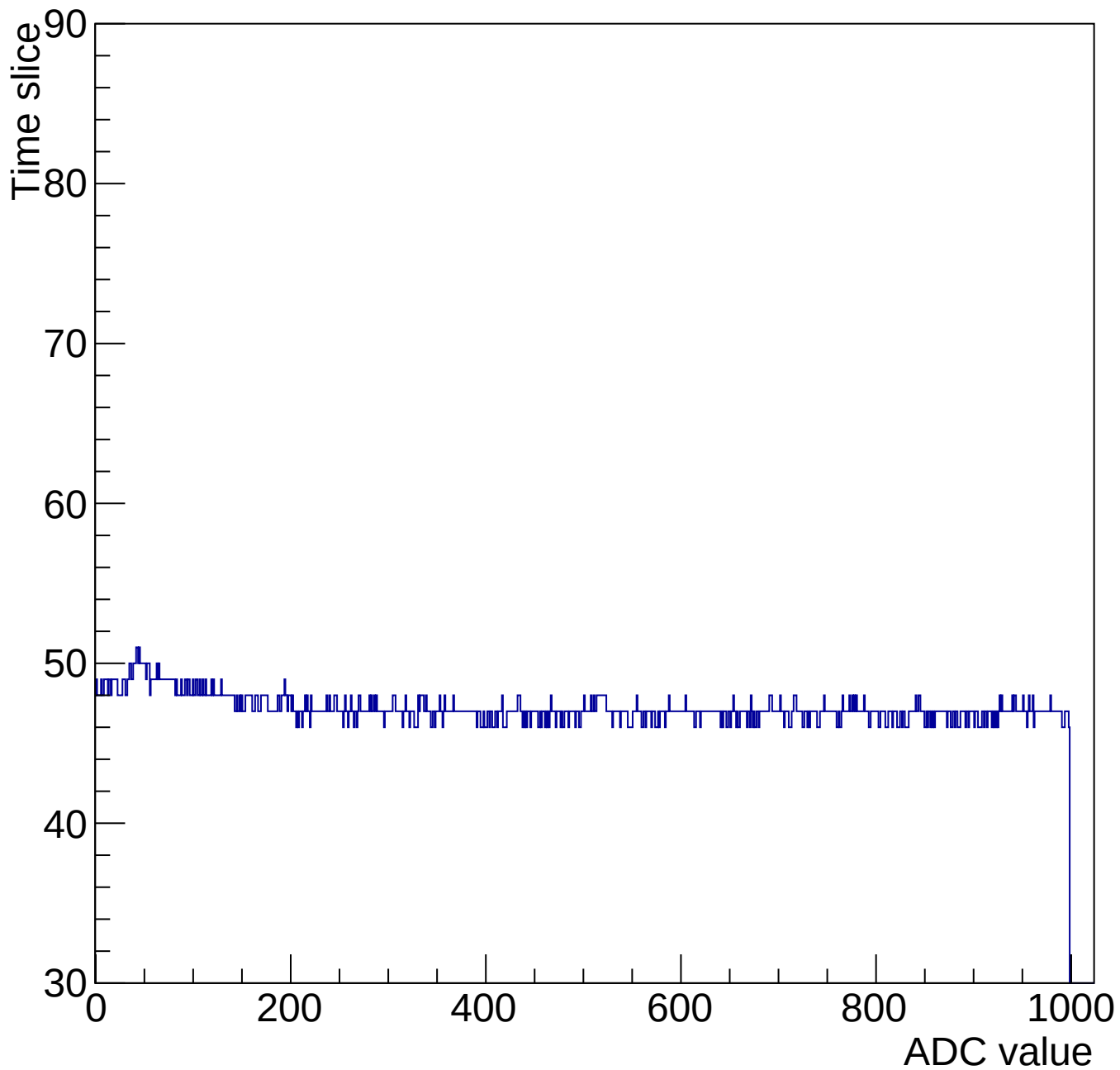
Evt 10 Time mcm 2004 chip 4 chan 4 1000-gain0-shaper0



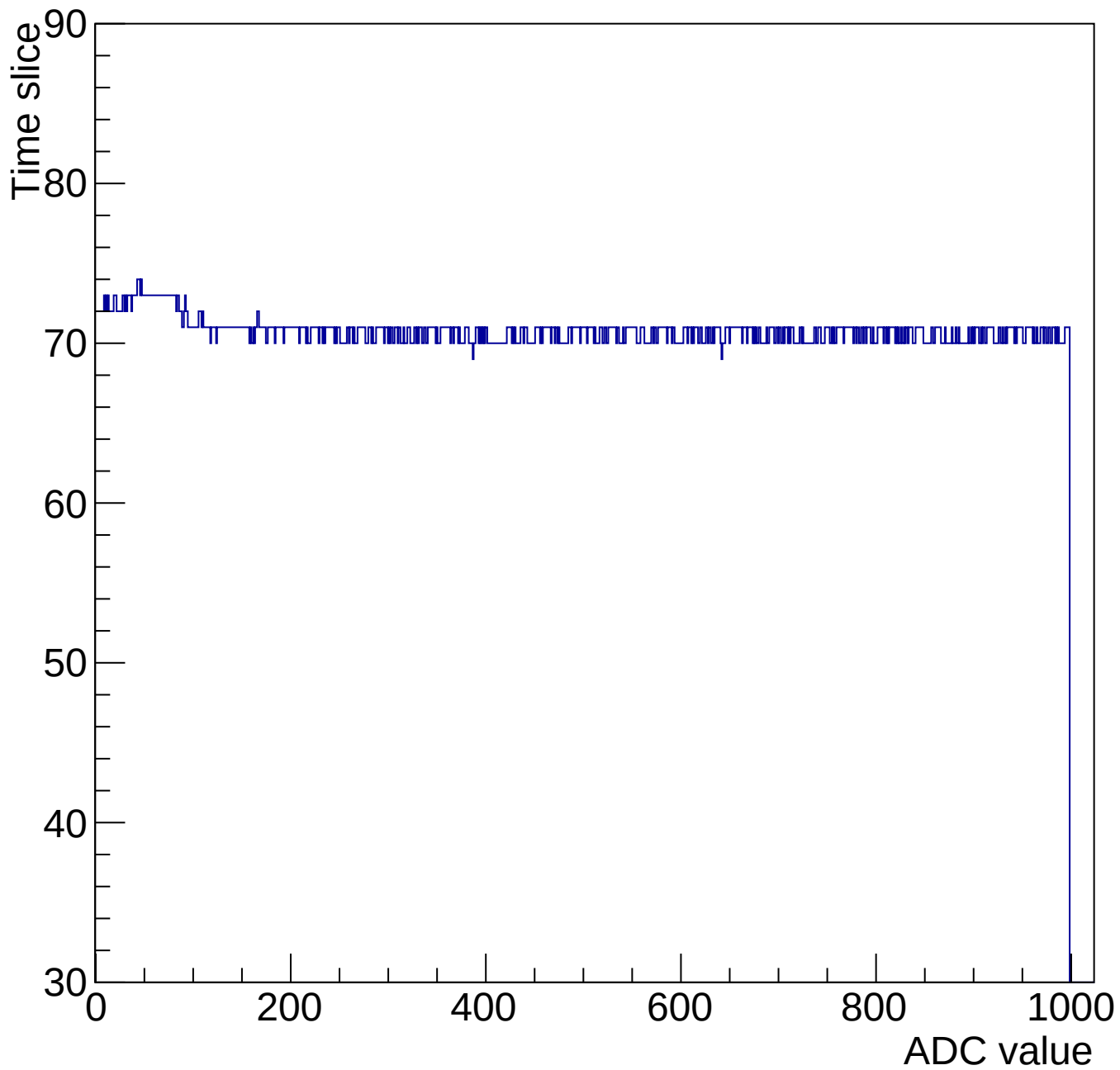
Evt 10 Time mcm 2004 chip 4 chan 5 1000-gain0-shaper0



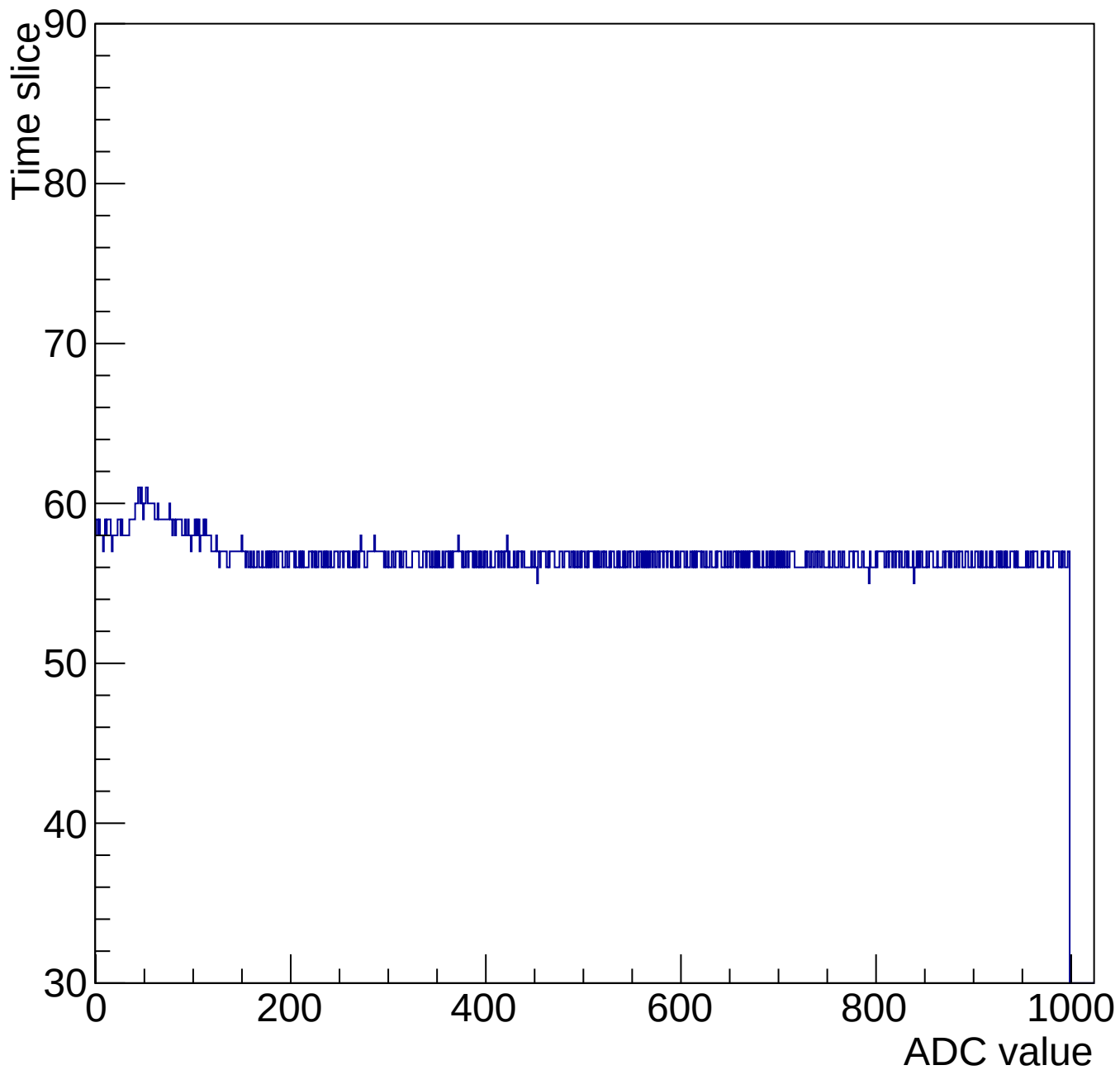
Evt 10 Time mcm 2004 chip 4 chan 6 1000-gain0-shaper0



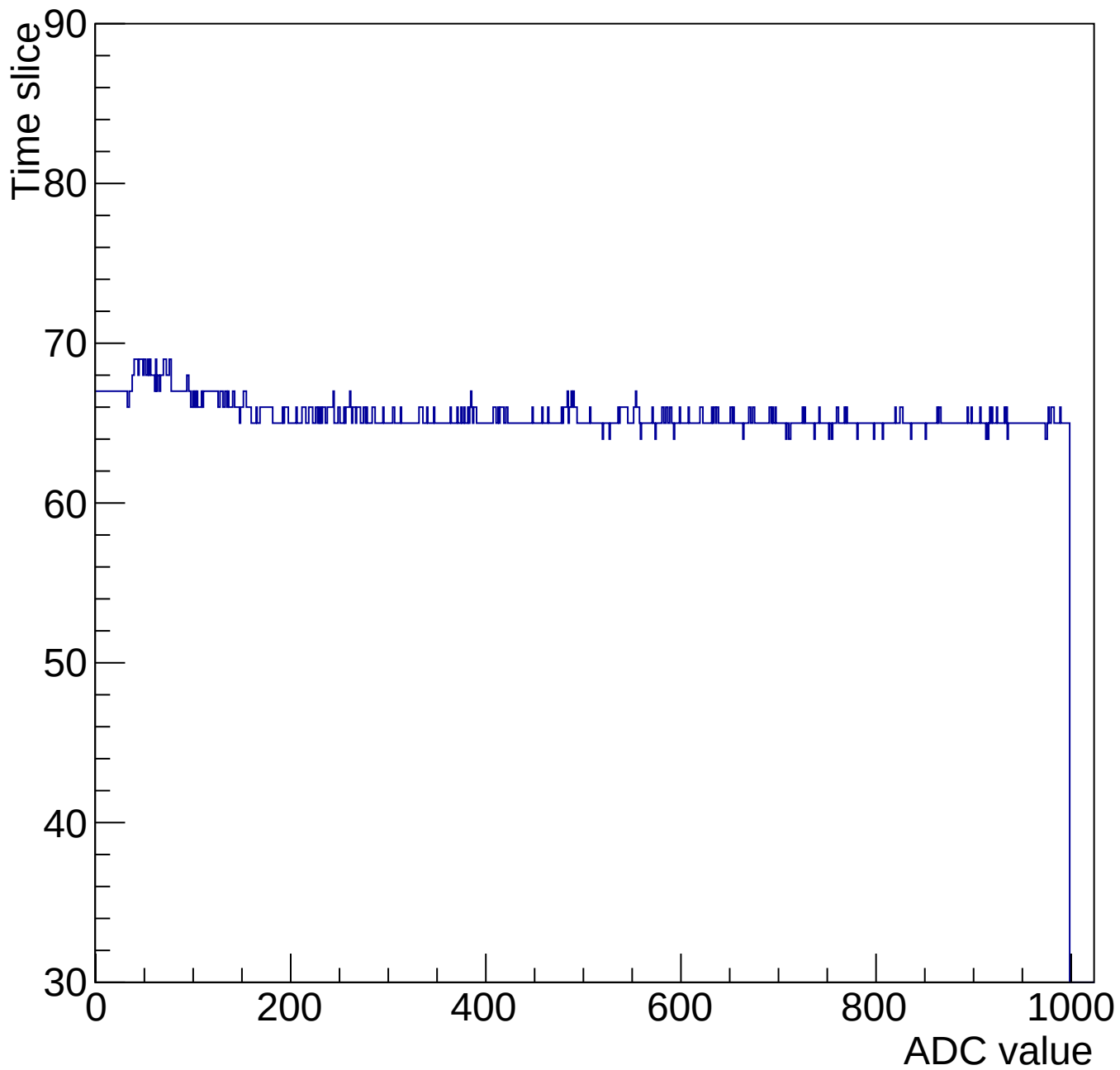
Evt 10 Time mcm 2004 chip 4 chan 7 1000-gain0-shaper0



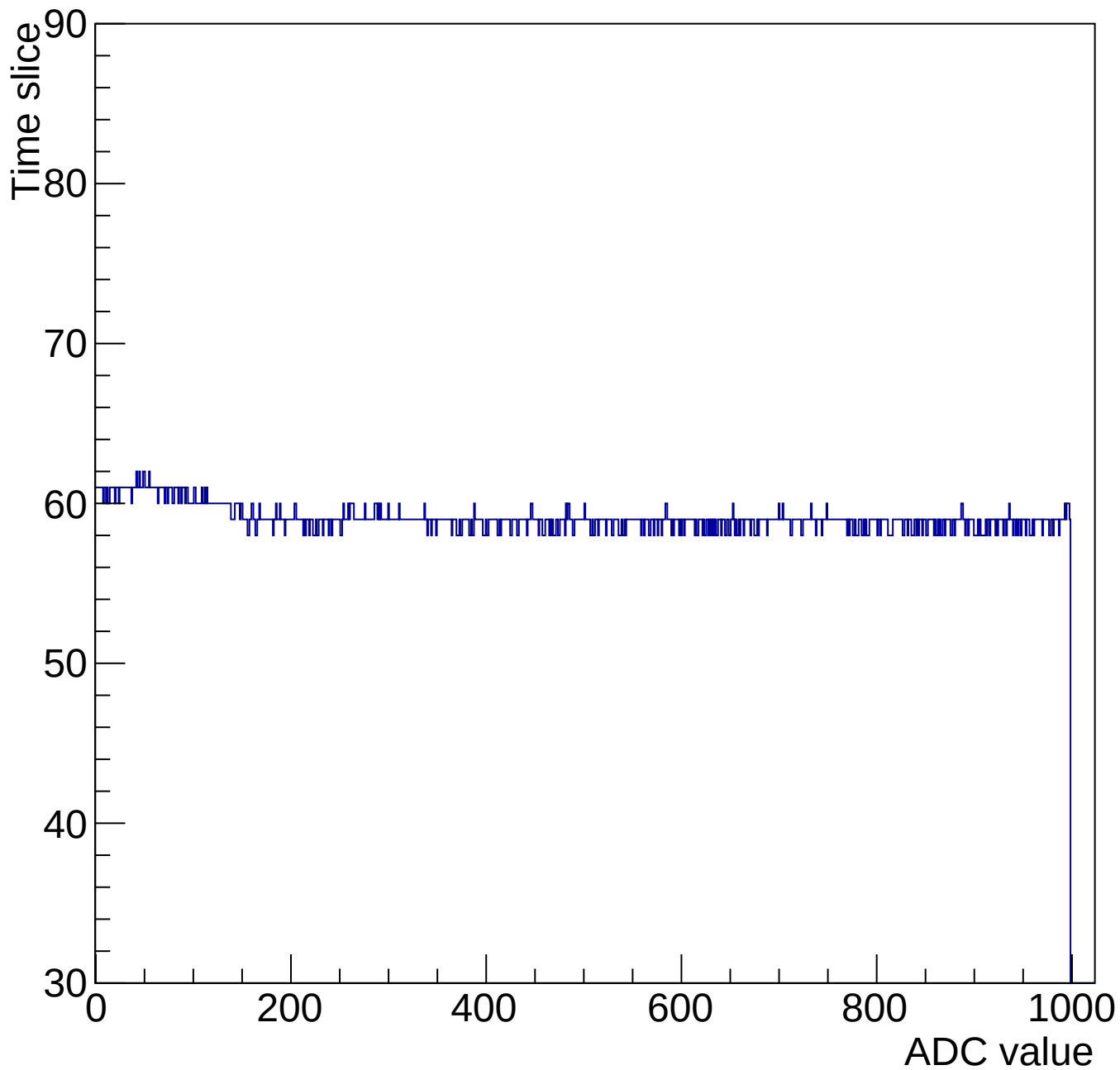
Evt 10 Time mcm 2004 chip 4 chan 8 1000-gain0-shaper0



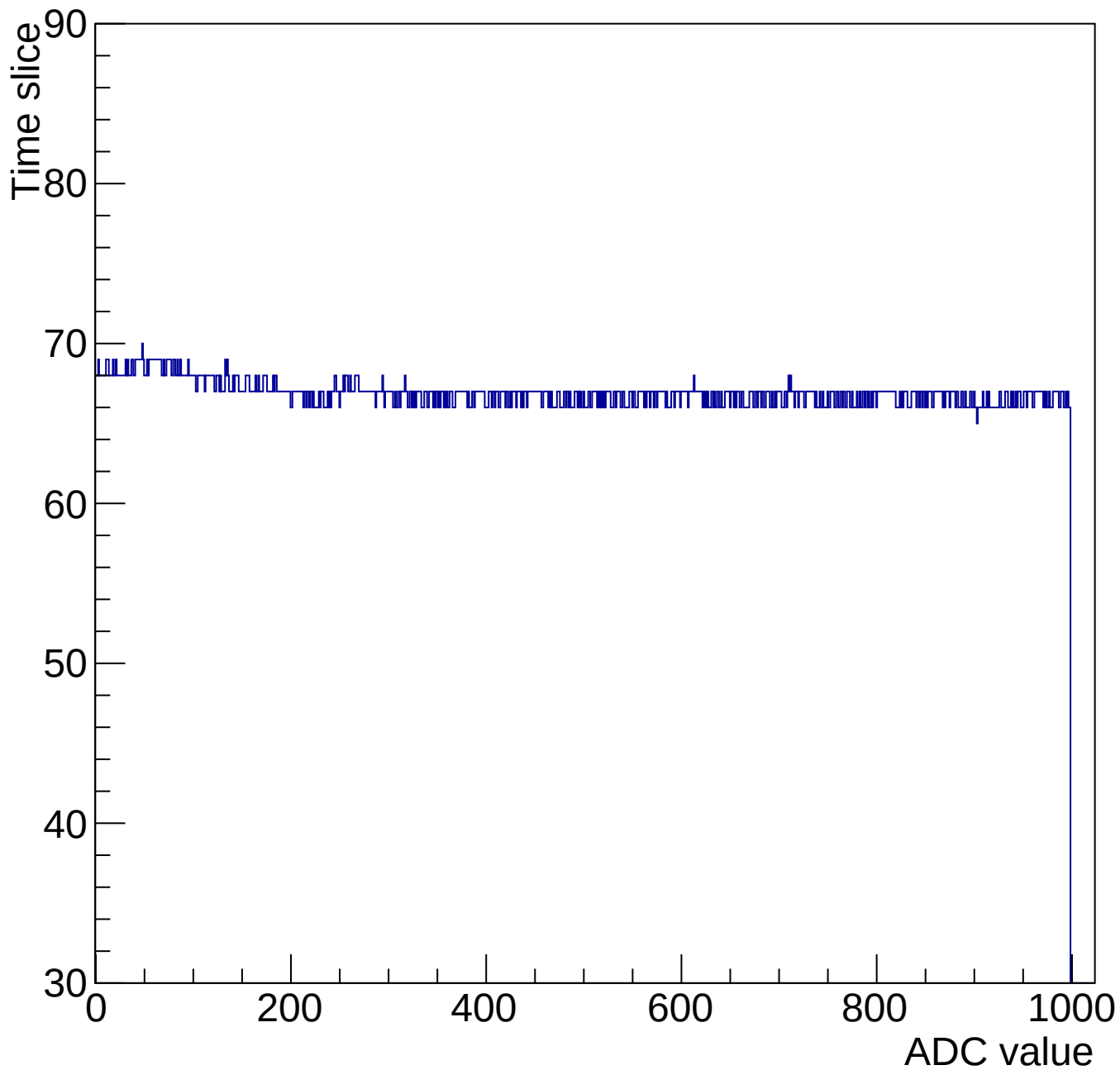
Evt 10 Time mcm 2004 chip 4 chan 9 1000-gain0-shaper0



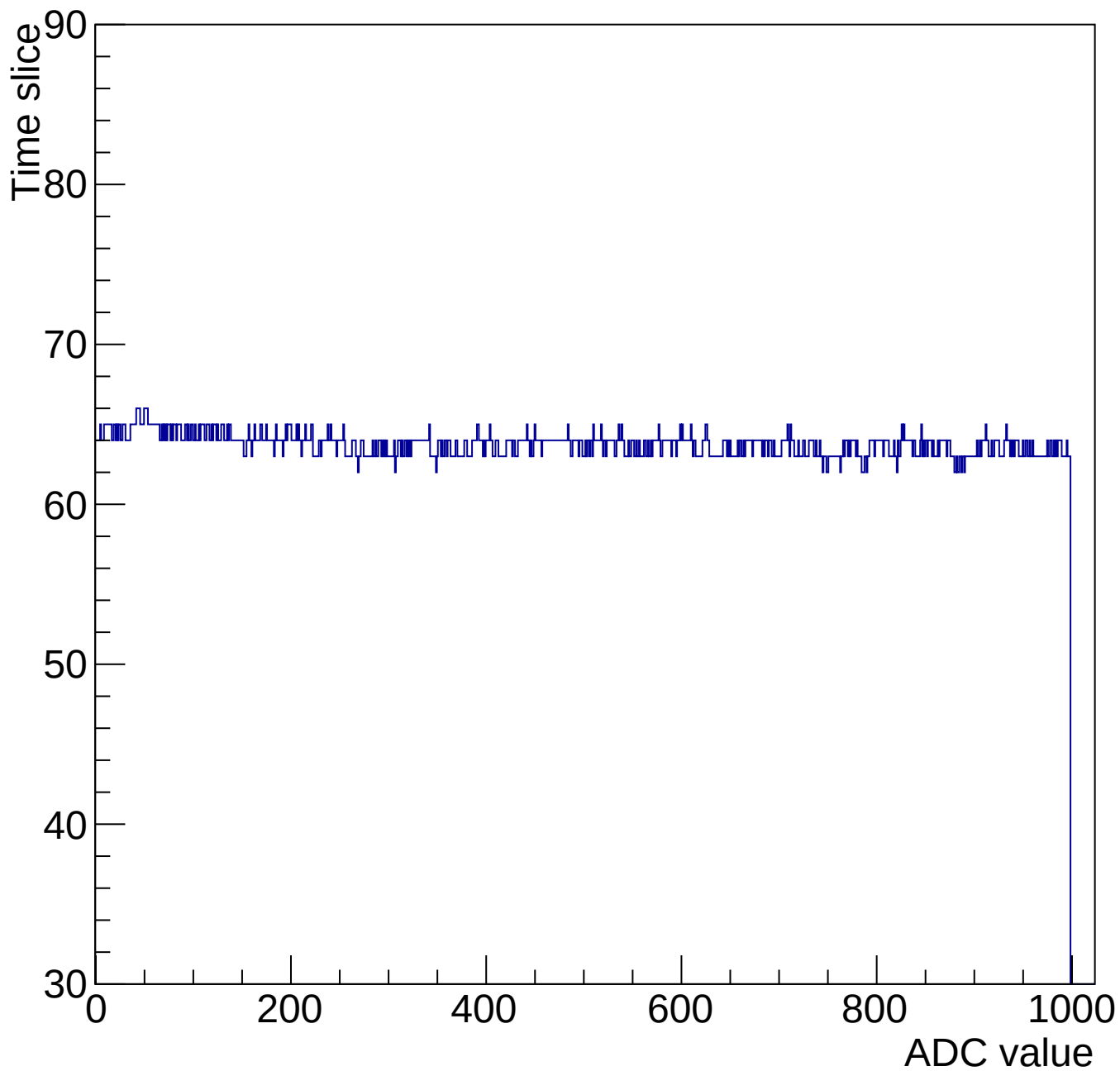
Evt 10 Time mcm 2004 chip 4 chan 10 1000-gain0-shaper0



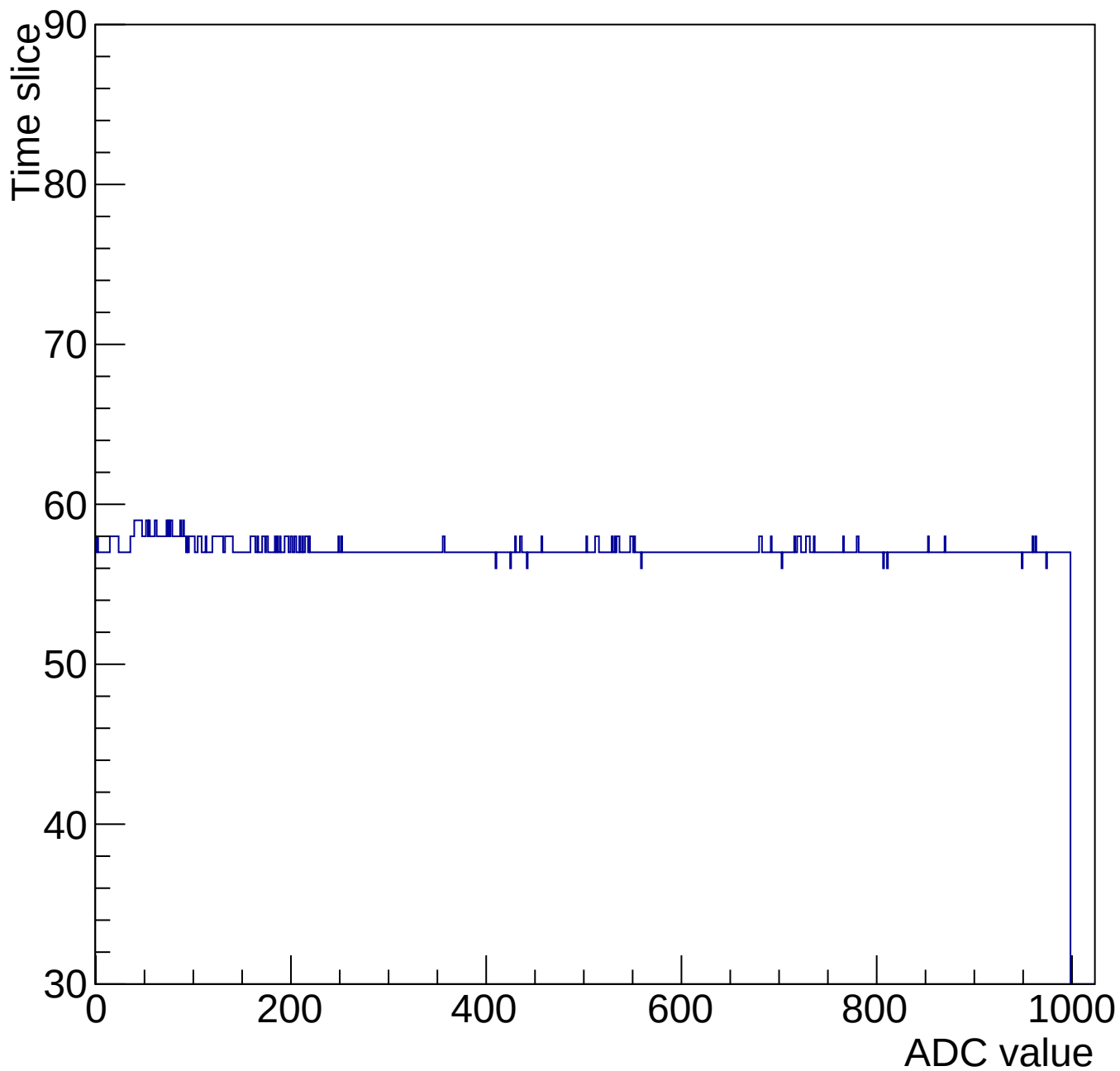
Evt 10 Time mcm 2004 chip 4 chan 11 1000-gain0-shaper0



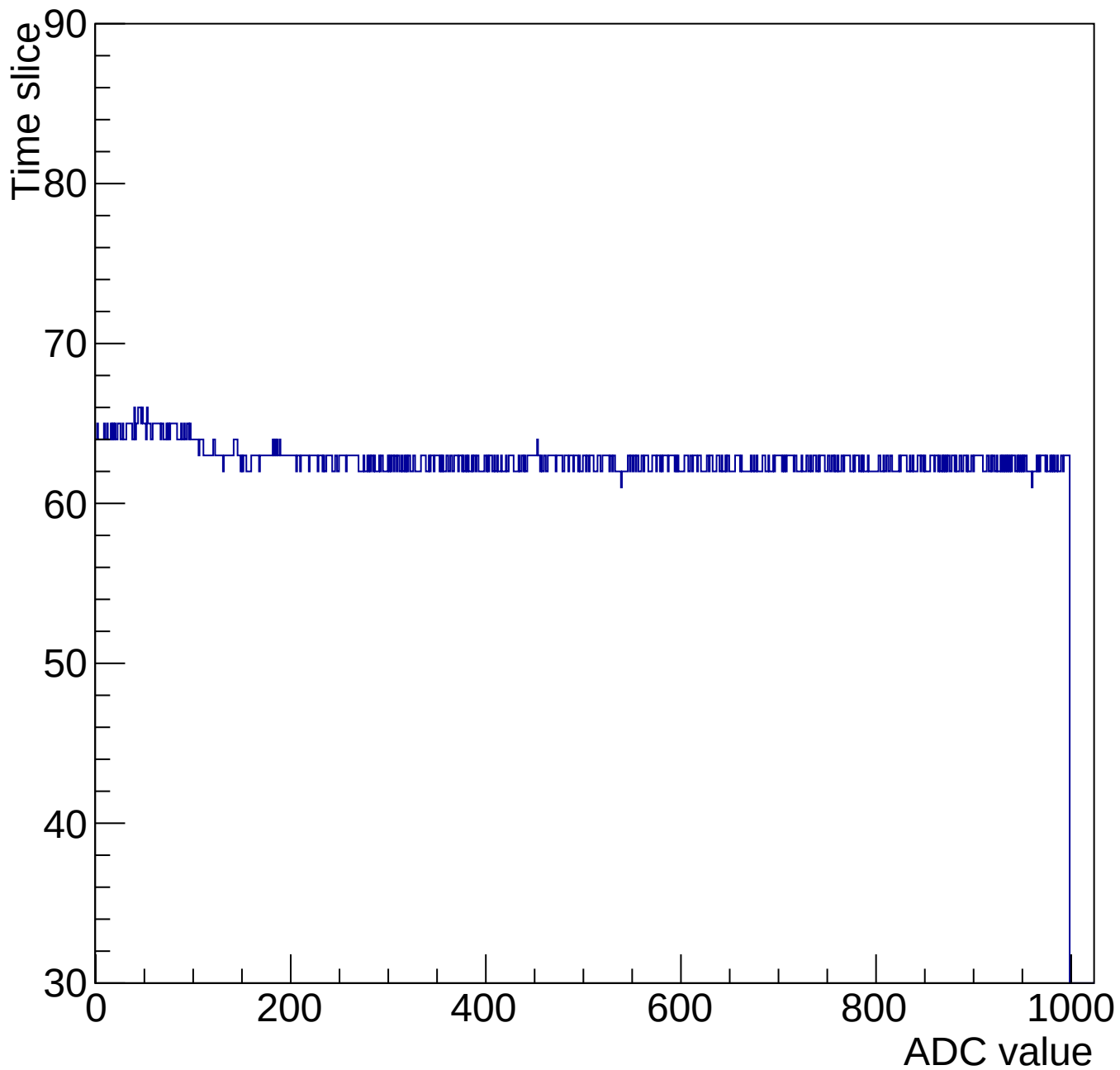
Evt 10 Time mcm 2004 chip 4 chan 12 1000-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 13 1000-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 14 1000-gain0-shaper0



Evt 10 Time mcm 2004 chip 4 chan 15 1000-gain0-shaper0

