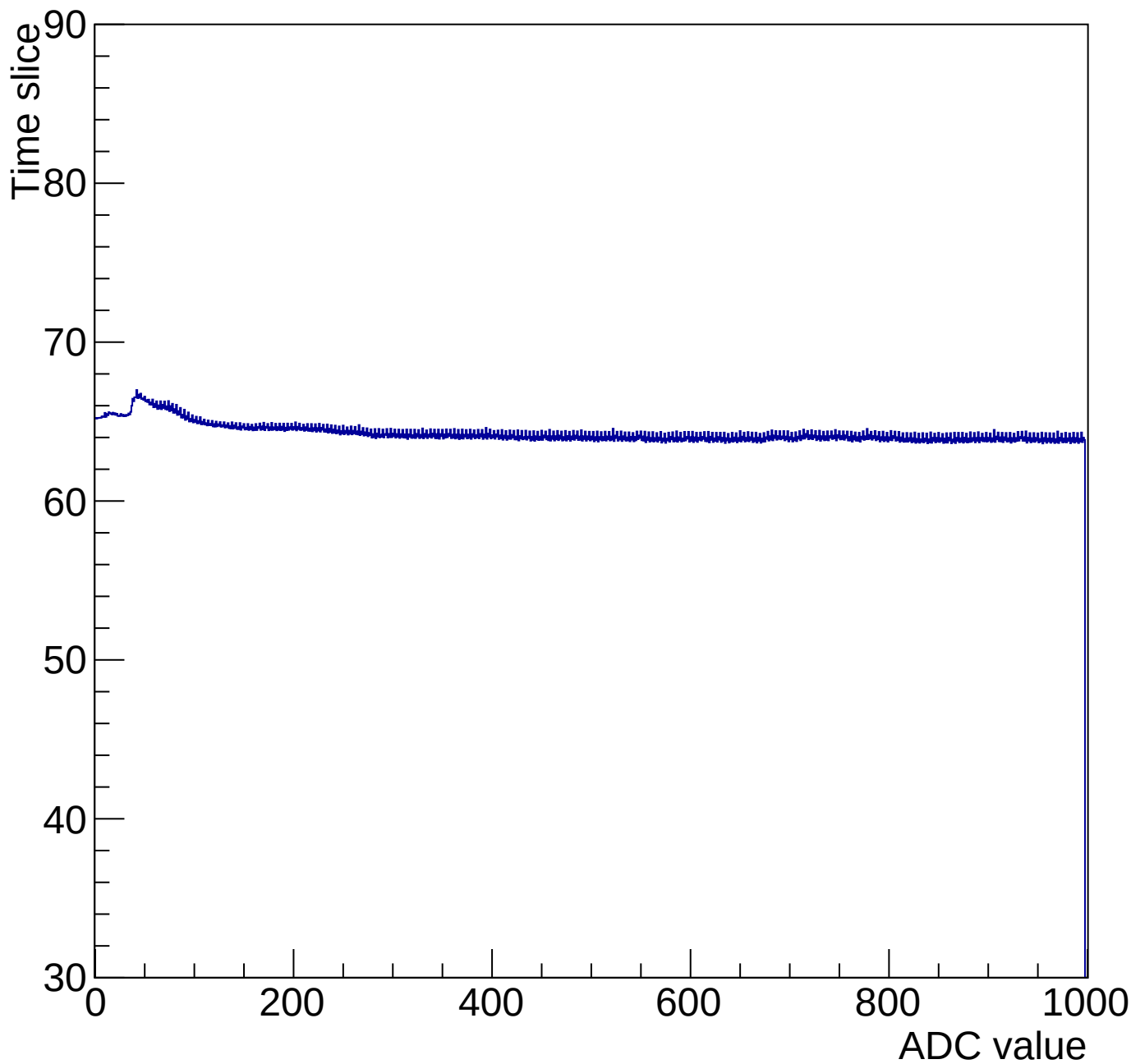
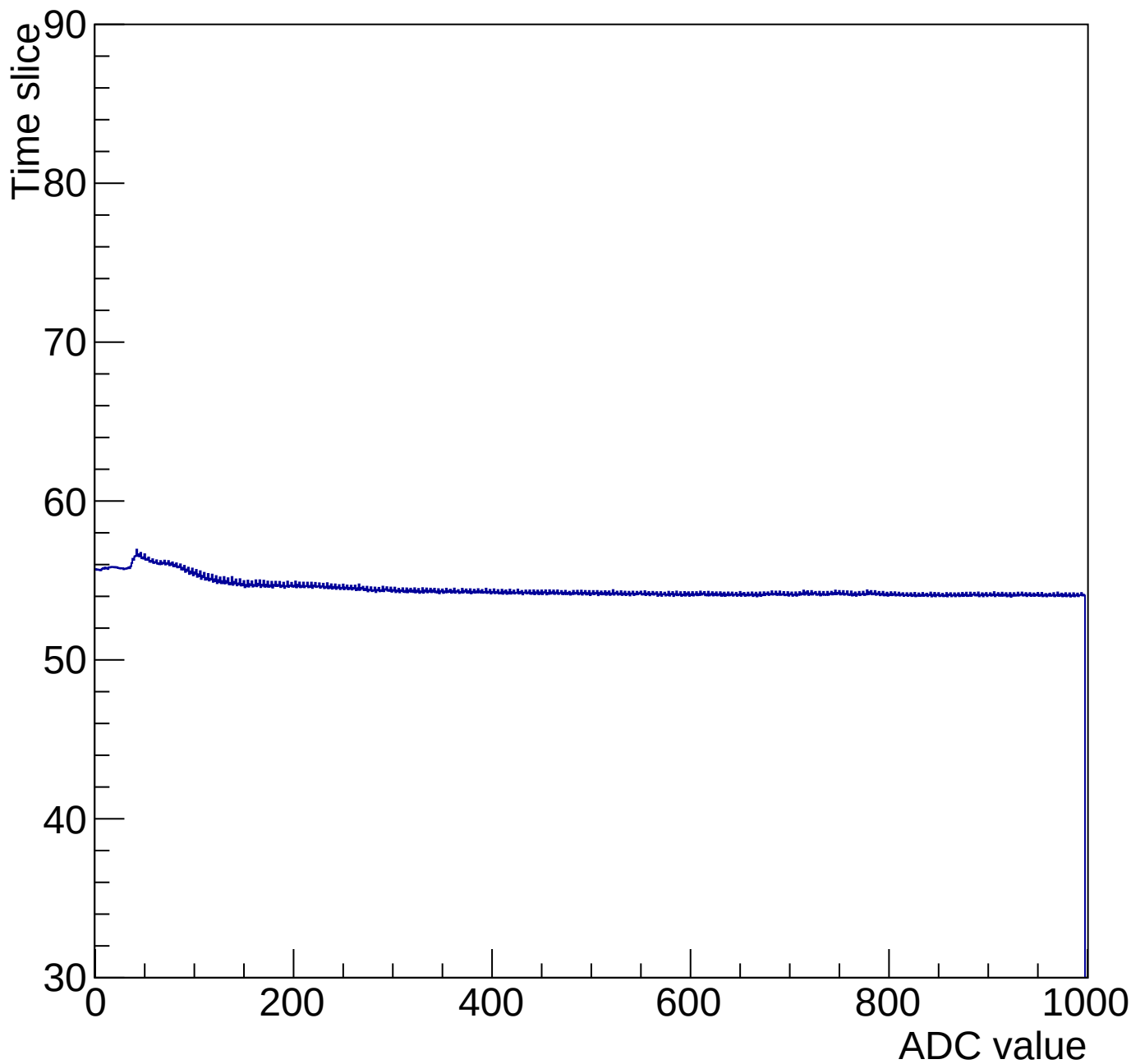


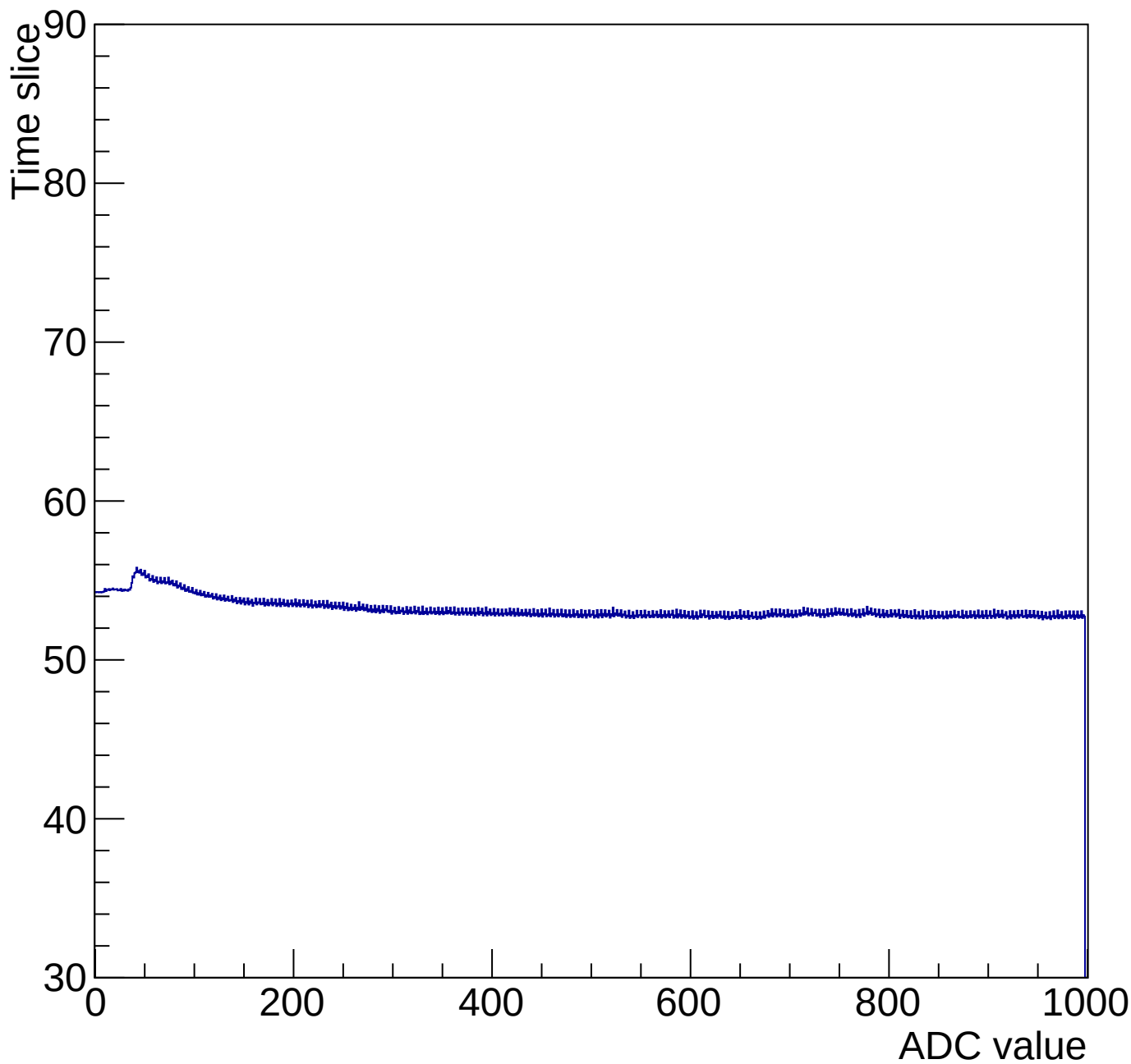
Time mcm 2001 chip 1 chan 0 1000-gain0-shaper0



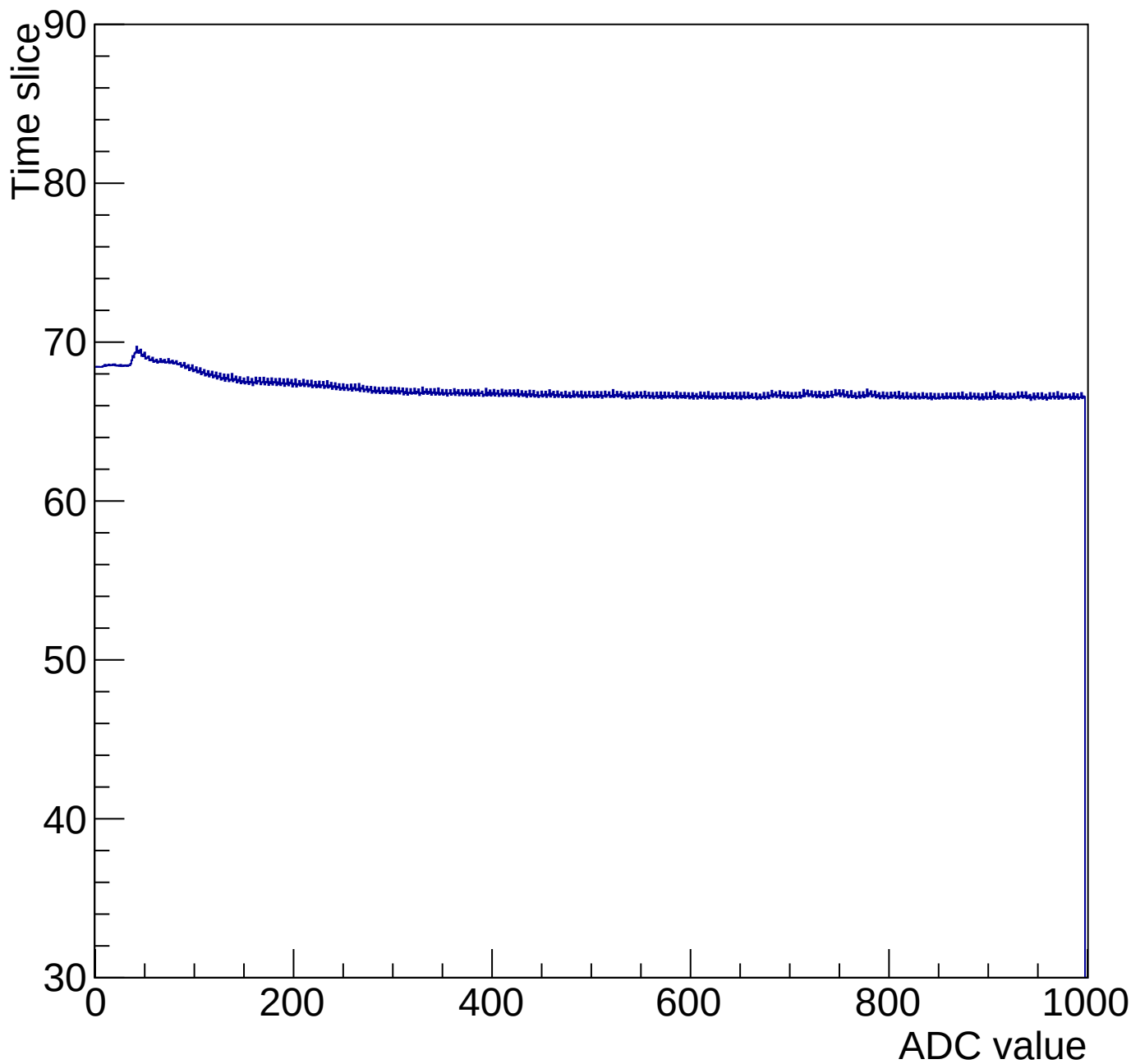
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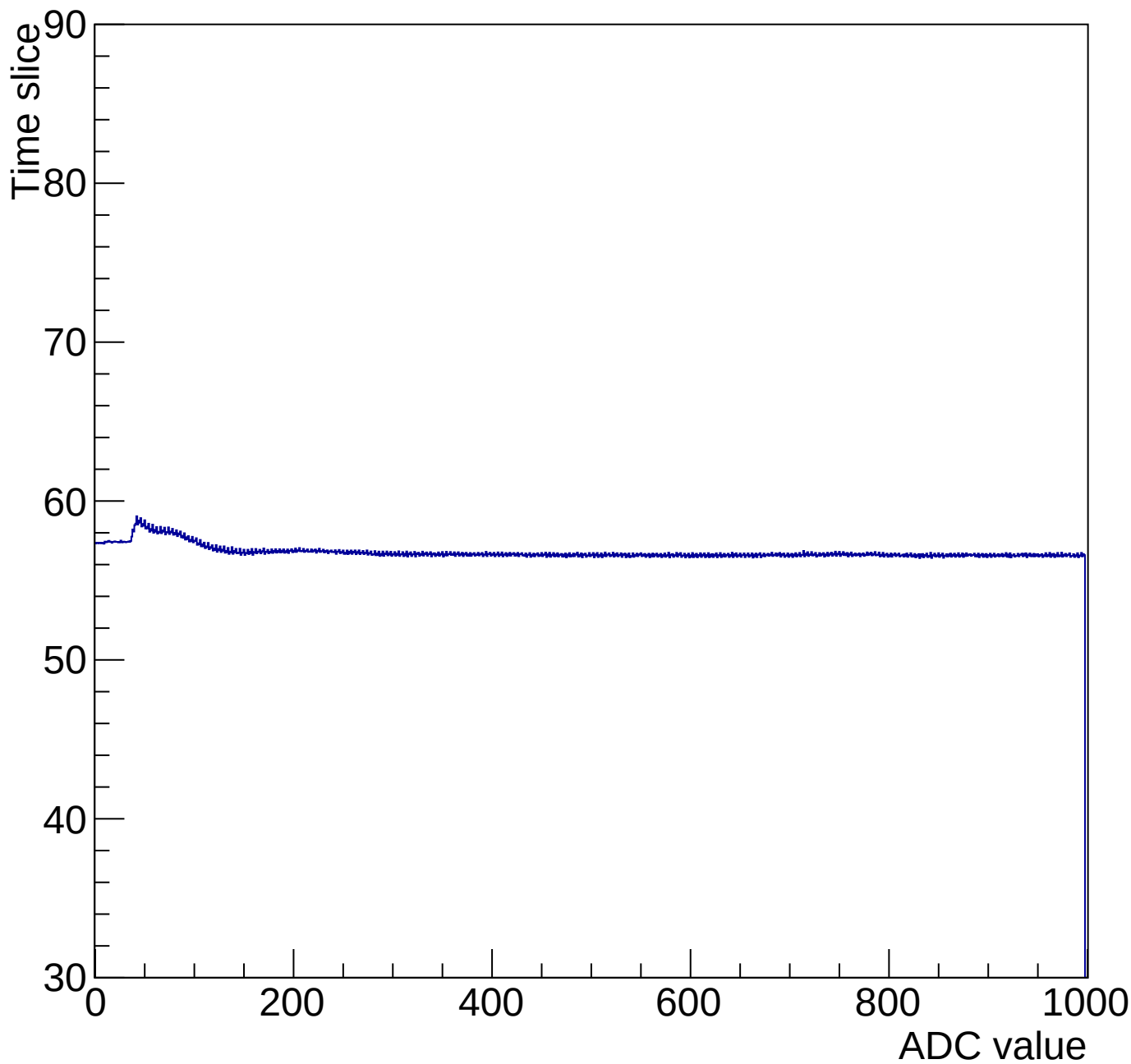
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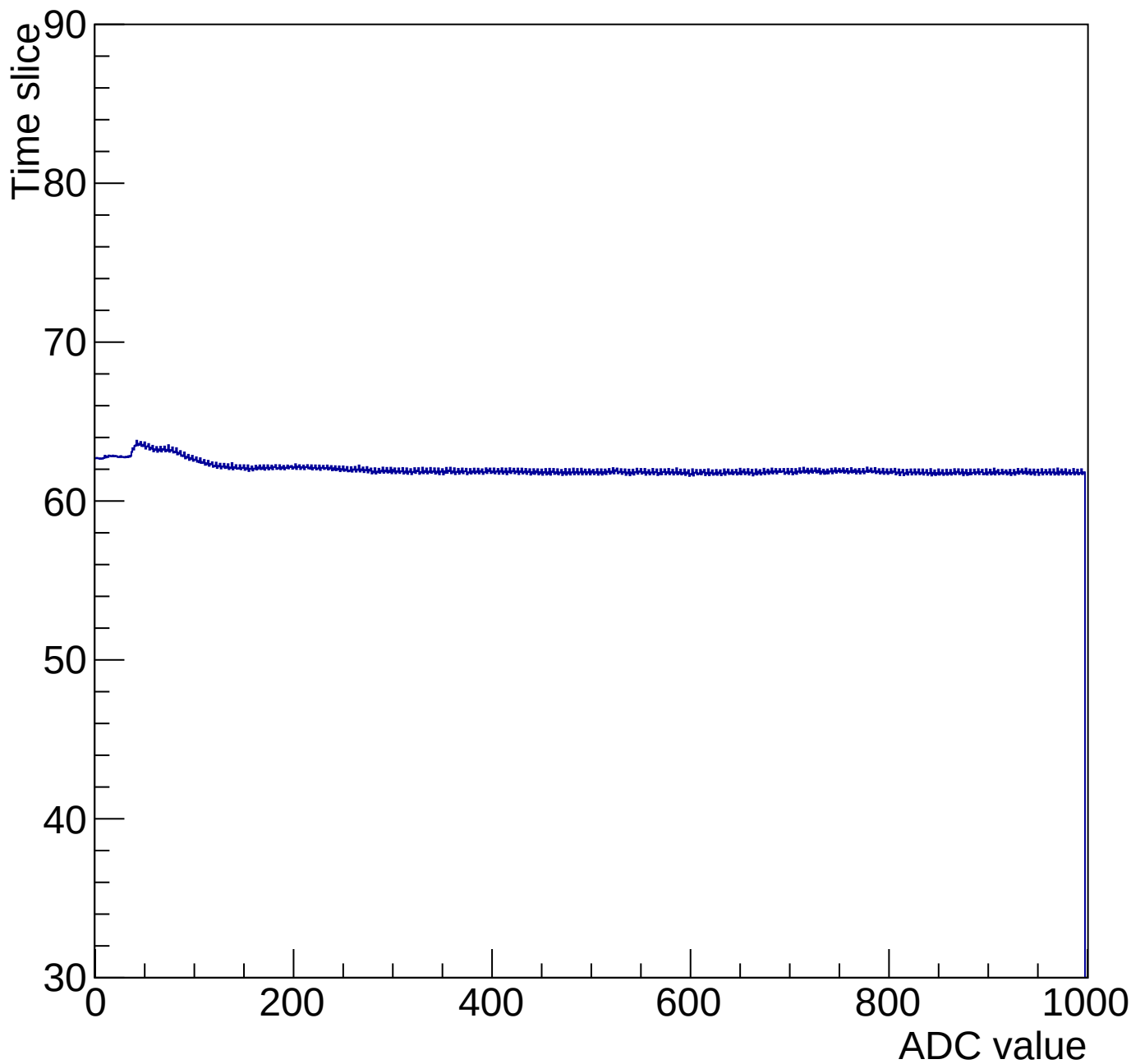
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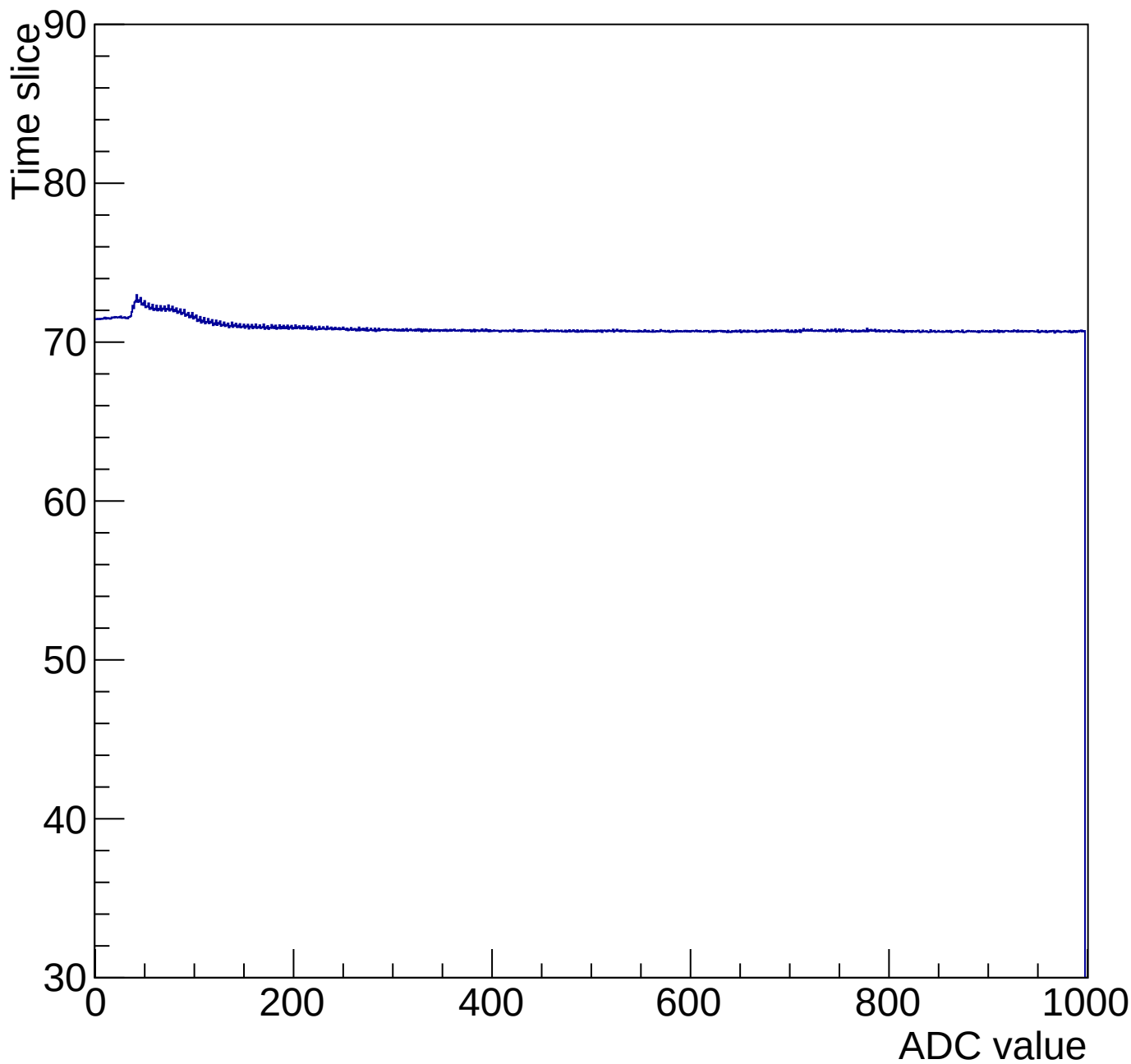
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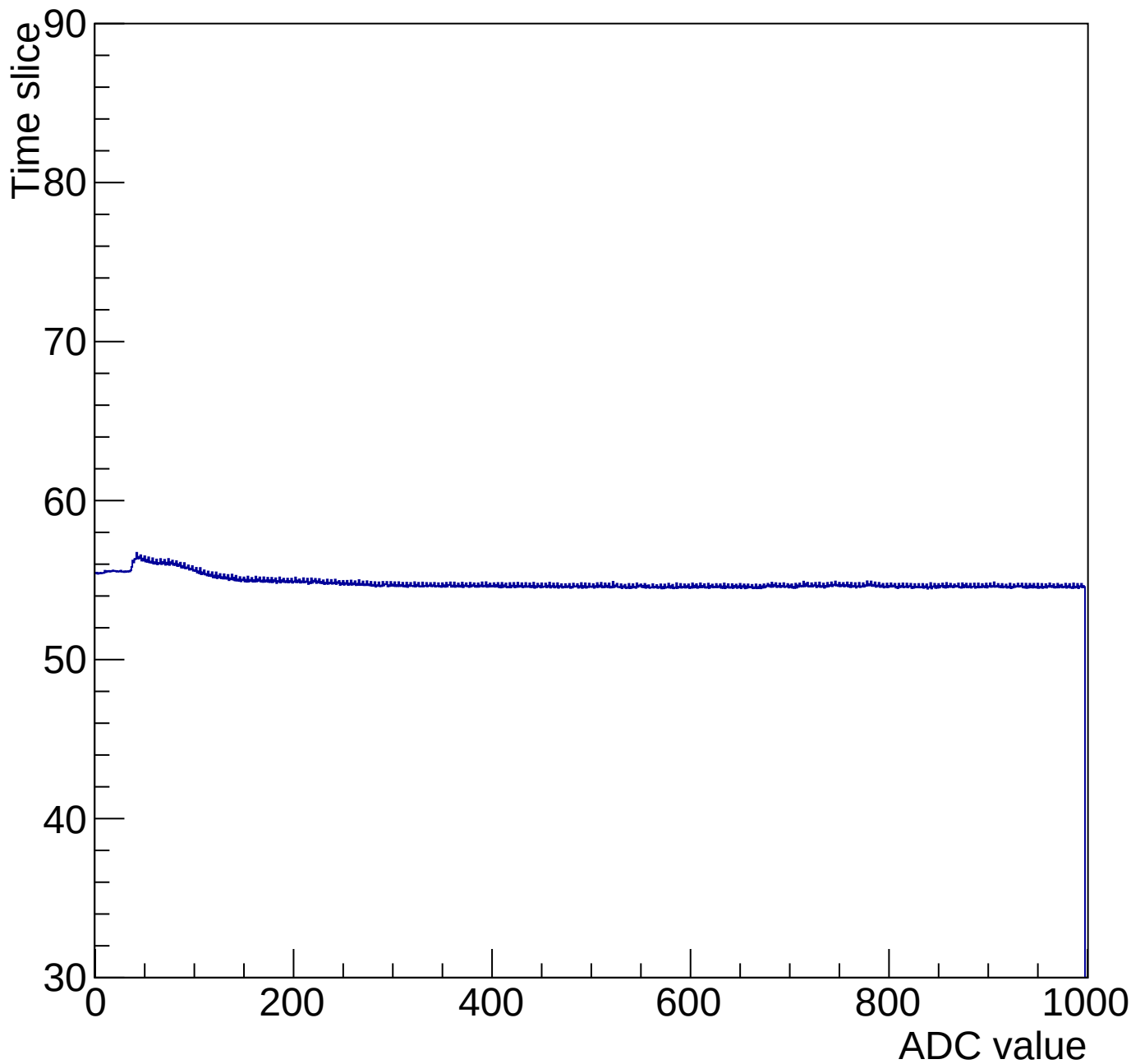
Time mcm 2001 chip 1 chan 5 1000-gain0-shaper0



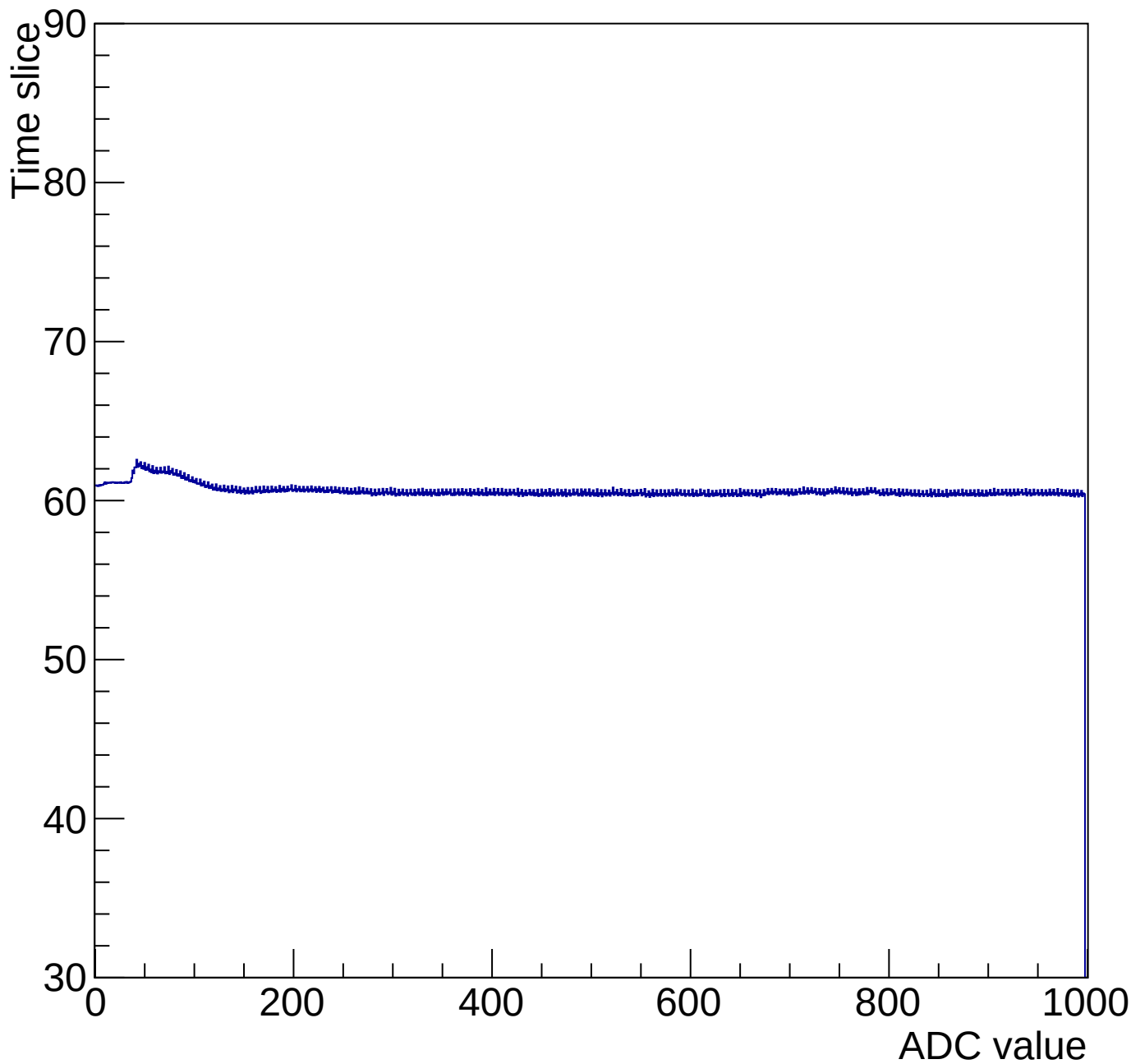
Time mcm 2001 chip 1 chan 6 1000-gain0-shaper0



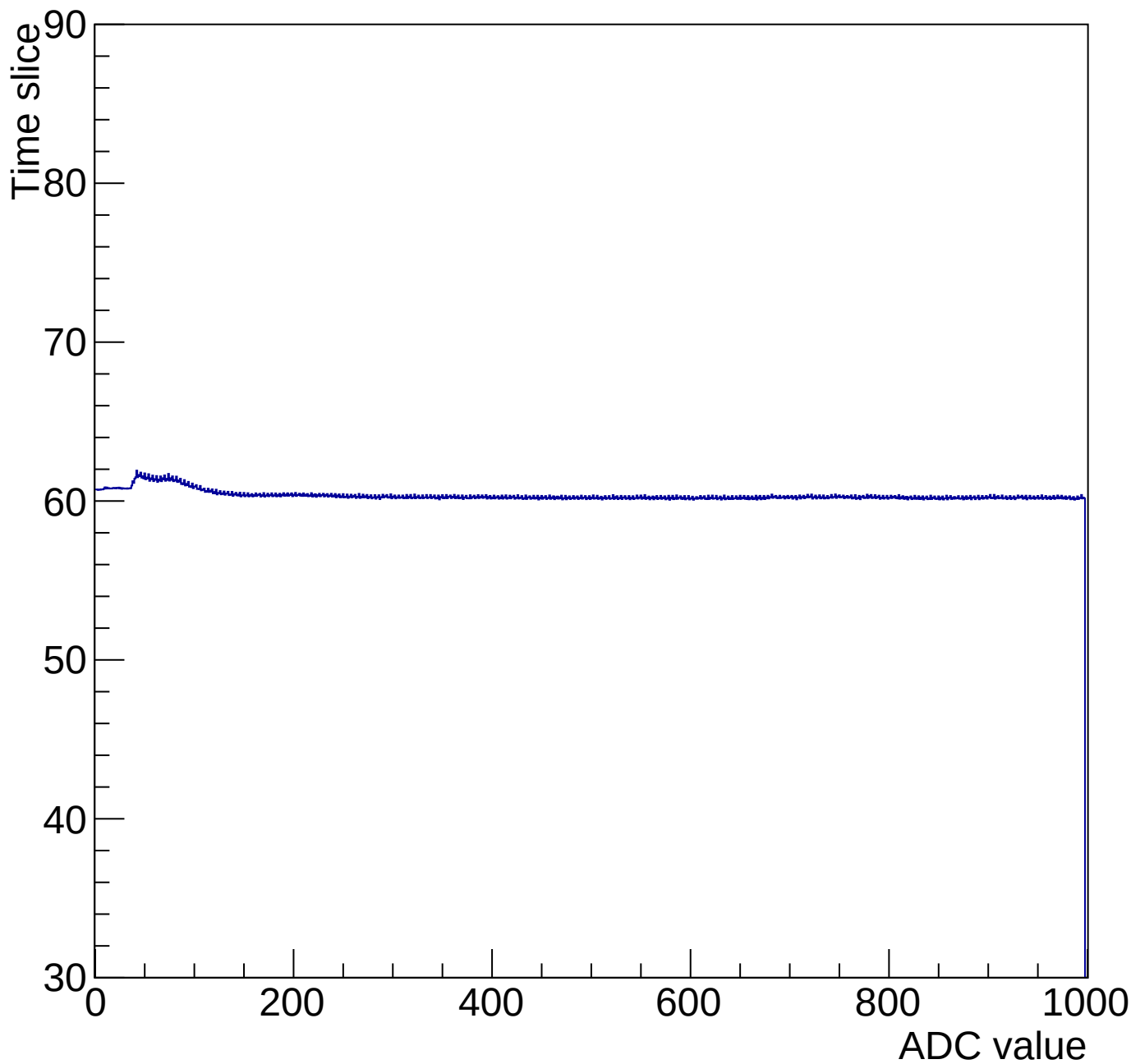
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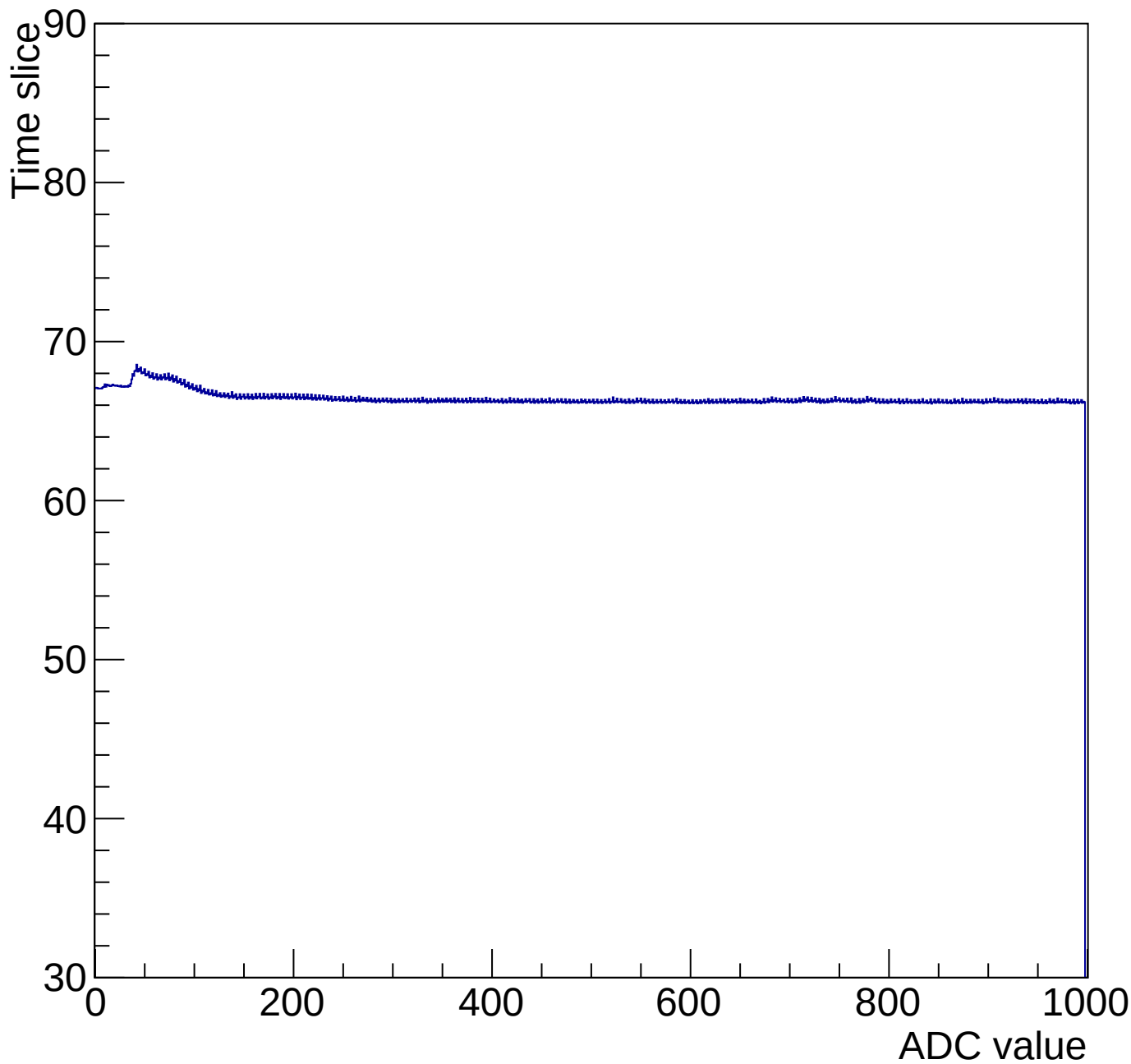
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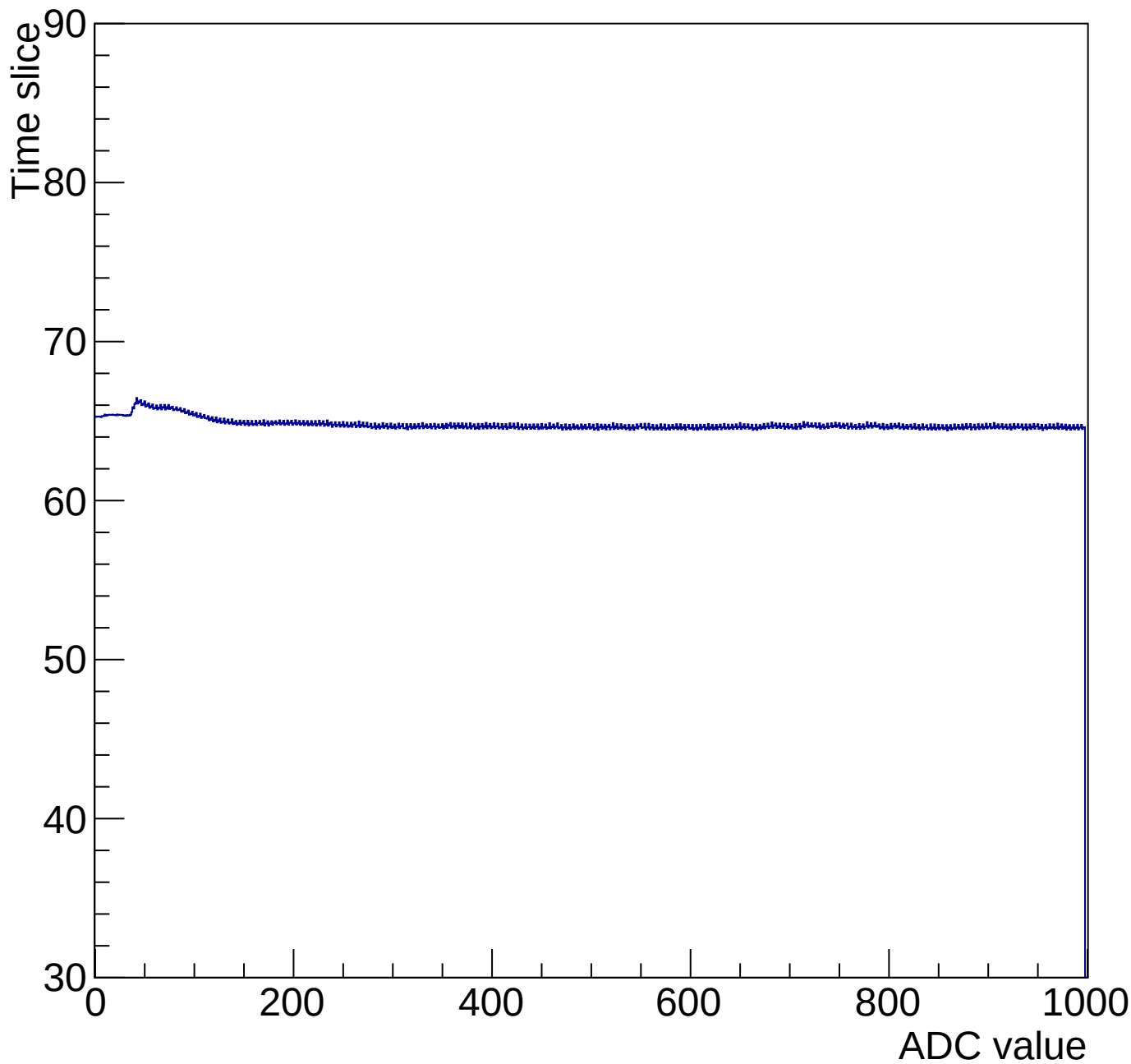
Time mcm 2001 chip 1 chan 9 1000-gain0-shaper0



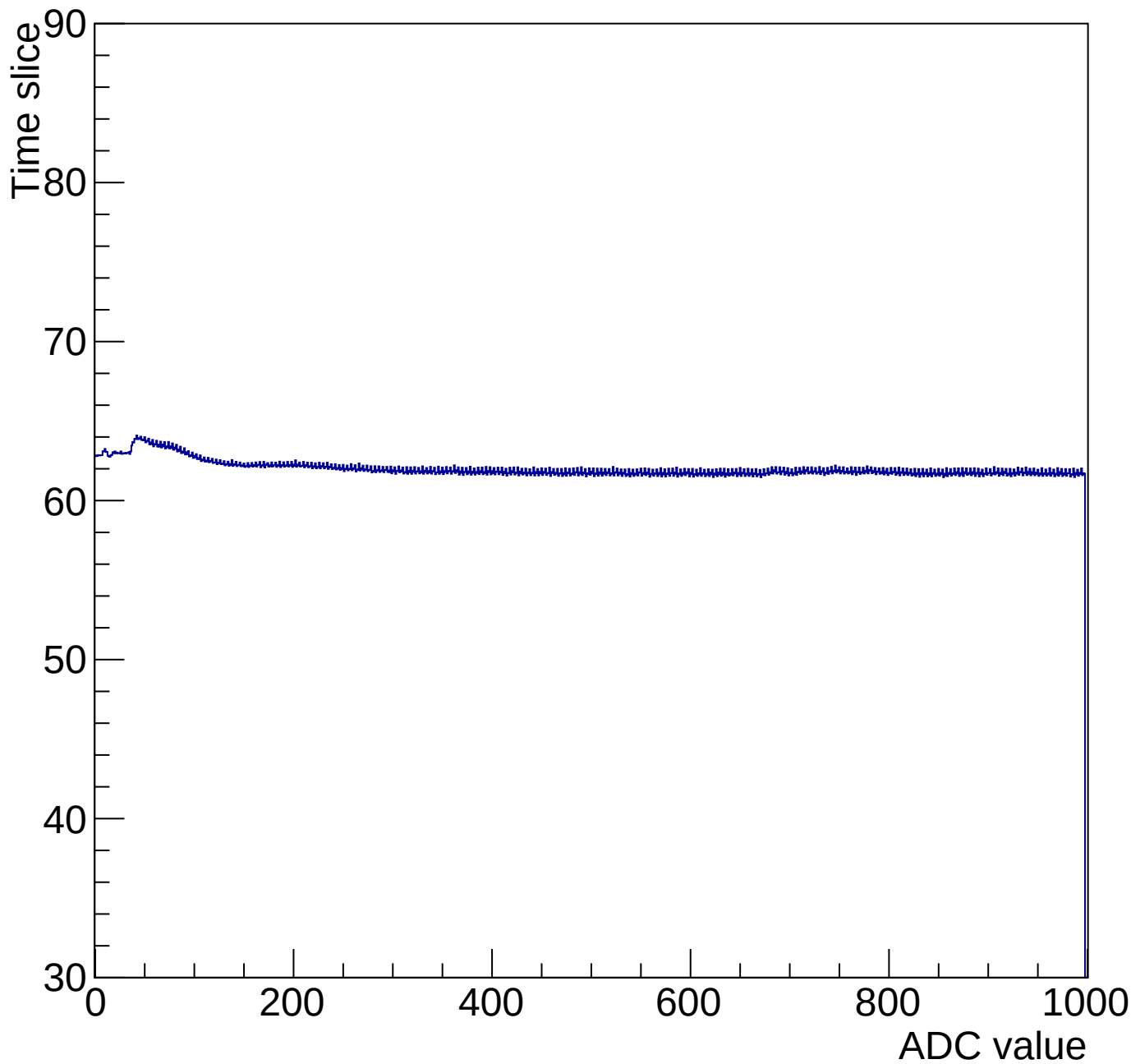
Time mcm 2001 chip 1 chan 10 1000-gain0-shaper0



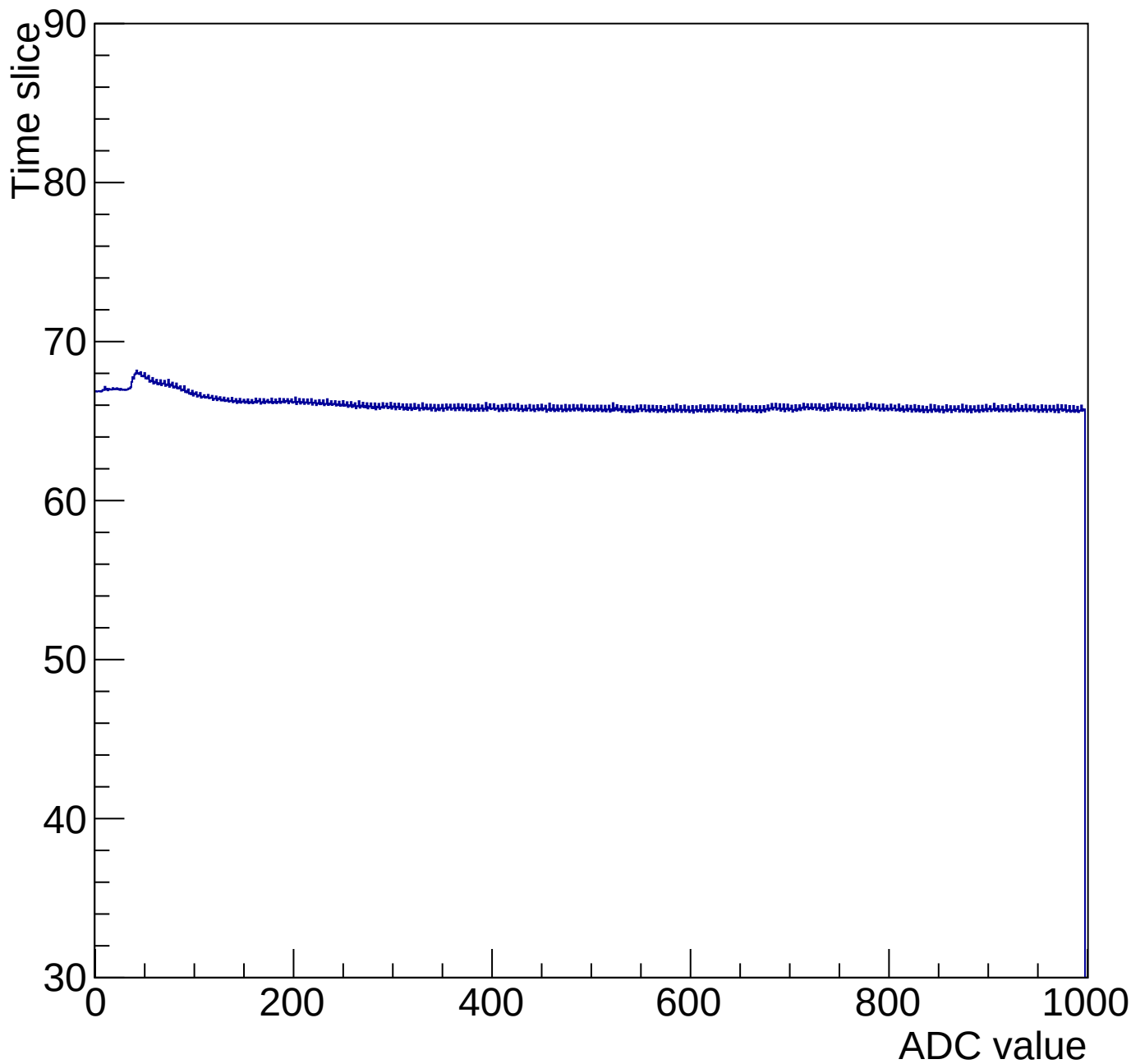
Time mcm 2001 chip 1 chan 11 1000-gain0-shaper0



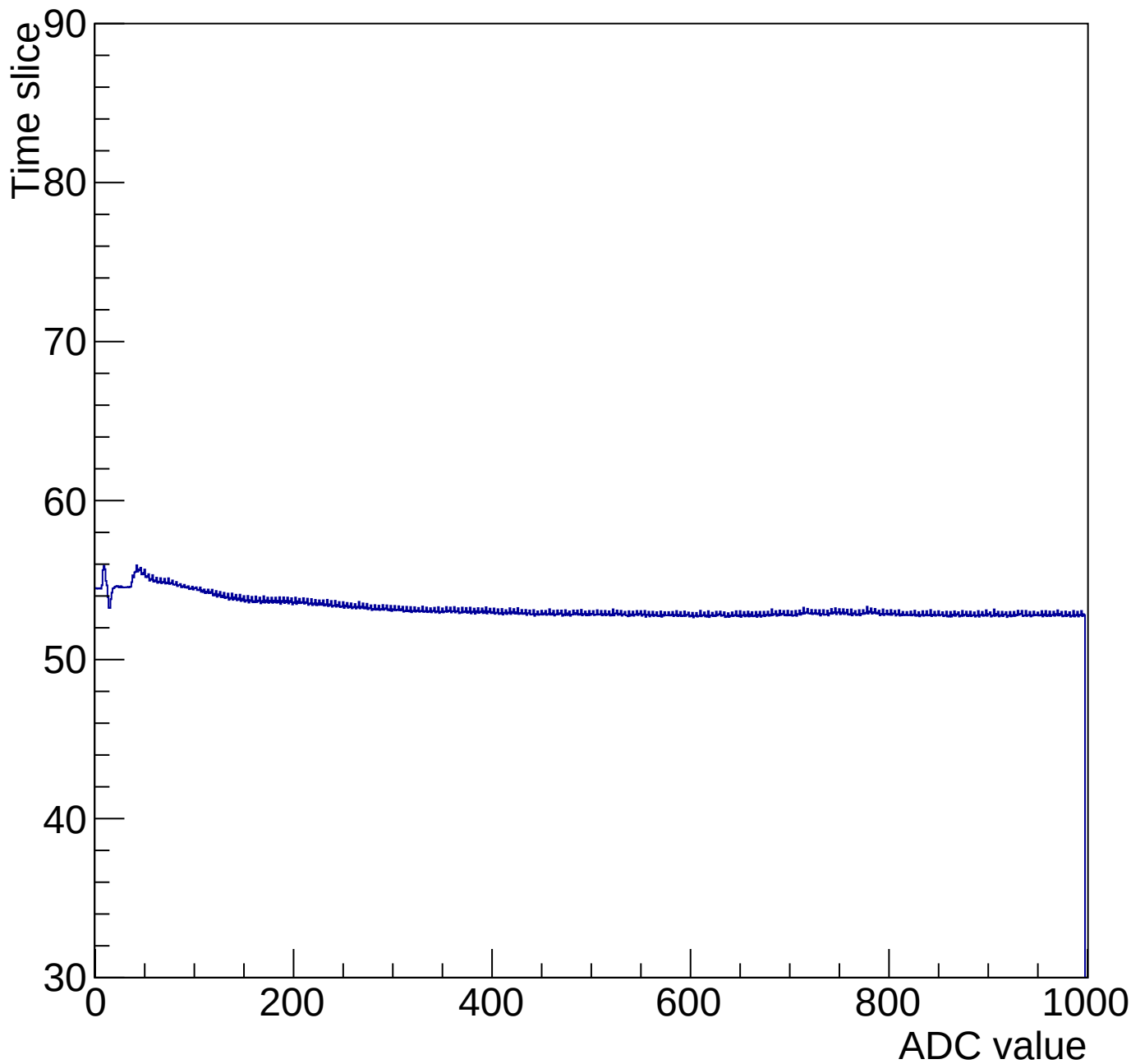
Time mcm 2001 chip 1 chan 12 1000-gain0-shaper0



Time mcm 2001 chip 1 chan 13 1000-gain0-shaper0



Time mcm 2001 chip 1 chan 14 1000-gain0-shaper0



Time mcm 2001 chip 1 chan 15 1000-gain0-shaper0

