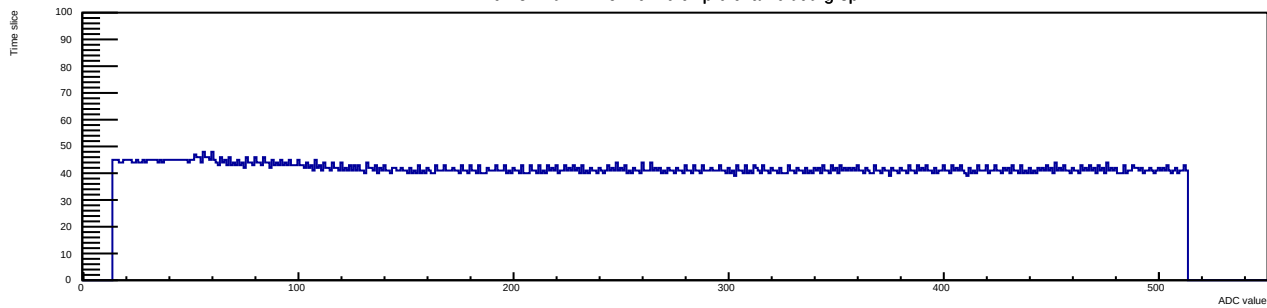
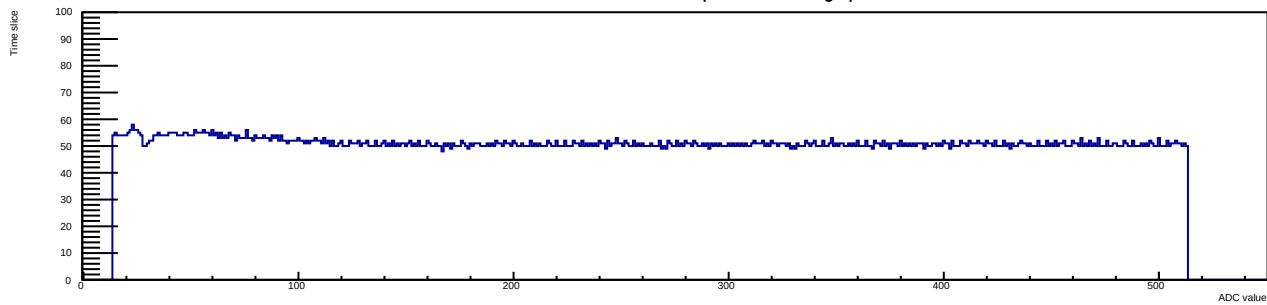


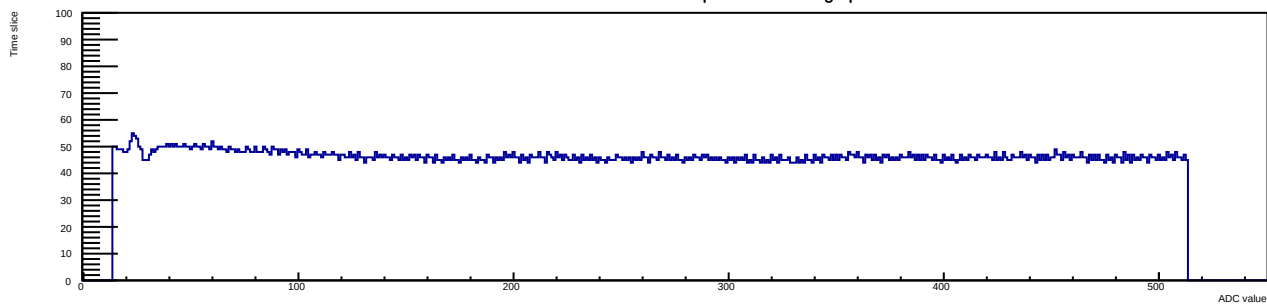
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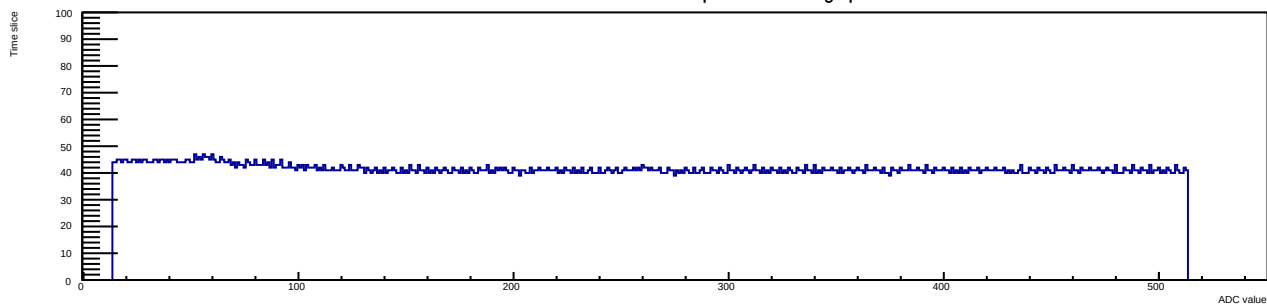
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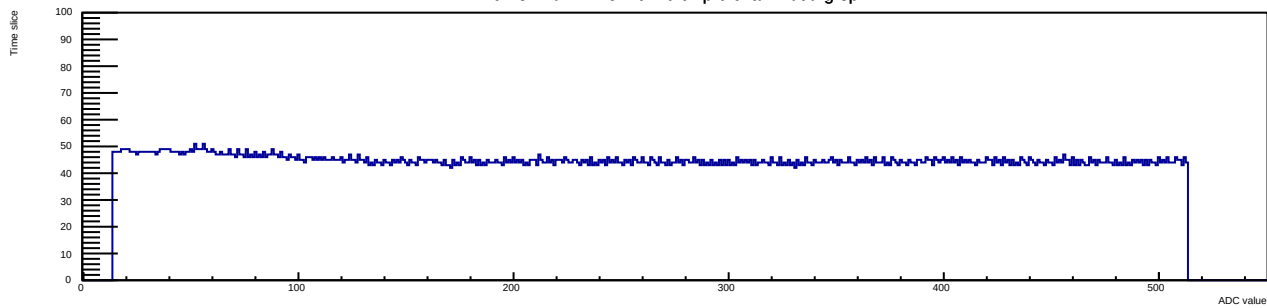
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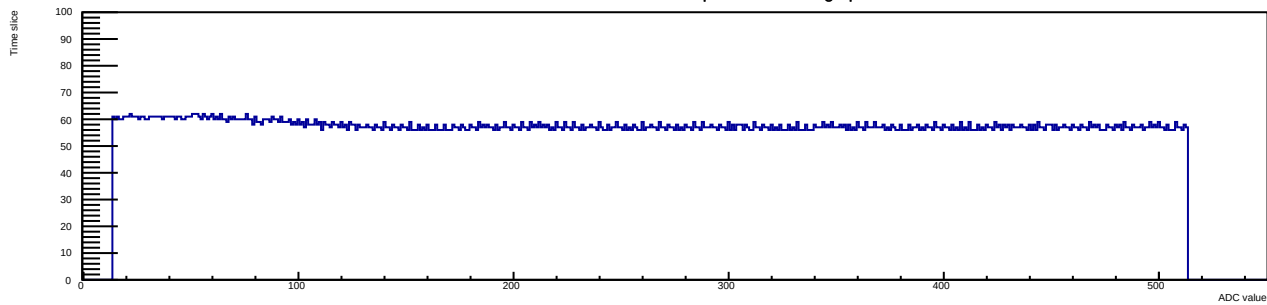
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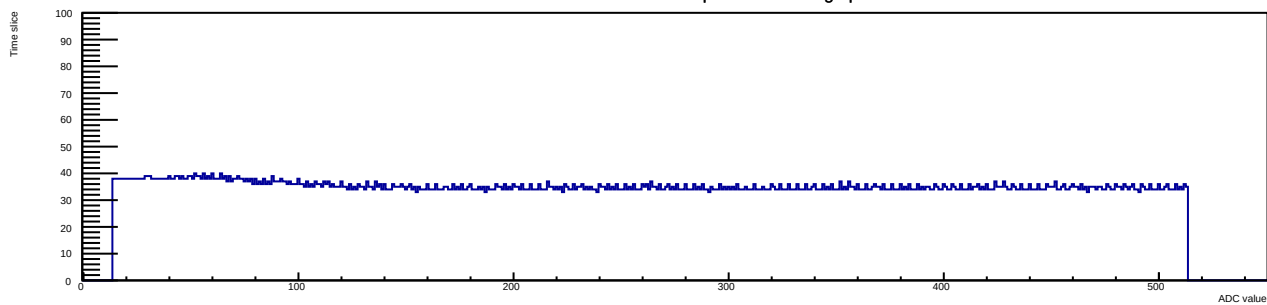
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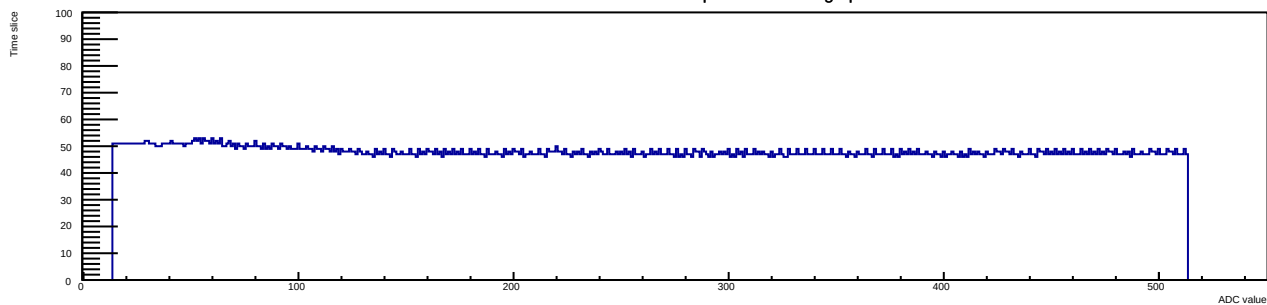
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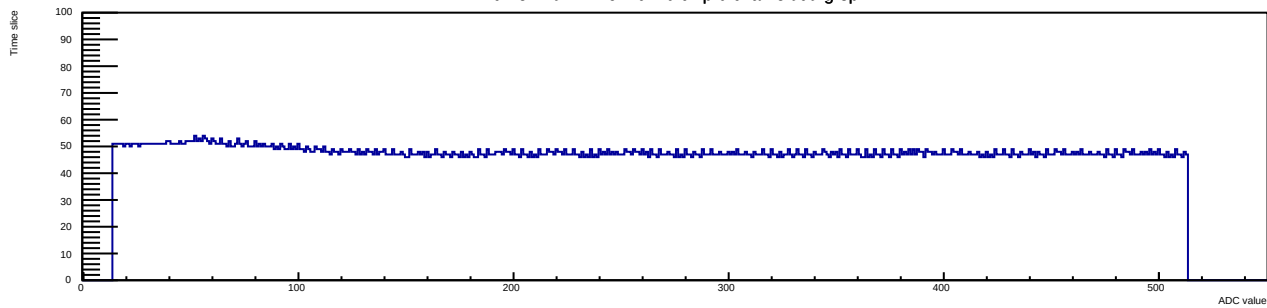
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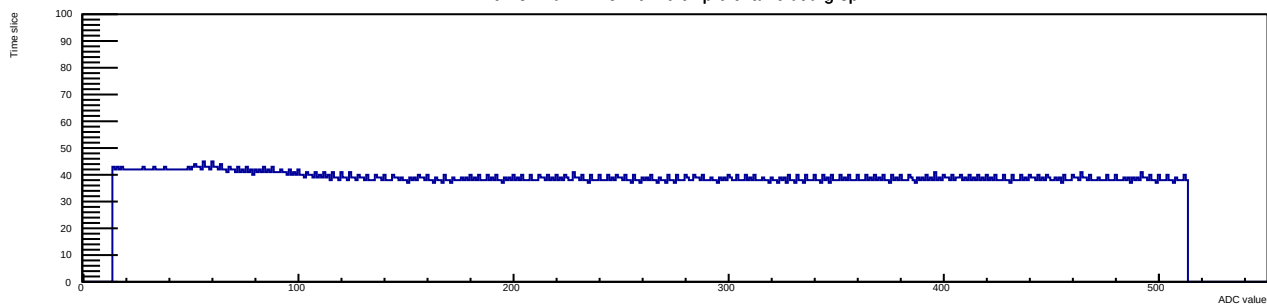
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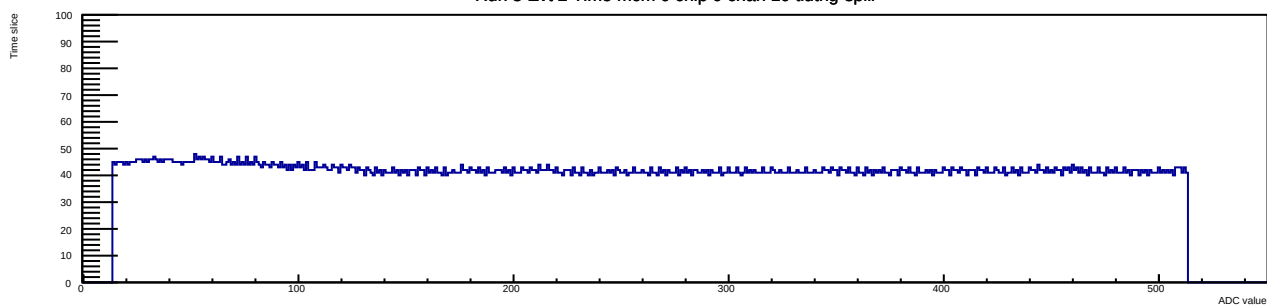
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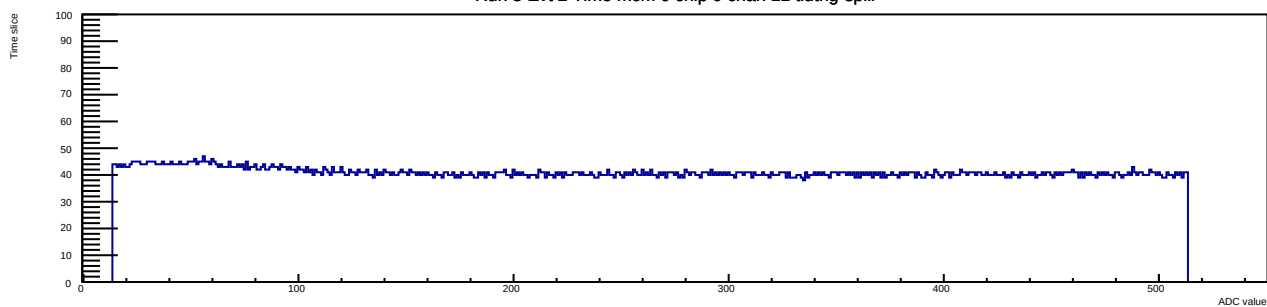
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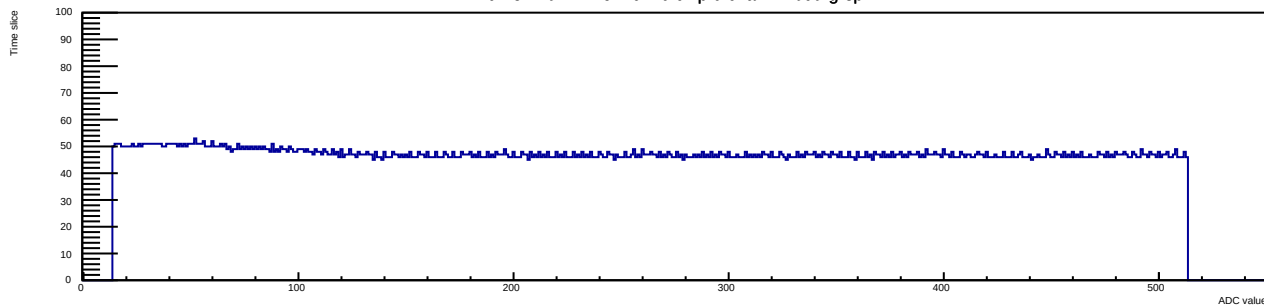
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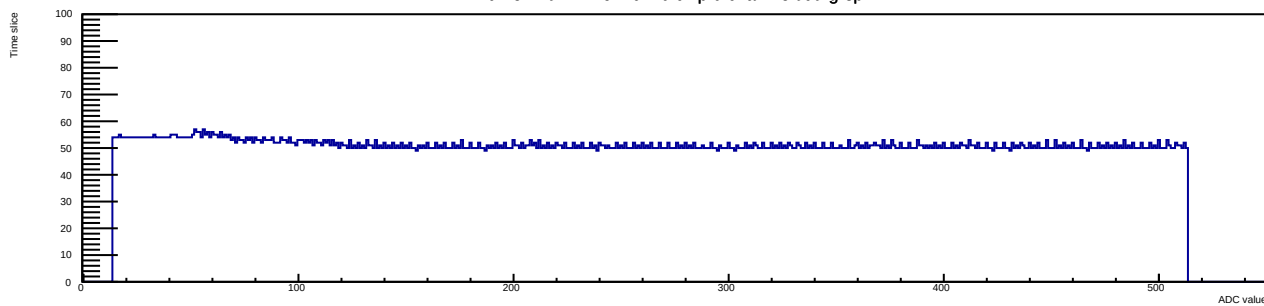
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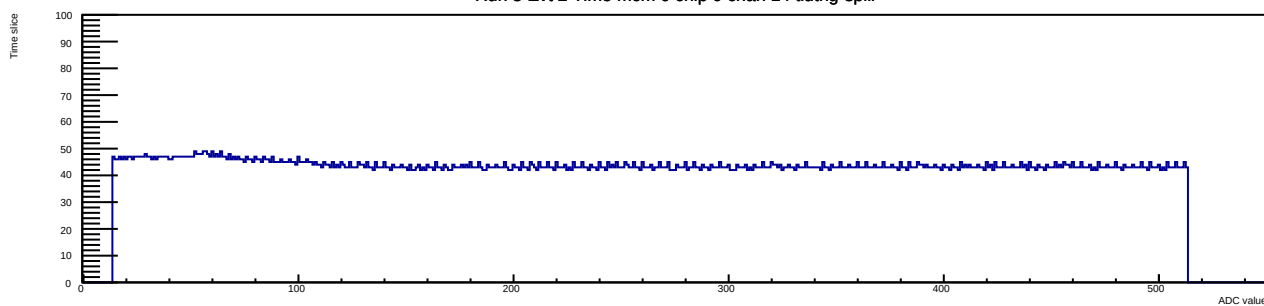
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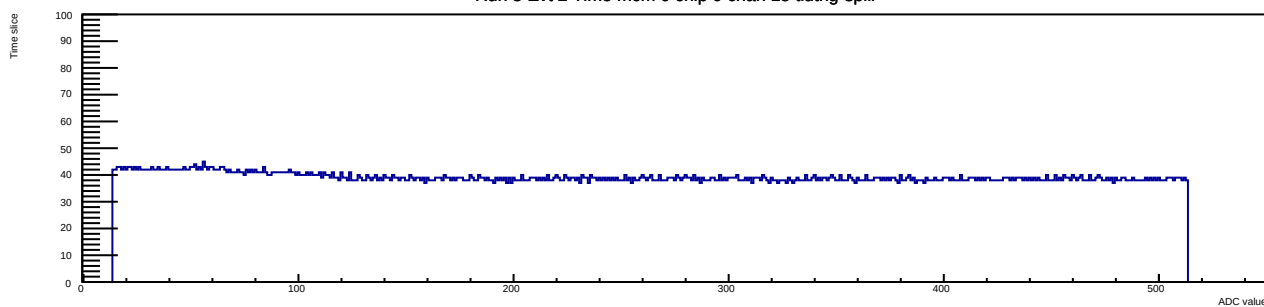
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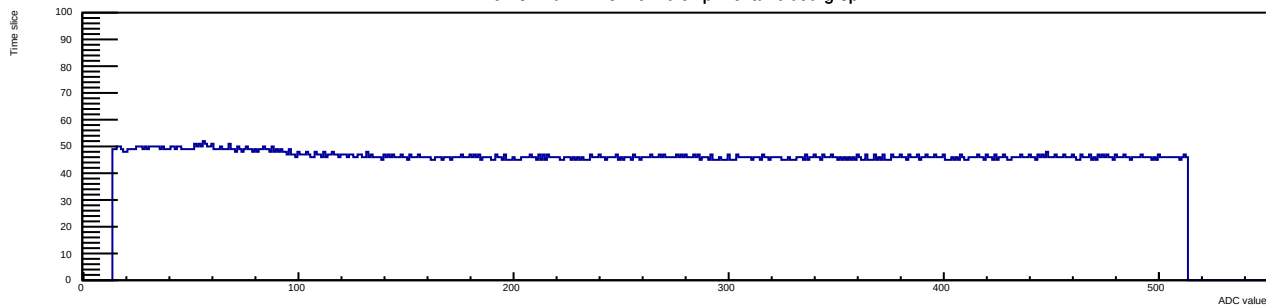
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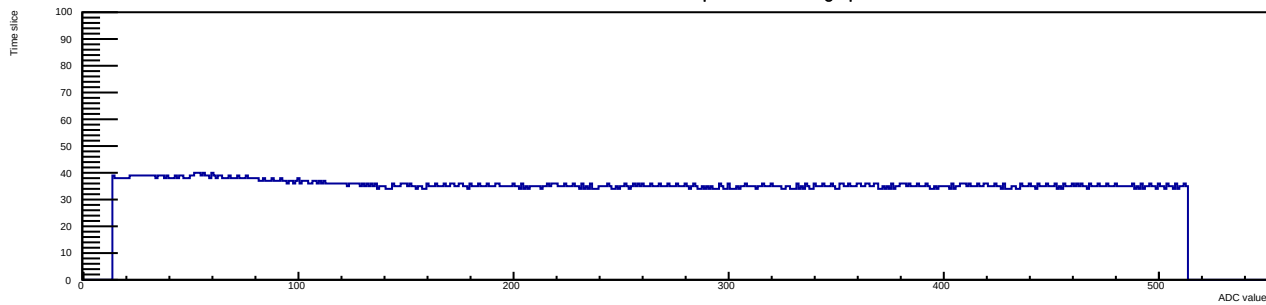
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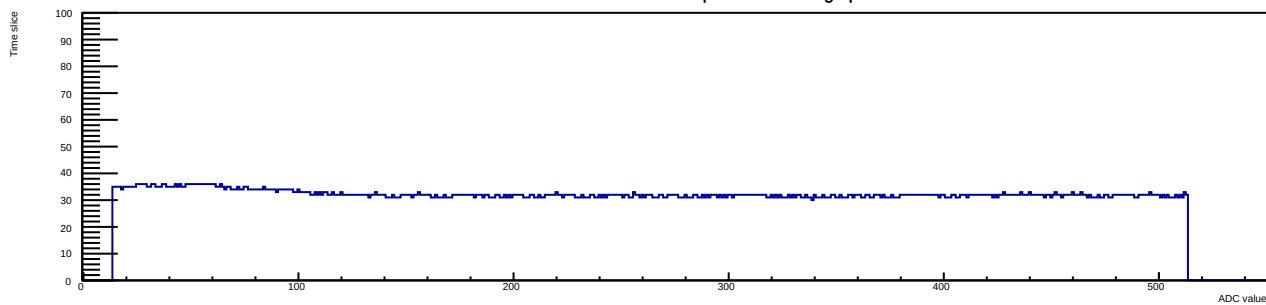
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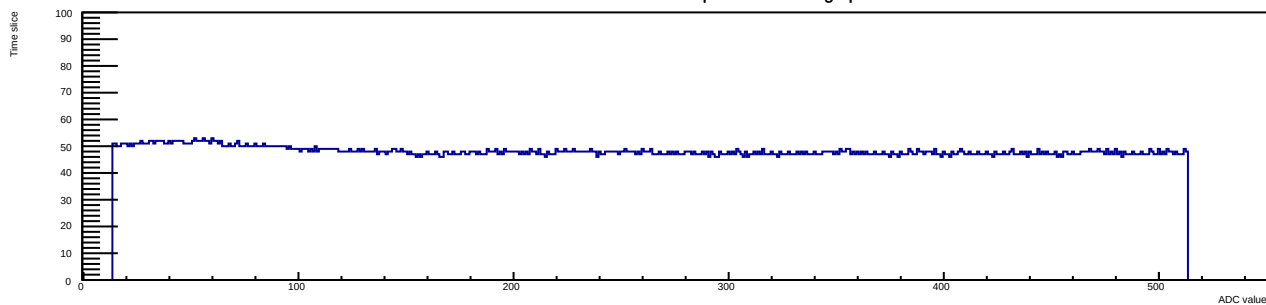
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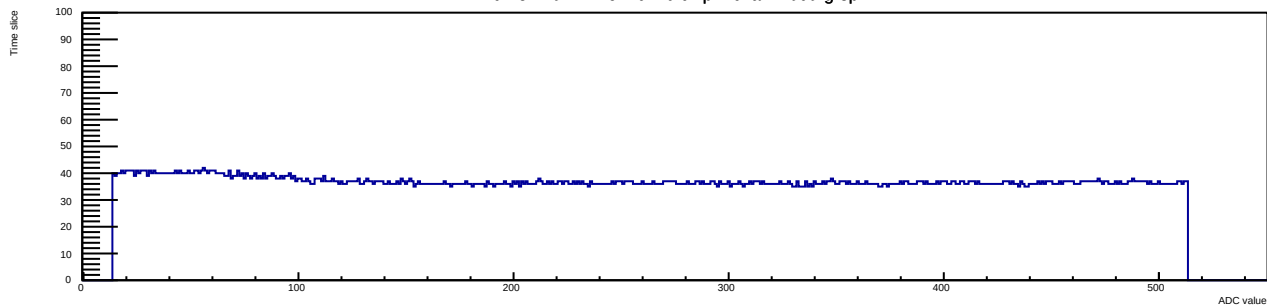
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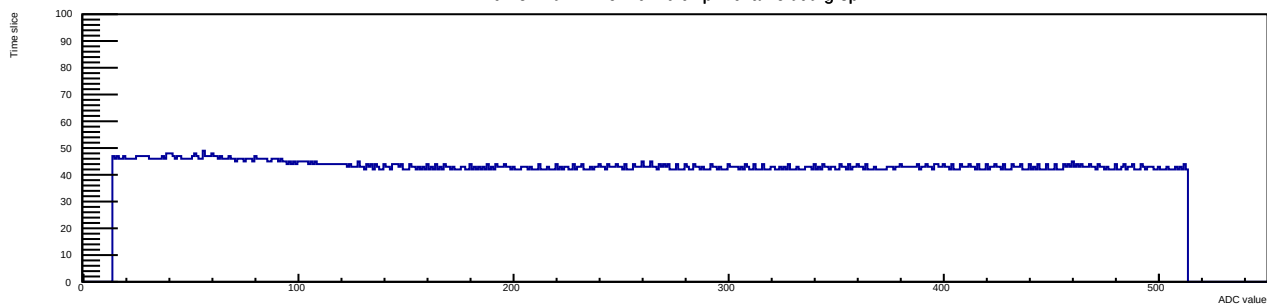
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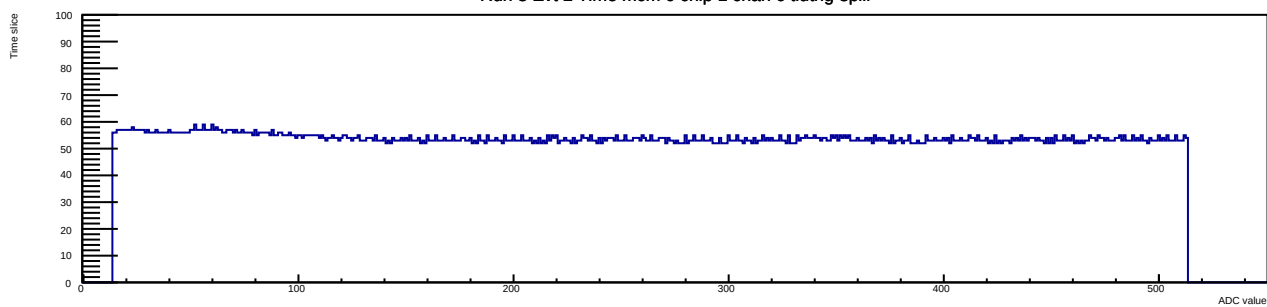
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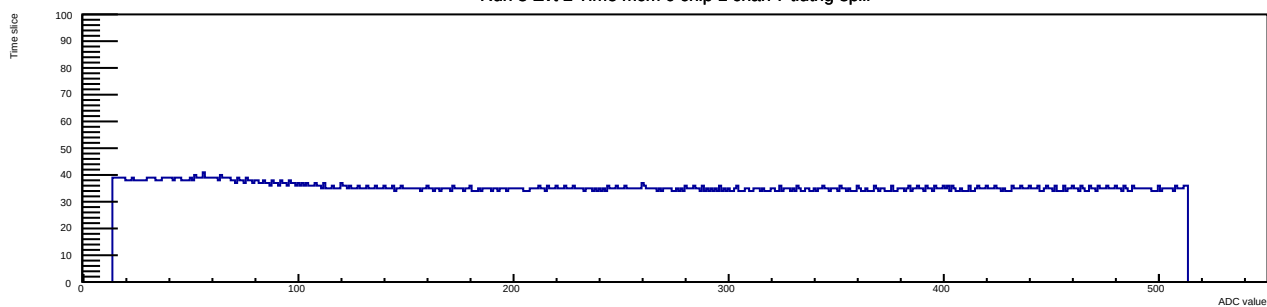
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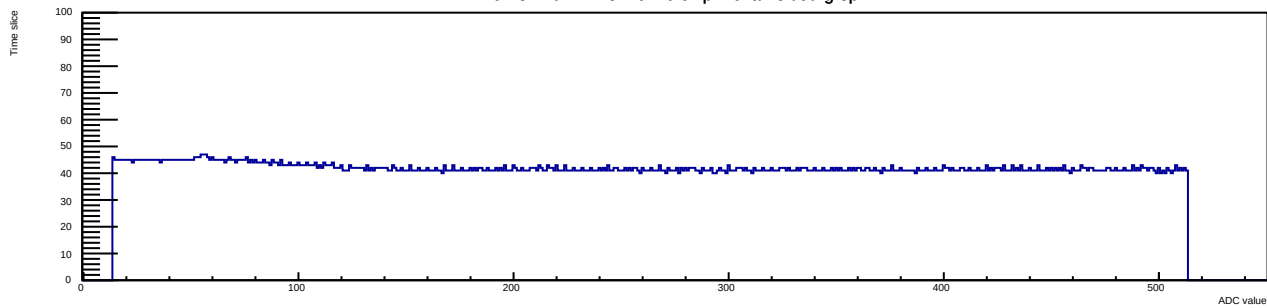
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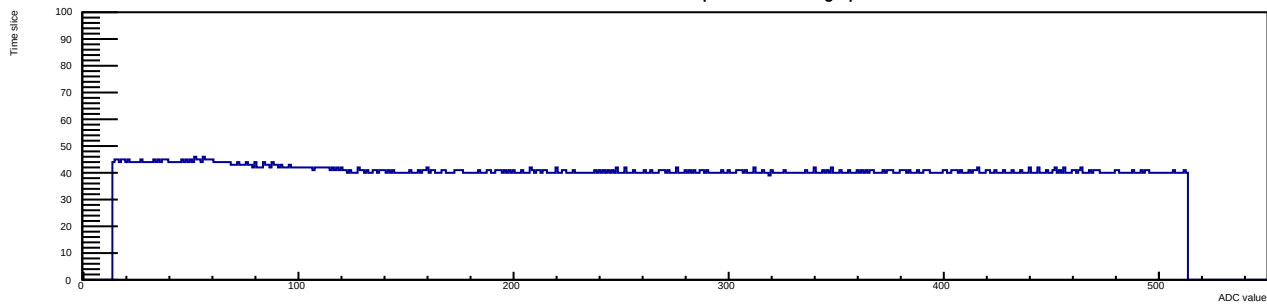
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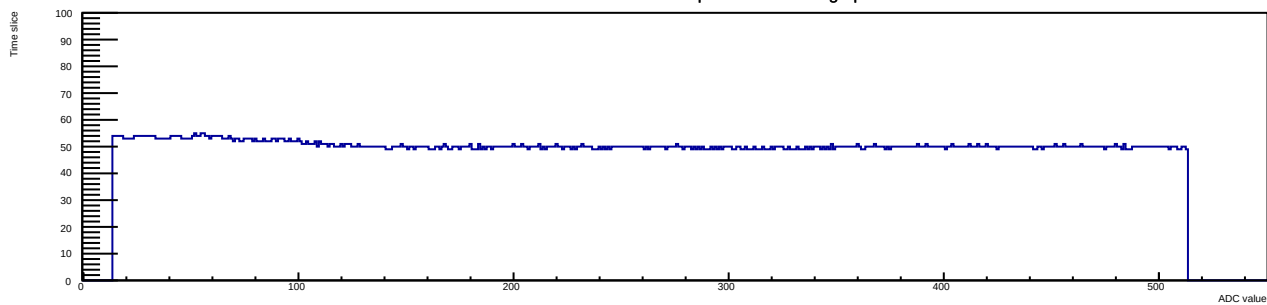
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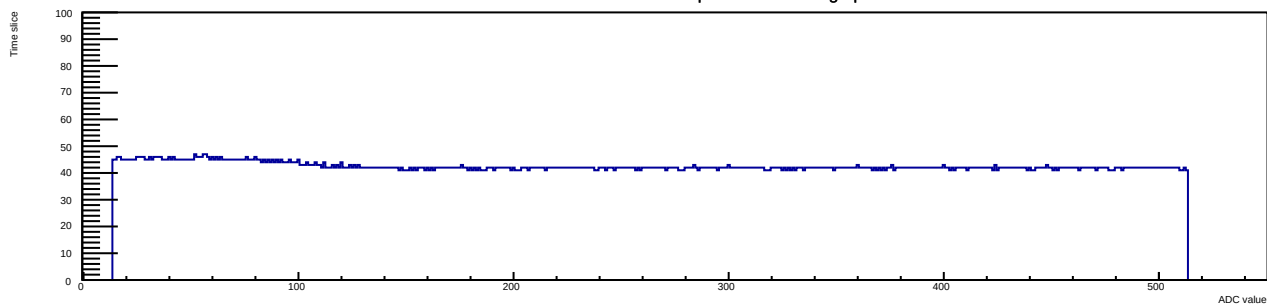
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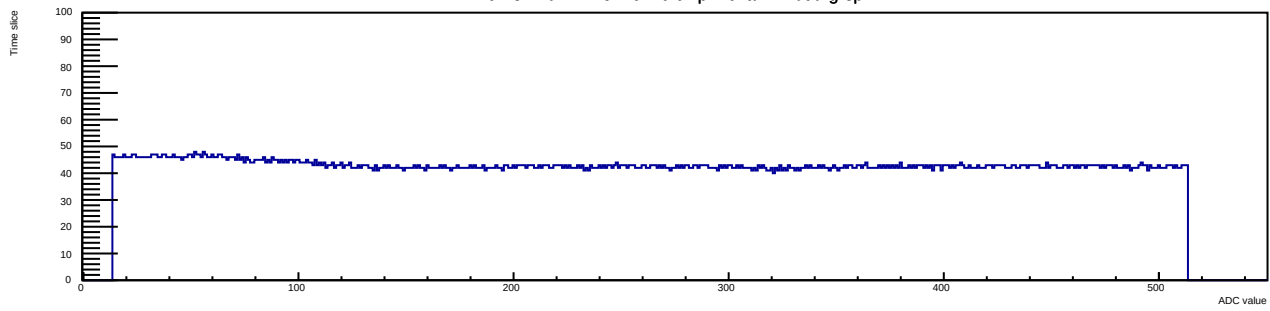
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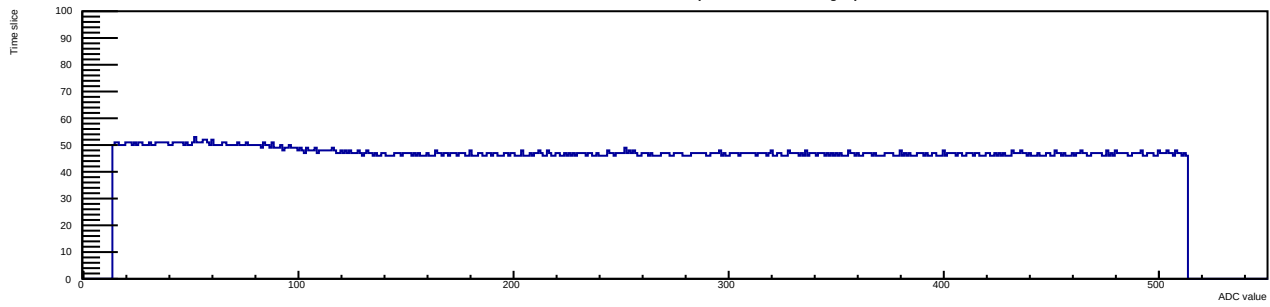
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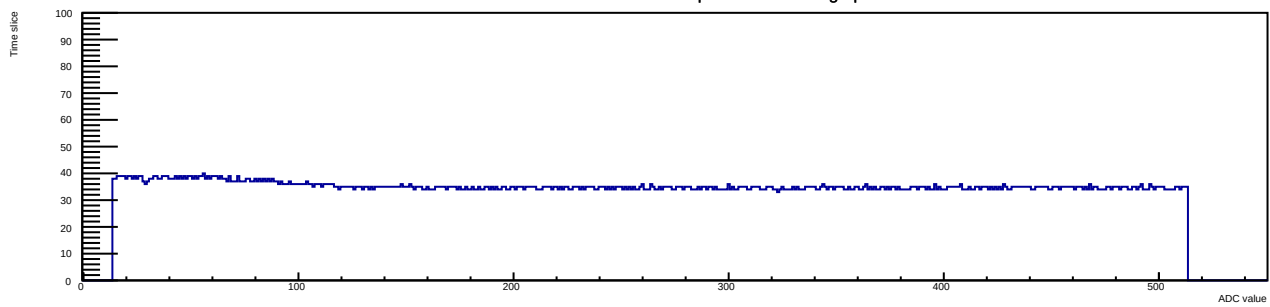
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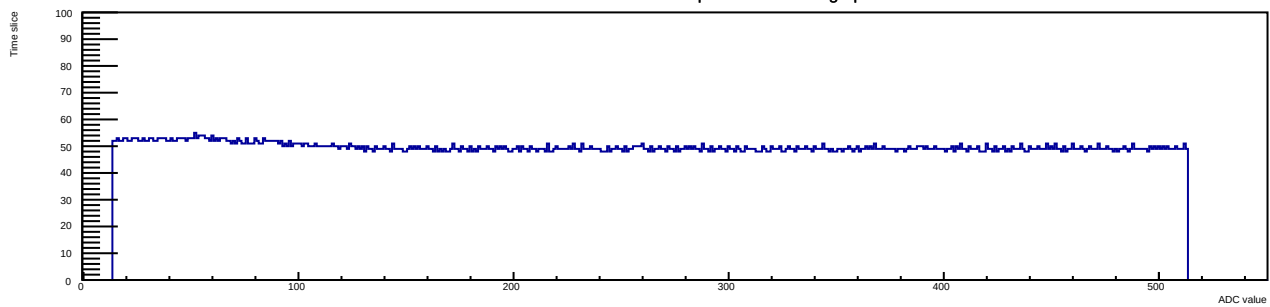
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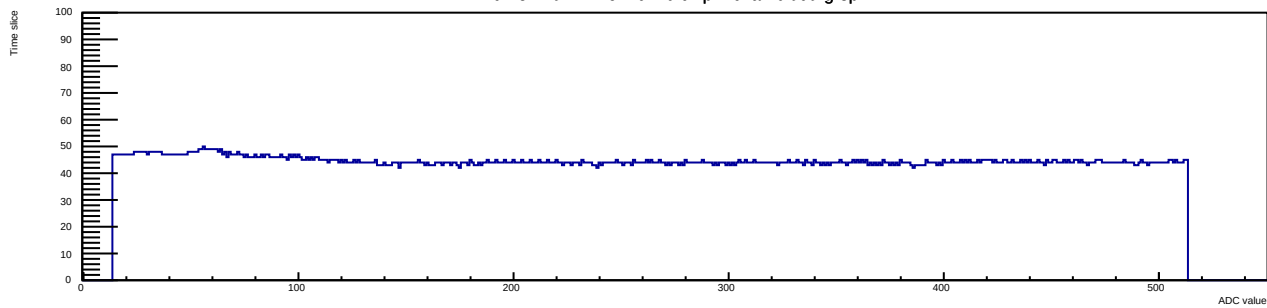
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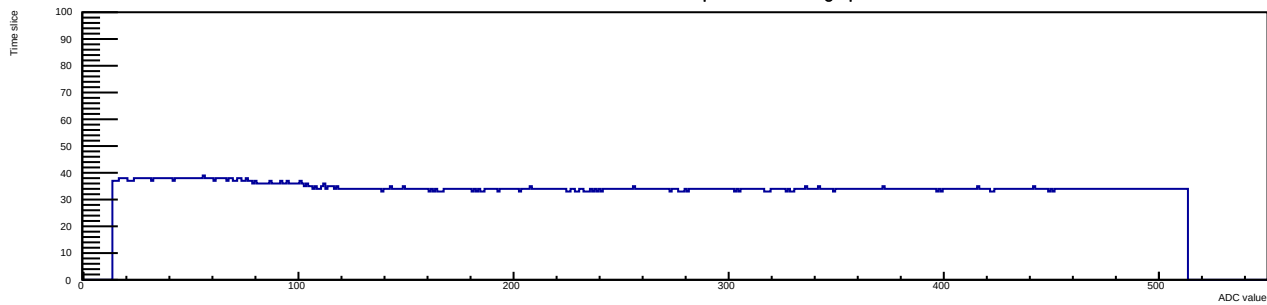
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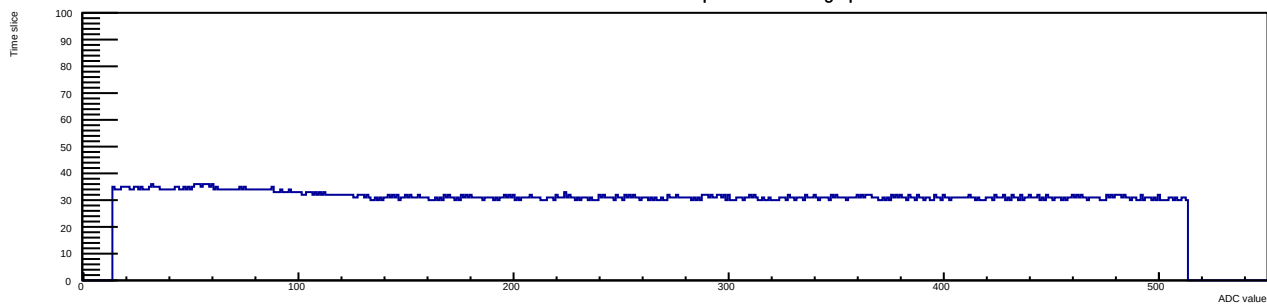
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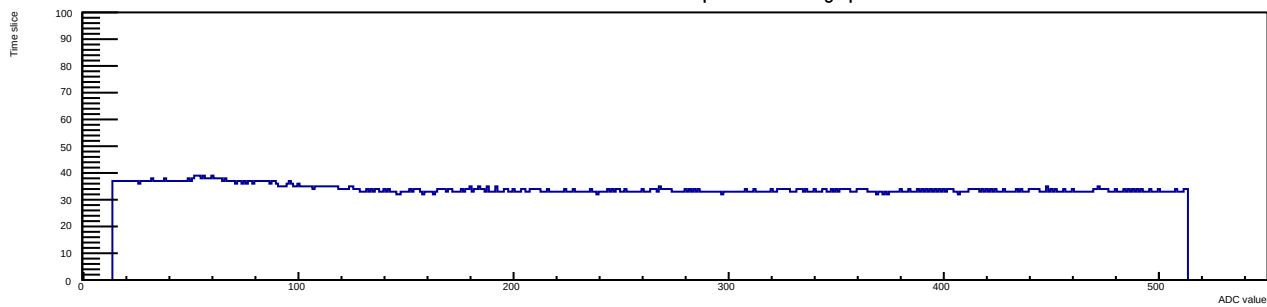
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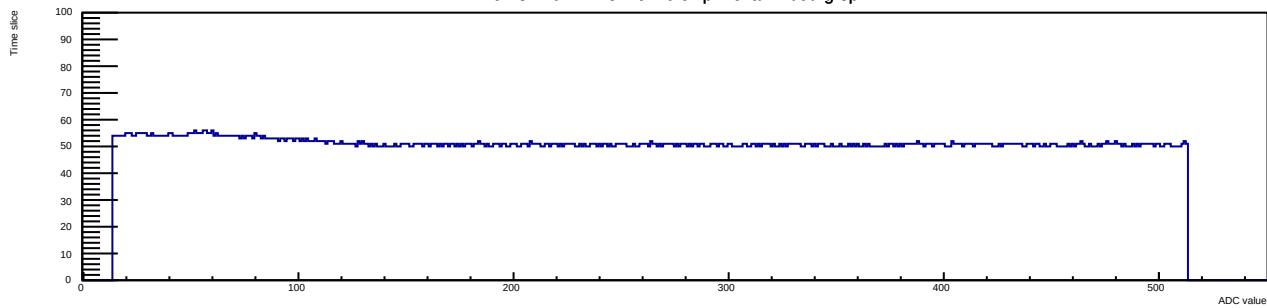
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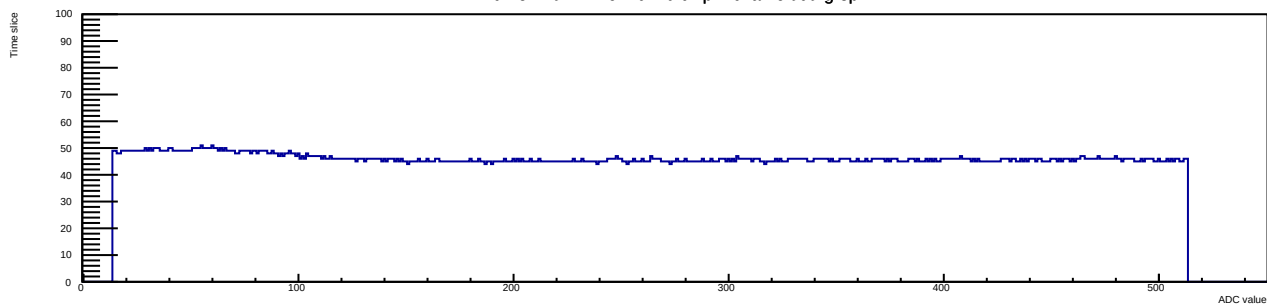
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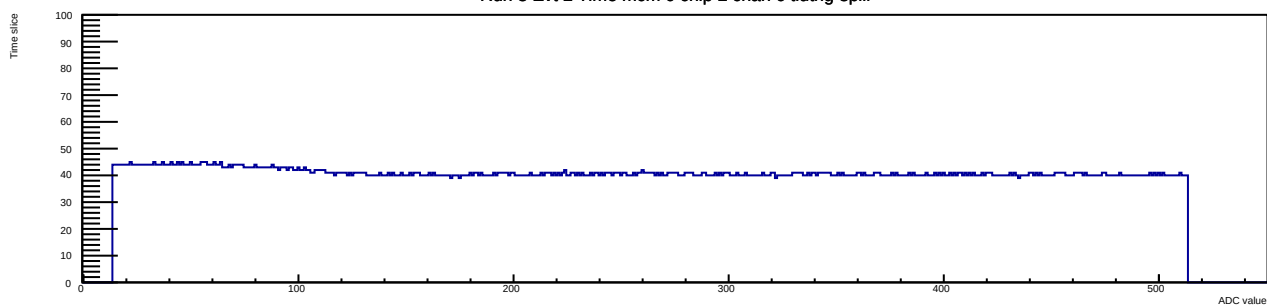
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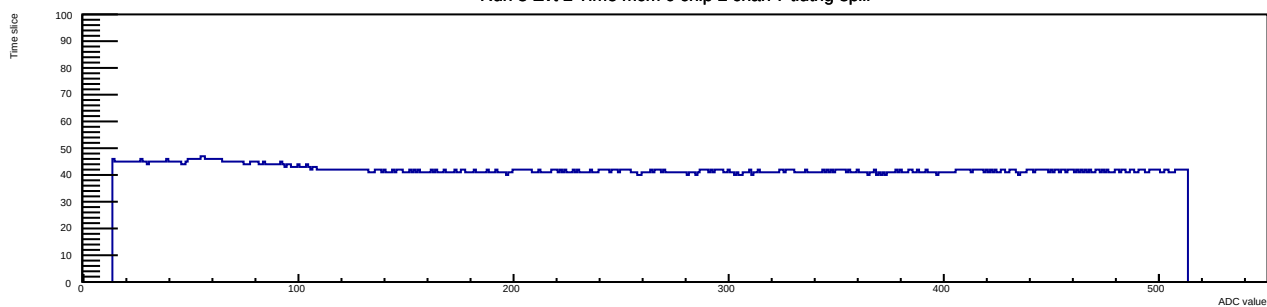
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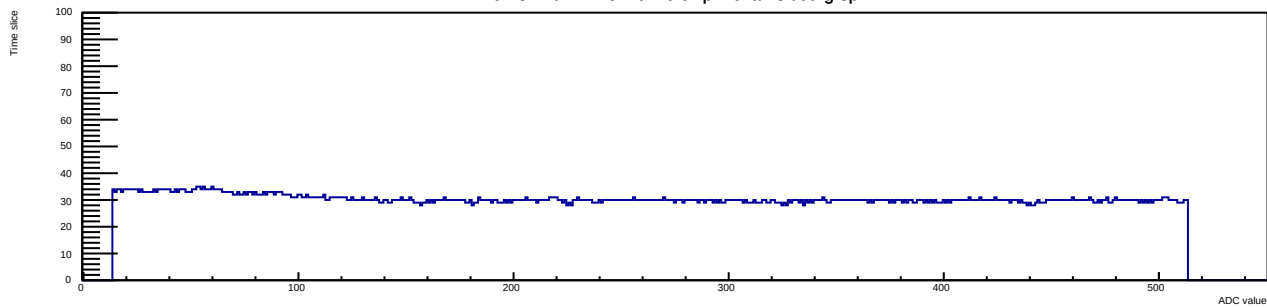
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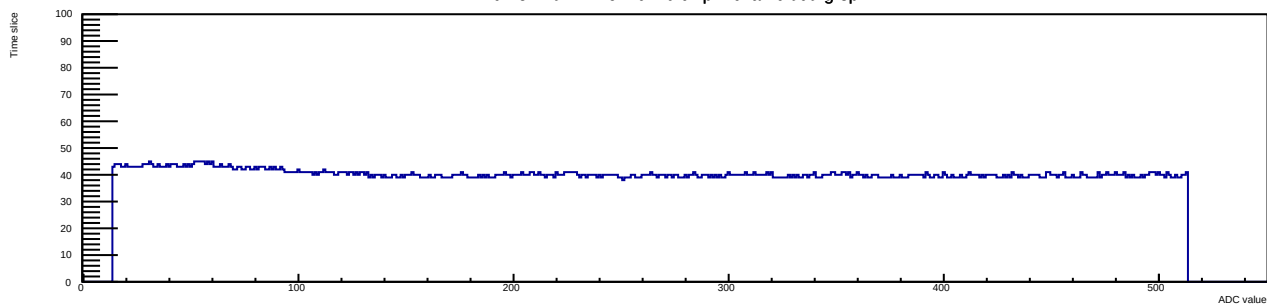
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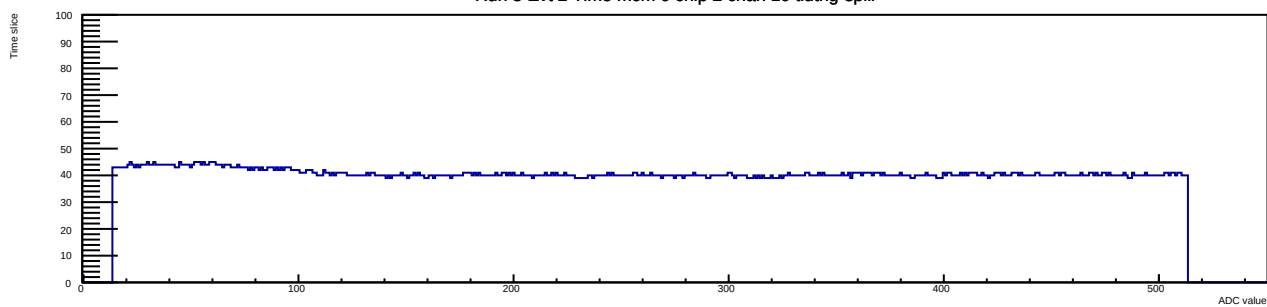
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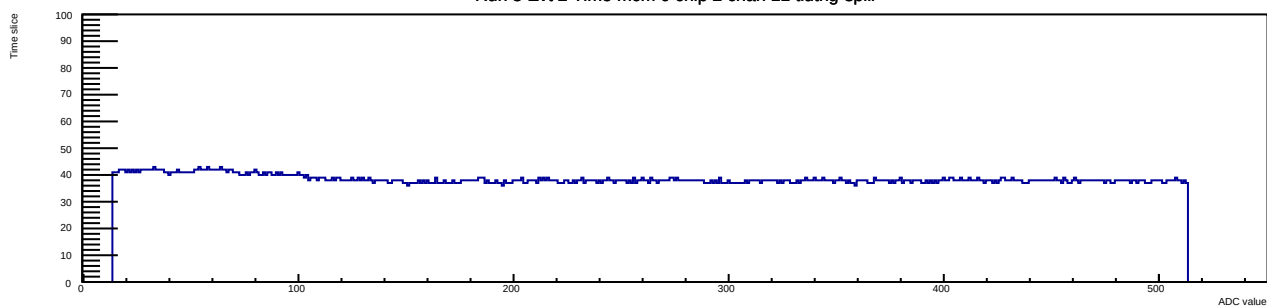
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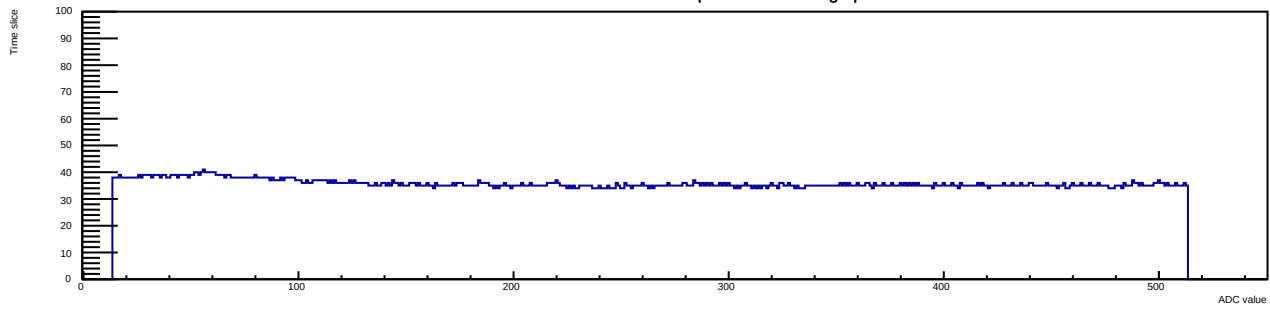
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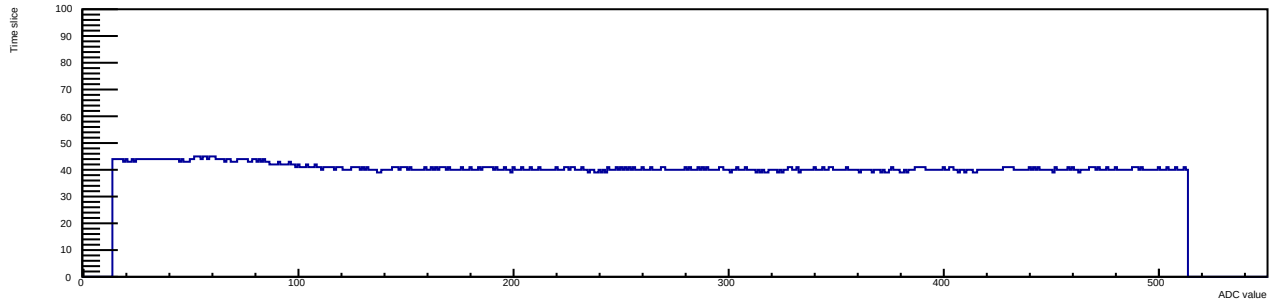
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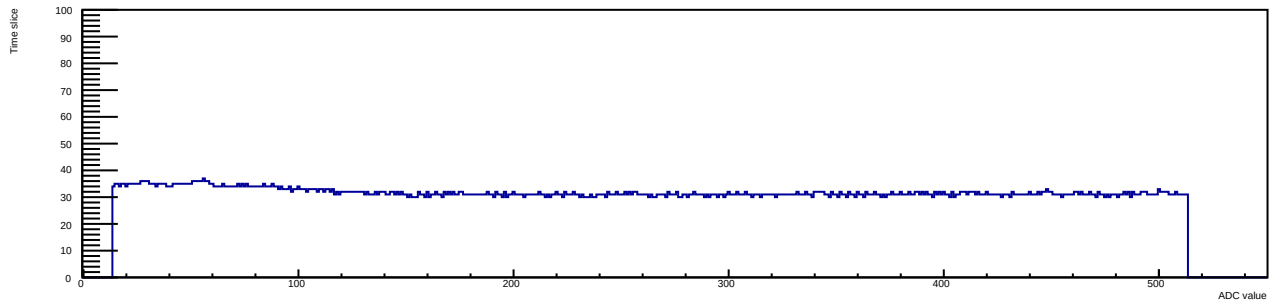
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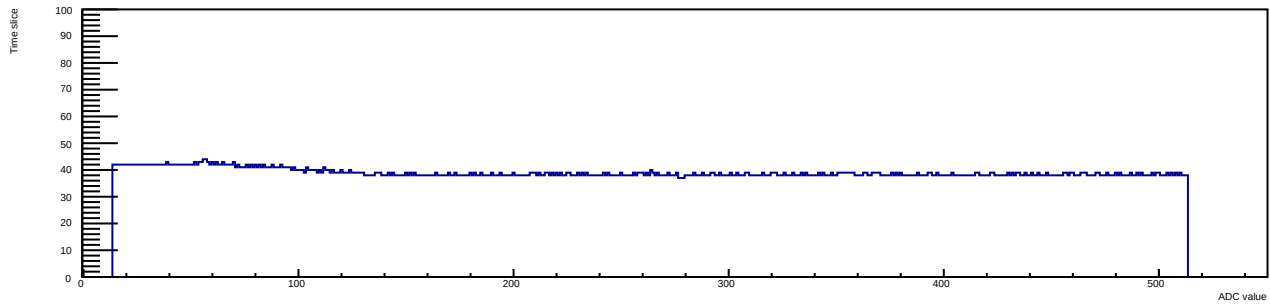
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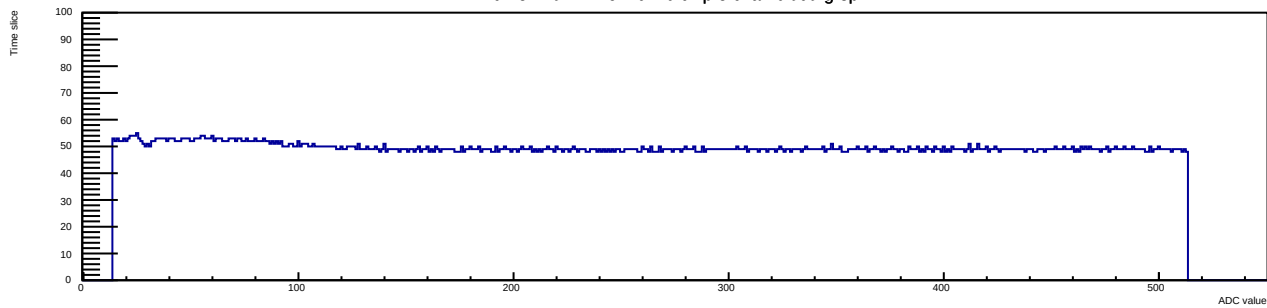
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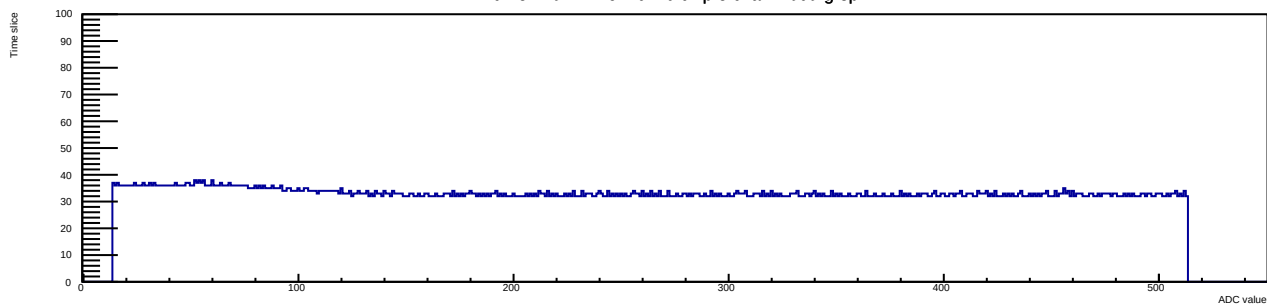
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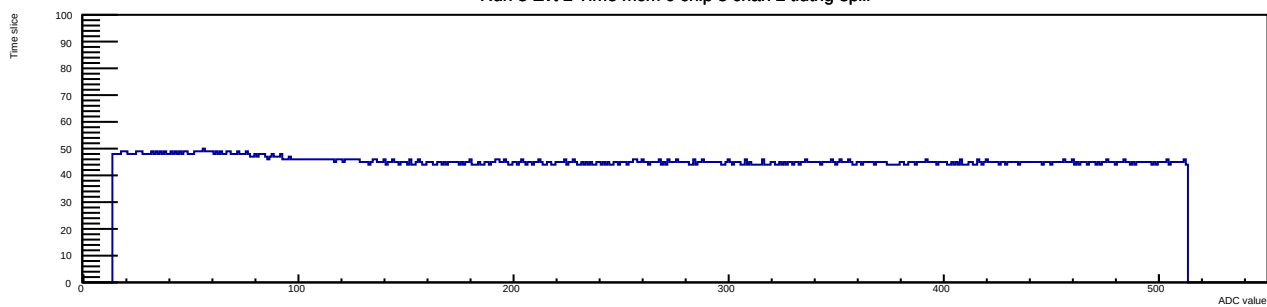
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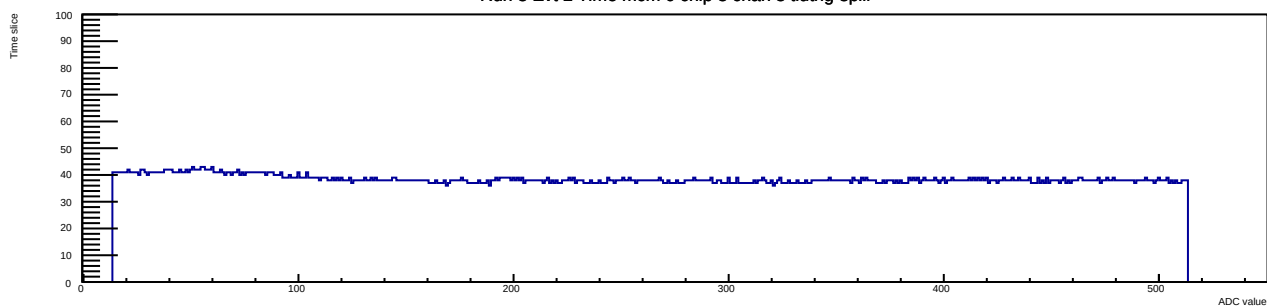
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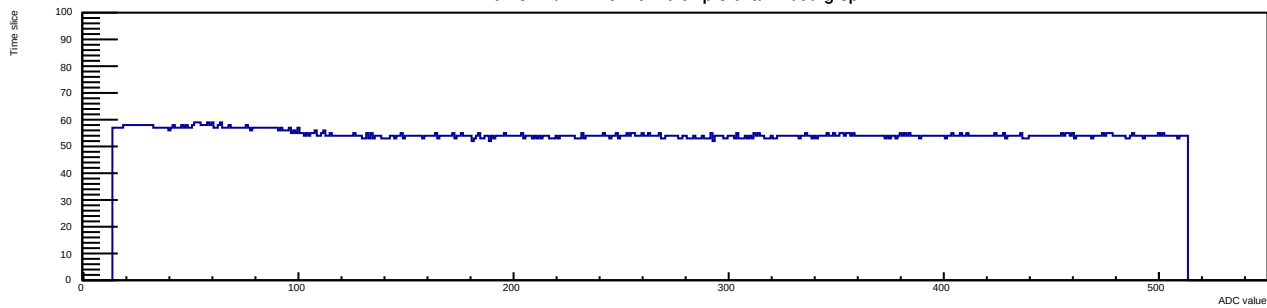
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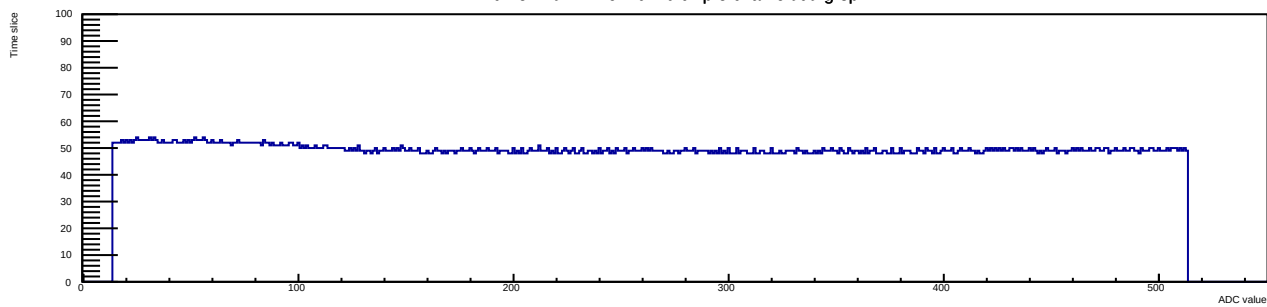
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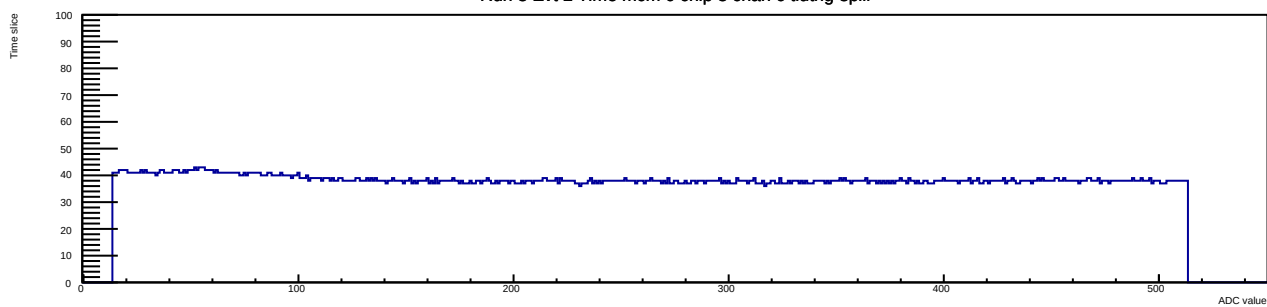
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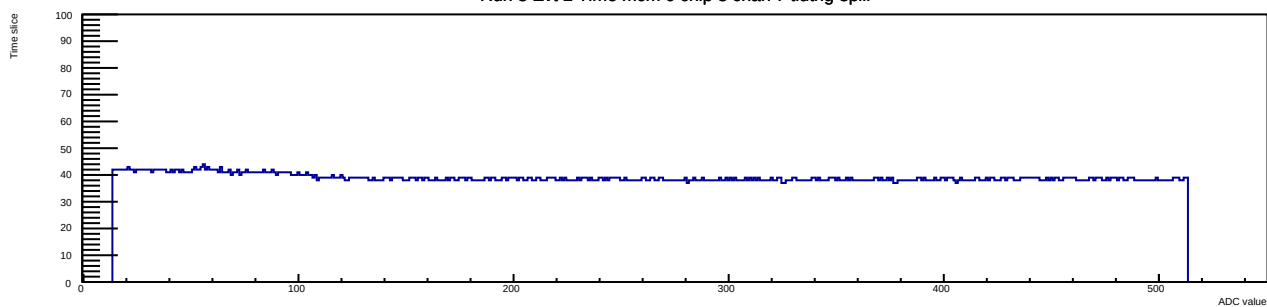
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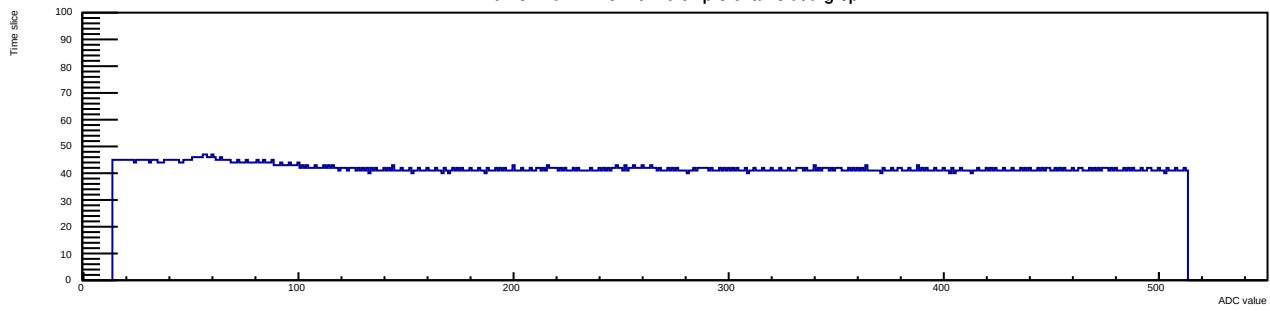
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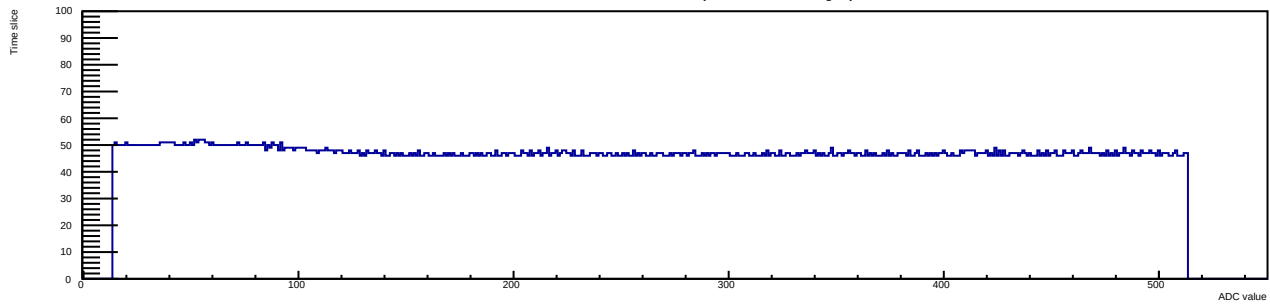
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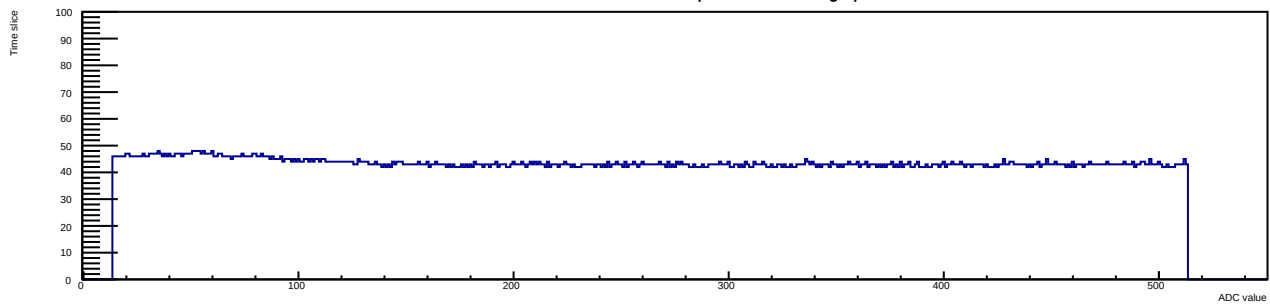
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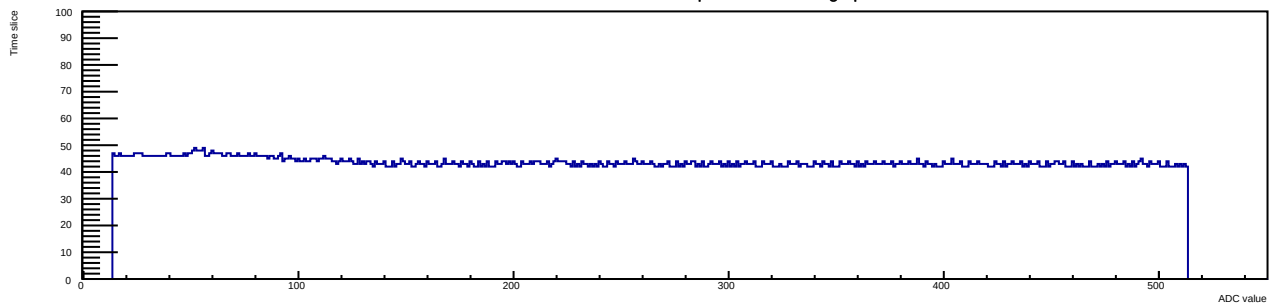
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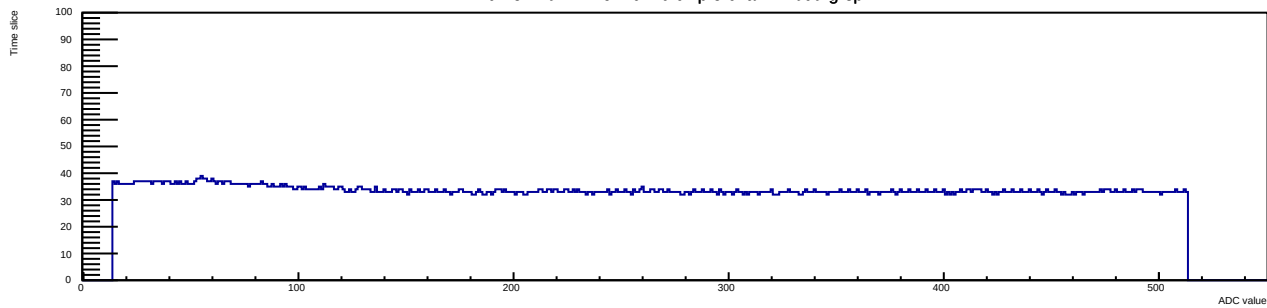
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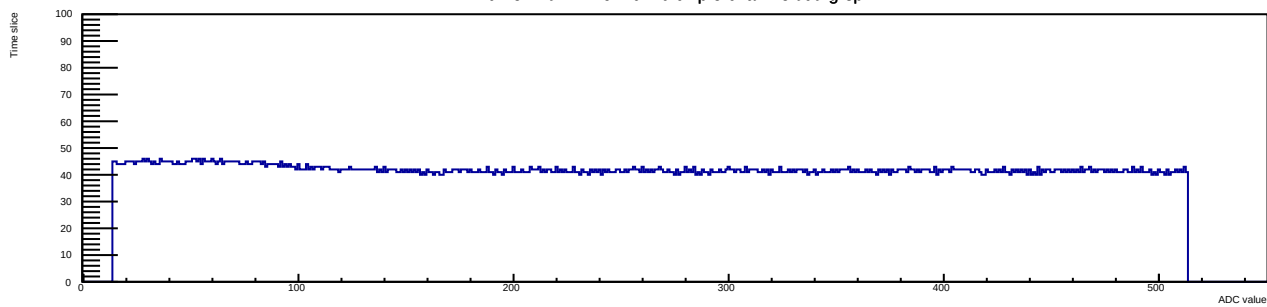
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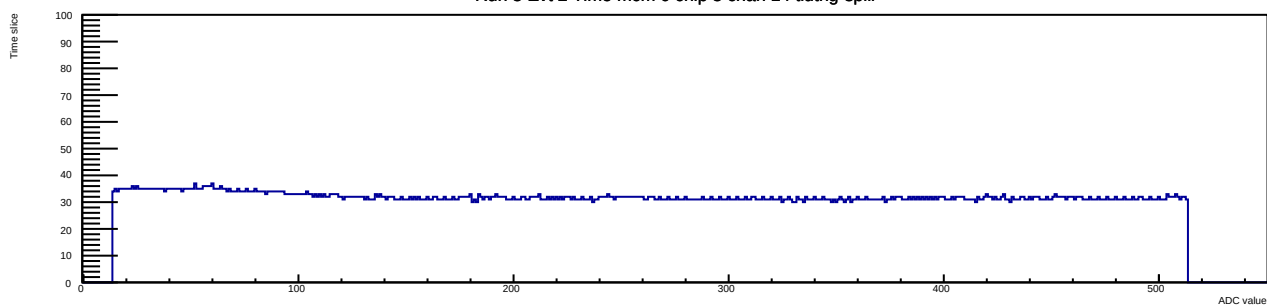
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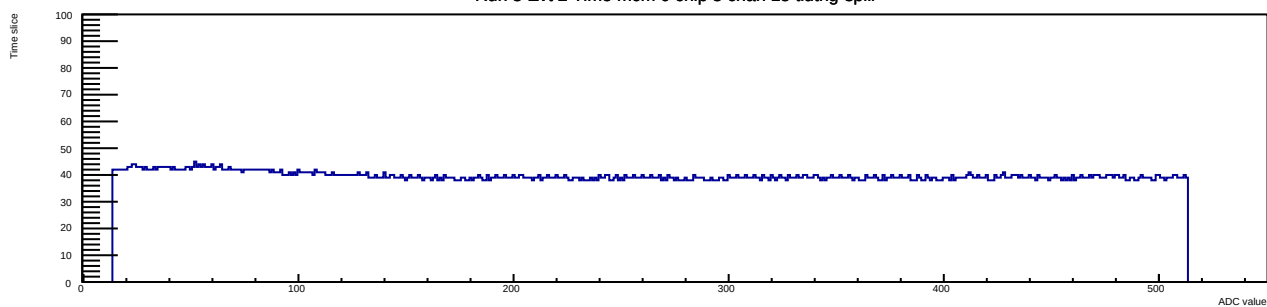
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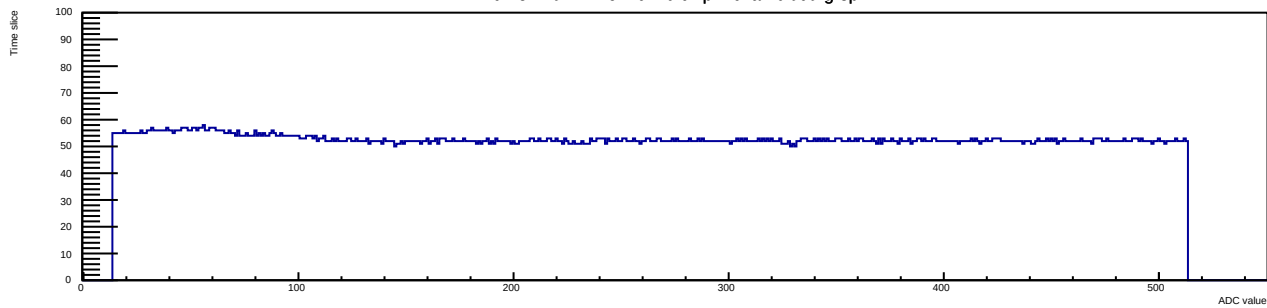
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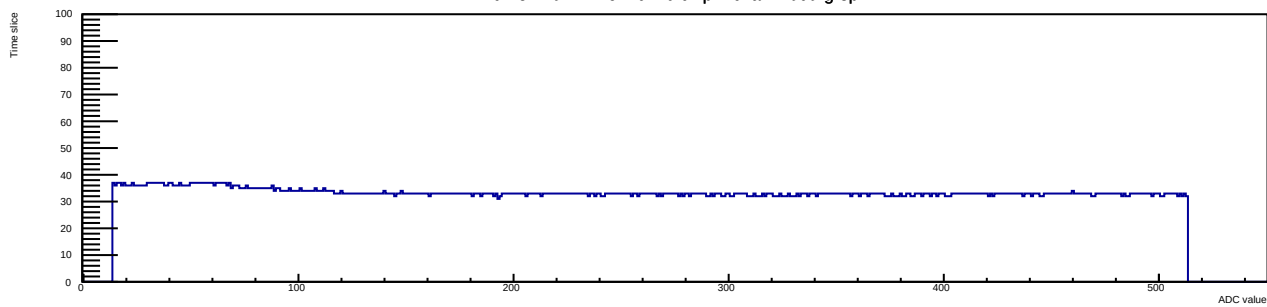
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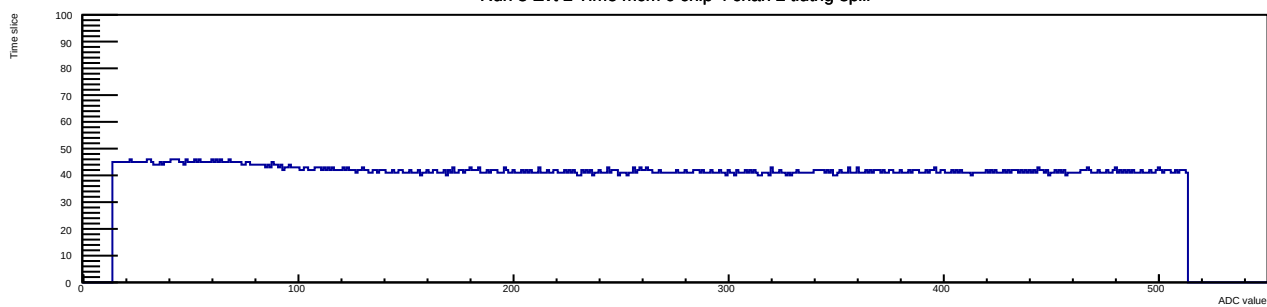
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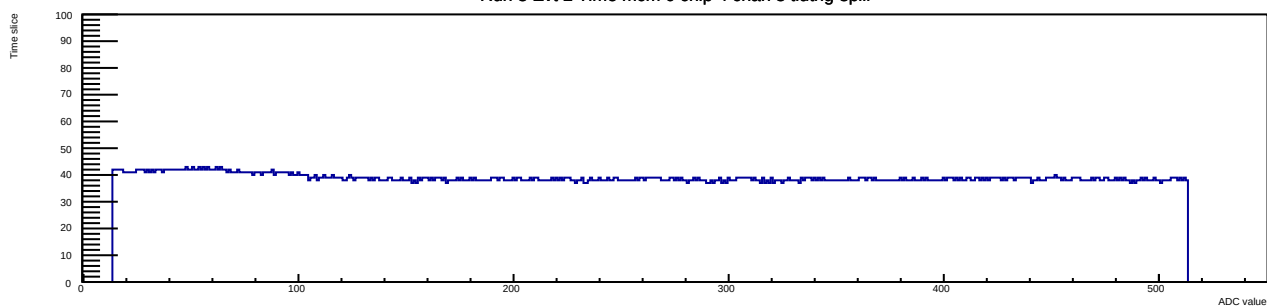
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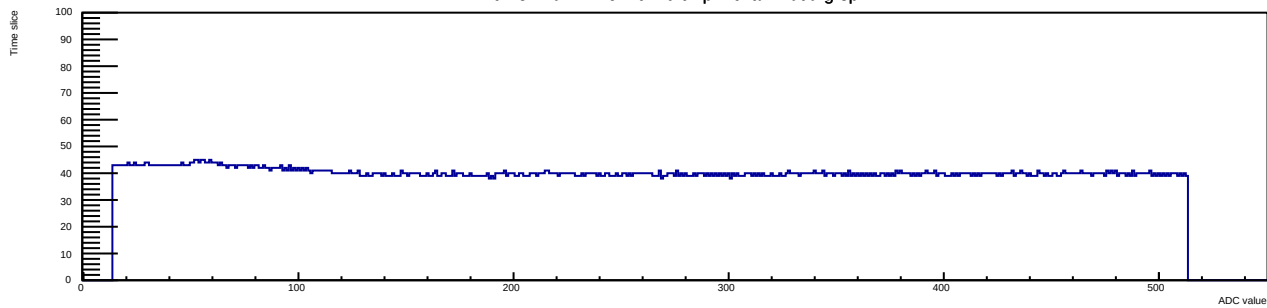
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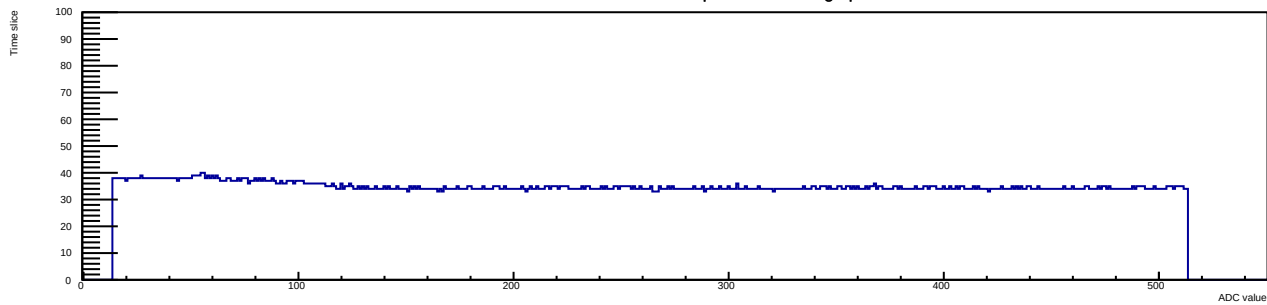
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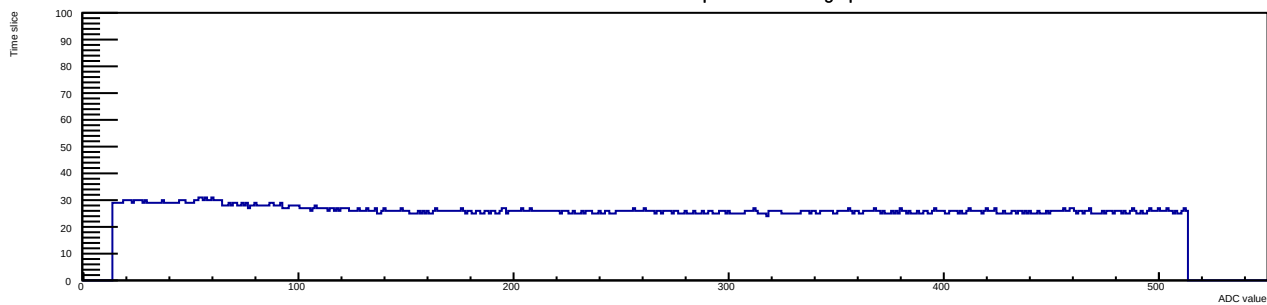
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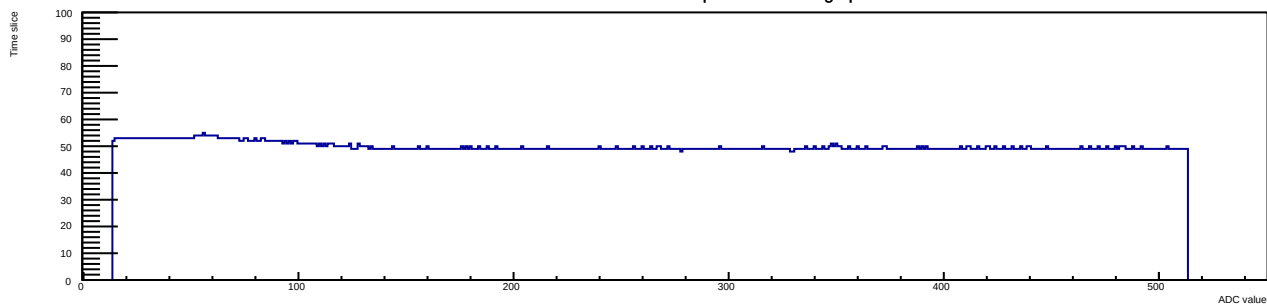
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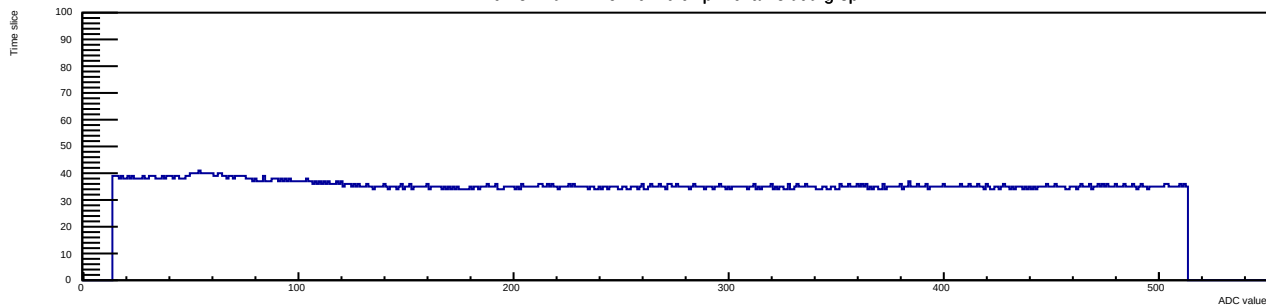
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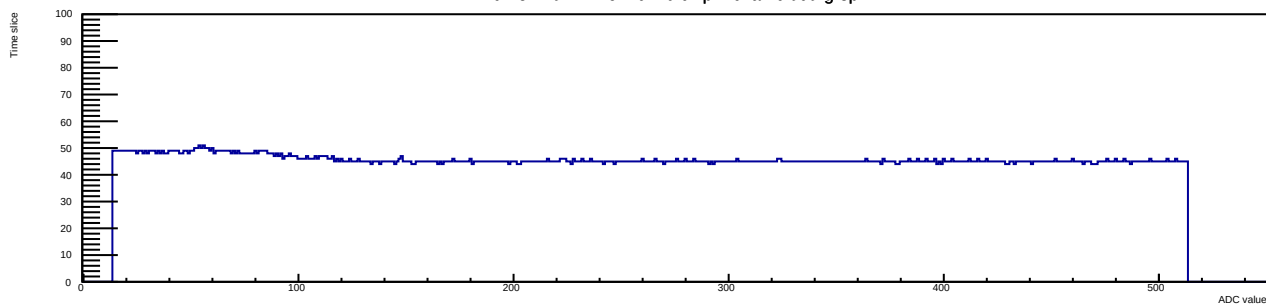
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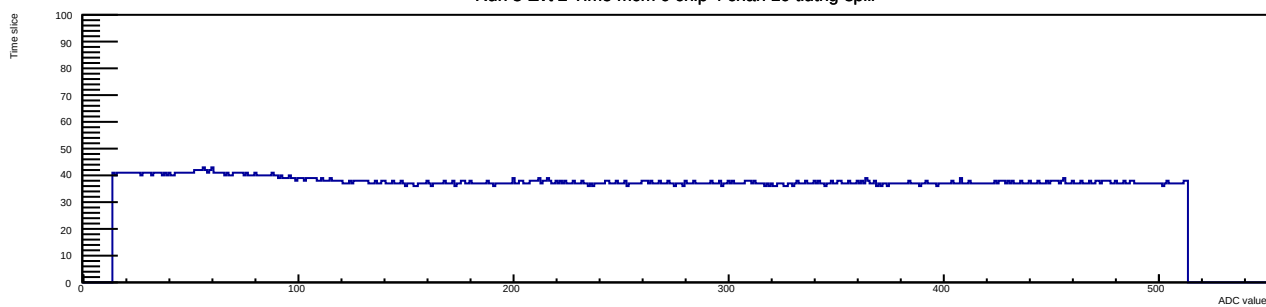
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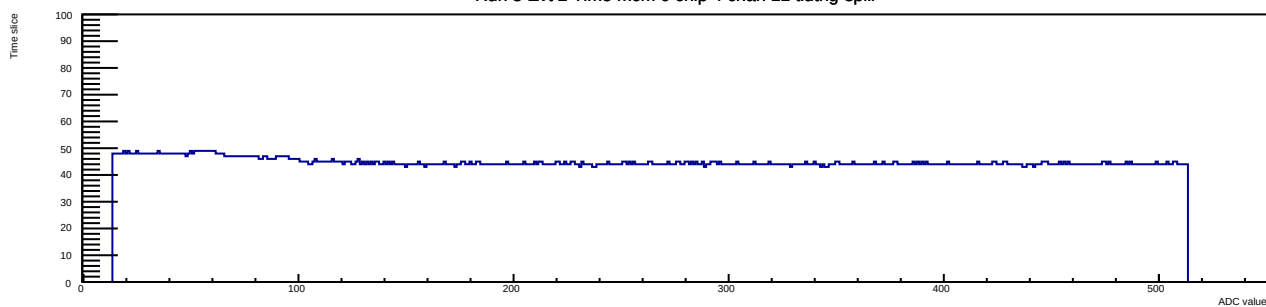
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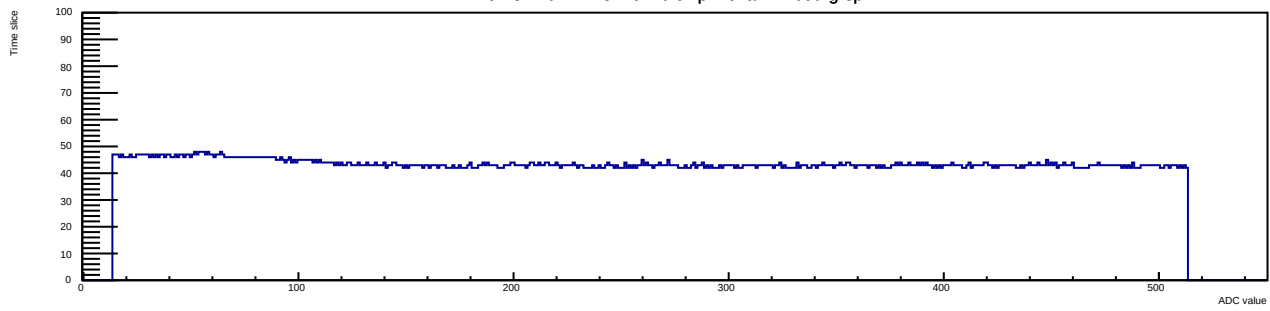
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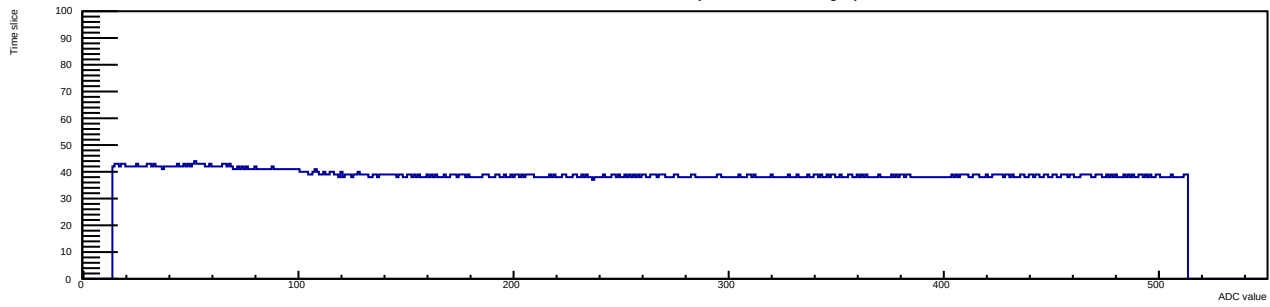
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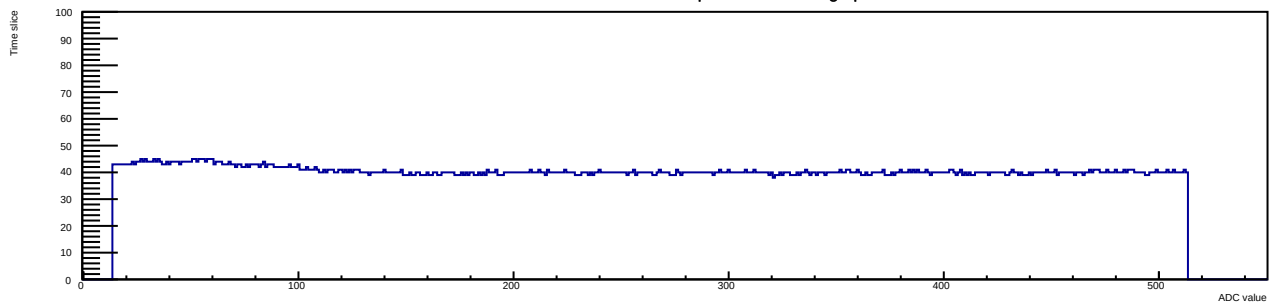
Run 8 Evt 2 Time mcm 0 chip 4 chan 12 tlutrig-spill



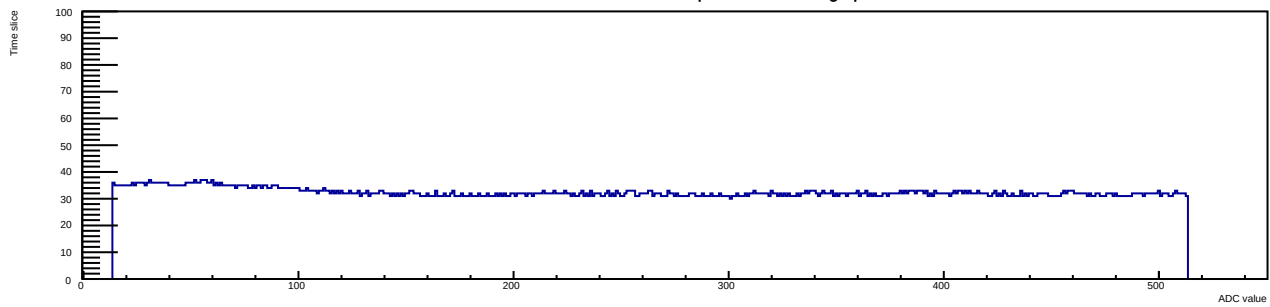
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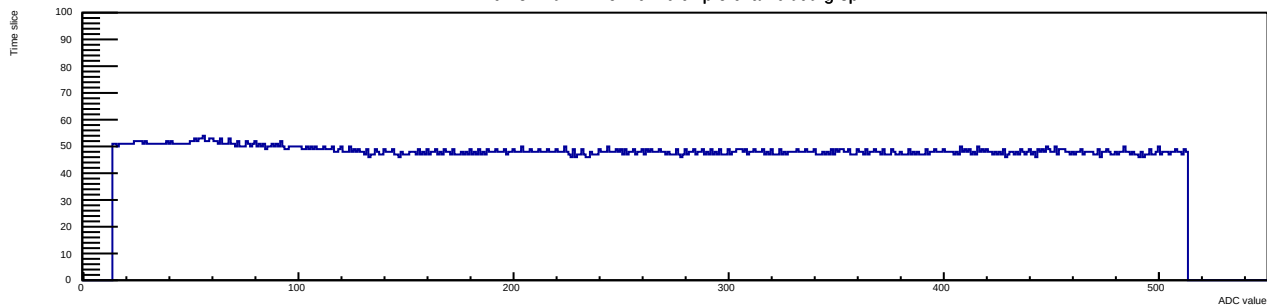
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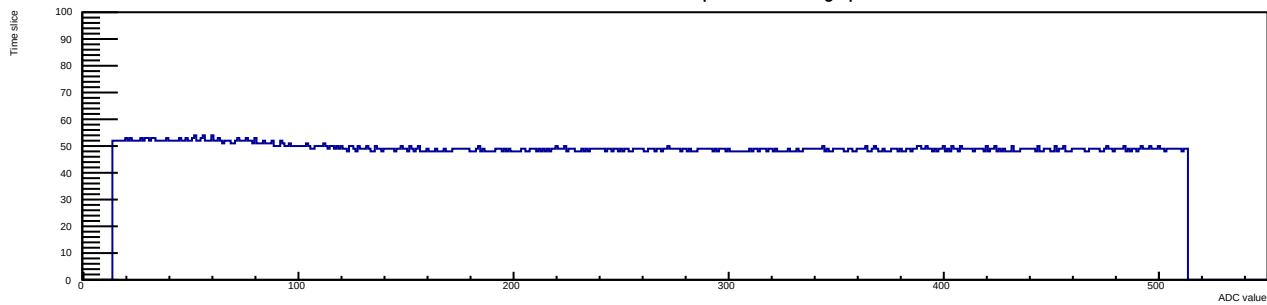
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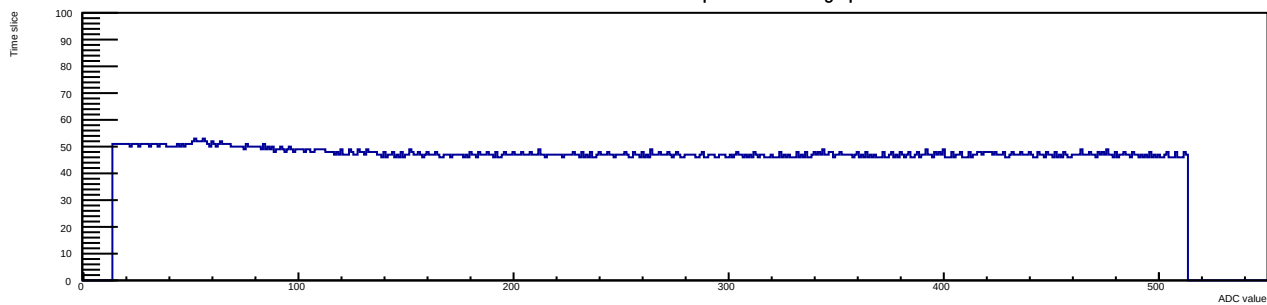
Run 8 Evt 2 Time mcm 0 chip 5 chan 0 tlutrig-spill



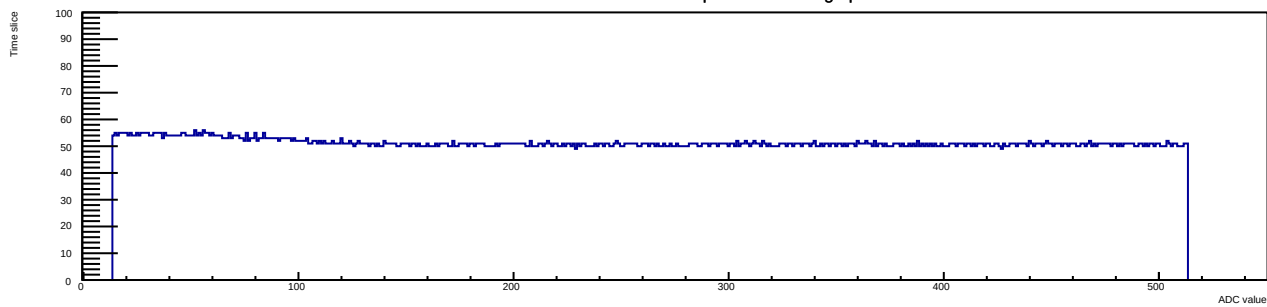
Run 8 Evt 2 Time mcm 0 chip 5 chan 1 tlutrig-spill



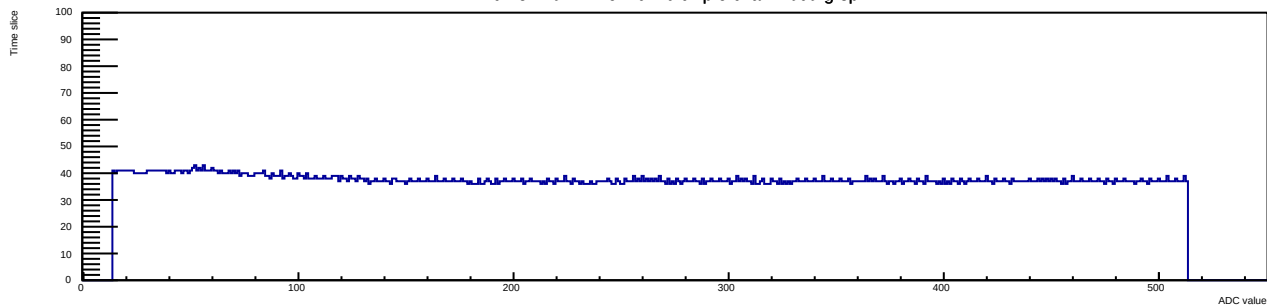
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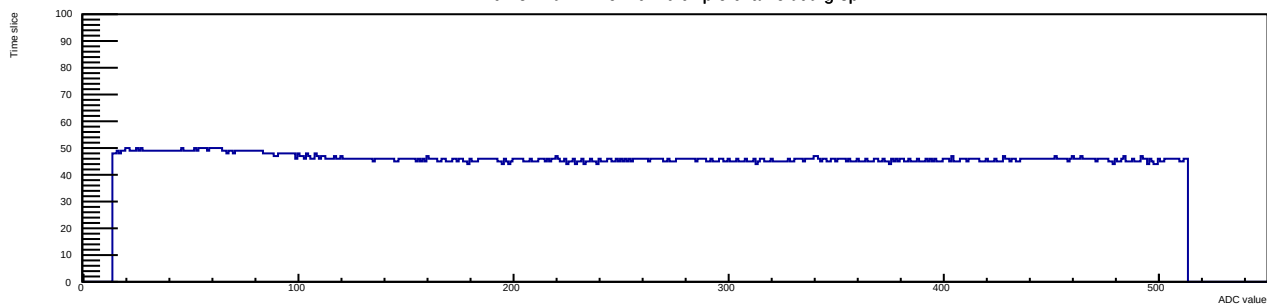
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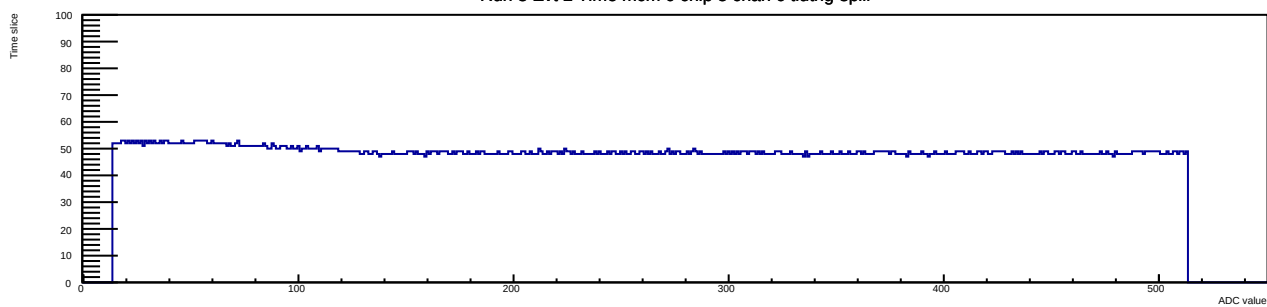
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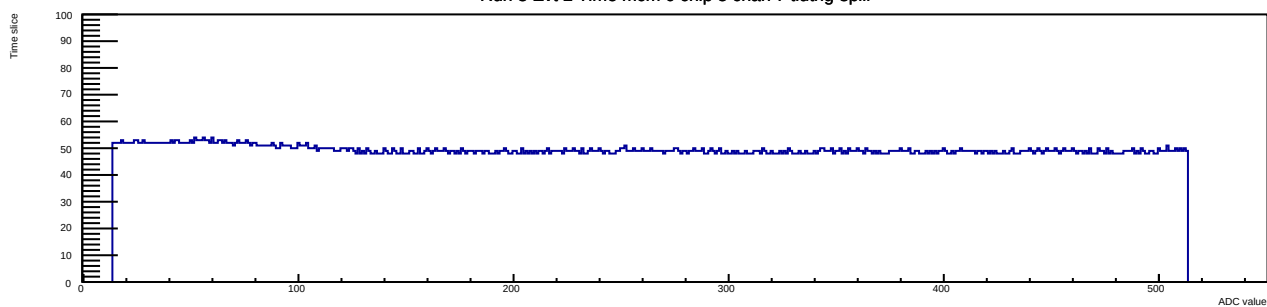
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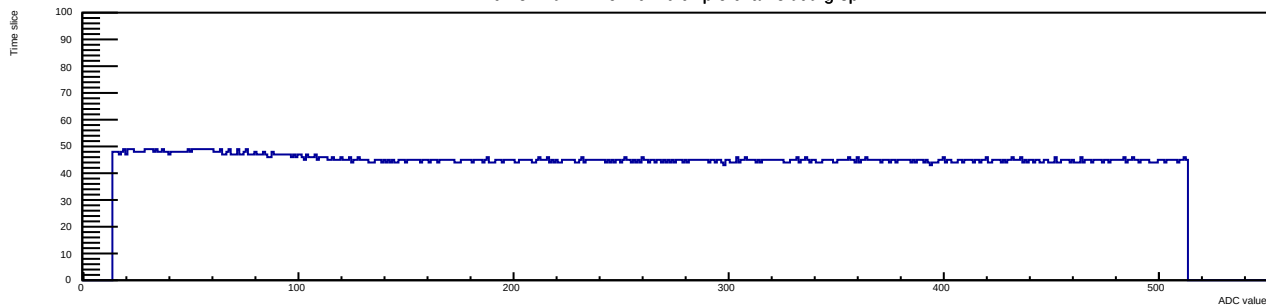
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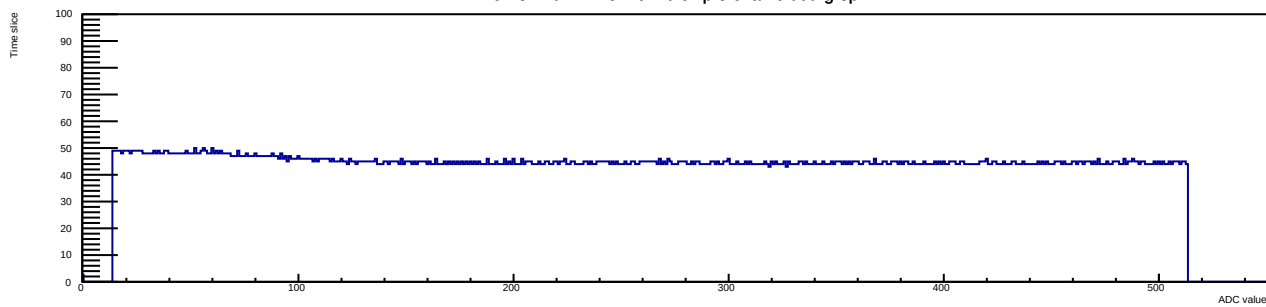
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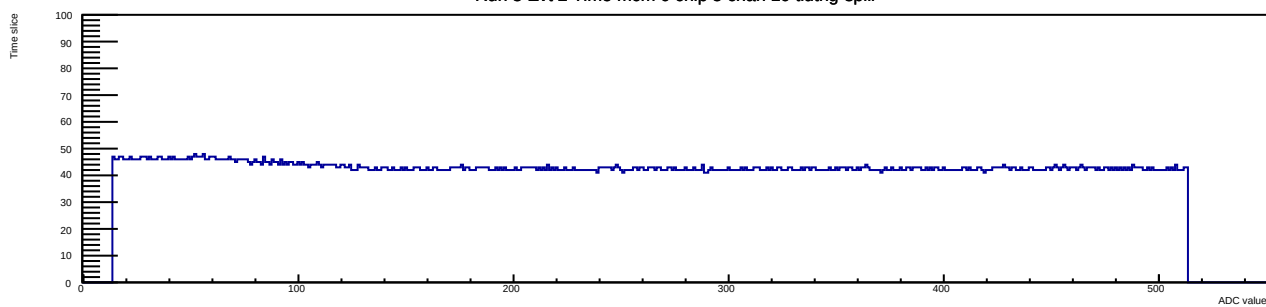
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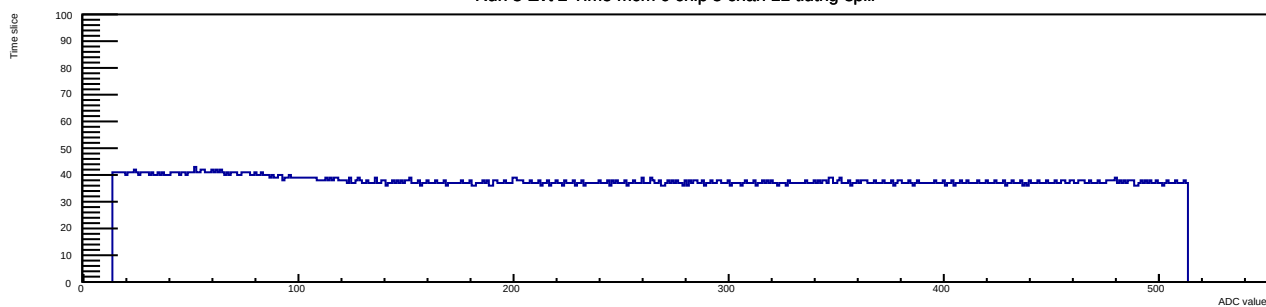
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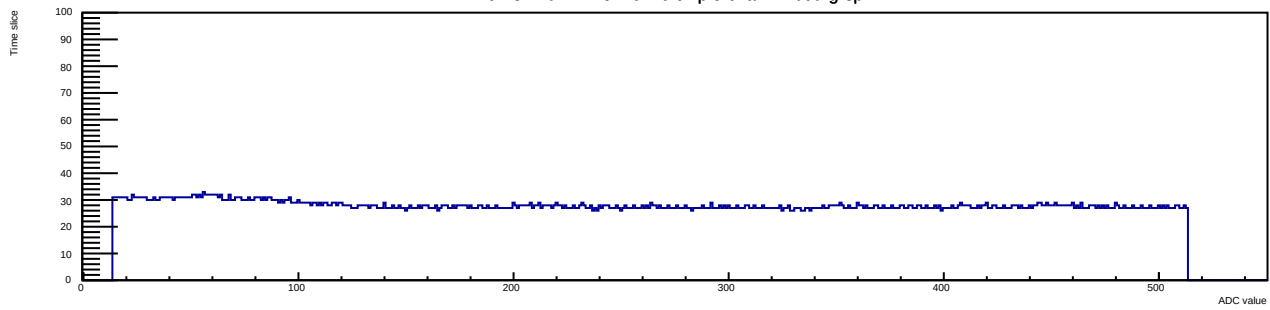
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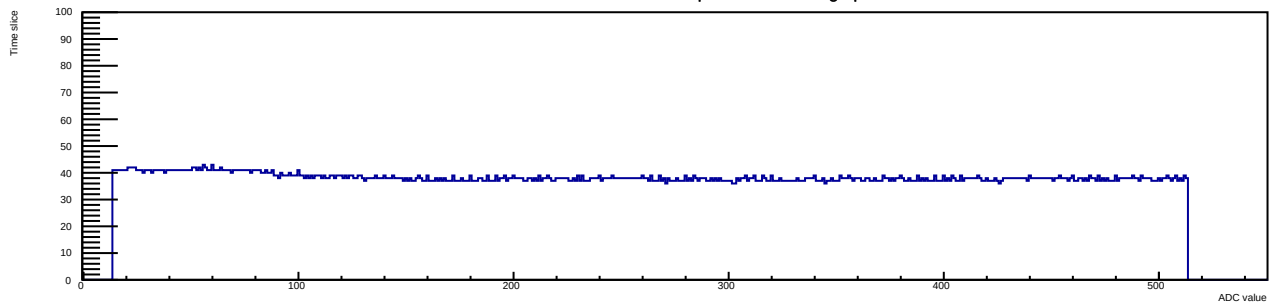
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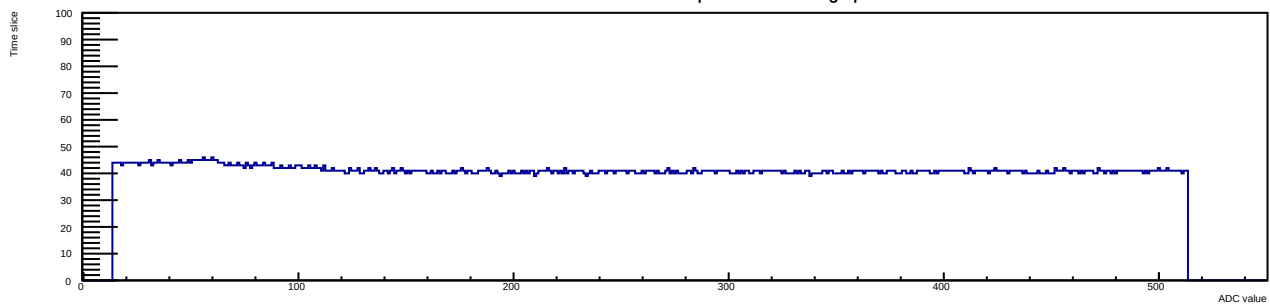
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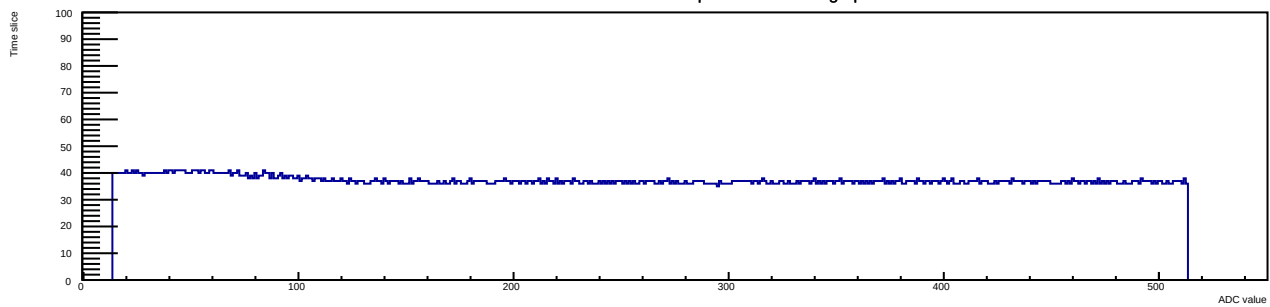
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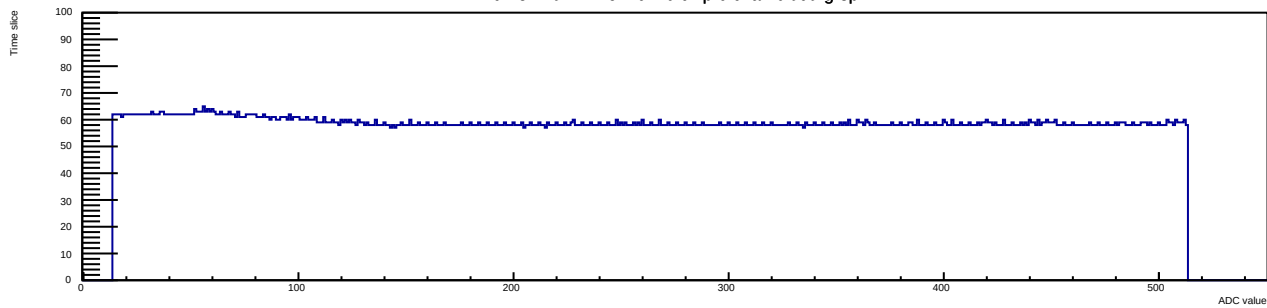
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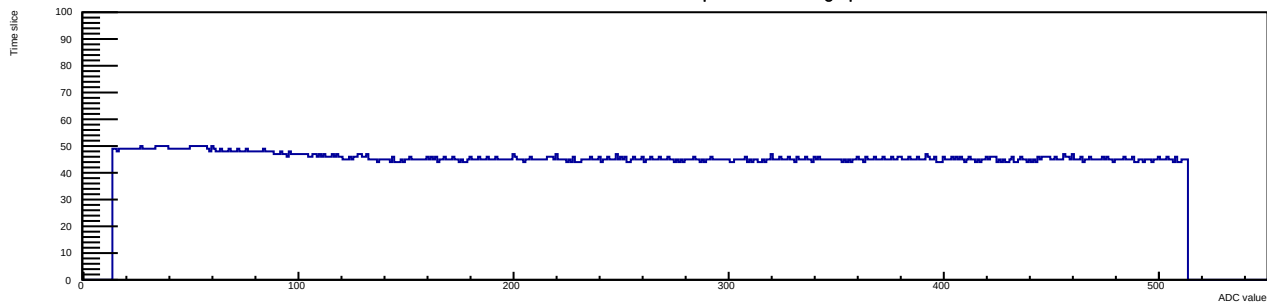
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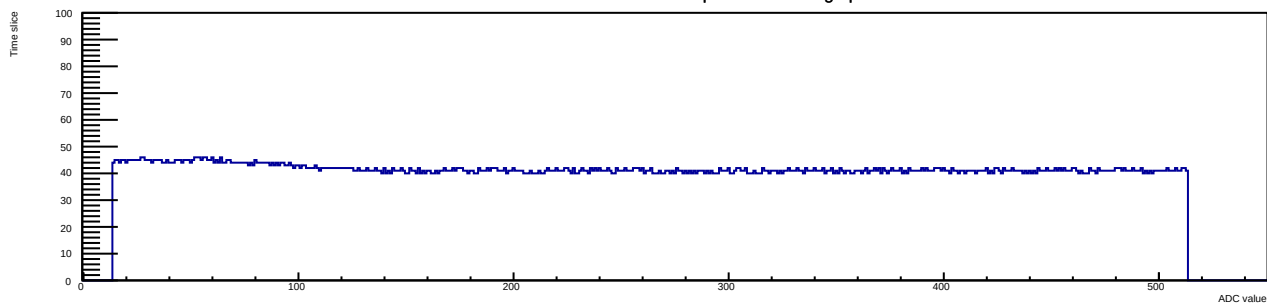
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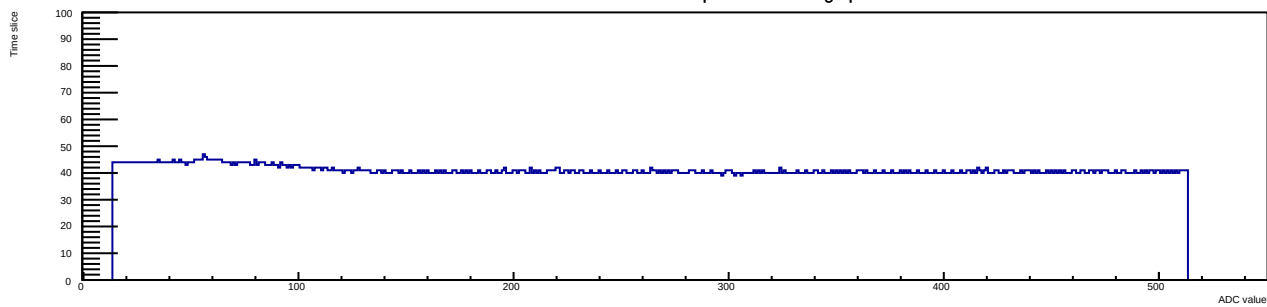
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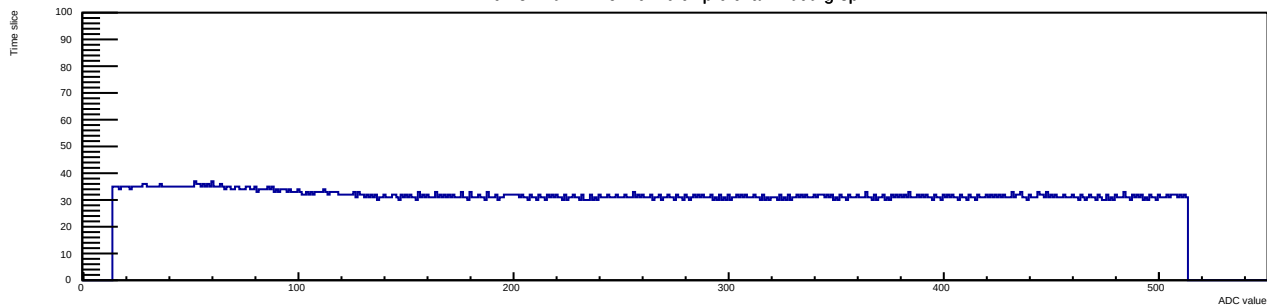
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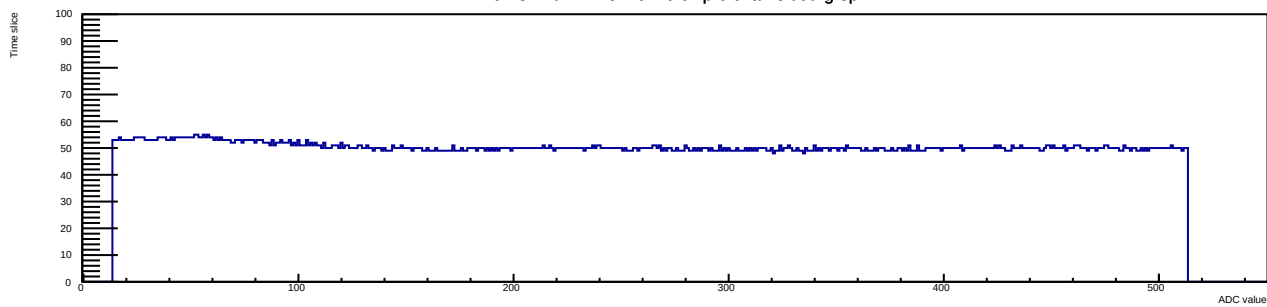
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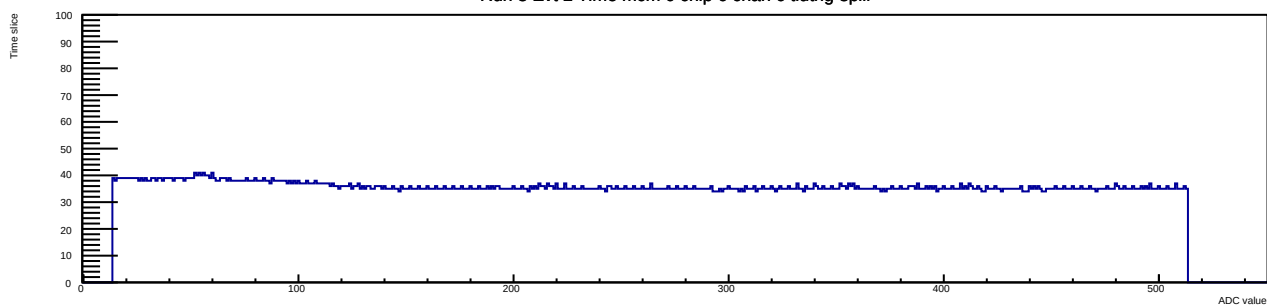
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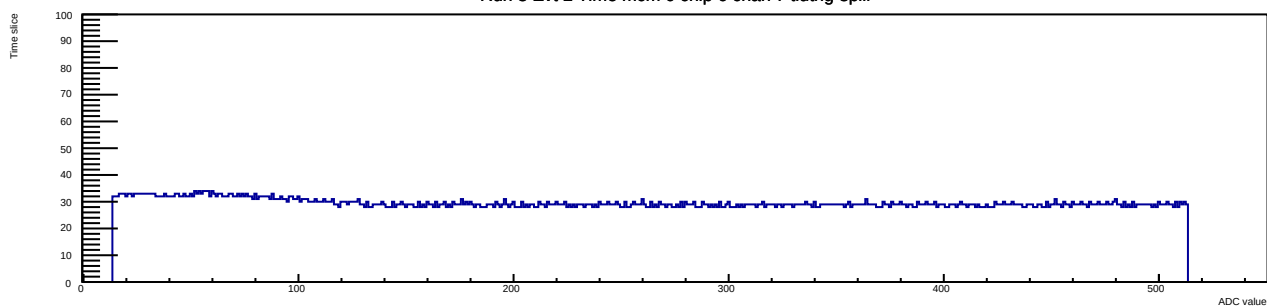
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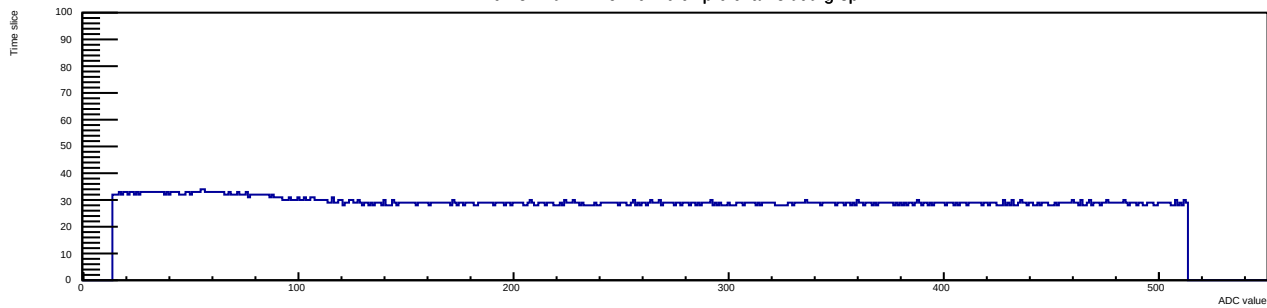
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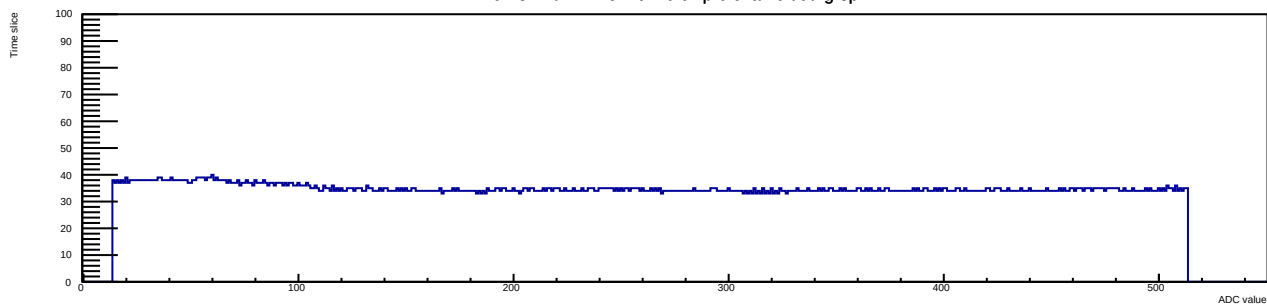
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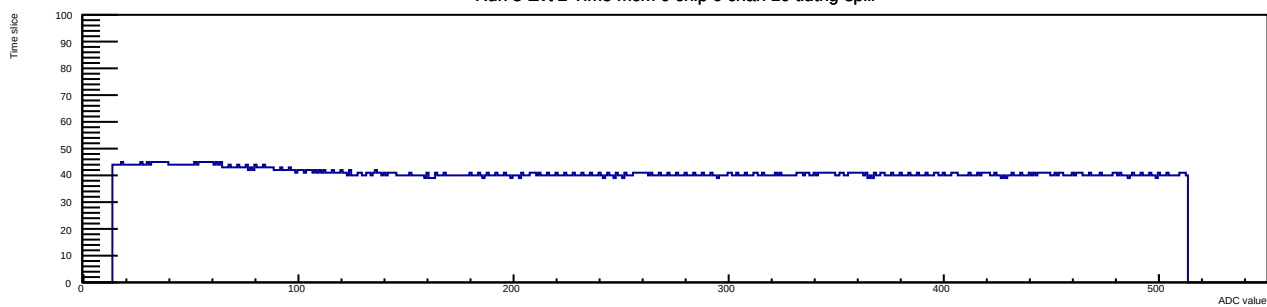
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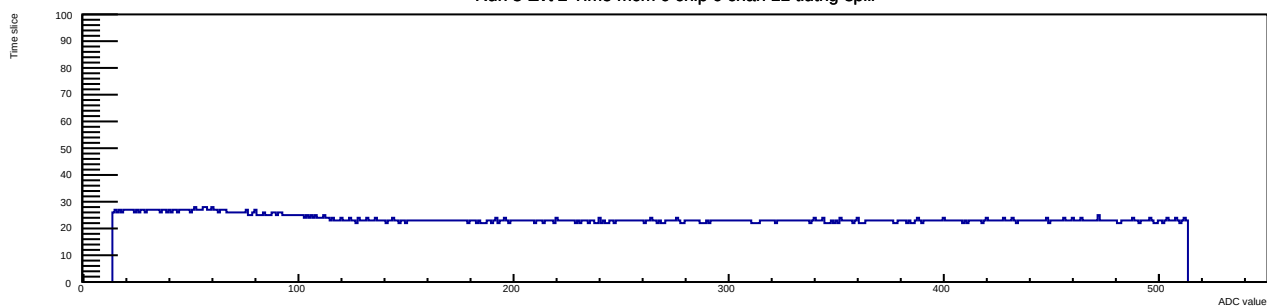
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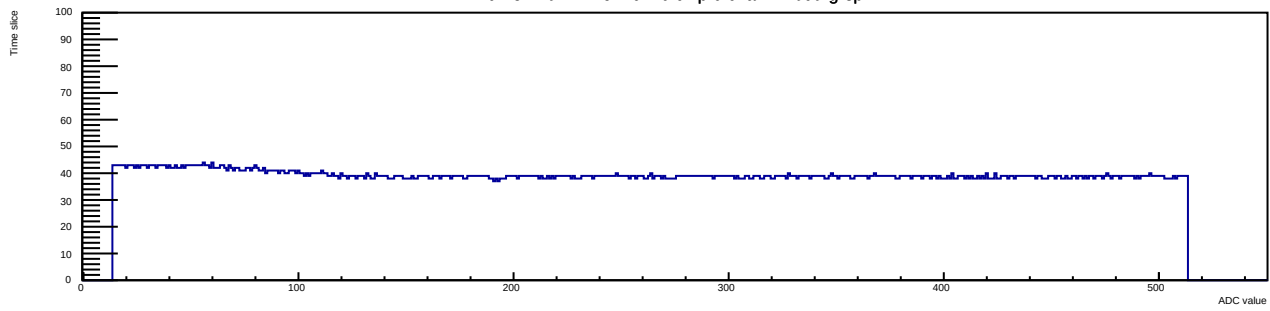
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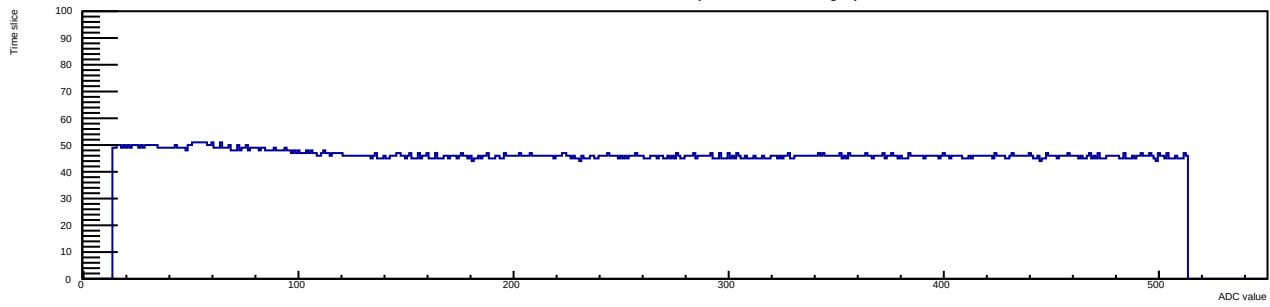
Run 8 Evt 2 Time mcm 0 chip 6 chan 11 tlutrig-spill



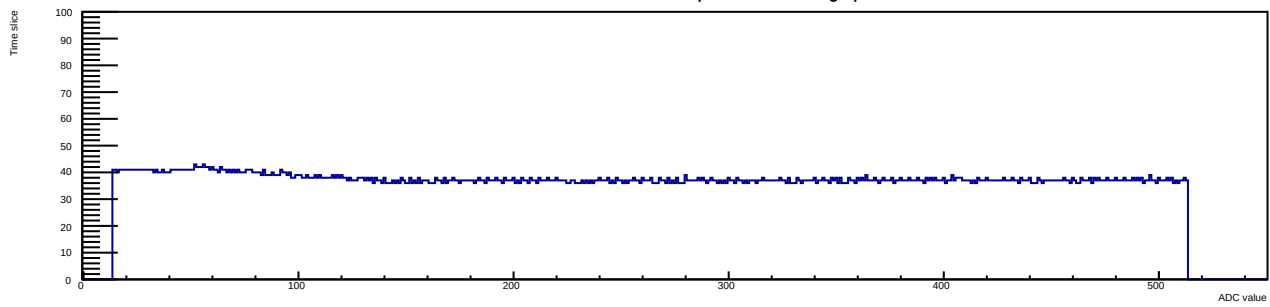
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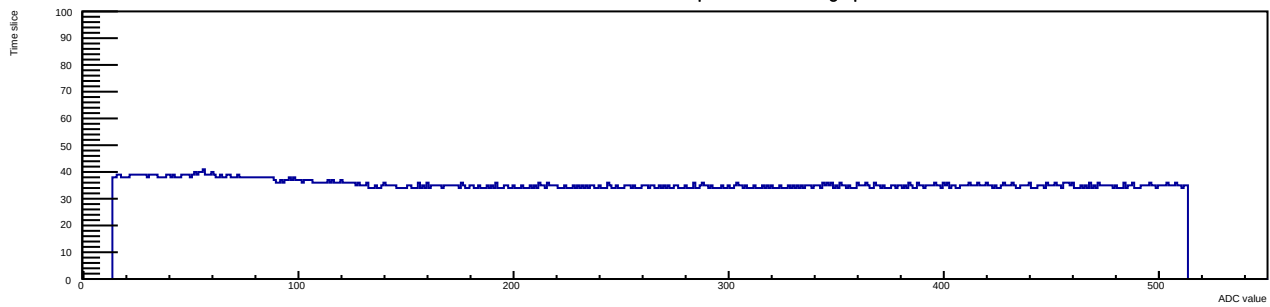
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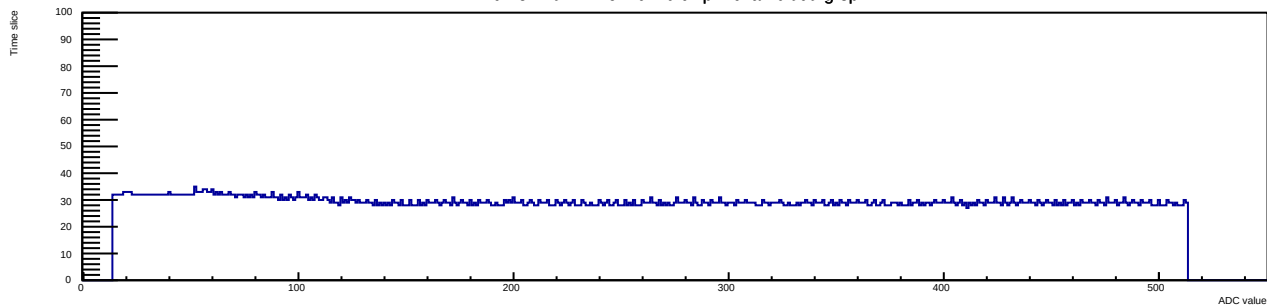
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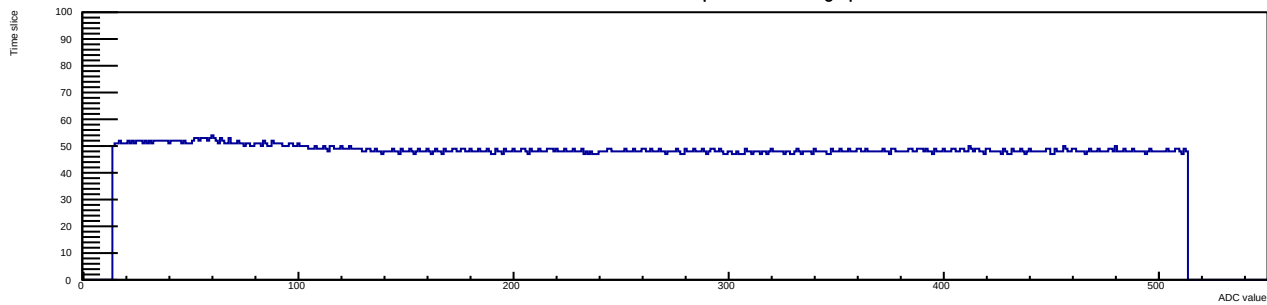
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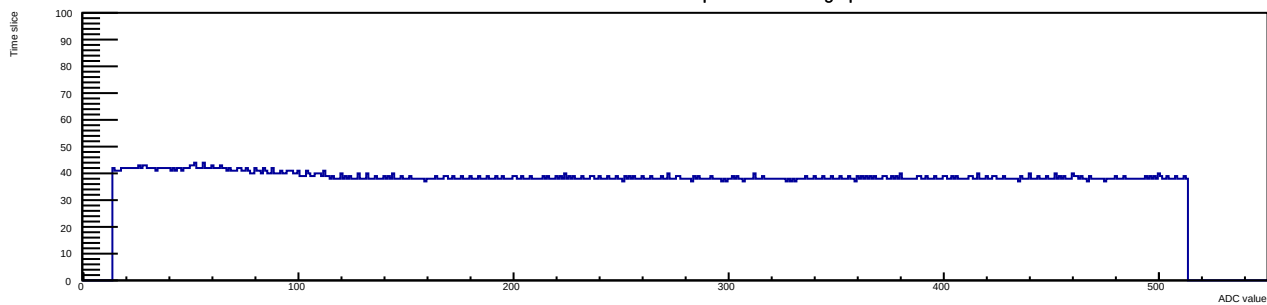
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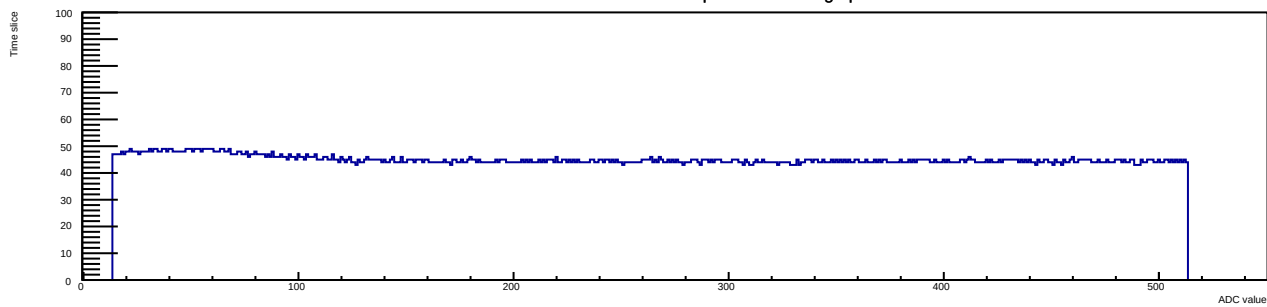
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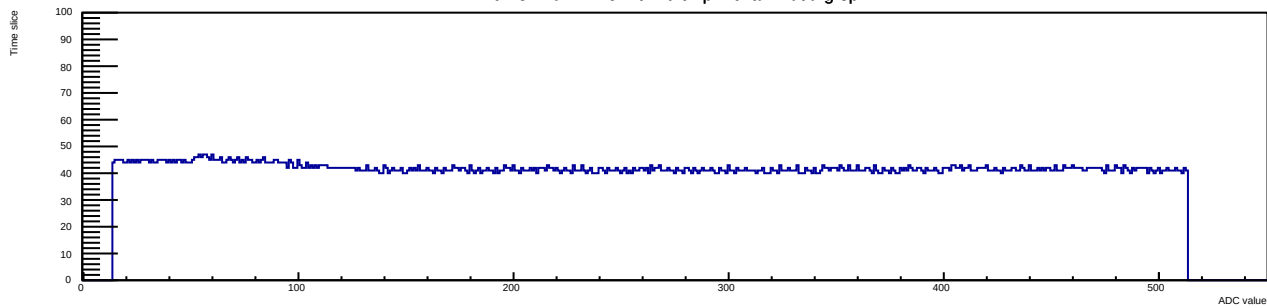
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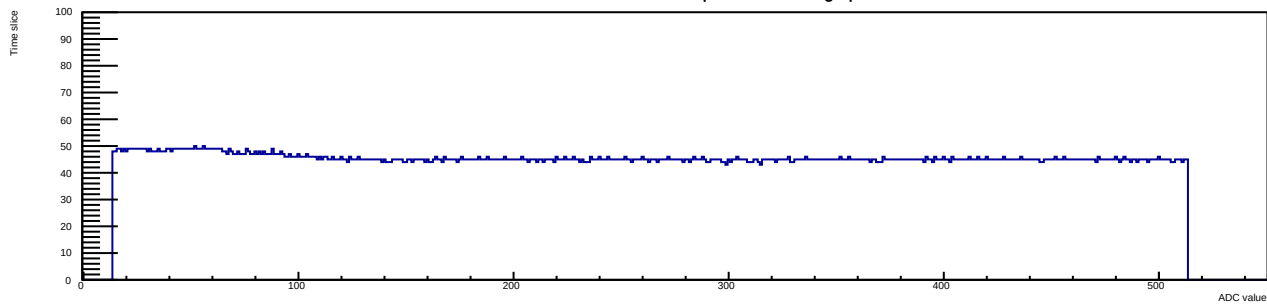
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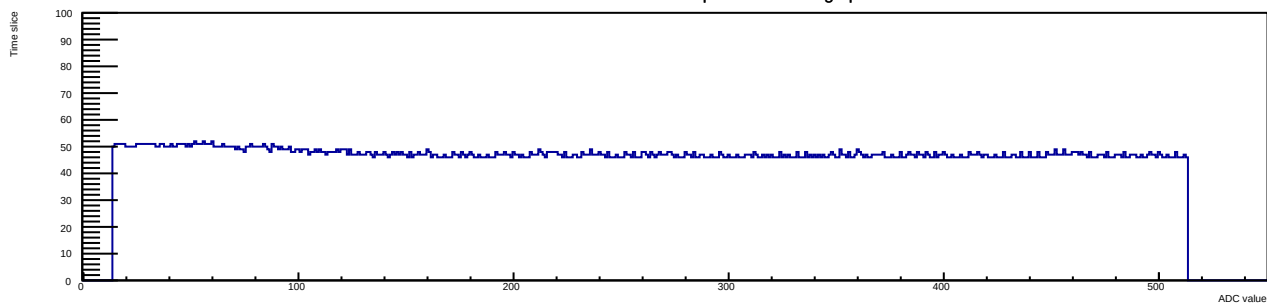
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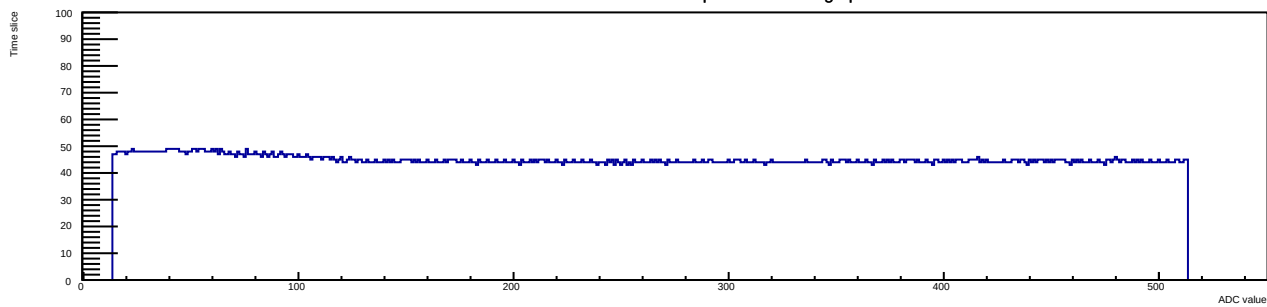
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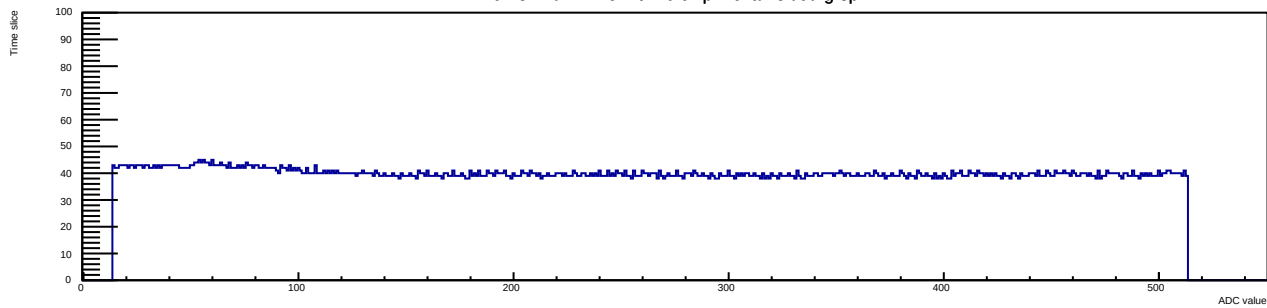
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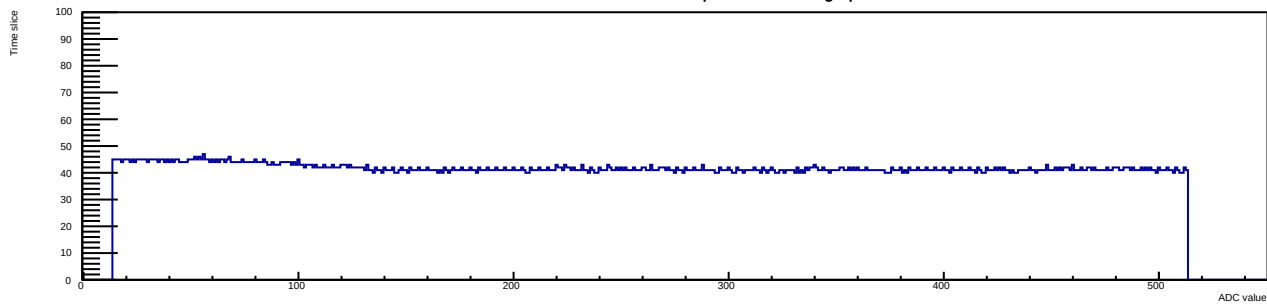
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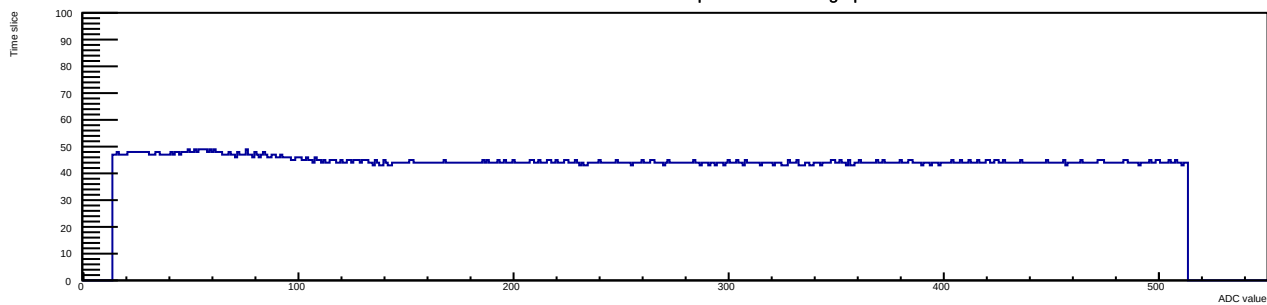
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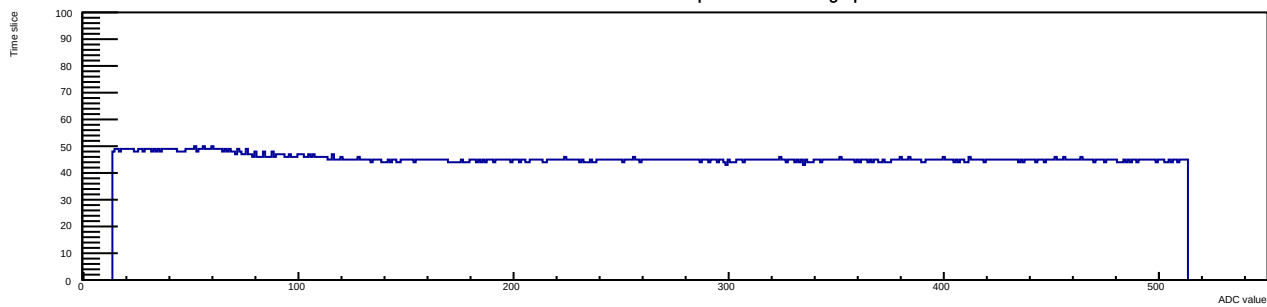
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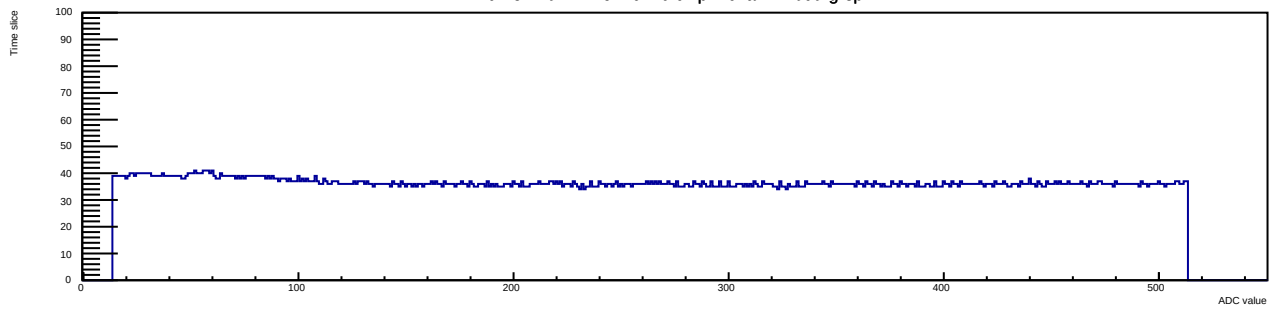
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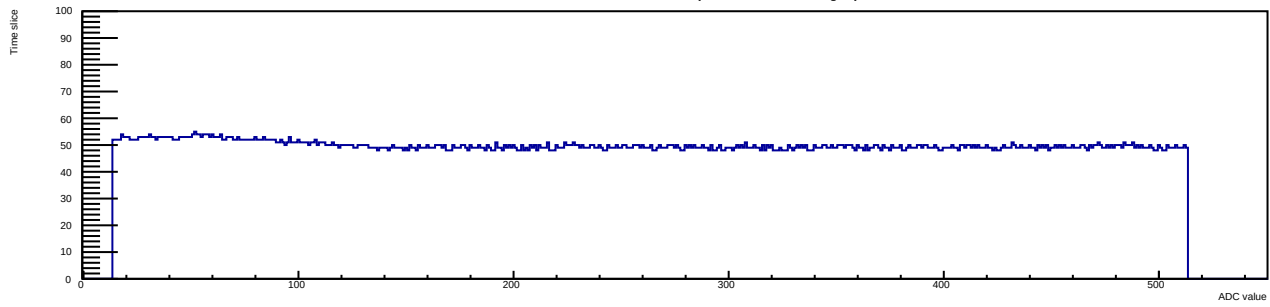
Run 8 Evt 2 Time mcm 0 chip 7 chan 11 tlutrig-spill



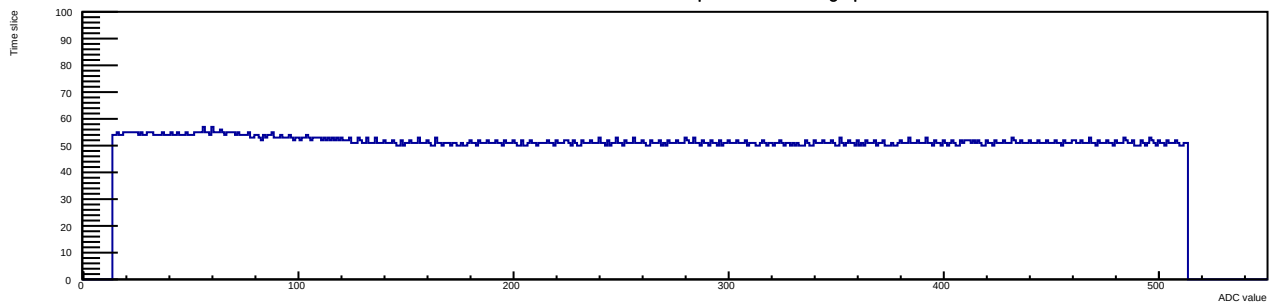
Run 8 Evt 2 Time mcm 0 chip 7 chan 12 tlutrig-spill



Run 8 Evt 2 Time mcm 0 chip 7 chan 13 tlutrig-spill



Run 8 Evt 2 Time mcm 0 chip 7 chan 14 tlutrig-spill



Run 8 Evt 2 Time mcm 0 chip 7 chan 15 tlutrig-spill

