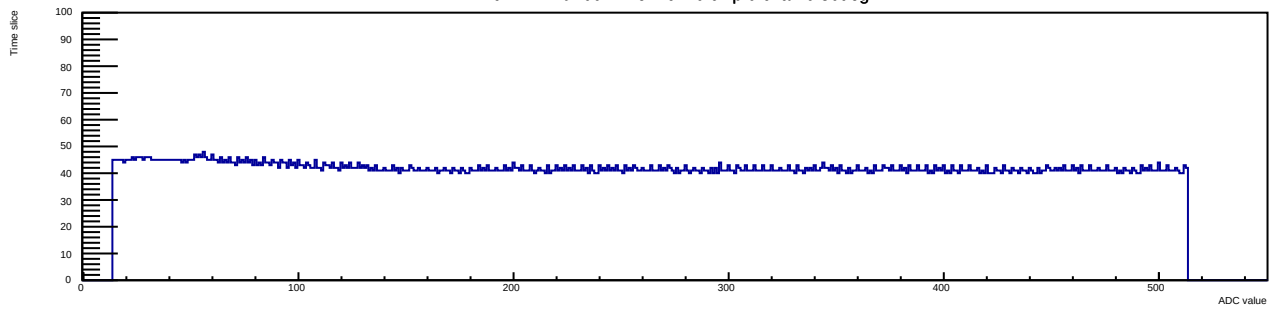
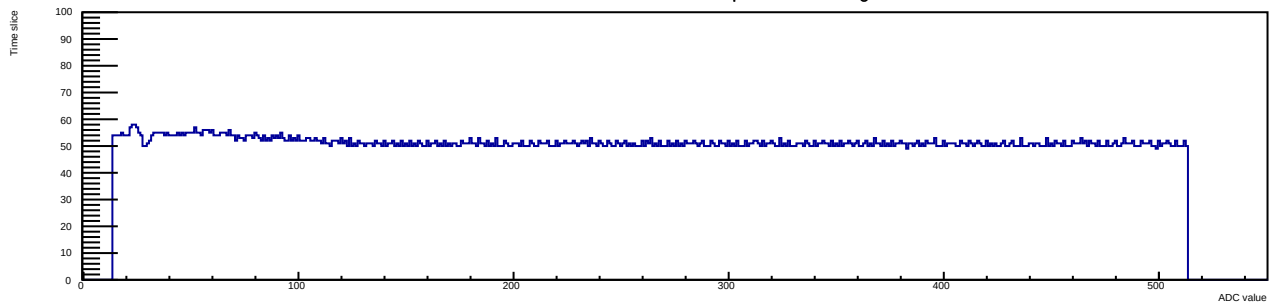


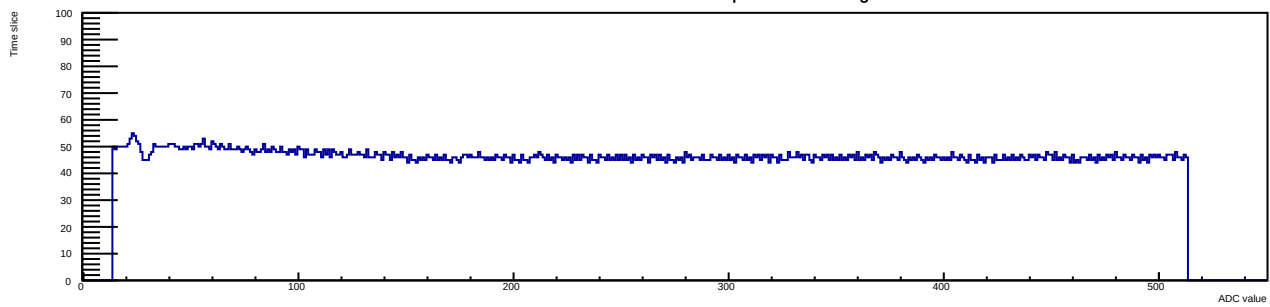
Run 12 Evt 100 Time mcm 0 chip 0 chan 0 30deg



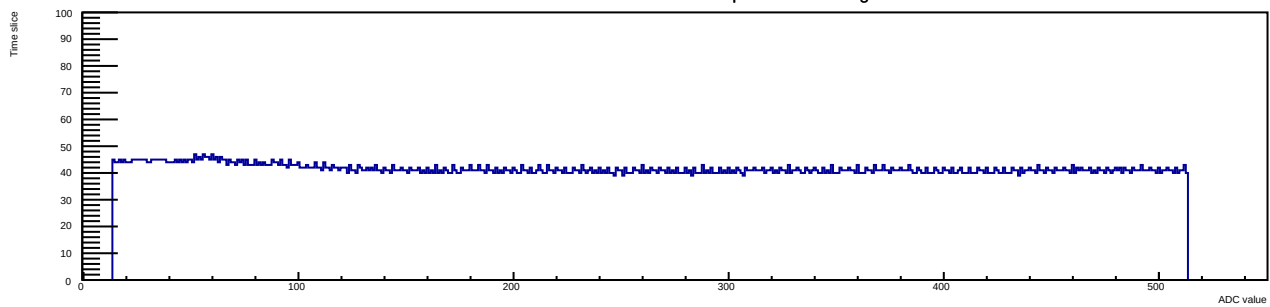
Run 12 Evt 100 Time mcm 0 chip 0 chan 1 30deg



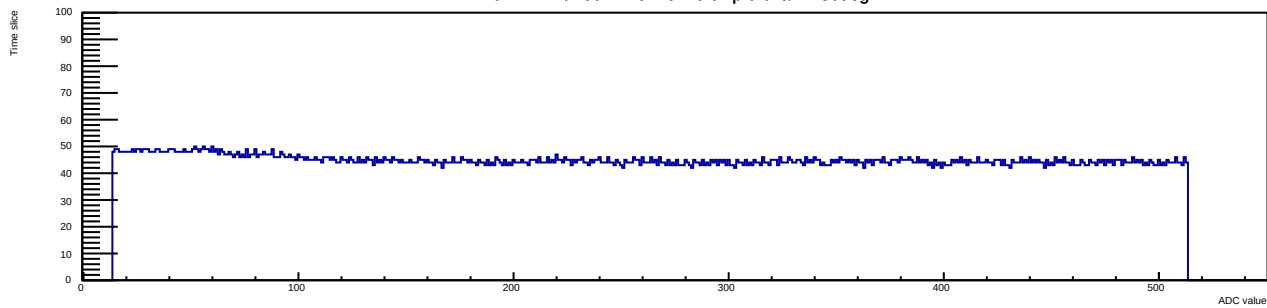
Run 12 Evt 100 Time mcm 0 chip 0 chan 2 30deg



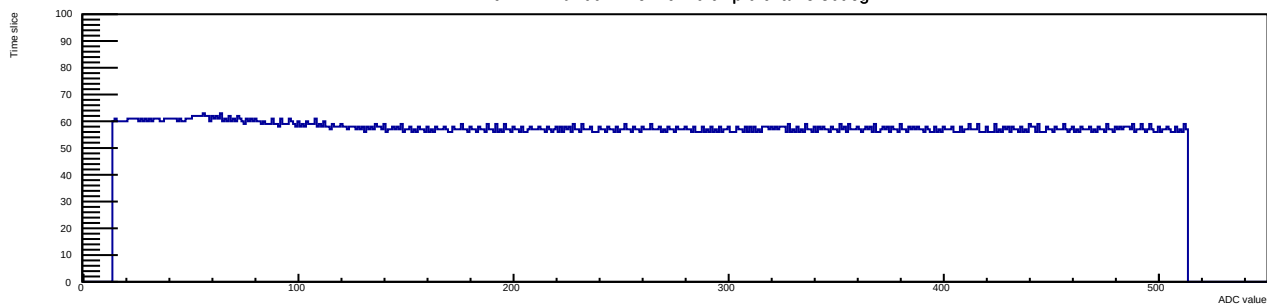
Run 12 Evt 100 Time mcm 0 chip 0 chan 3 30deg



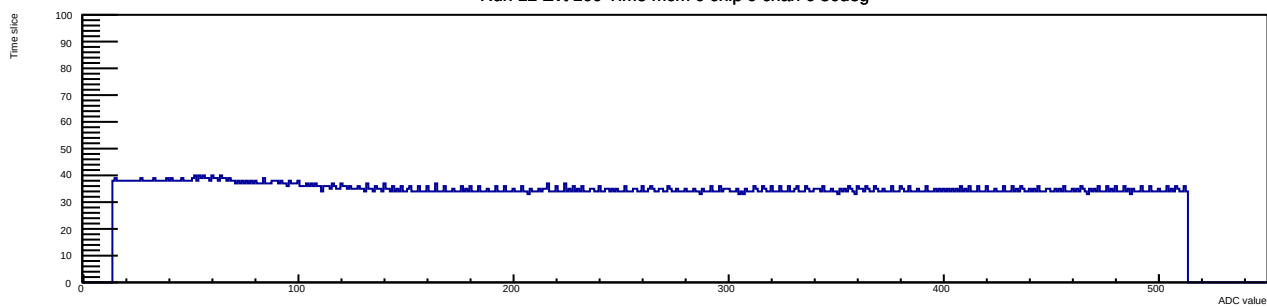
Run 12 Evt 100 Time mcm 0 chip 0 chan 4 30deg



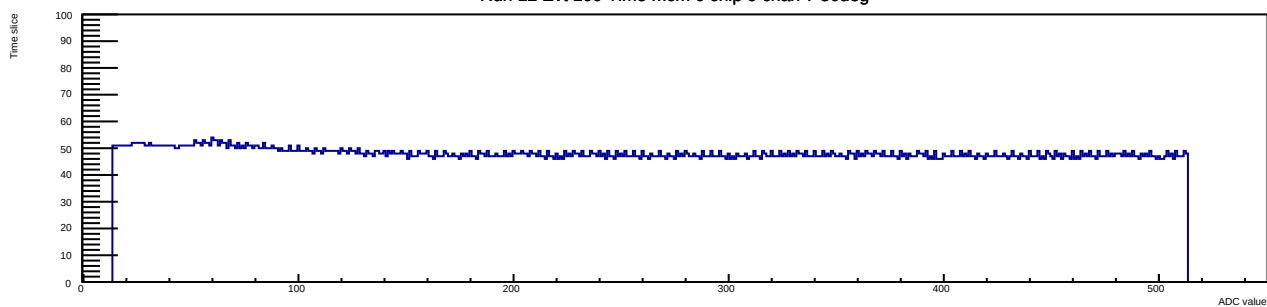
Run 12 Evt 100 Time mcm 0 chip 0 chan 5 30deg



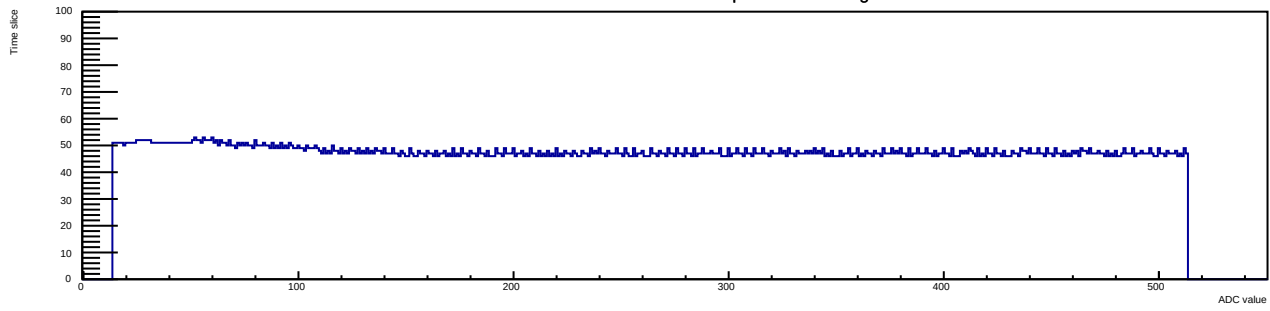
Run 12 Evt 100 Time mcm 0 chip 0 chan 6 30deg



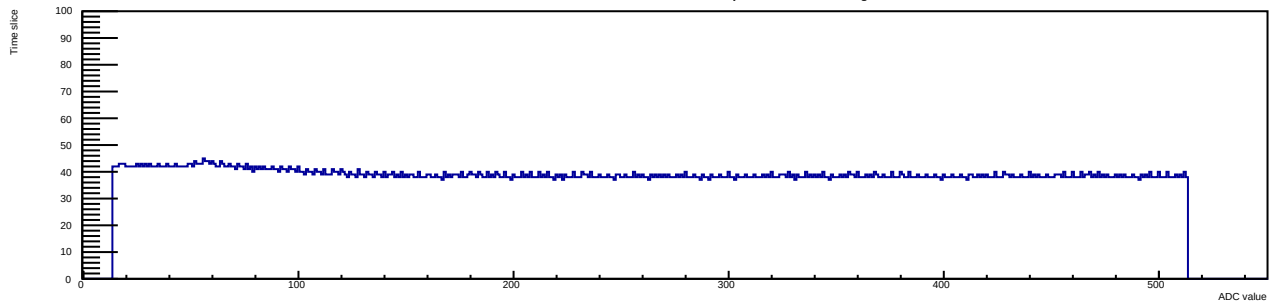
Run 12 Evt 100 Time mcm 0 chip 0 chan 7 30deg



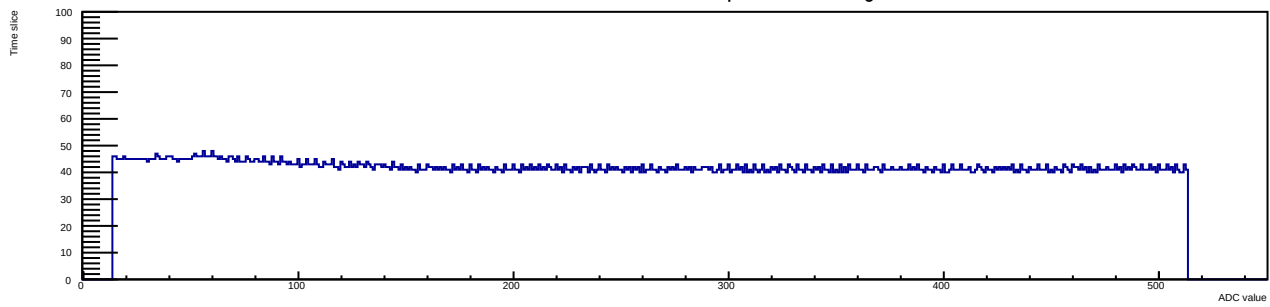
Run 12 Evt 100 Time mcm 0 chip 0 chan 8 30deg



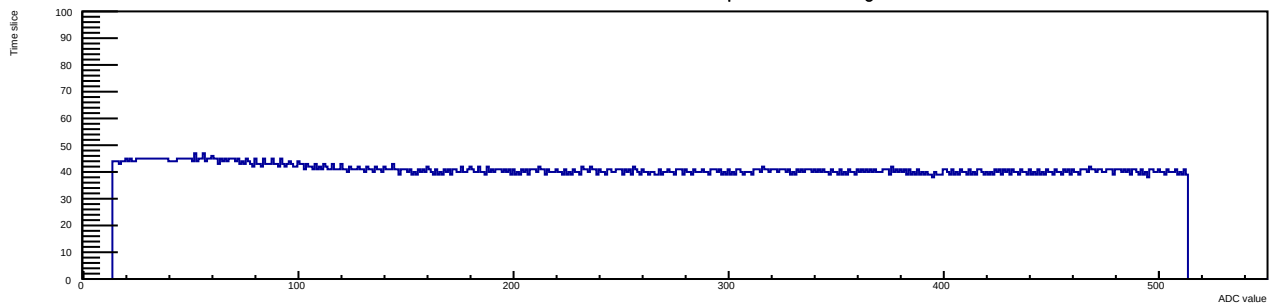
Run 12 Evt 100 Time mcm 0 chip 0 chan 9 30deg



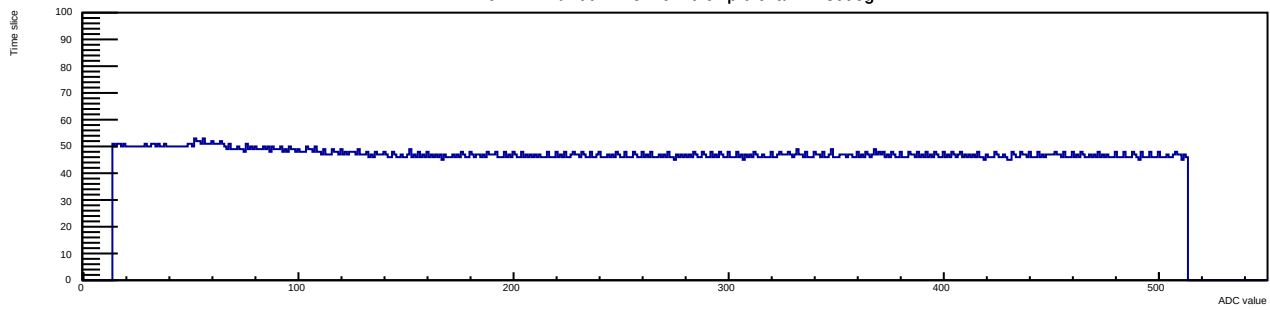
Run 12 Evt 100 Time mcm 0 chip 0 chan 10 30deg



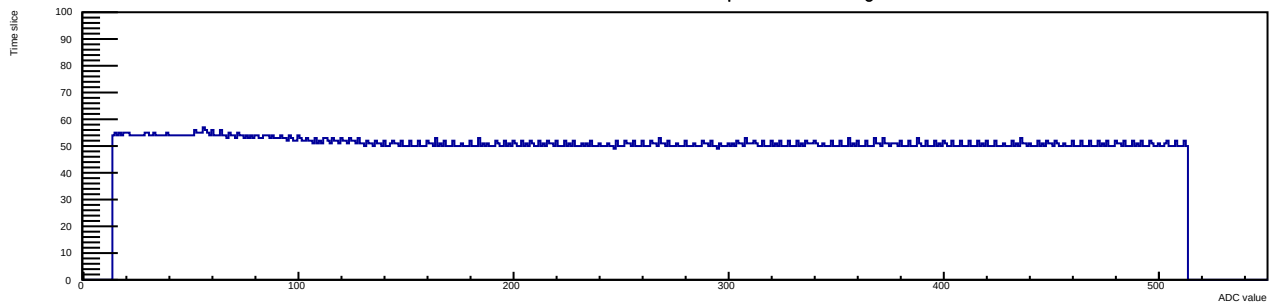
Run 12 Evt 100 Time mcm 0 chip 0 chan 11 30deg



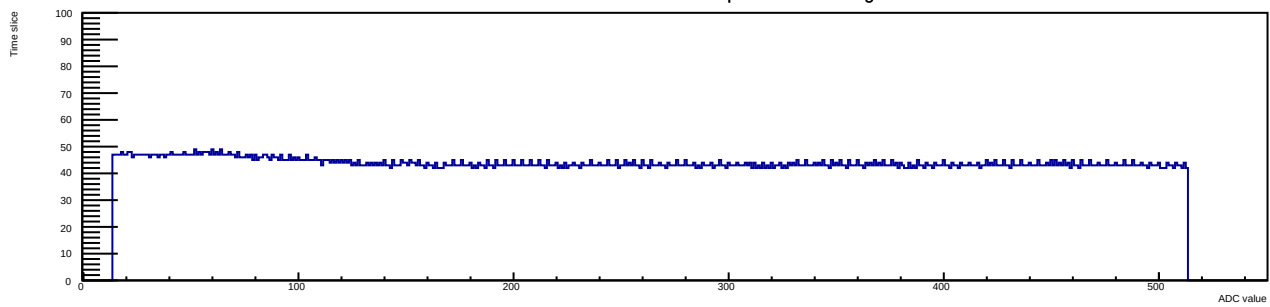
Run 12 Evt 100 Time mcm 0 chip 0 chan 12 30deg



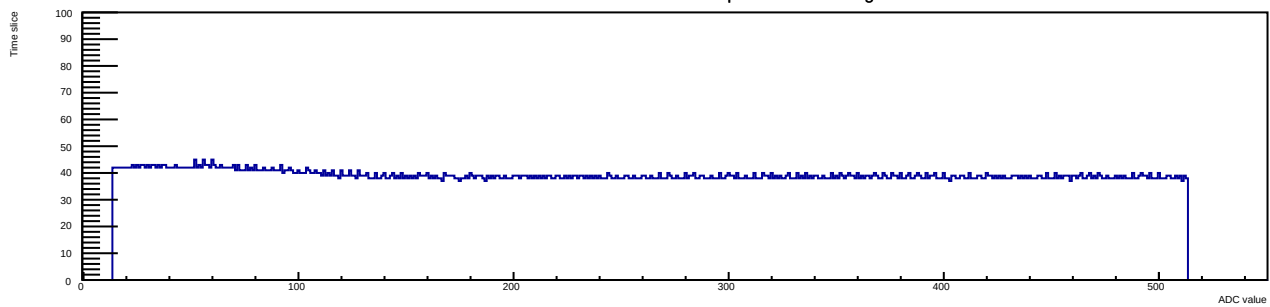
Run 12 Evt 100 Time mcm 0 chip 0 chan 13 30deg



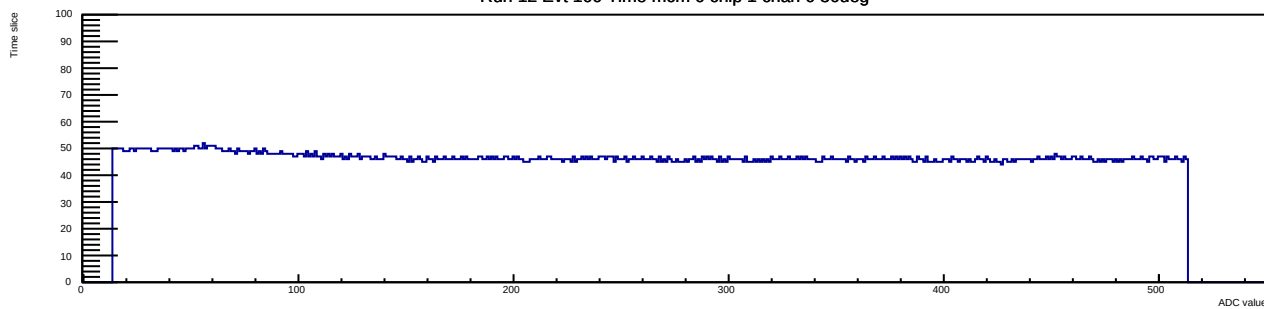
Run 12 Evt 100 Time mcm 0 chip 0 chan 14 30deg



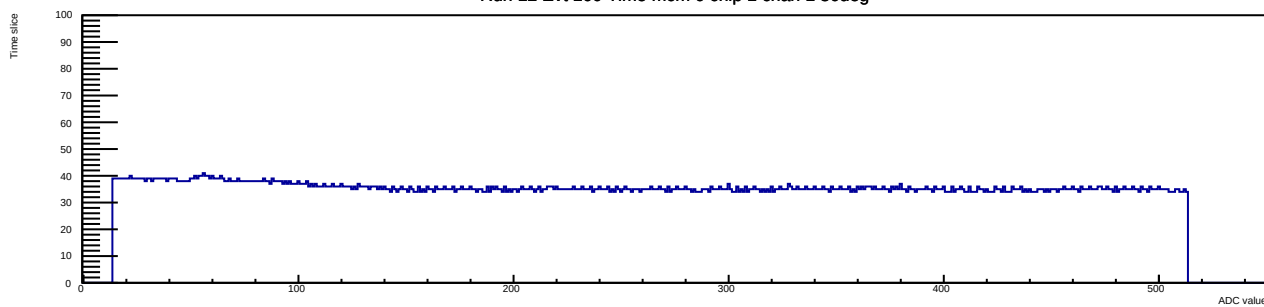
Run 12 Evt 100 Time mcm 0 chip 0 chan 15 30deg



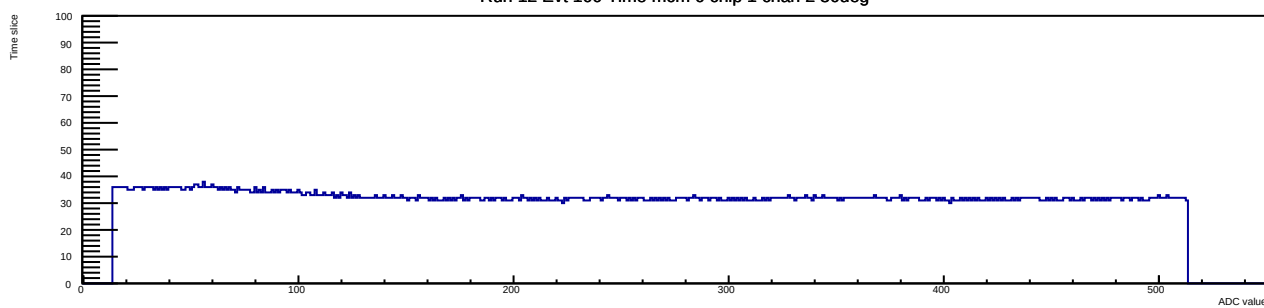
Run 12 Evt 100 Time mcm 0 chip 1 chan 0 30deg



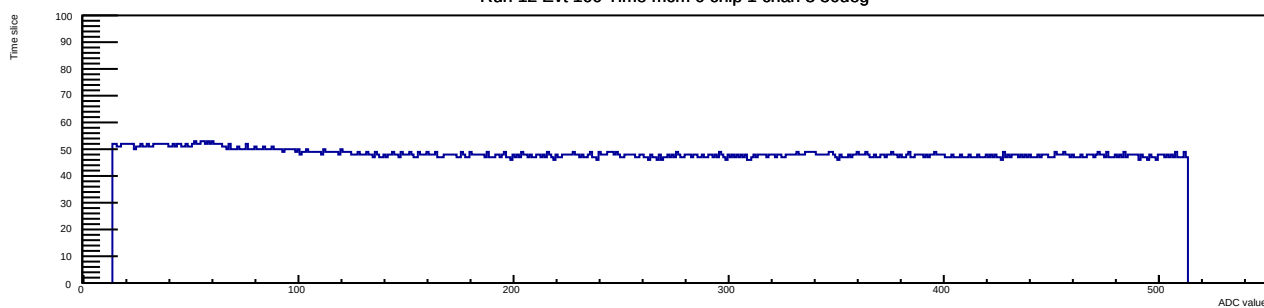
Run 12 Evt 100 Time mcm 0 chip 1 chan 1 30deg



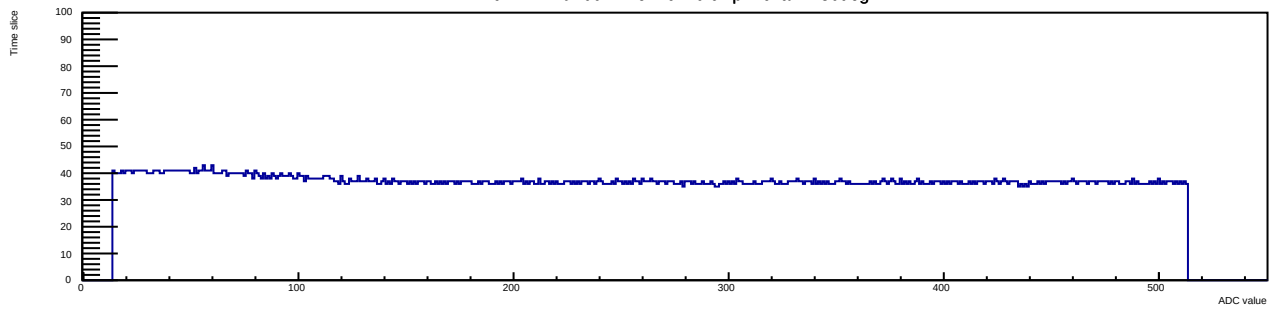
Run 12 Evt 100 Time mcm 0 chip 1 chan 2 30deg



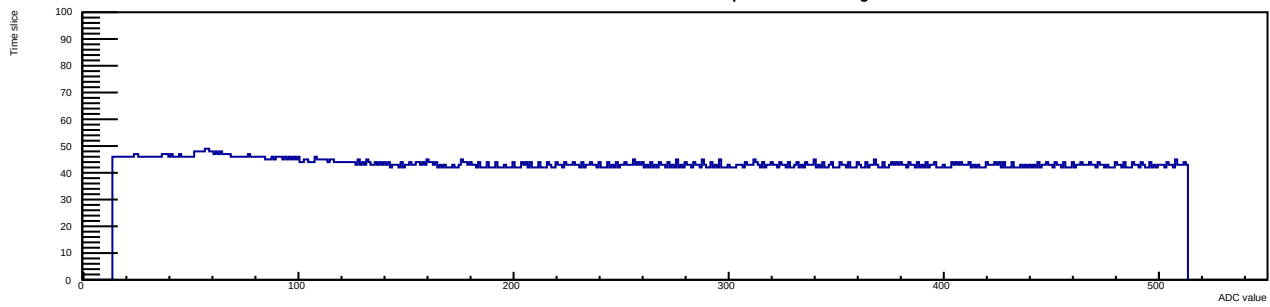
Run 12 Evt 100 Time mcm 0 chip 1 chan 3 30deg



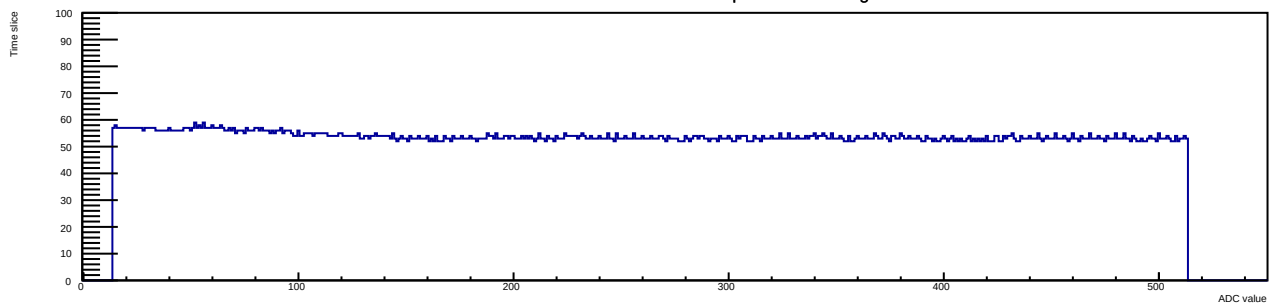
Run 12 Evt 100 Time mcm 0 chip 1 chan 4 30deg



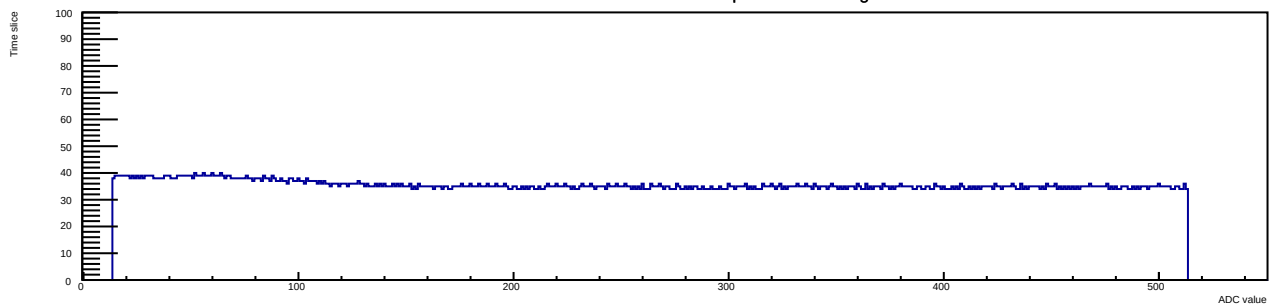
Run 12 Evt 100 Time mcm 0 chip 1 chan 5 30deg



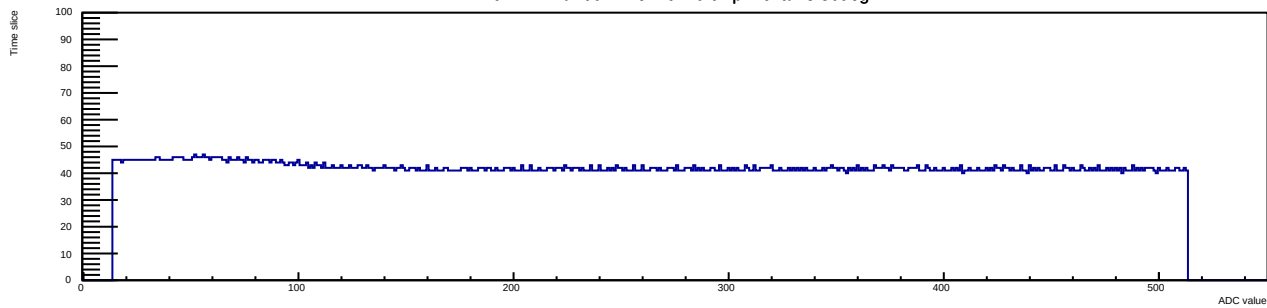
Run 12 Evt 100 Time mcm 0 chip 1 chan 6 30deg



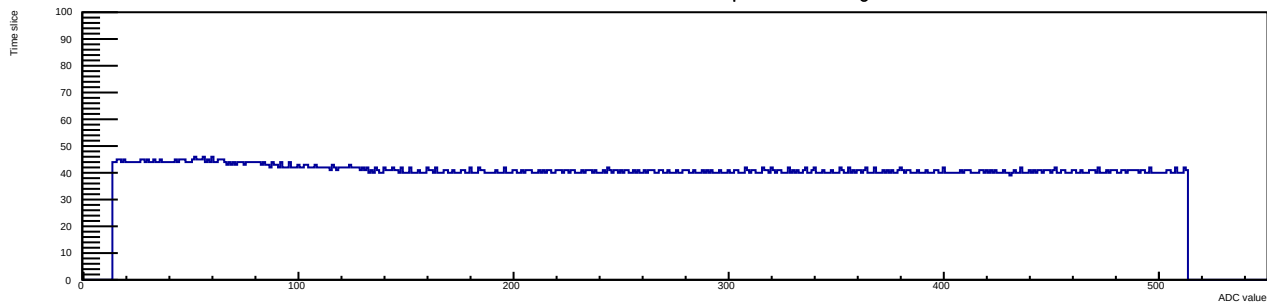
Run 12 Evt 100 Time mcm 0 chip 1 chan 7 30deg



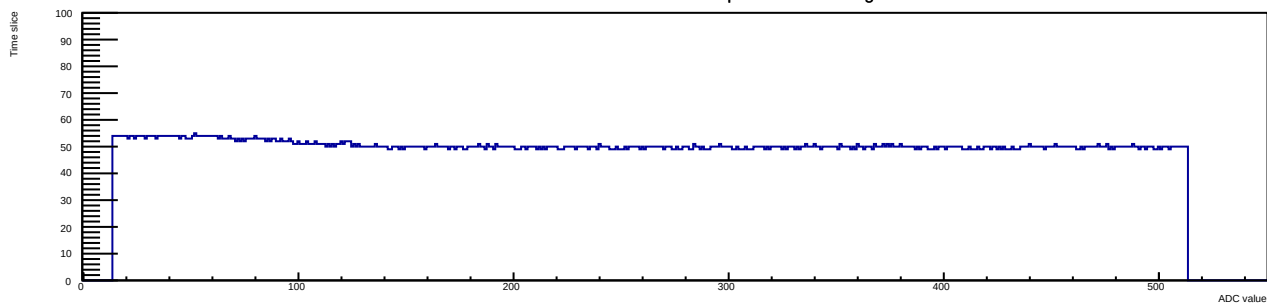
Run 12 Evt 100 Time mcm 0 chip 1 chan 8 30deg



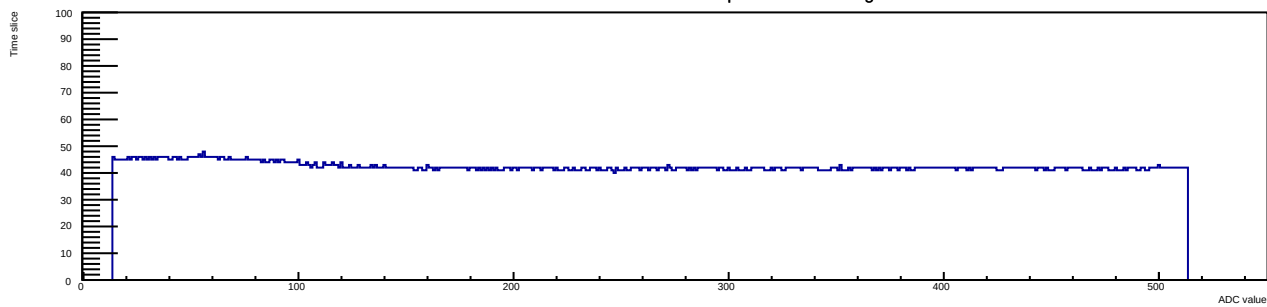
Run 12 Evt 100 Time mcm 0 chip 1 chan 9 30deg



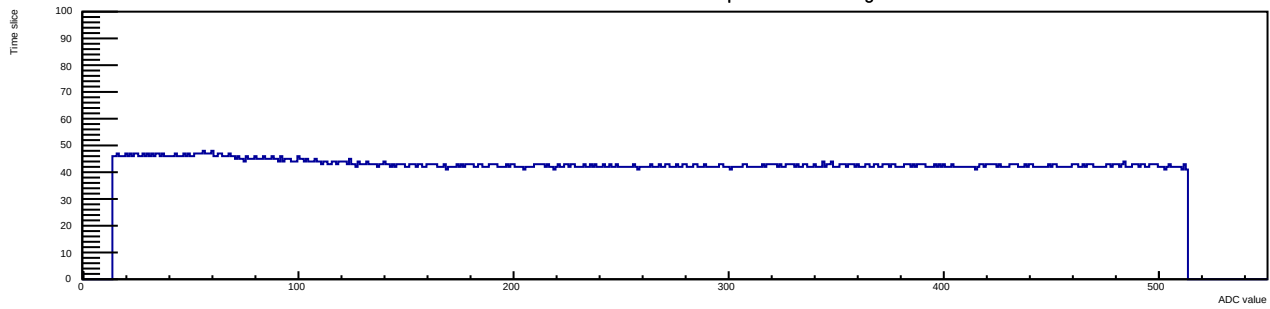
Run 12 Evt 100 Time mcm 0 chip 1 chan 10 30deg



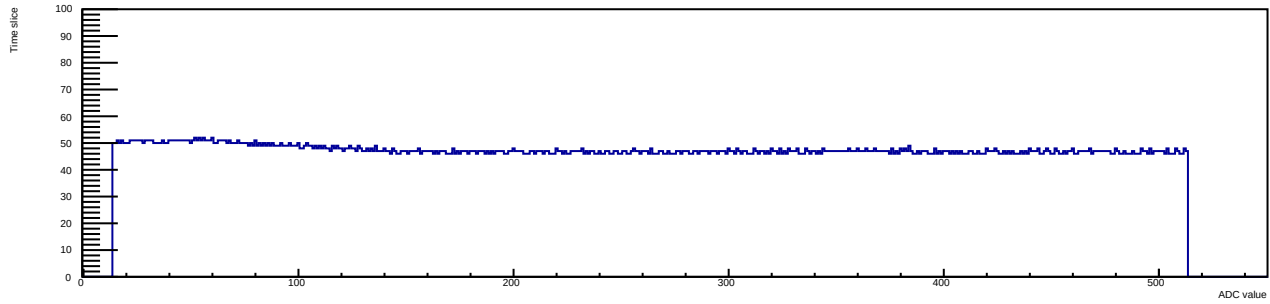
Run 12 Evt 100 Time mcm 0 chip 1 chan 11 30deg



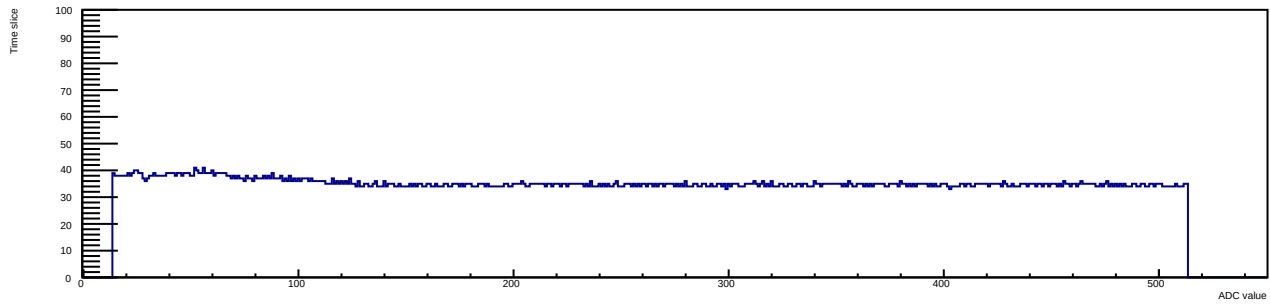
Run 12 Evt 100 Time mcm 0 chip 1 chan 12 30deg



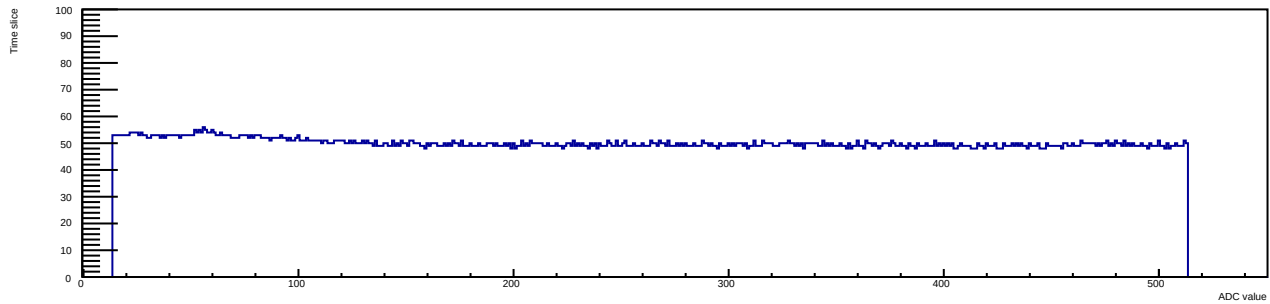
Run 12 Evt 100 Time mcm 0 chip 1 chan 13 30deg



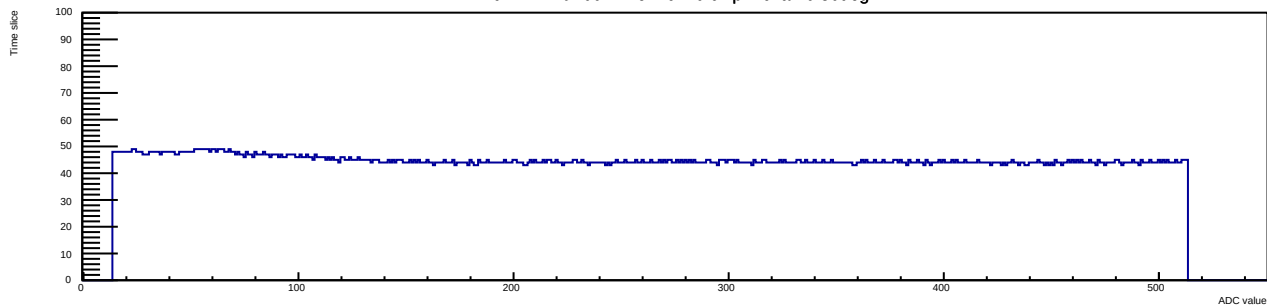
Run 12 Evt 100 Time mcm 0 chip 1 chan 14 30deg



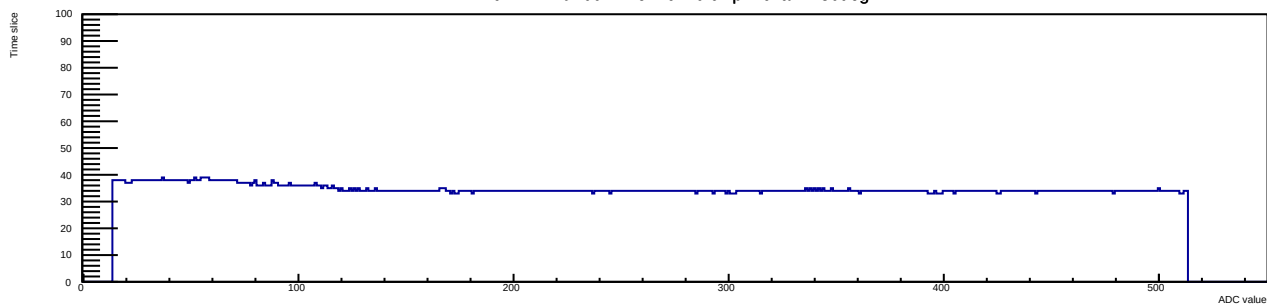
Run 12 Evt 100 Time mcm 0 chip 1 chan 15 30deg



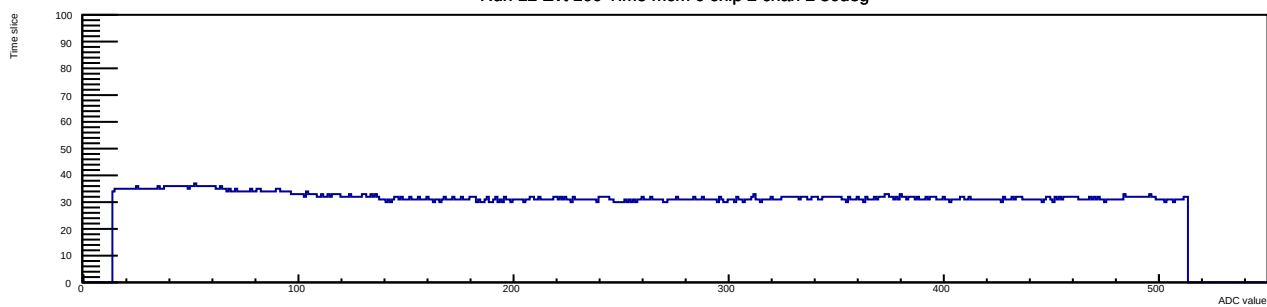
Run 12 Evt 100 Time mcm 0 chip 2 chan 0 30deg



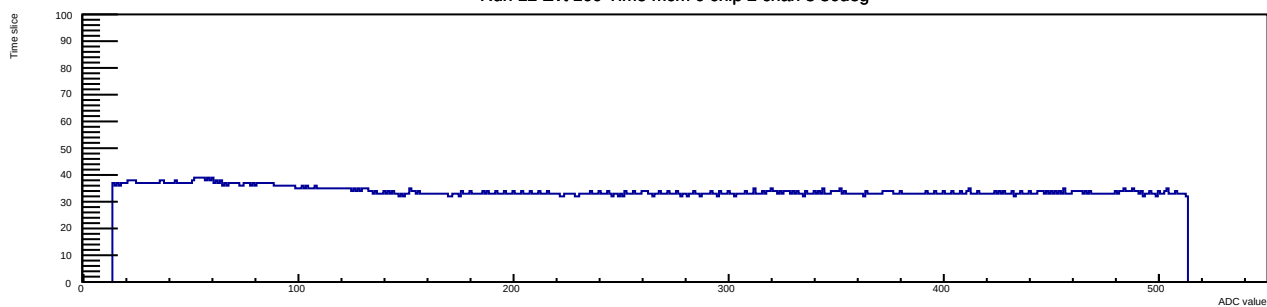
Run 12 Evt 100 Time mcm 0 chip 2 chan 1 30deg



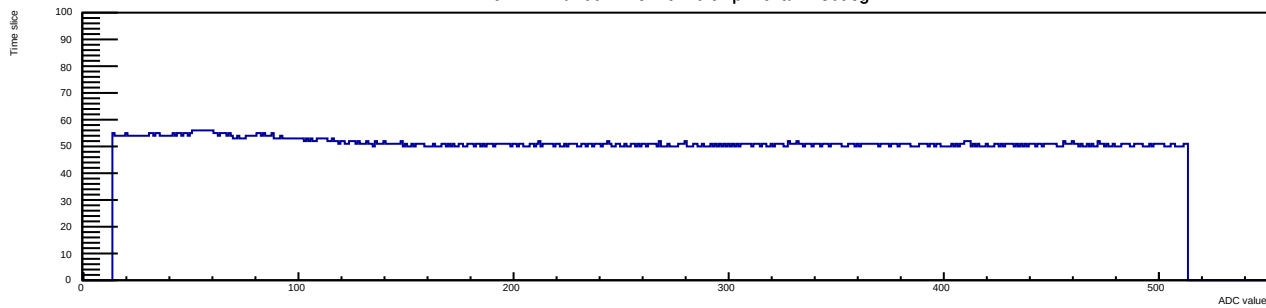
Run 12 Evt 100 Time mcm 0 chip 2 chan 2 30deg



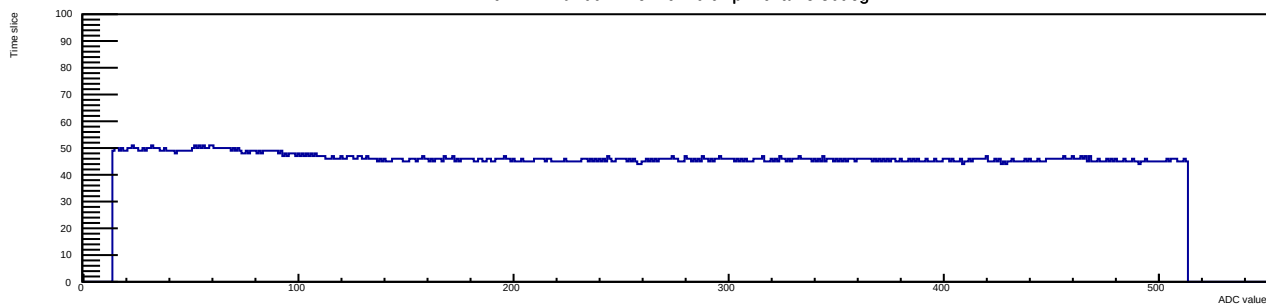
Run 12 Evt 100 Time mcm 0 chip 2 chan 3 30deg



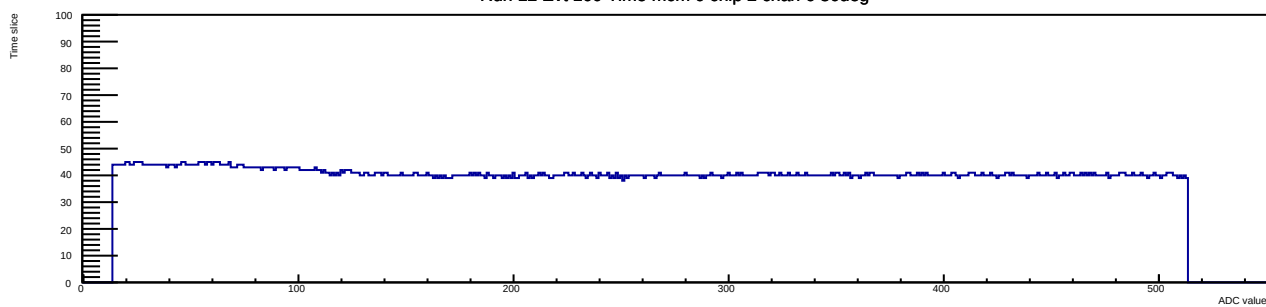
Run 12 Evt 100 Time mcm 0 chip 2 chan 4 30deg



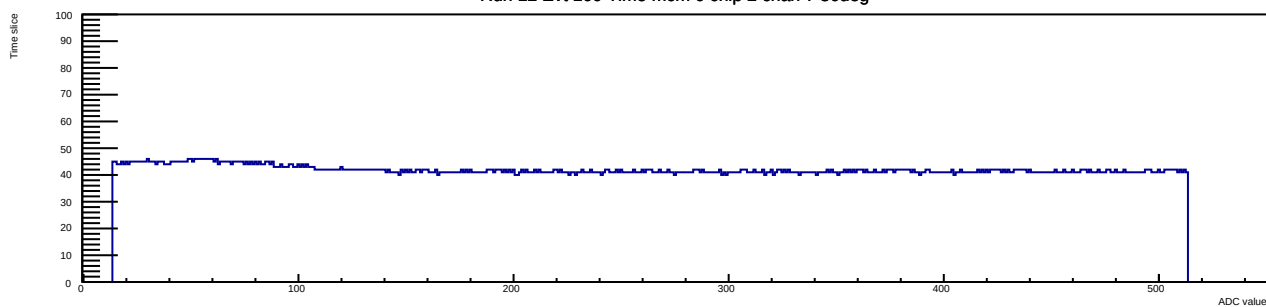
Run 12 Evt 100 Time mcm 0 chip 2 chan 5 30deg



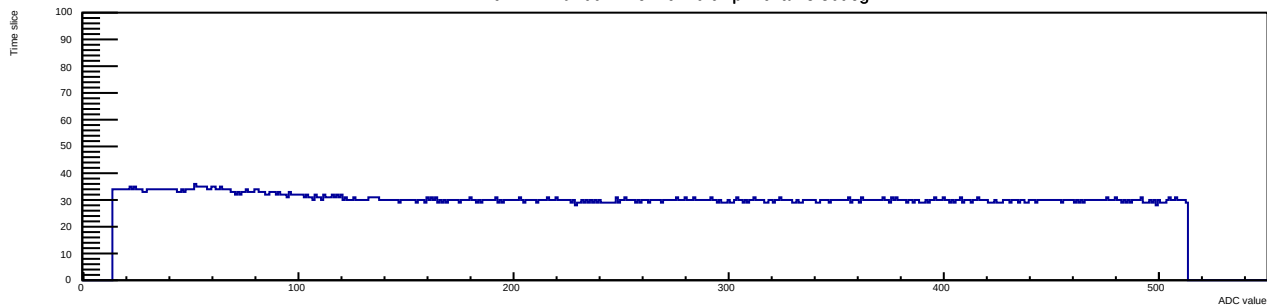
Run 12 Evt 100 Time mcm 0 chip 2 chan 6 30deg



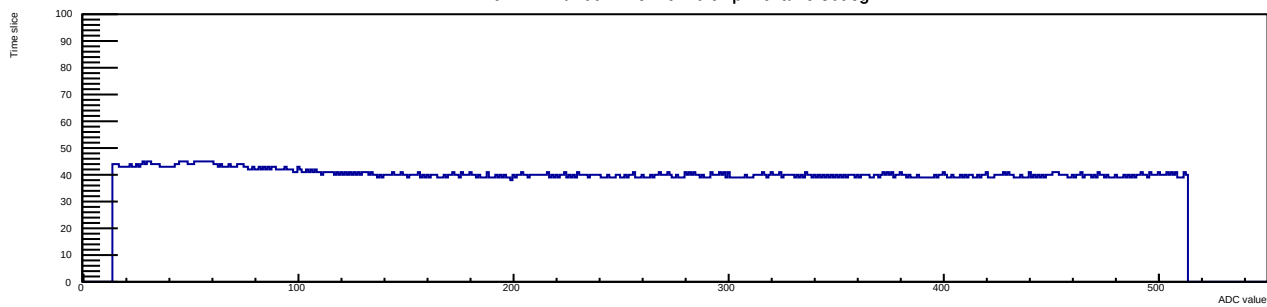
Run 12 Evt 100 Time mcm 0 chip 2 chan 7 30deg



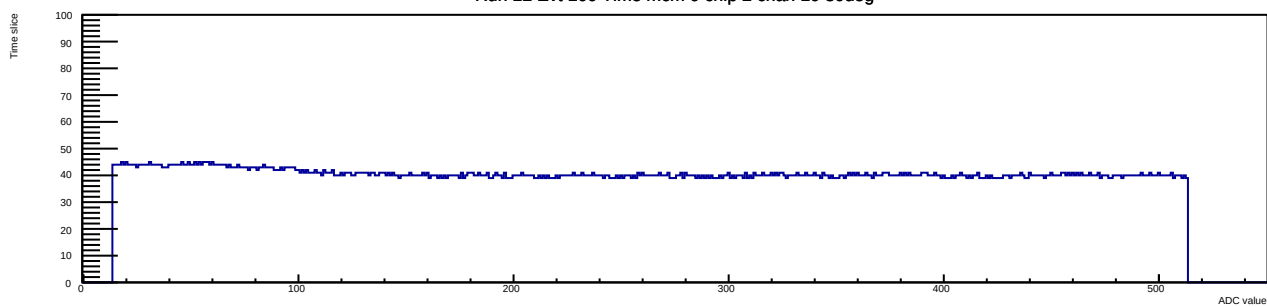
Run 12 Evt 100 Time mcm 0 chip 2 chan 8 30deg



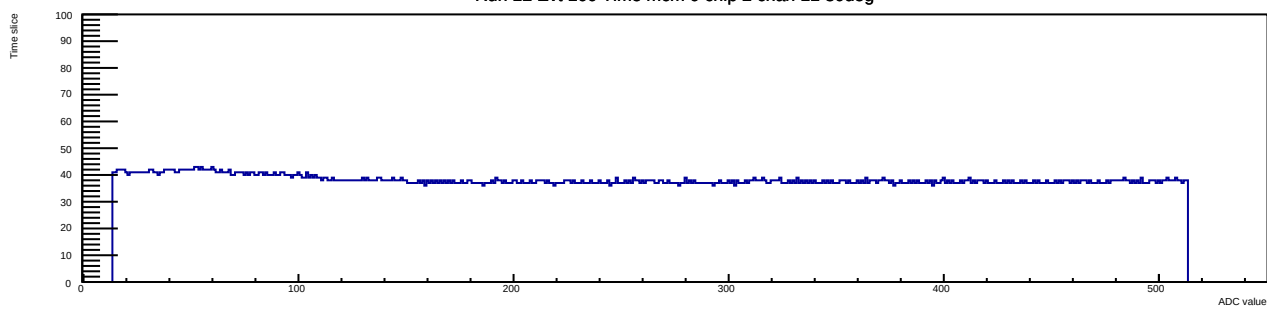
Run 12 Evt 100 Time mcm 0 chip 2 chan 9 30deg



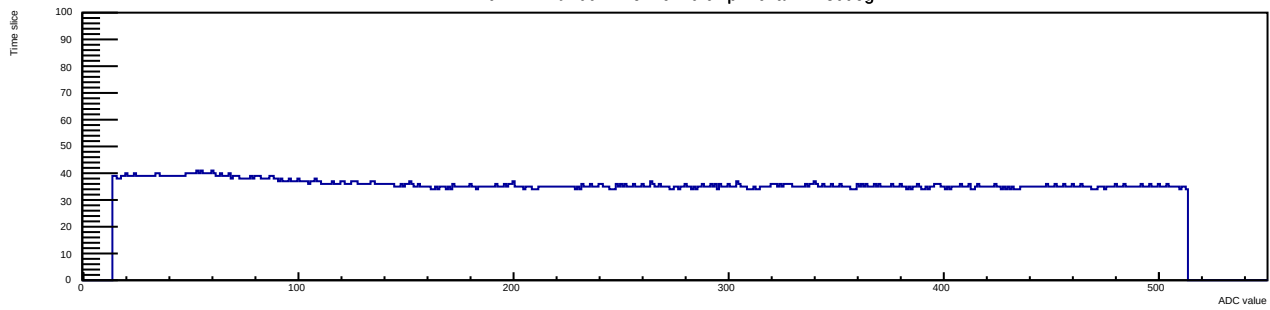
Run 12 Evt 100 Time mcm 0 chip 2 chan 10 30deg



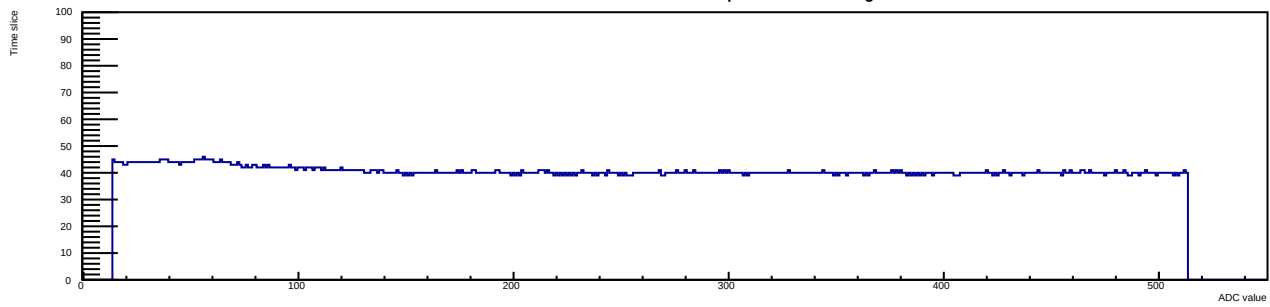
Run 12 Evt 100 Time mcm 0 chip 2 chan 11 30deg



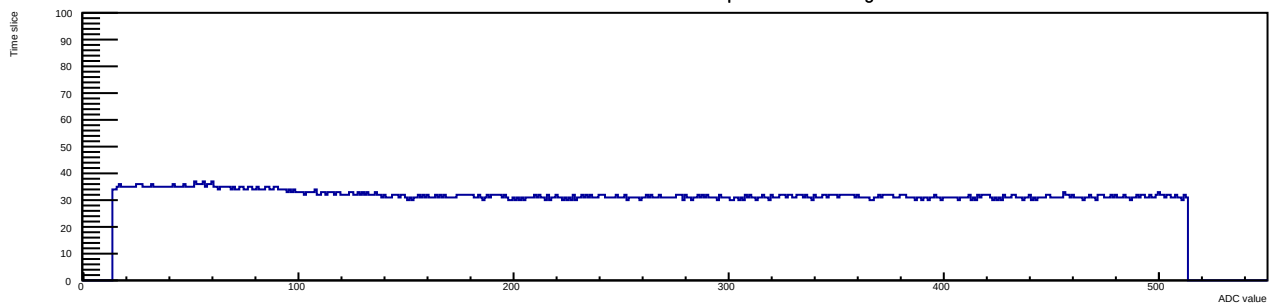
Run 12 Evt 100 Time mcm 0 chip 2 chan 12 30deg



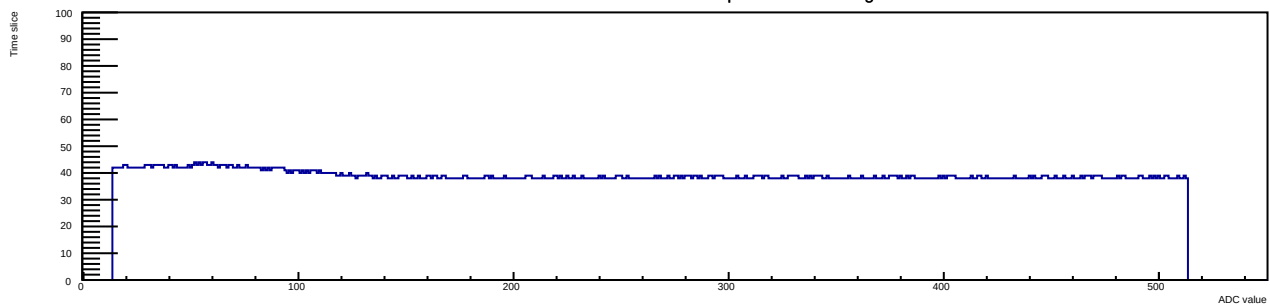
Run 12 Evt 100 Time mcm 0 chip 2 chan 13 30deg



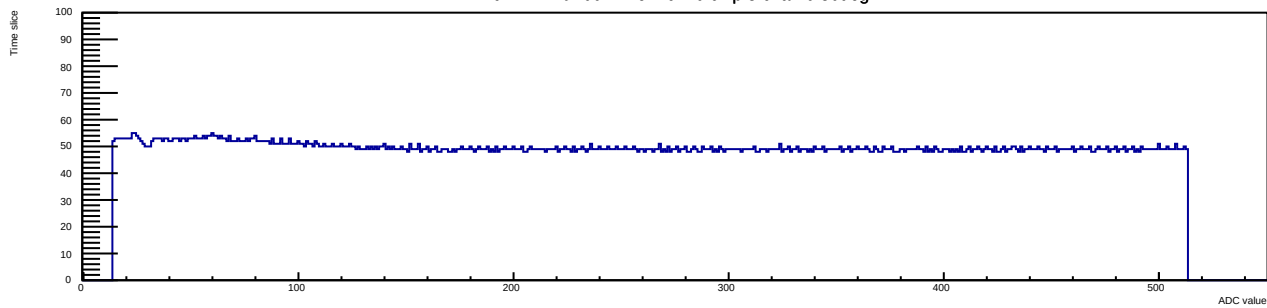
Run 12 Evt 100 Time mcm 0 chip 2 chan 14 30deg



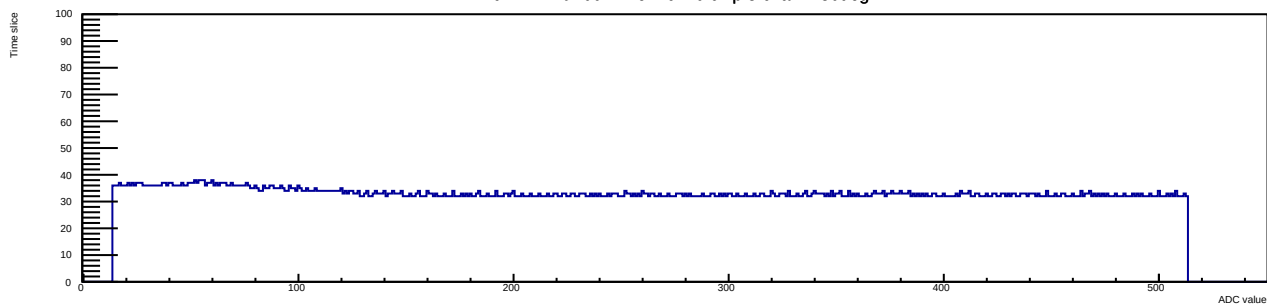
Run 12 Evt 100 Time mcm 0 chip 2 chan 15 30deg



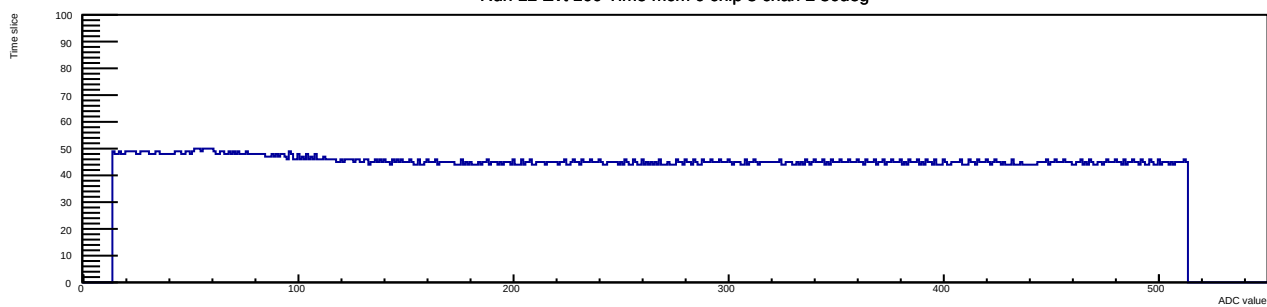
Run 12 Evt 100 Time mcm 0 chip 3 chan 0 30deg



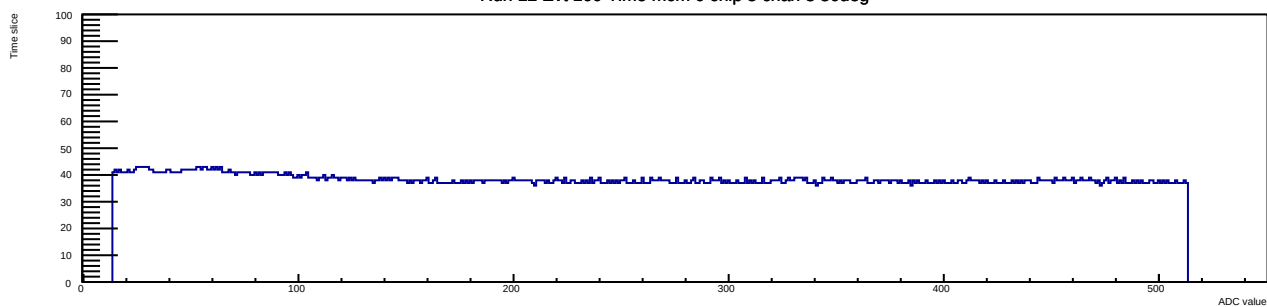
Run 12 Evt 100 Time mcm 0 chip 3 chan 1 30deg



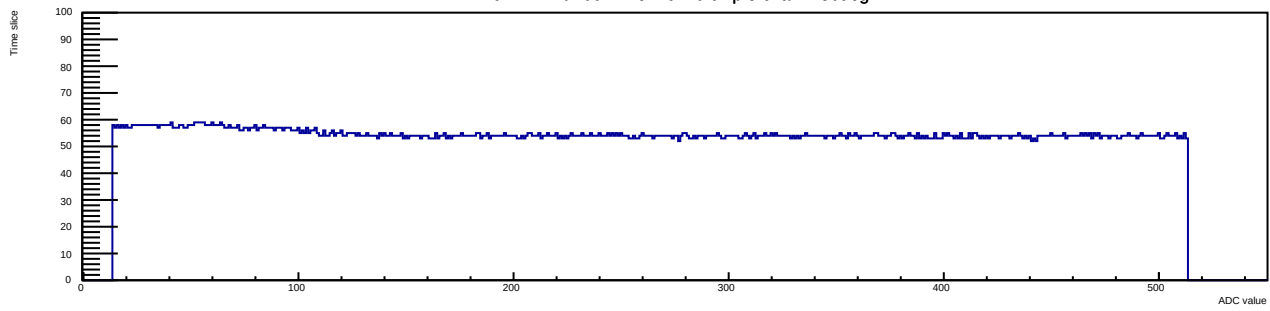
Run 12 Evt 100 Time mcm 0 chip 3 chan 2 30deg



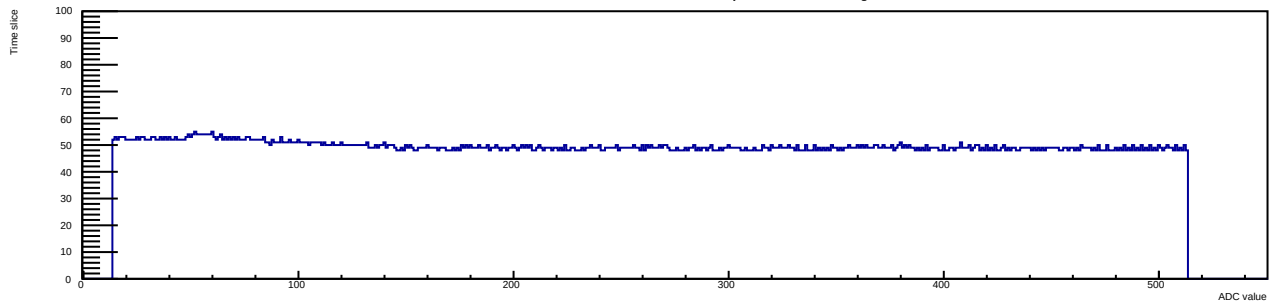
Run 12 Evt 100 Time mcm 0 chip 3 chan 3 30deg



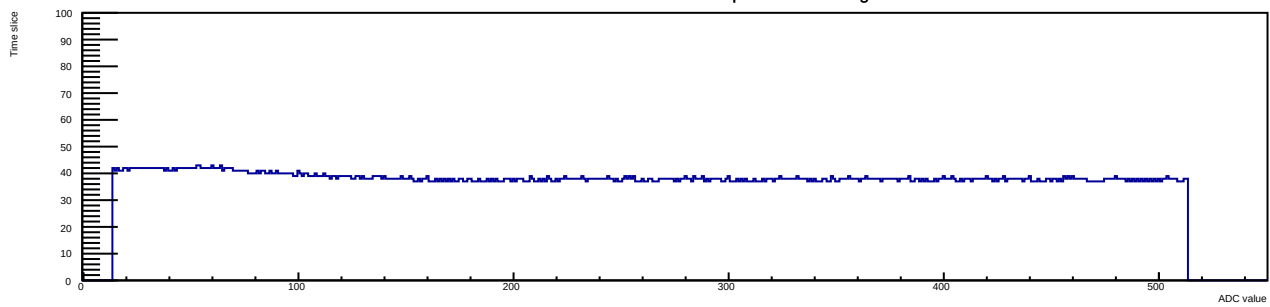
Run 12 Evt 100 Time mcm 0 chip 3 chan 4 30deg



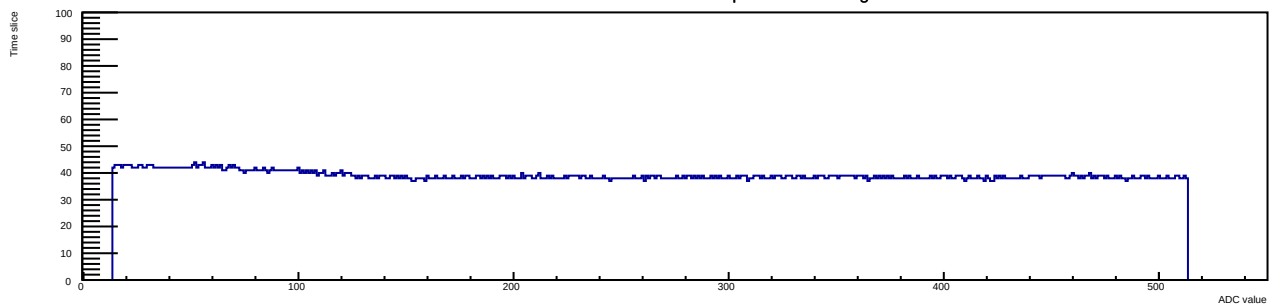
Run 12 Evt 100 Time mcm 0 chip 3 chan 5 30deg



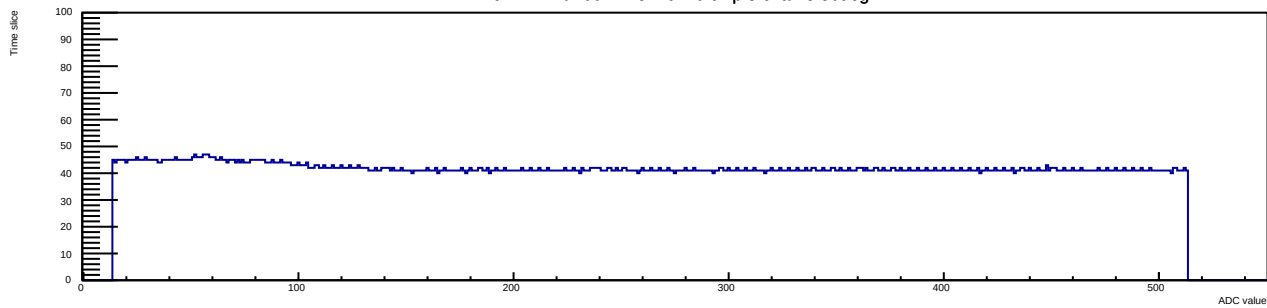
Run 12 Evt 100 Time mcm 0 chip 3 chan 6 30deg



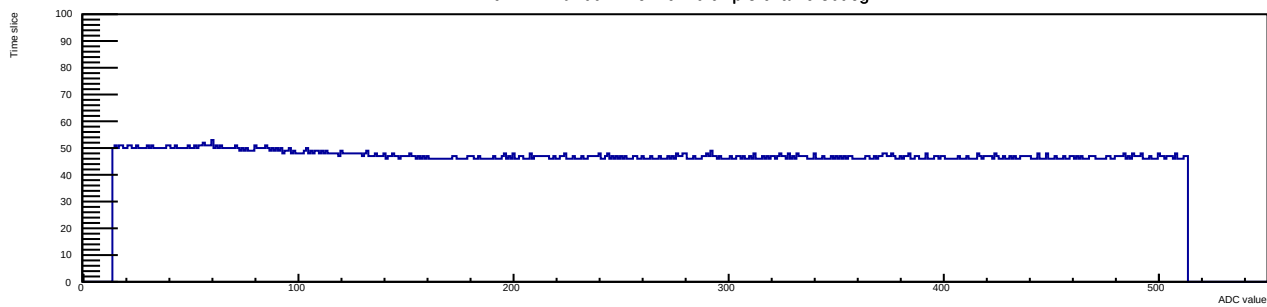
Run 12 Evt 100 Time mcm 0 chip 3 chan 7 30deg



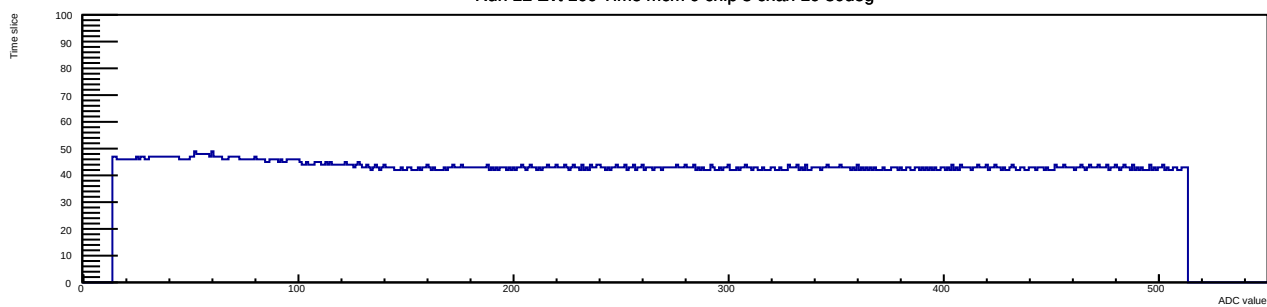
Run 12 Evt 100 Time mcm 0 chip 3 chan 8 30deg



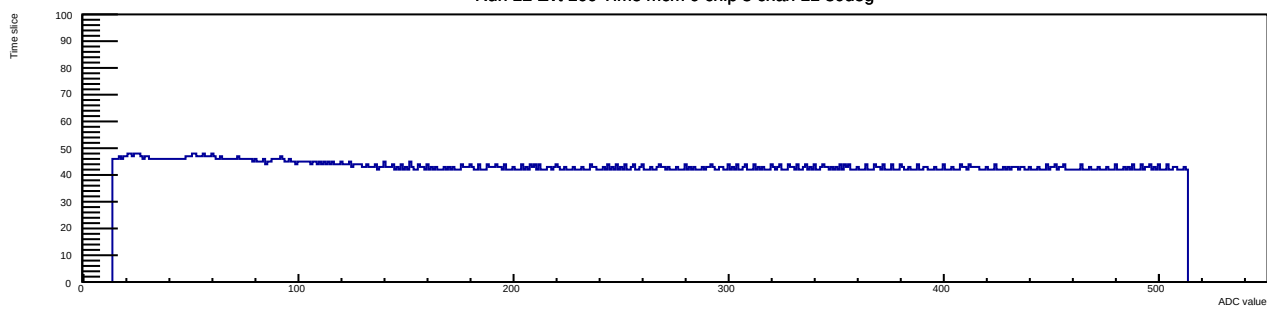
Run 12 Evt 100 Time mcm 0 chip 3 chan 9 30deg



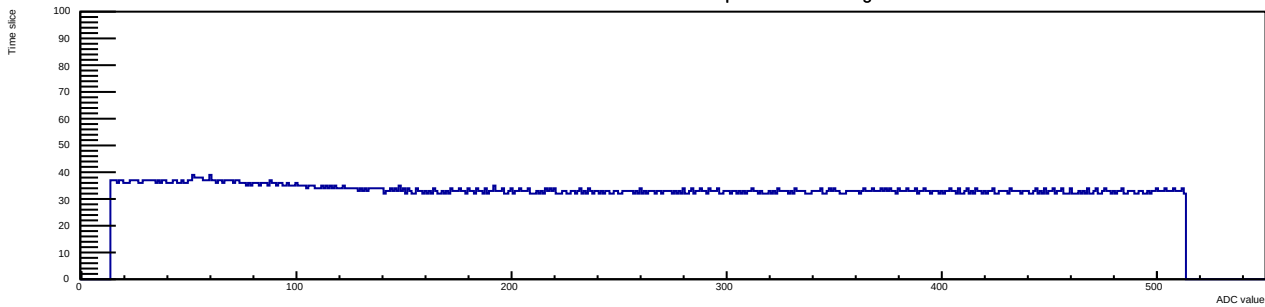
Run 12 Evt 100 Time mcm 0 chip 3 chan 10 30deg



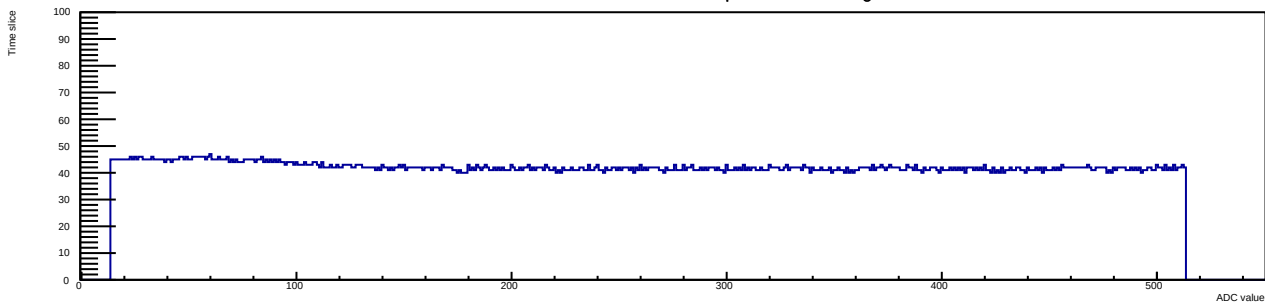
Run 12 Evt 100 Time mcm 0 chip 3 chan 11 30deg



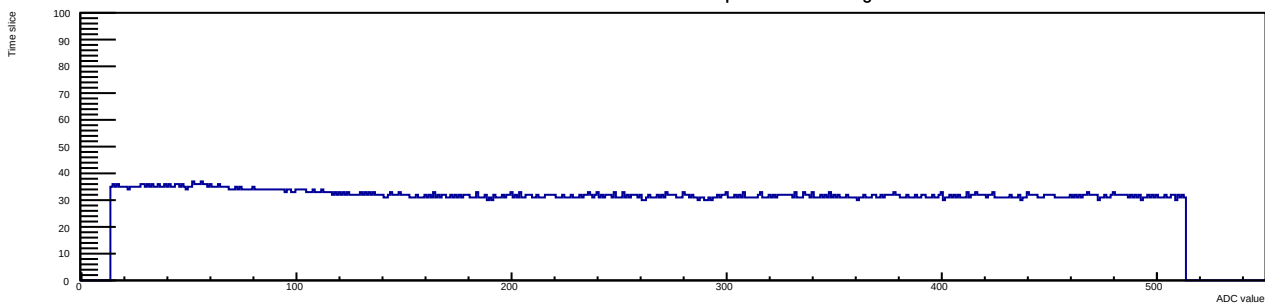
Run 12 Evt 100 Time mcm 0 chip 3 chan 12 30deg



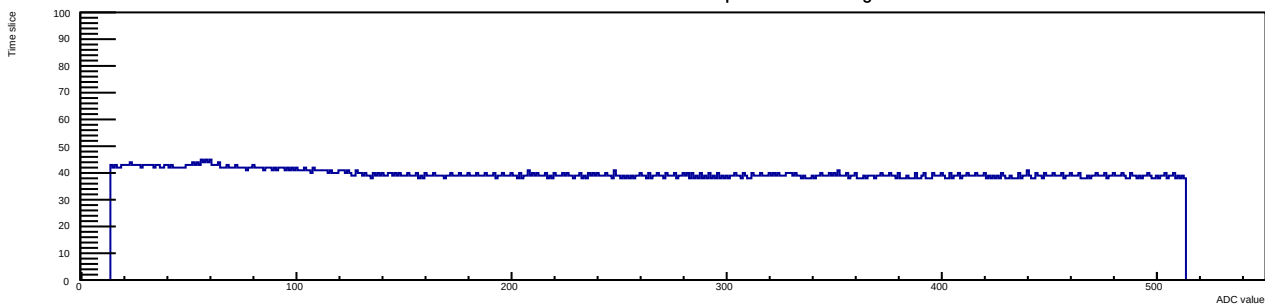
Run 12 Evt 100 Time mcm 0 chip 3 chan 13 30deg



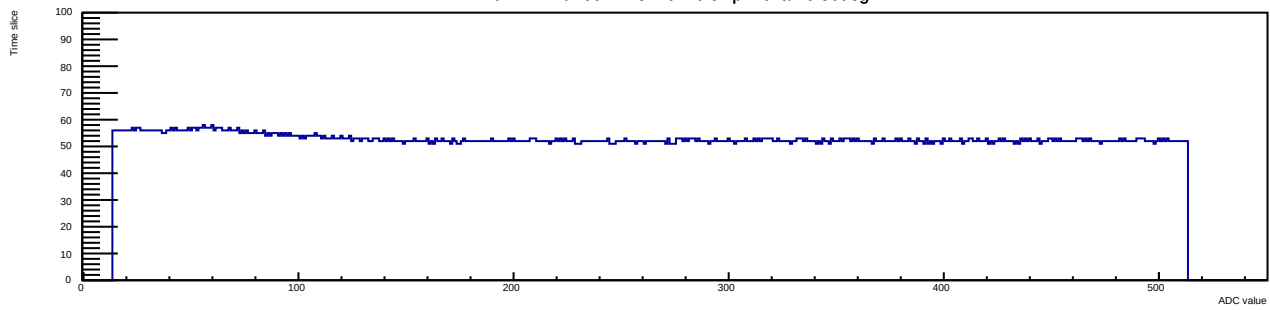
Run 12 Evt 100 Time mcm 0 chip 3 chan 14 30deg



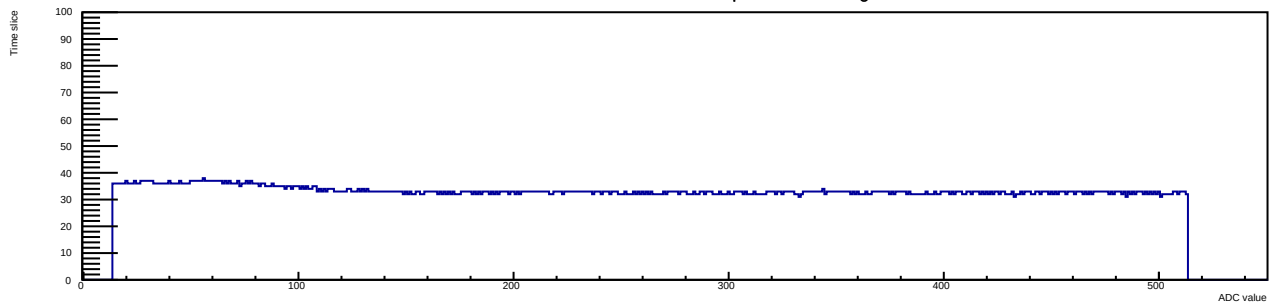
Run 12 Evt 100 Time mcm 0 chip 3 chan 15 30deg



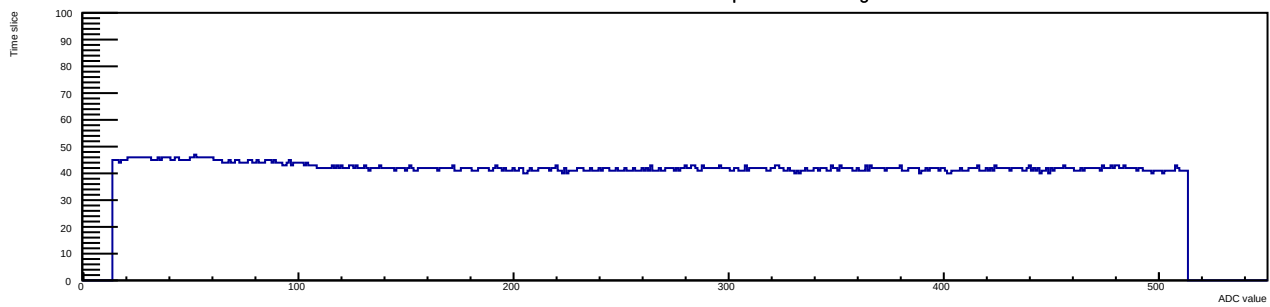
Run 12 Evt 100 Time mcm 0 chip 4 chan 0 30deg



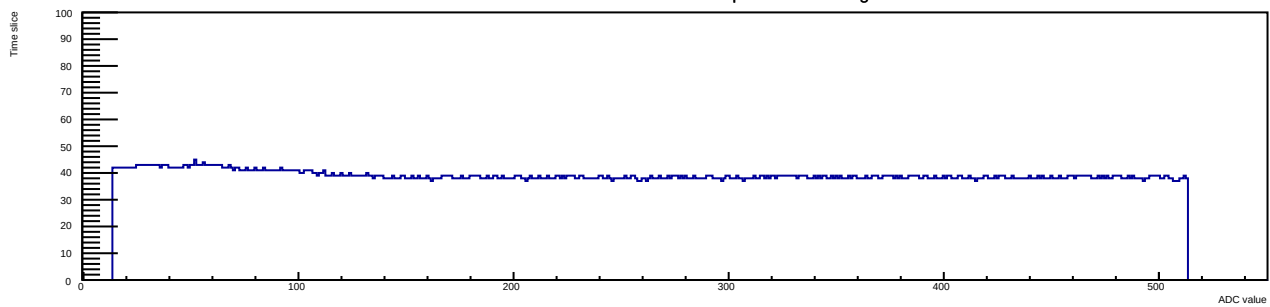
Run 12 Evt 100 Time mcm 0 chip 4 chan 1 30deg



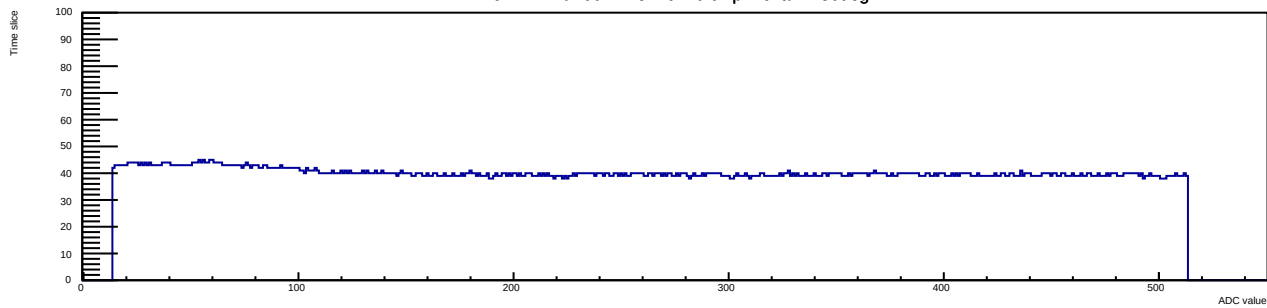
Run 12 Evt 100 Time mcm 0 chip 4 chan 2 30deg



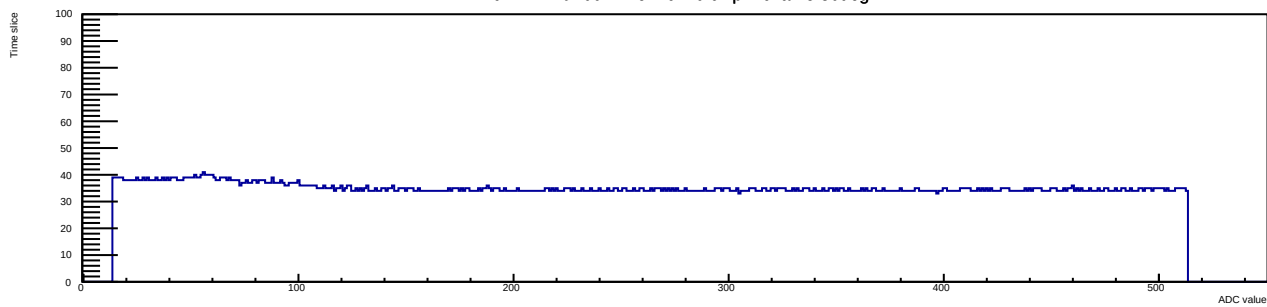
Run 12 Evt 100 Time mcm 0 chip 4 chan 3 30deg



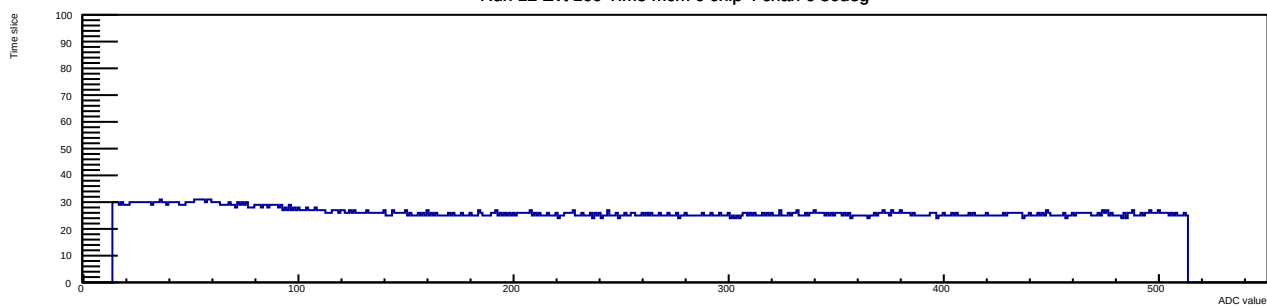
Run 12 Evt 100 Time mcm 0 chip 4 chan 4 30deg



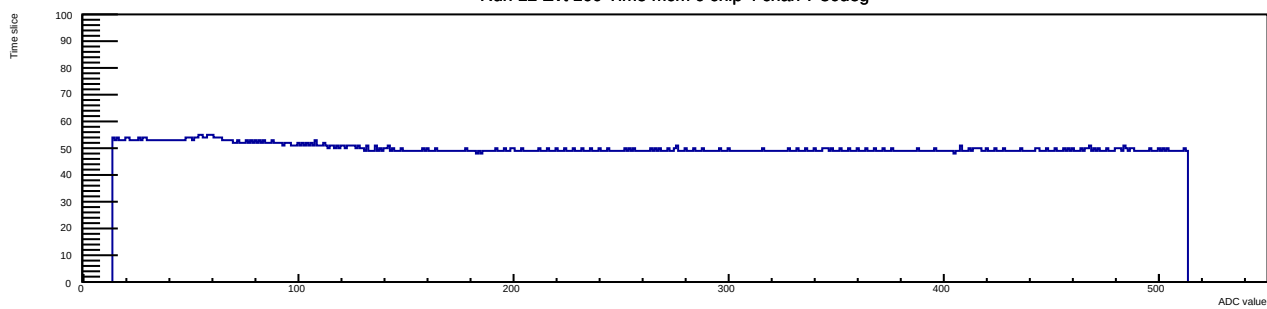
Run 12 Evt 100 Time mcm 0 chip 4 chan 5 30deg



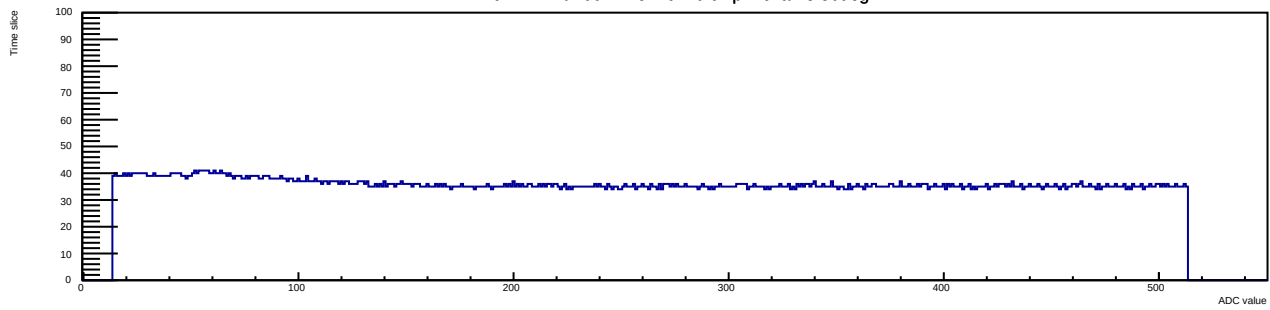
Run 12 Evt 100 Time mcm 0 chip 4 chan 6 30deg



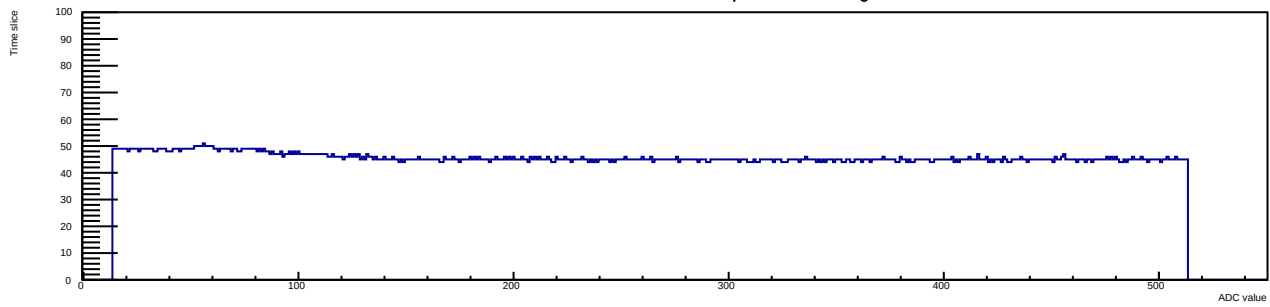
Run 12 Evt 100 Time mcm 0 chip 4 chan 7 30deg



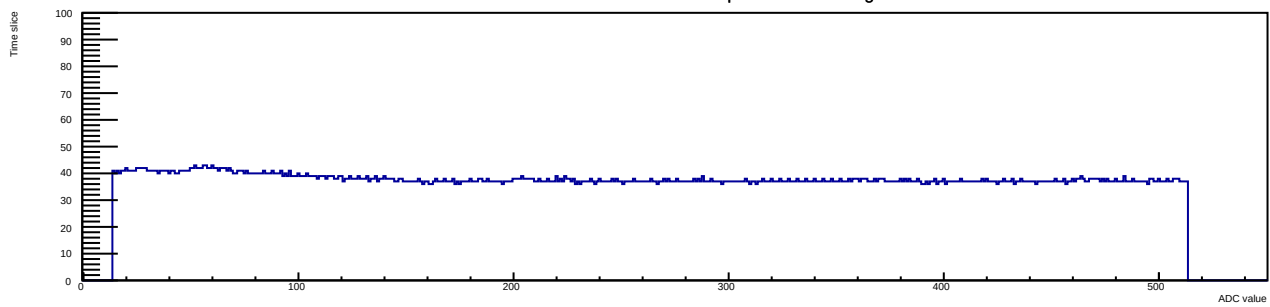
Run 12 Evt 100 Time mcm 0 chip 4 chan 8 30deg



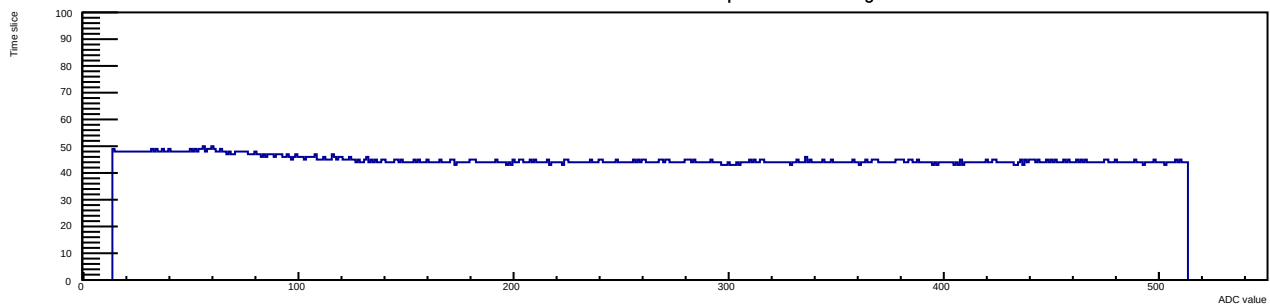
Run 12 Evt 100 Time mcm 0 chip 4 chan 9 30deg



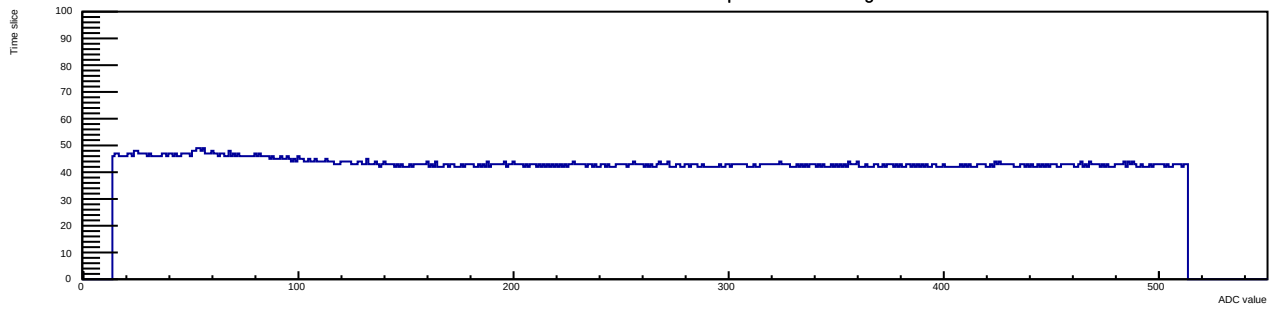
Run 12 Evt 100 Time mcm 0 chip 4 chan 10 30deg



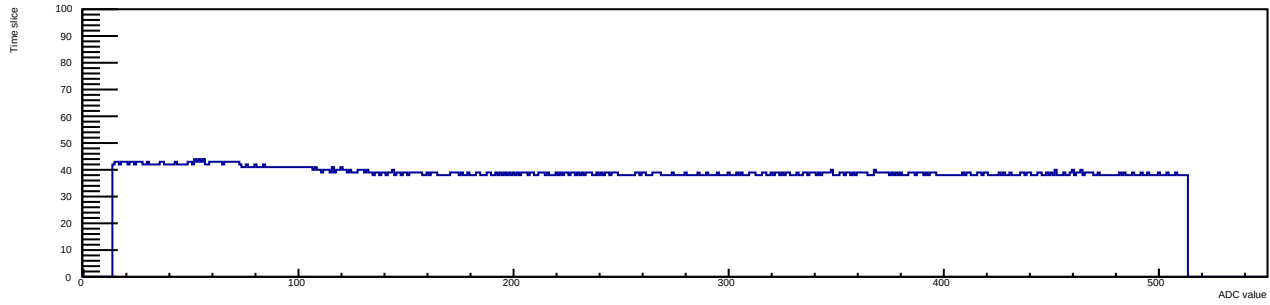
Run 12 Evt 100 Time mcm 0 chip 4 chan 11 30deg



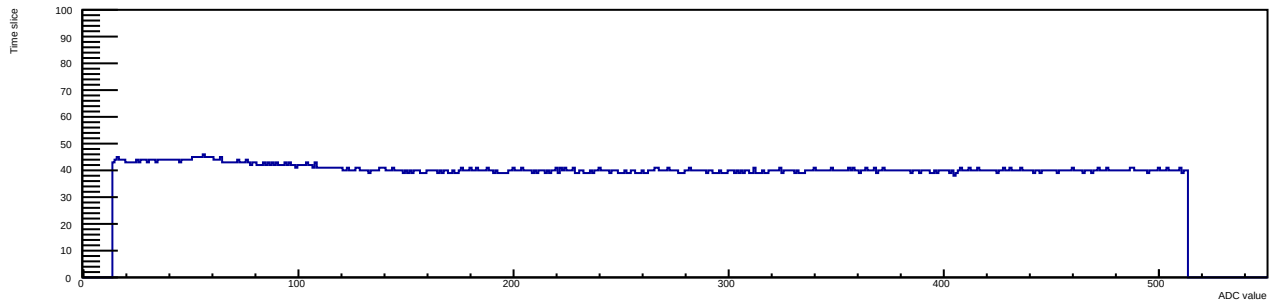
Run 12 Evt 100 Time mcm 0 chip 4 chan 12 30deg



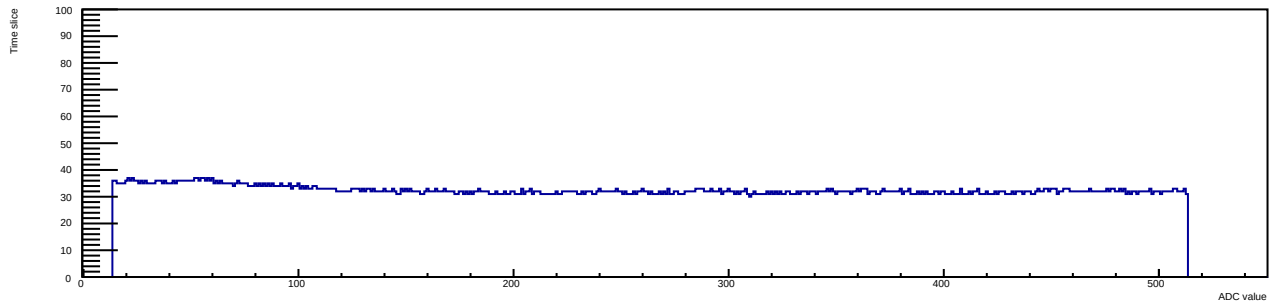
Run 12 Evt 100 Time mcm 0 chip 4 chan 13 30deg



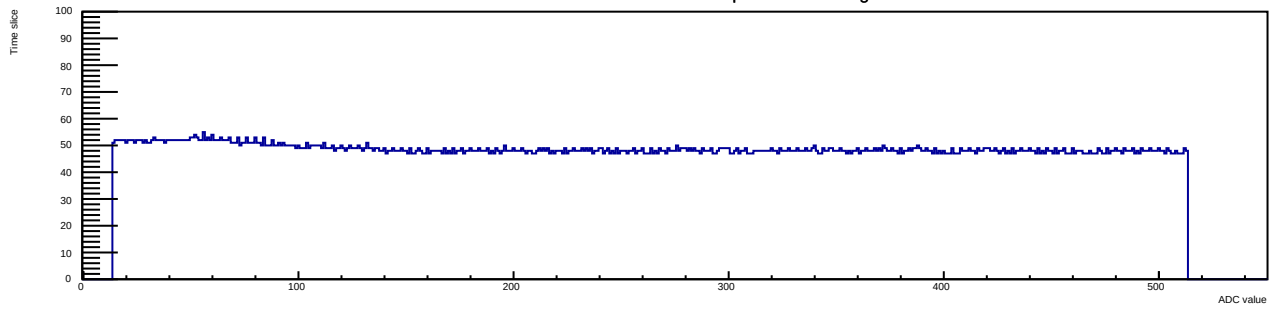
Run 12 Evt 100 Time mcm 0 chip 4 chan 14 30deg



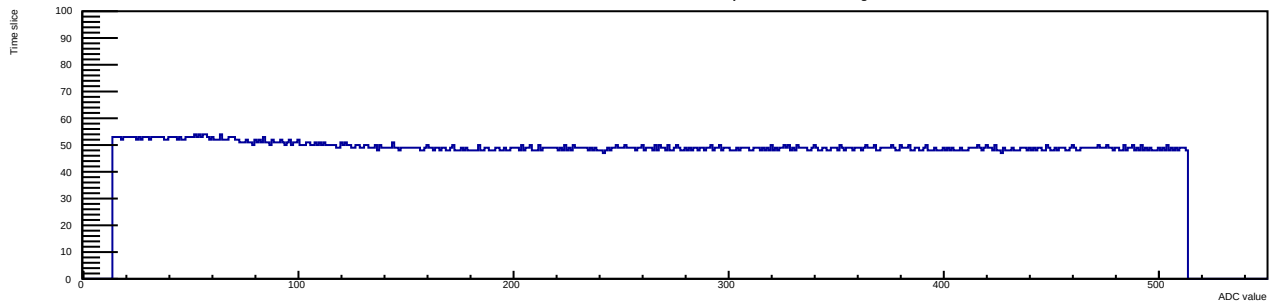
Run 12 Evt 100 Time mcm 0 chip 4 chan 15 30deg



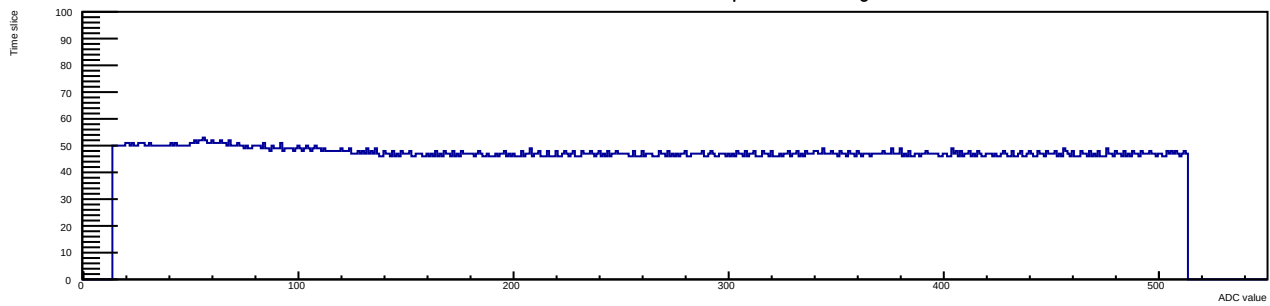
Run 12 Evt 100 Time mcm 0 chip 5 chan 0 30deg



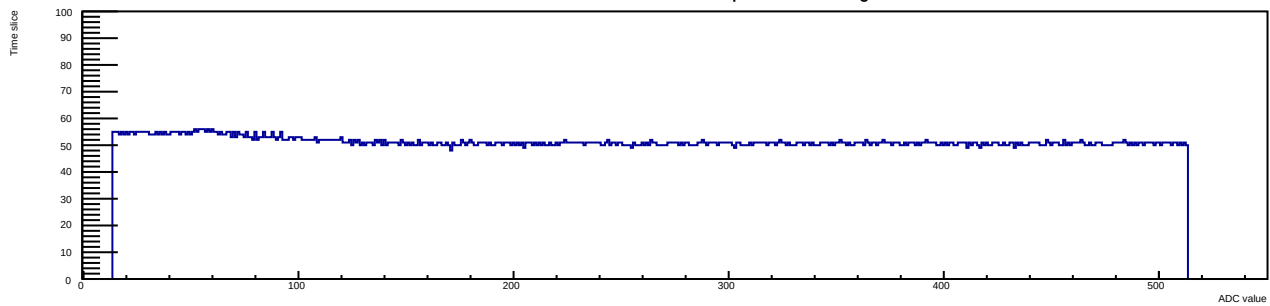
Run 12 Evt 100 Time mcm 0 chip 5 chan 1 30deg



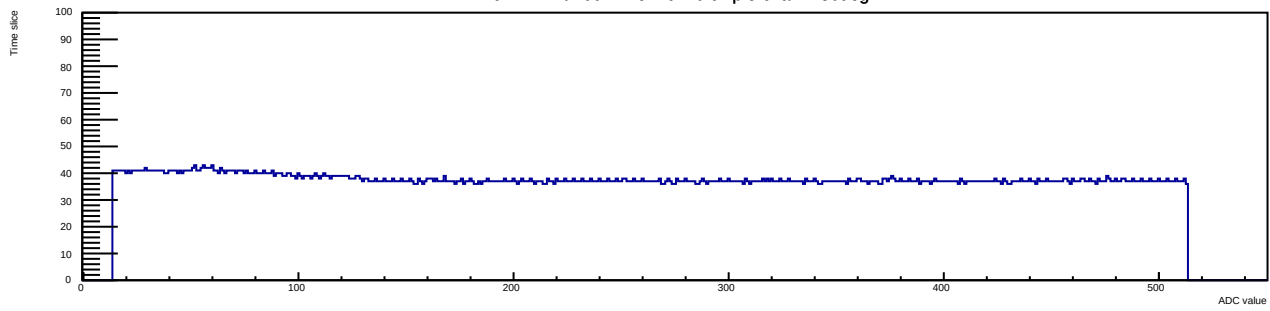
Run 12 Evt 100 Time mcm 0 chip 5 chan 2 30deg



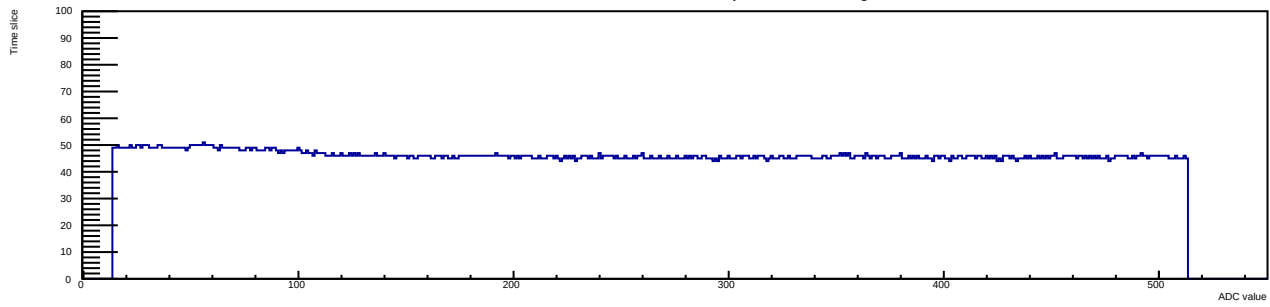
Run 12 Evt 100 Time mcm 0 chip 5 chan 3 30deg



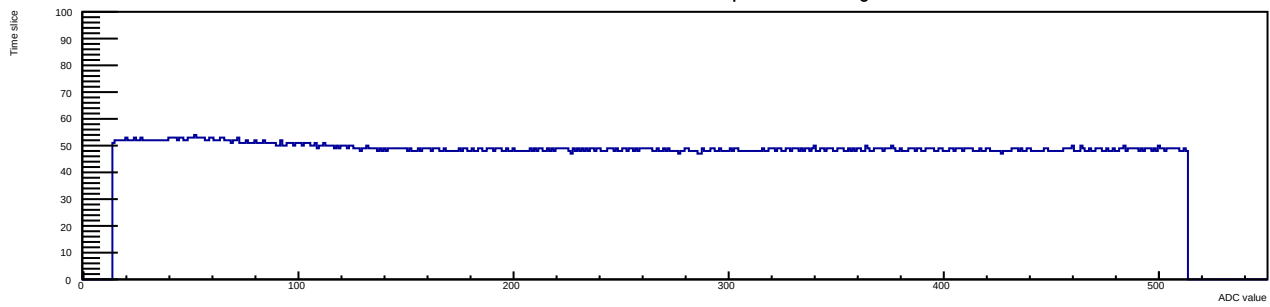
Run 12 Evt 100 Time mcm 0 chip 5 chan 4 30deg



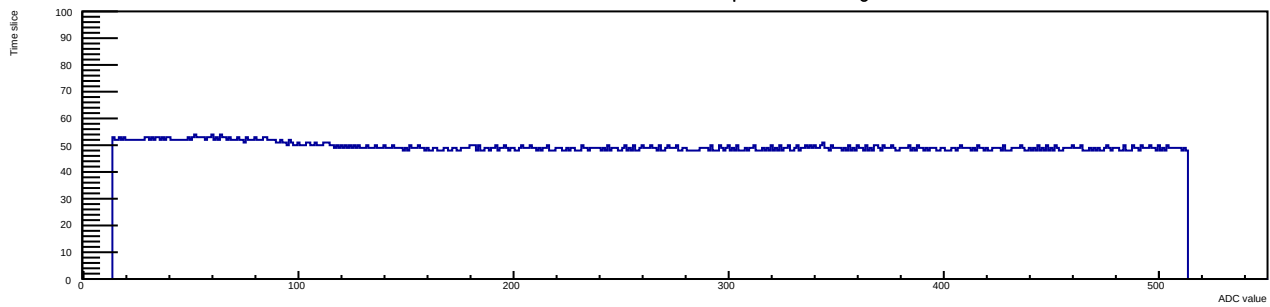
Run 12 Evt 100 Time mcm 0 chip 5 chan 5 30deg



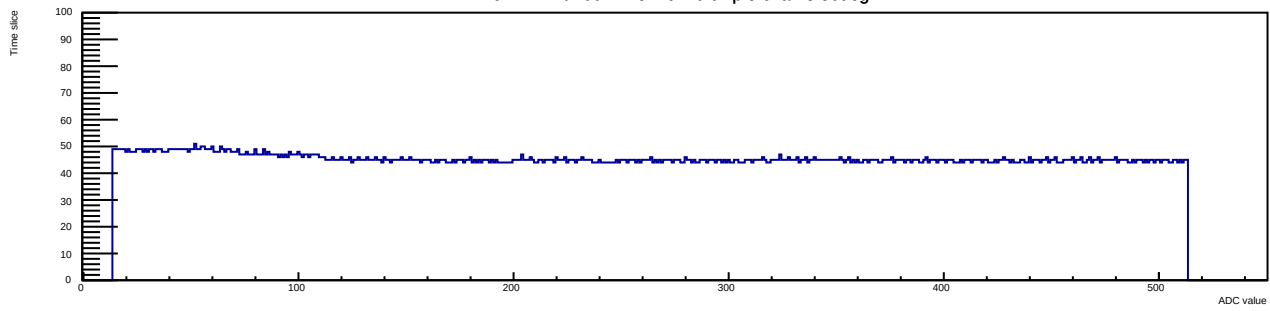
Run 12 Evt 100 Time mcm 0 chip 5 chan 6 30deg



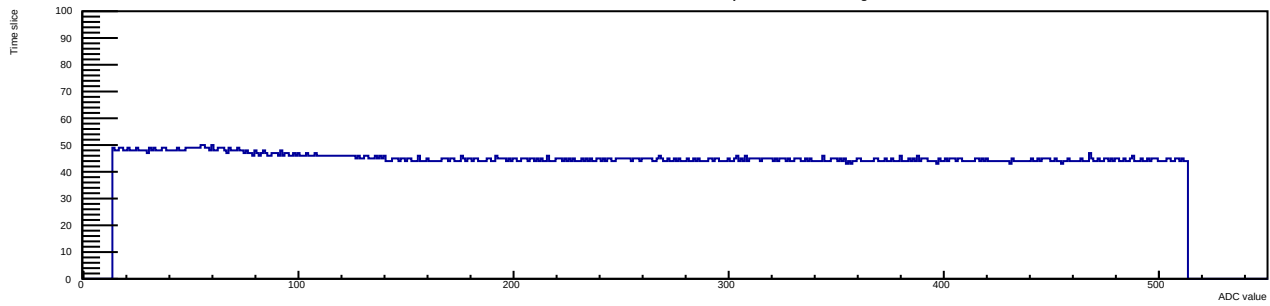
Run 12 Evt 100 Time mcm 0 chip 5 chan 7 30deg



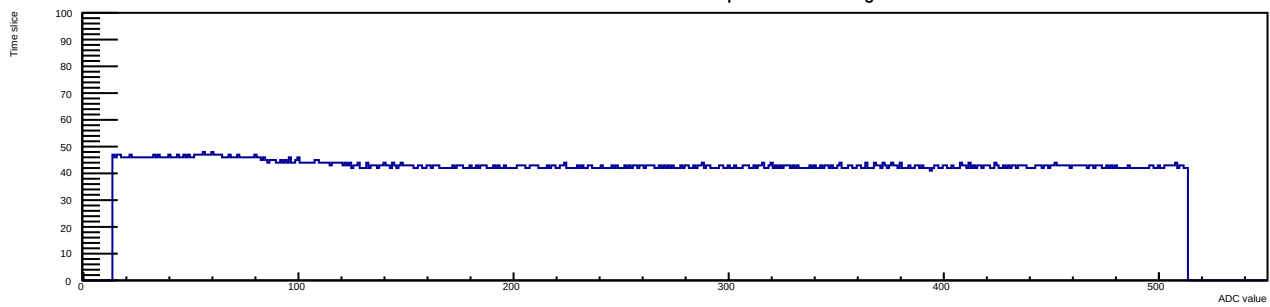
Run 12 Evt 100 Time mcm 0 chip 5 chan 8 30deg



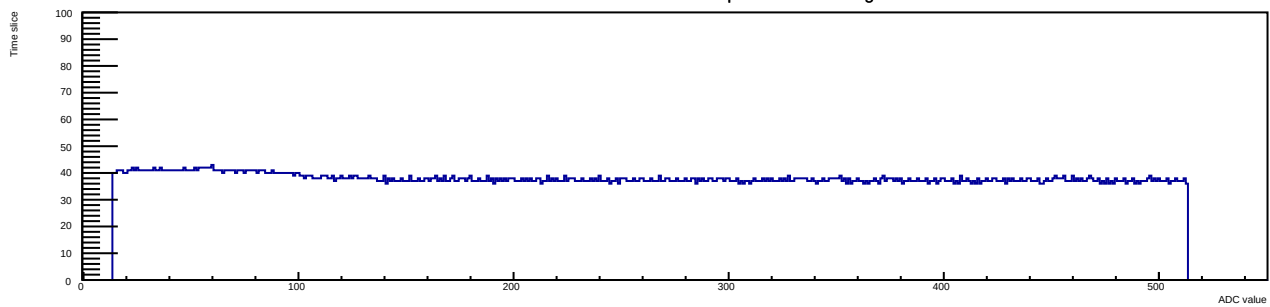
Run 12 Evt 100 Time mcm 0 chip 5 chan 9 30deg



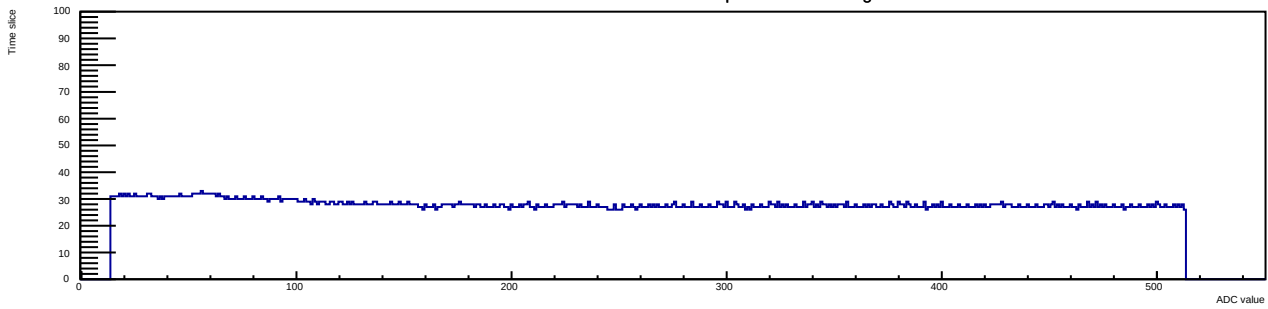
Run 12 Evt 100 Time mcm 0 chip 5 chan 10 30deg



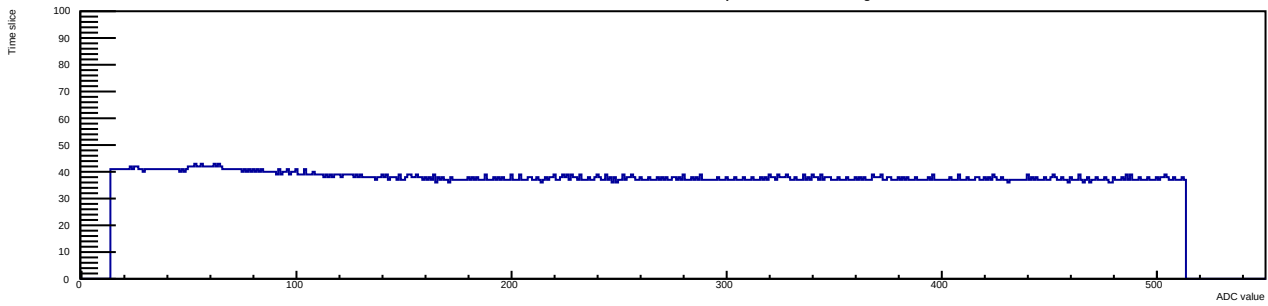
Run 12 Evt 100 Time mcm 0 chip 5 chan 11 30deg



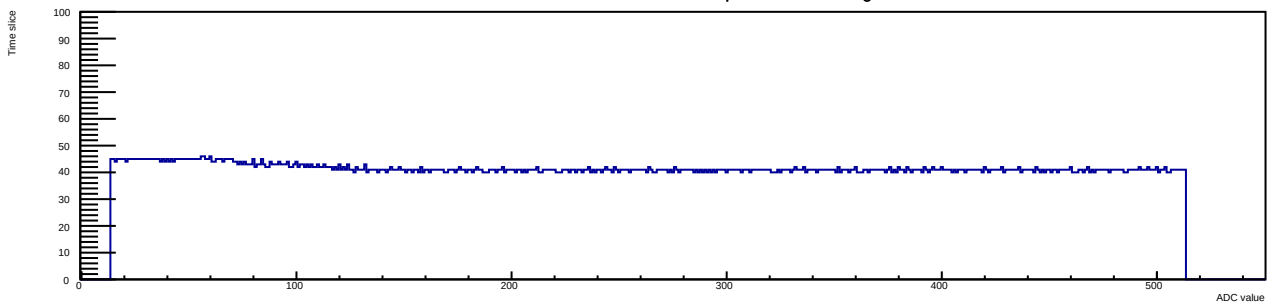
Run 12 Evt 100 Time mcm 0 chip 5 chan 12 30deg



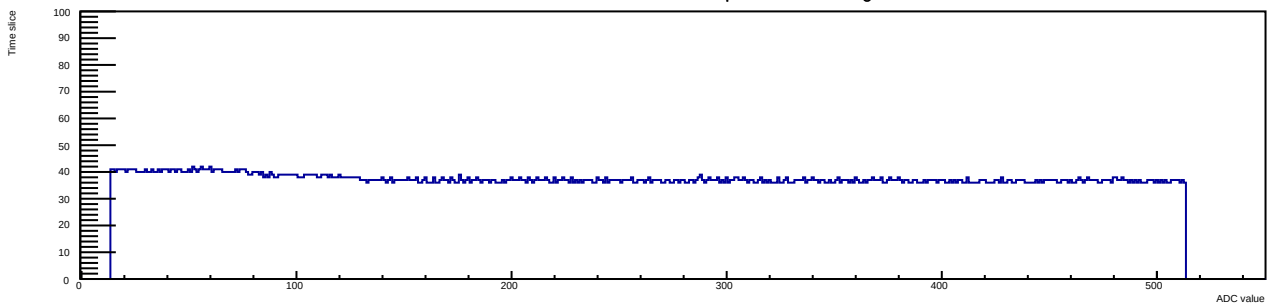
Run 12 Evt 100 Time mcm 0 chip 5 chan 13 30deg



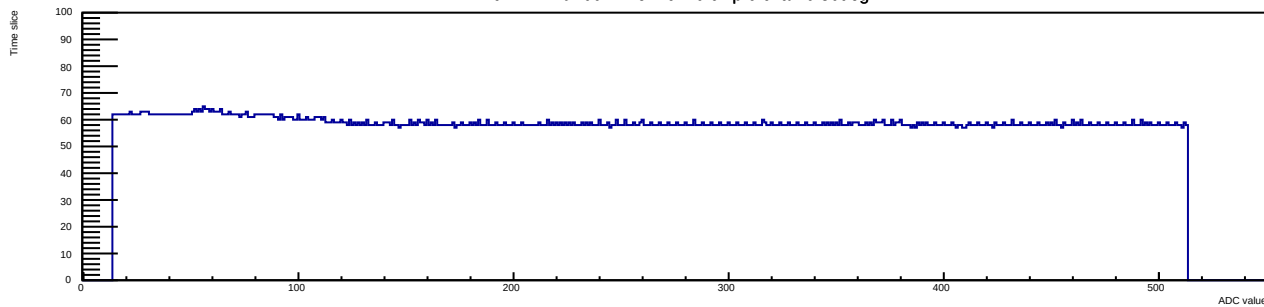
Run 12 Evt 100 Time mcm 0 chip 5 chan 14 30deg



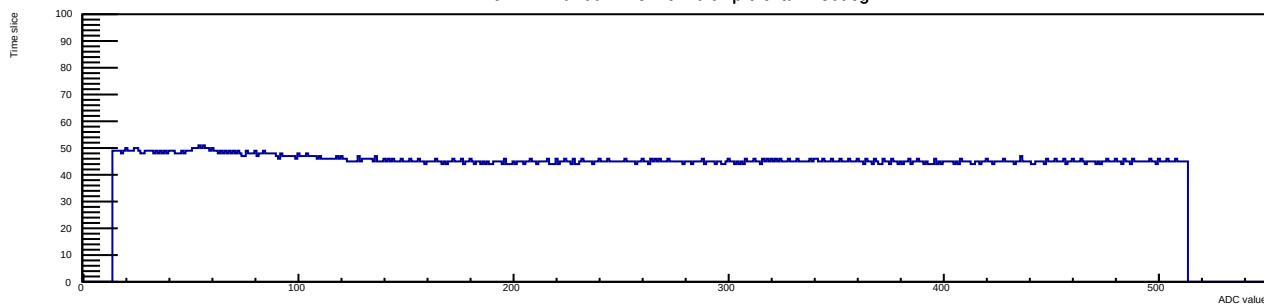
Run 12 Evt 100 Time mcm 0 chip 5 chan 15 30deg



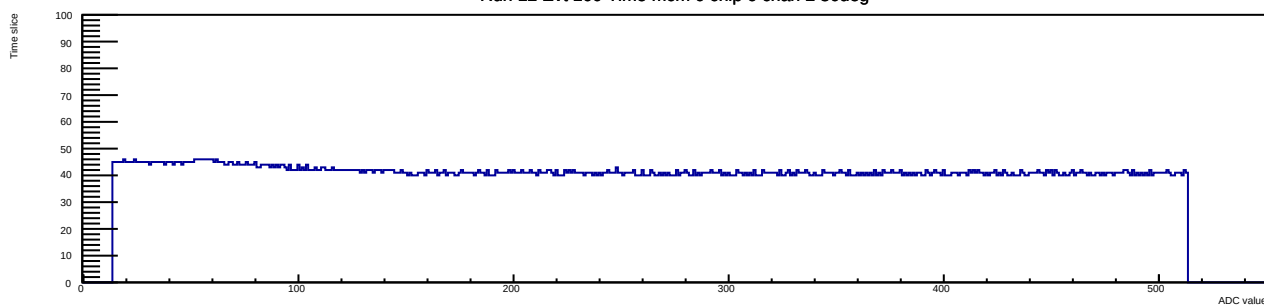
Run 12 Evt 100 Time mcm 0 chip 6 chan 0 30deg



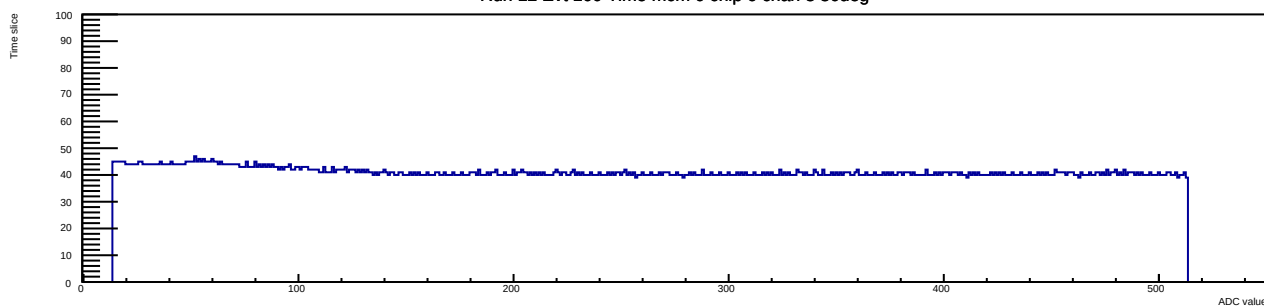
Run 12 Evt 100 Time mcm 0 chip 6 chan 1 30deg



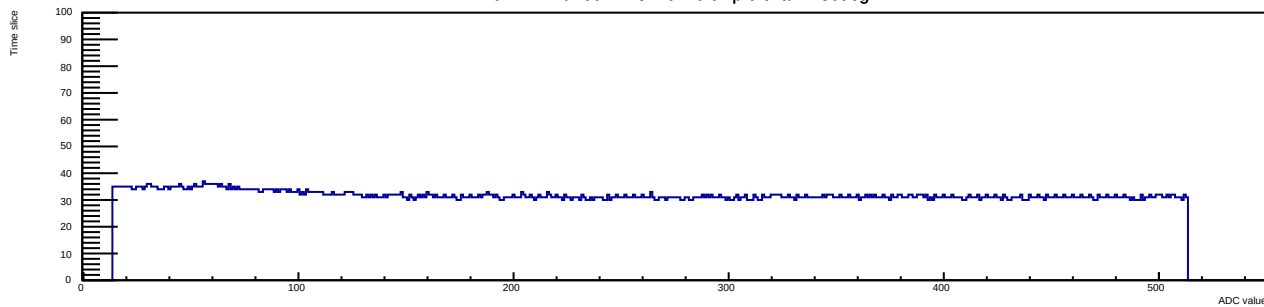
Run 12 Evt 100 Time mcm 0 chip 6 chan 2 30deg



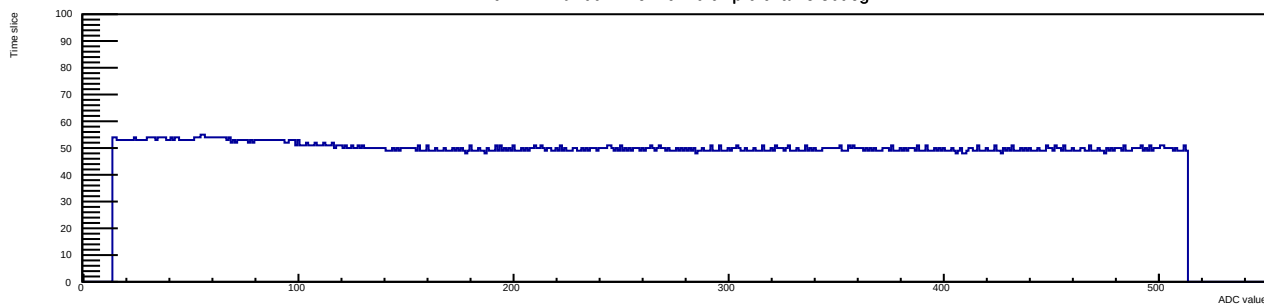
Run 12 Evt 100 Time mcm 0 chip 6 chan 3 30deg



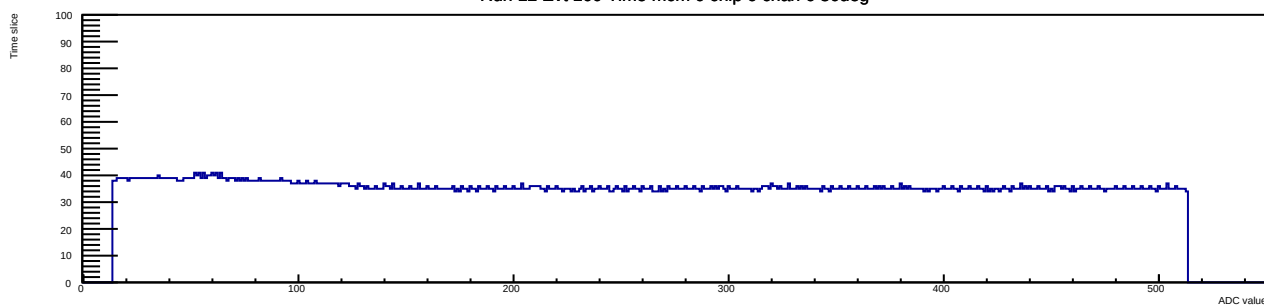
Run 12 Evt 100 Time mcm 0 chip 6 chan 4 30deg



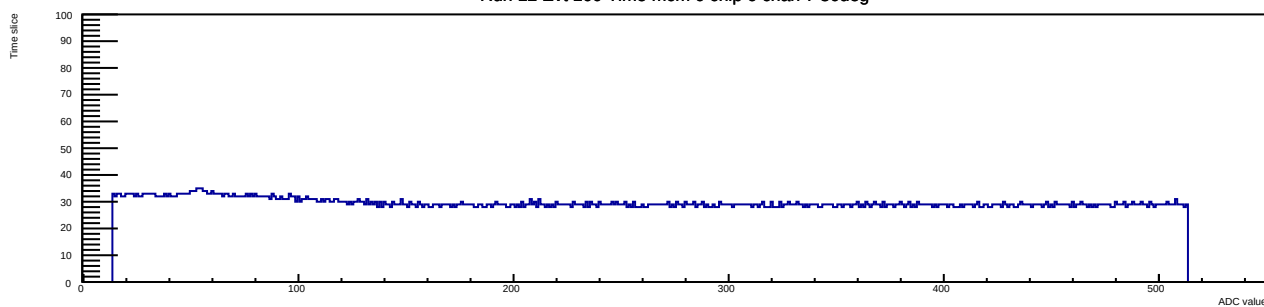
Run 12 Evt 100 Time mcm 0 chip 6 chan 5 30deg



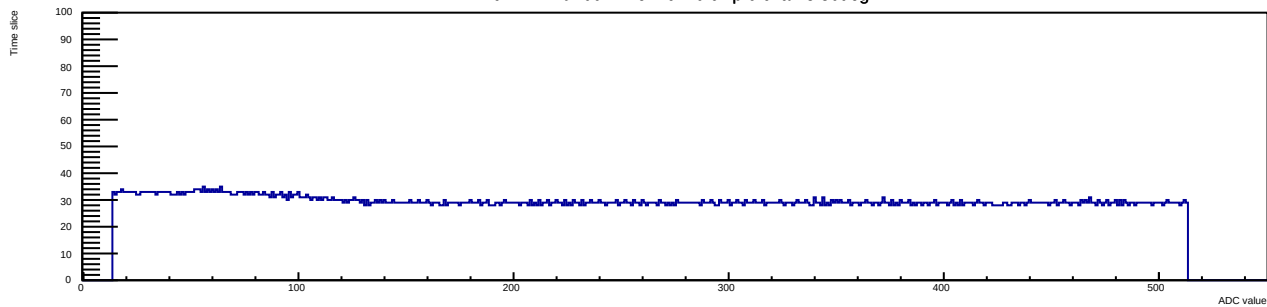
Run 12 Evt 100 Time mcm 0 chip 6 chan 6 30deg



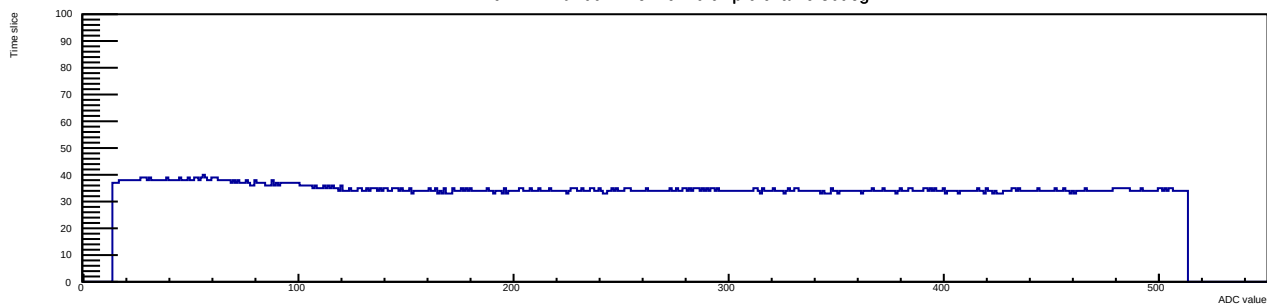
Run 12 Evt 100 Time mcm 0 chip 6 chan 7 30deg



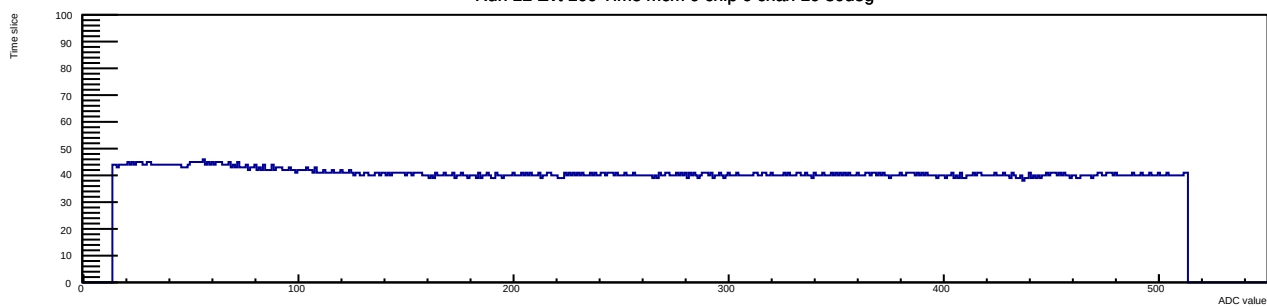
Run 12 Evt 100 Time mcm 0 chip 6 chan 8 30deg



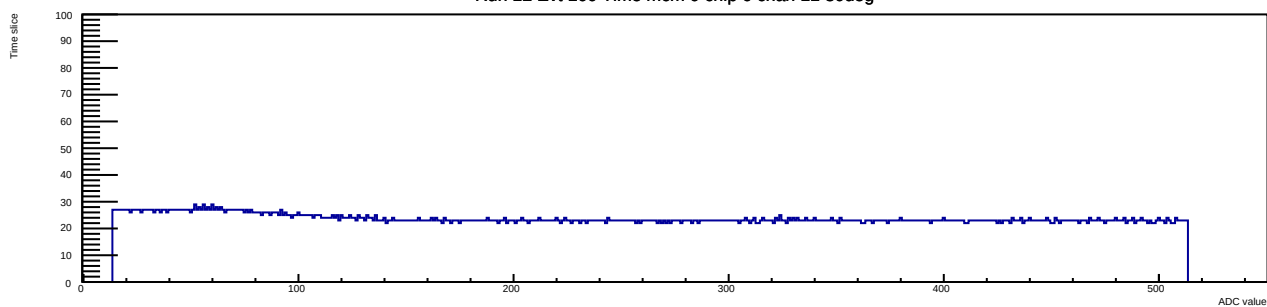
Run 12 Evt 100 Time mcm 0 chip 6 chan 9 30deg



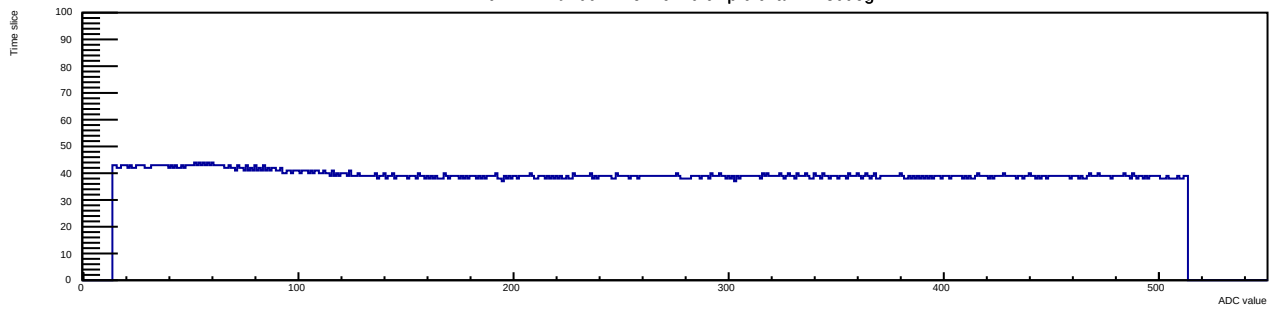
Run 12 Evt 100 Time mcm 0 chip 6 chan 10 30deg



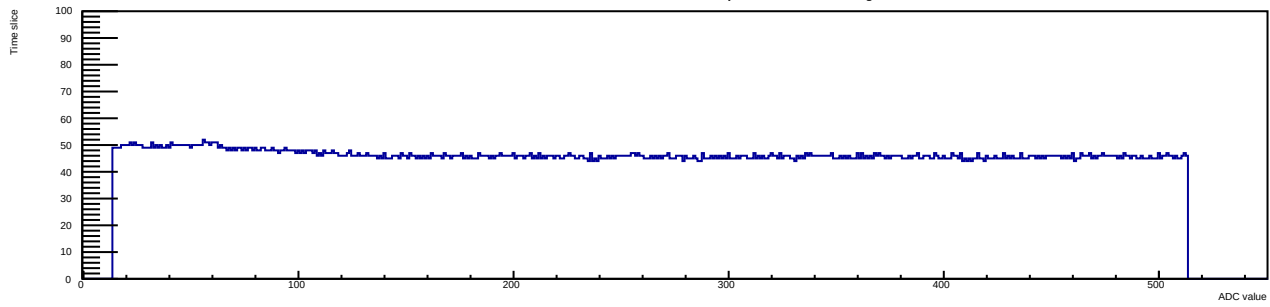
Run 12 Evt 100 Time mcm 0 chip 6 chan 11 30deg



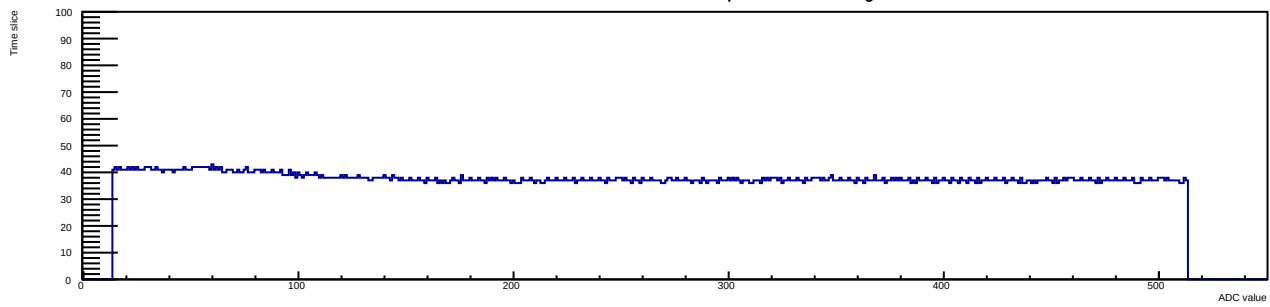
Run 12 Evt 100 Time mcm 0 chip 6 chan 12 30deg



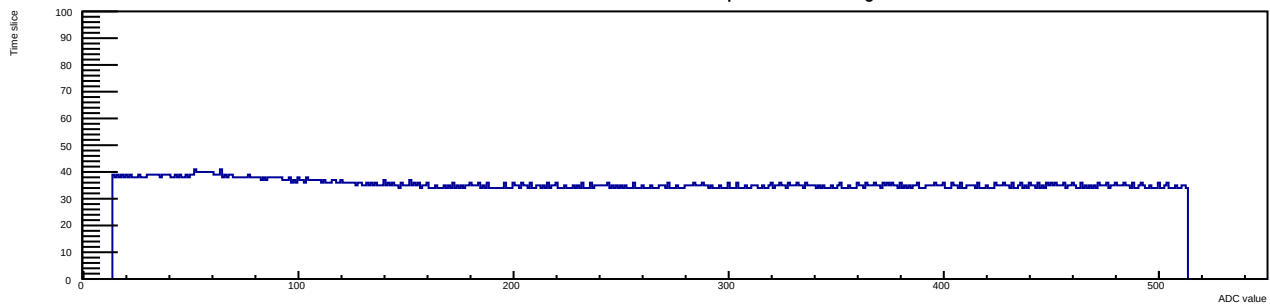
Run 12 Evt 100 Time mcm 0 chip 6 chan 13 30deg



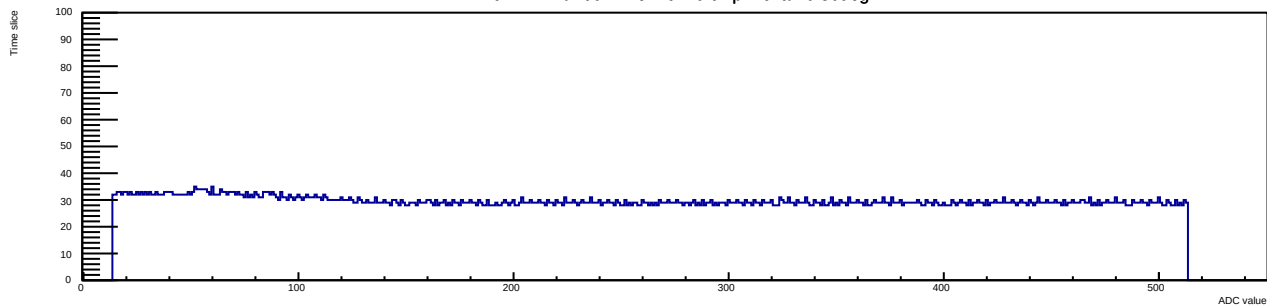
Run 12 Evt 100 Time mcm 0 chip 6 chan 14 30deg



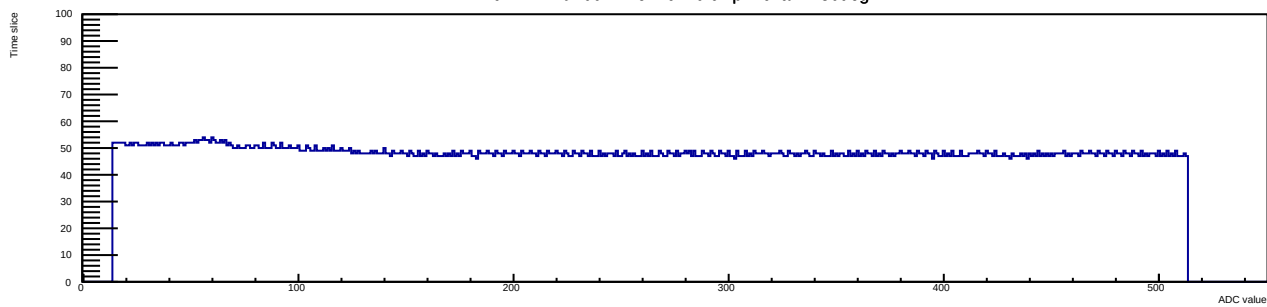
Run 12 Evt 100 Time mcm 0 chip 6 chan 15 30deg



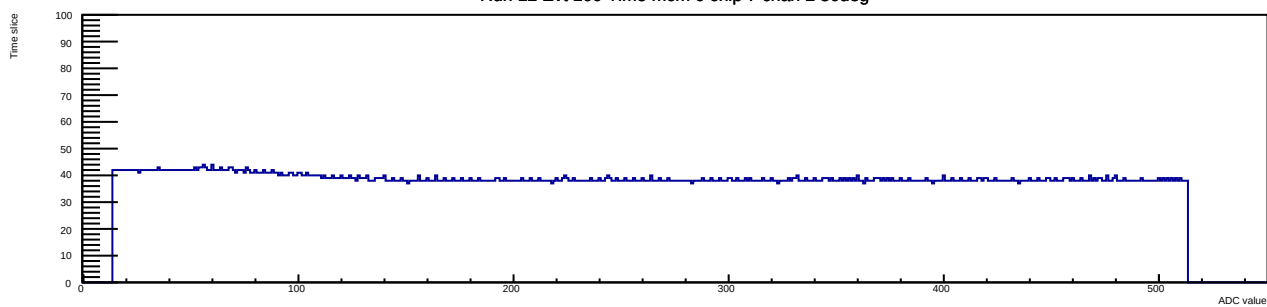
Run 12 Evt 100 Time mcm 0 chip 7 chan 0 30deg



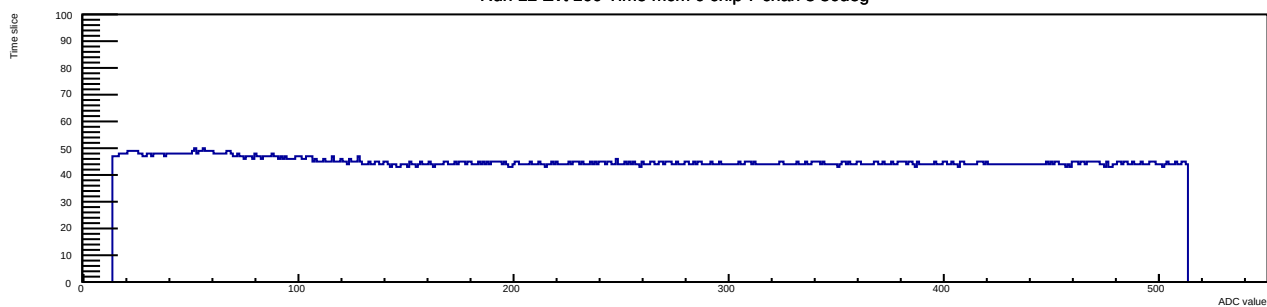
Run 12 Evt 100 Time mcm 0 chip 7 chan 1 30deg



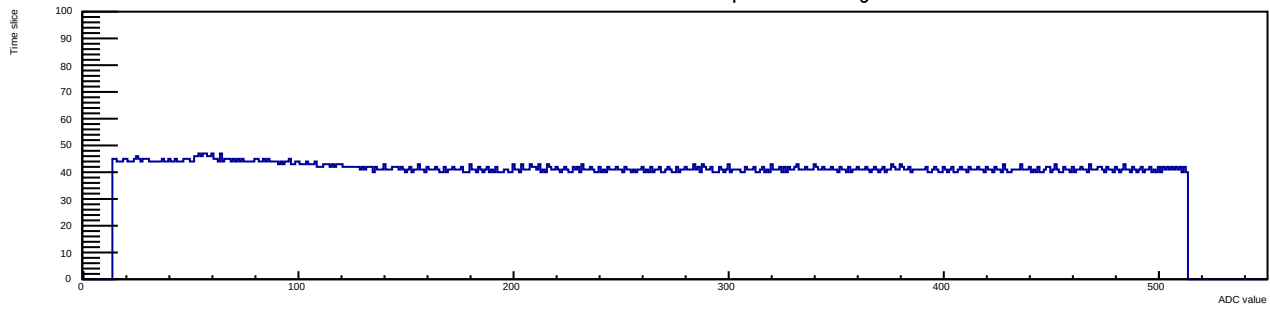
Run 12 Evt 100 Time mcm 0 chip 7 chan 2 30deg



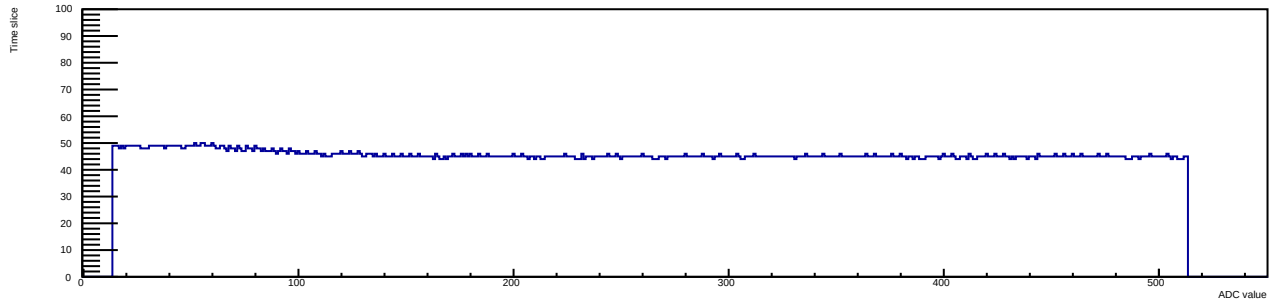
Run 12 Evt 100 Time mcm 0 chip 7 chan 3 30deg



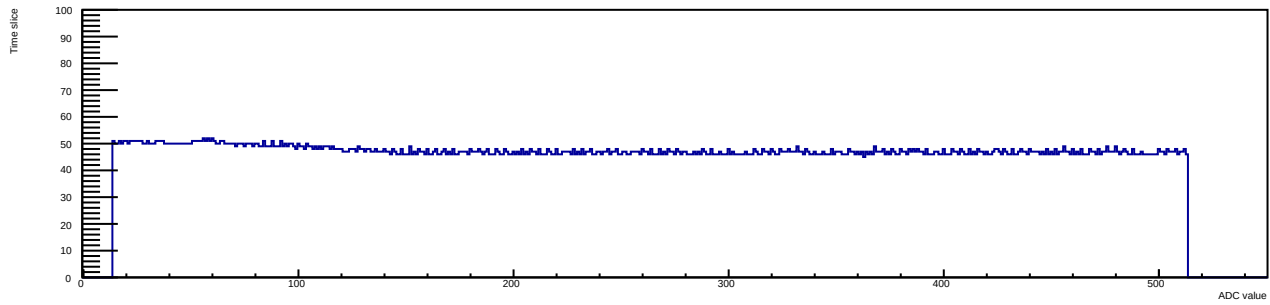
Run 12 Evt 100 Time mcm 0 chip 7 chan 4 30deg



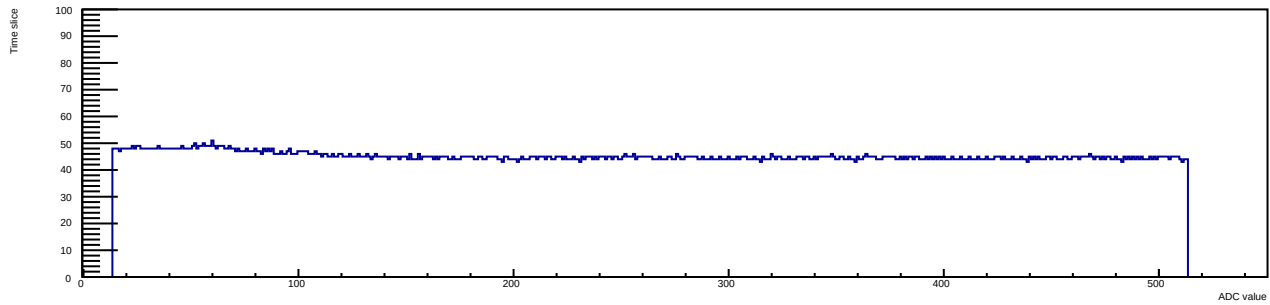
Run 12 Evt 100 Time mcm 0 chip 7 chan 5 30deg



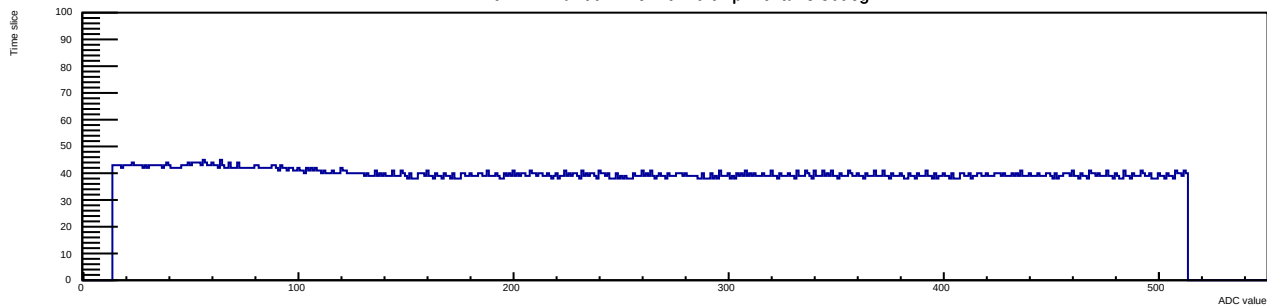
Run 12 Evt 100 Time mcm 0 chip 7 chan 6 30deg



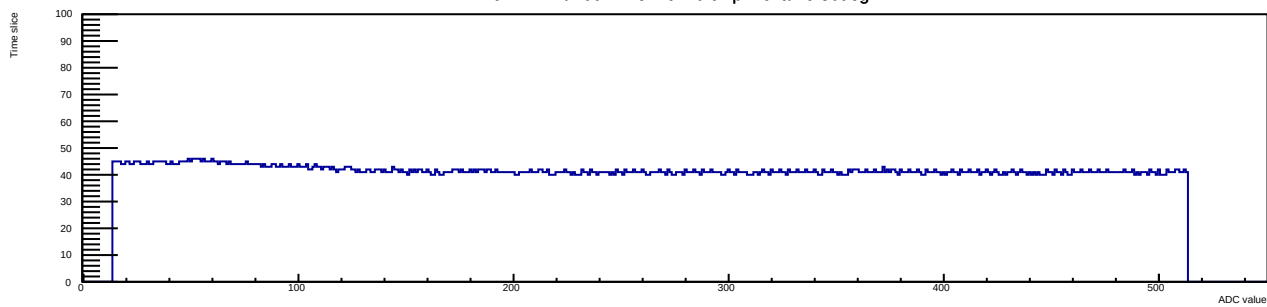
Run 12 Evt 100 Time mcm 0 chip 7 chan 7 30deg



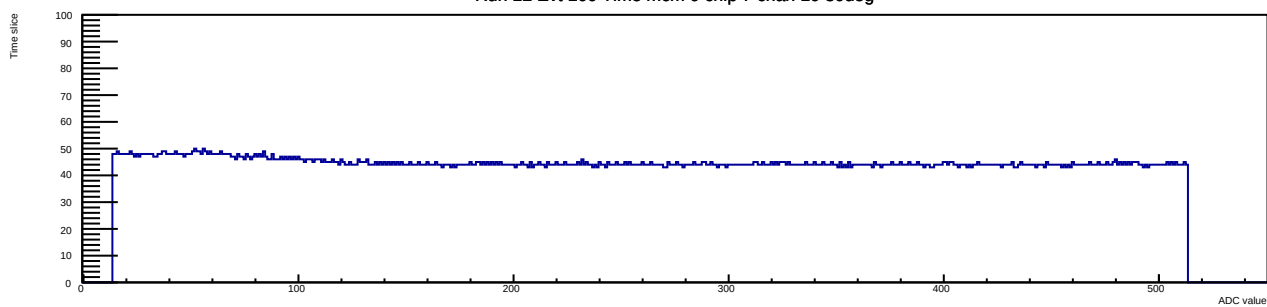
Run 12 Evt 100 Time mcm 0 chip 7 chan 8 30deg



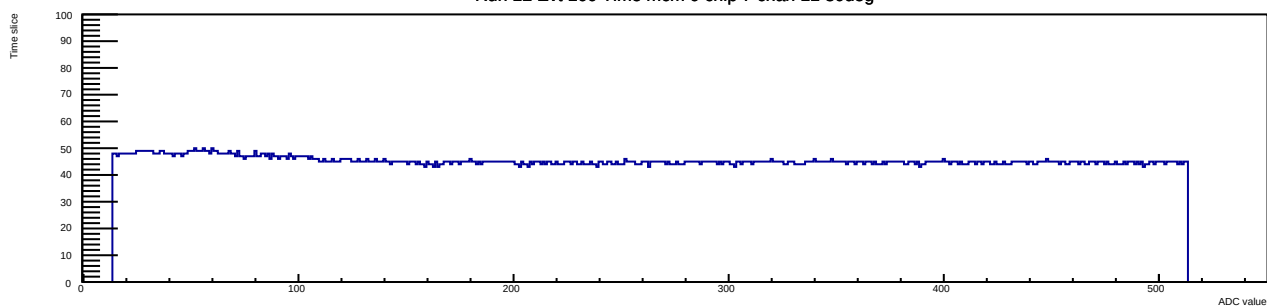
Run 12 Evt 100 Time mcm 0 chip 7 chan 9 30deg



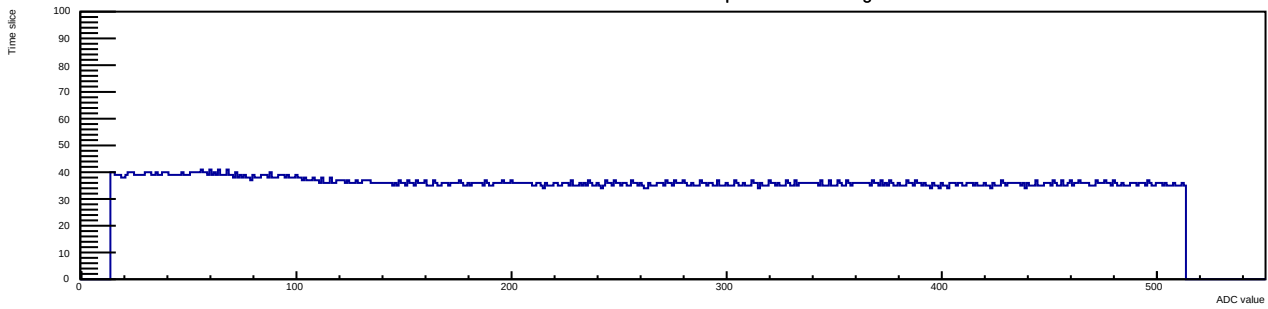
Run 12 Evt 100 Time mcm 0 chip 7 chan 10 30deg



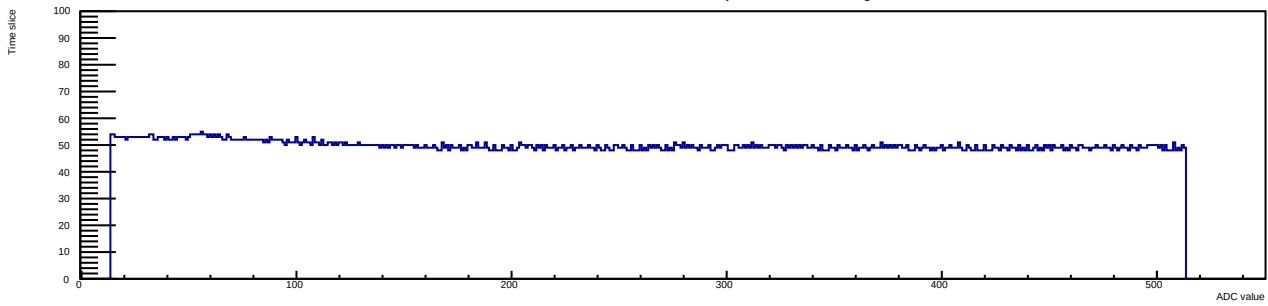
Run 12 Evt 100 Time mcm 0 chip 7 chan 11 30deg



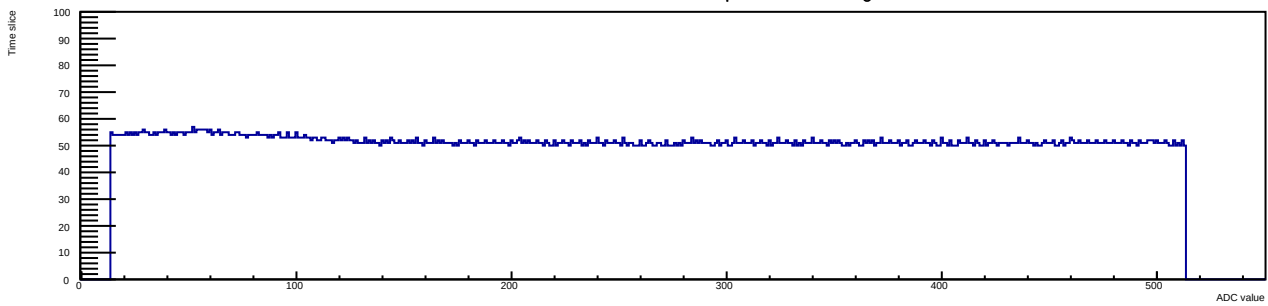
Run 12 Evt 100 Time mcm 0 chip 7 chan 12 30deg



Run 12 Evt 100 Time mcm 0 chip 7 chan 13 30deg



Run 12 Evt 100 Time mcm 0 chip 7 chan 14 30deg



Run 12 Evt 100 Time mcm 0 chip 7 chan 15 30deg

