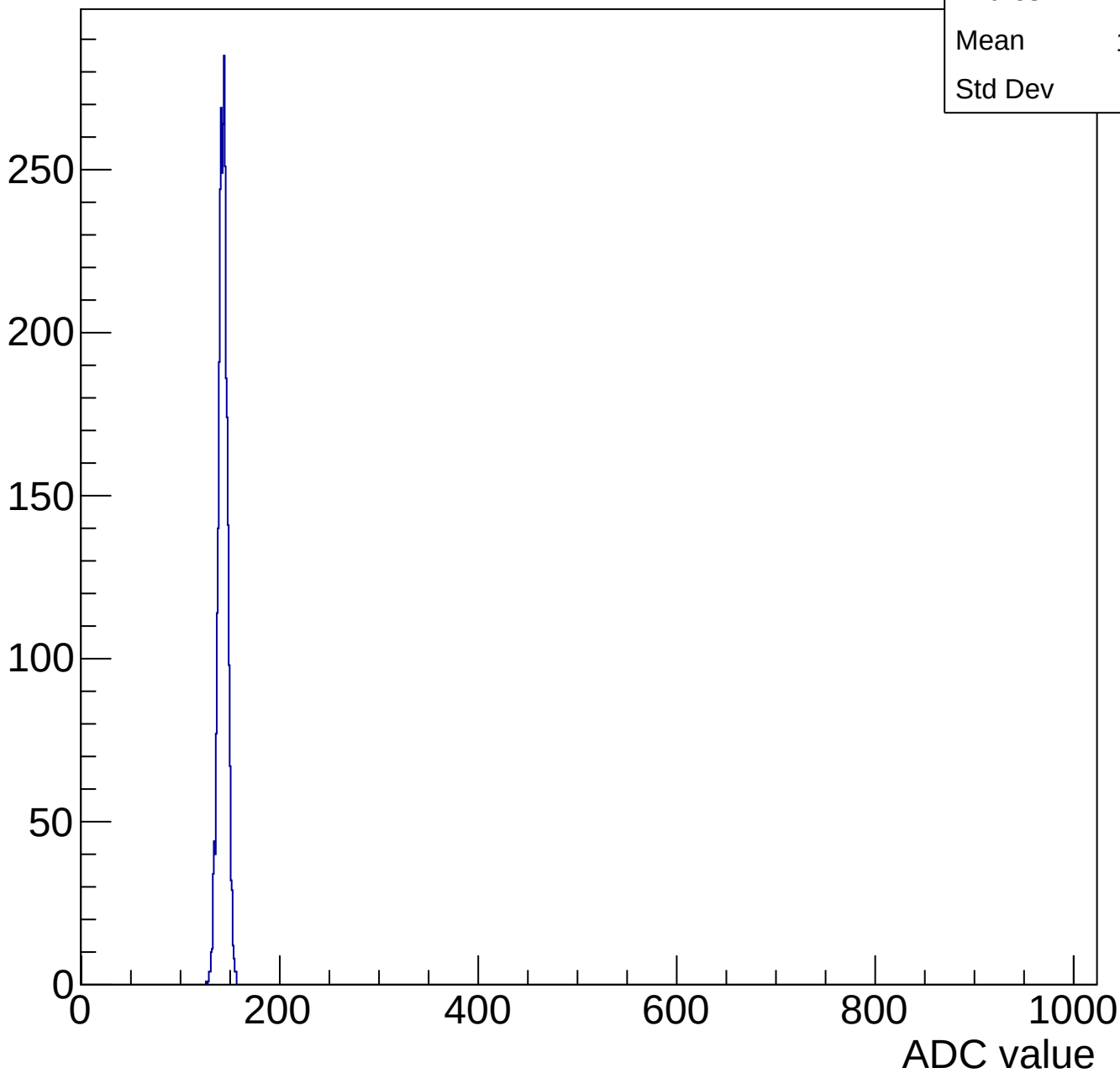


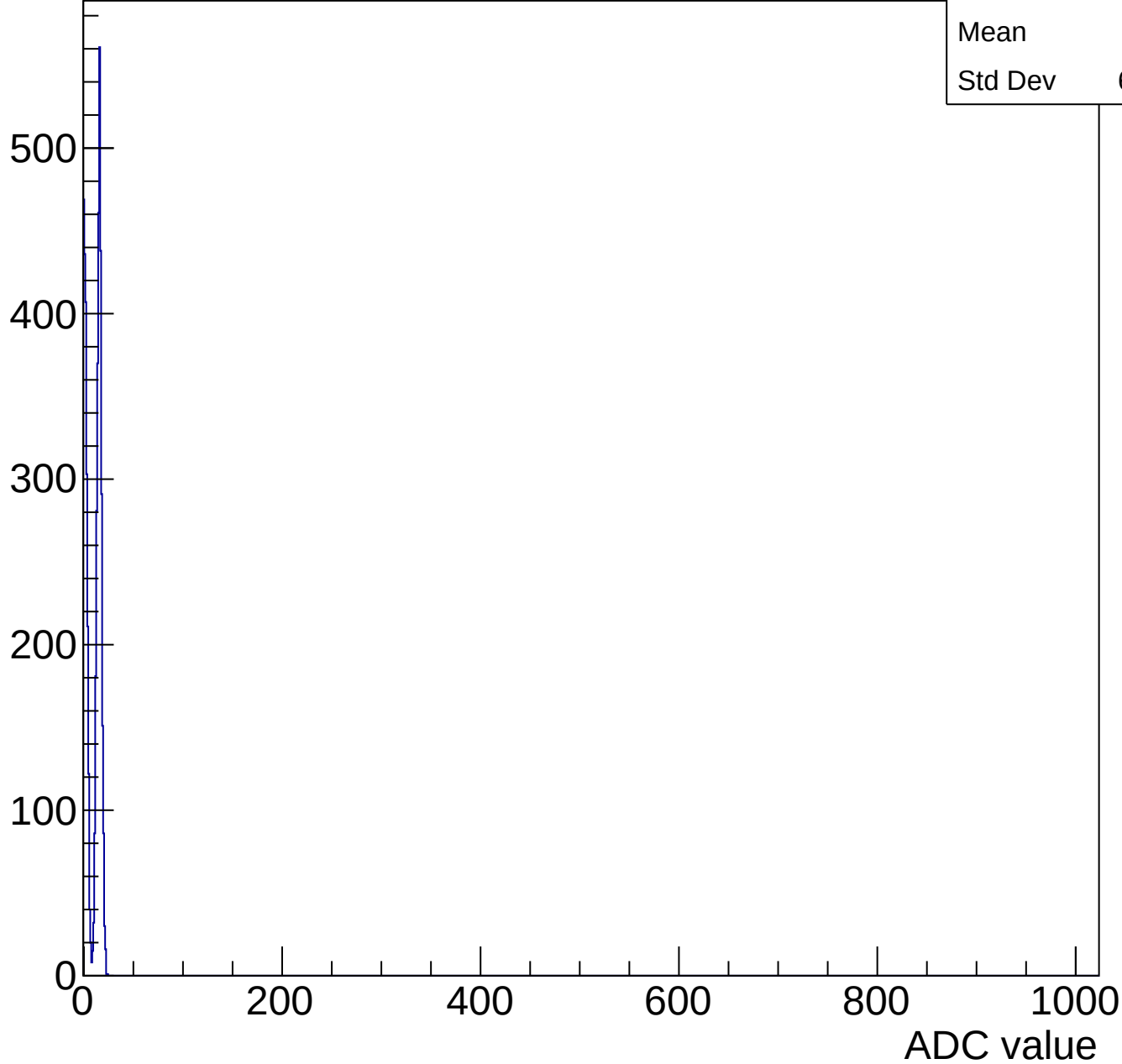
Pulstrain peak0 ch0 g3-s7-p1-s1-sh

Entries



Pulstrain peak0 ch1 g3-s7-p1-s1-sh

Entries



Pulstrain peak0 ch2 g3-s7-p1-s1-sh

Entries

220
200
180
160
140
120
100
80
60
40
20
0

Entries	2988
Mean	124
Std Dev	5.567

0 200

400

600

800

1000

ADC value

Pulstrain peak0 ch3 g3-s7-p1-s1-sh

Entries

500

400

300

200

100

0

0 200 400 600 800 1000

ADC value

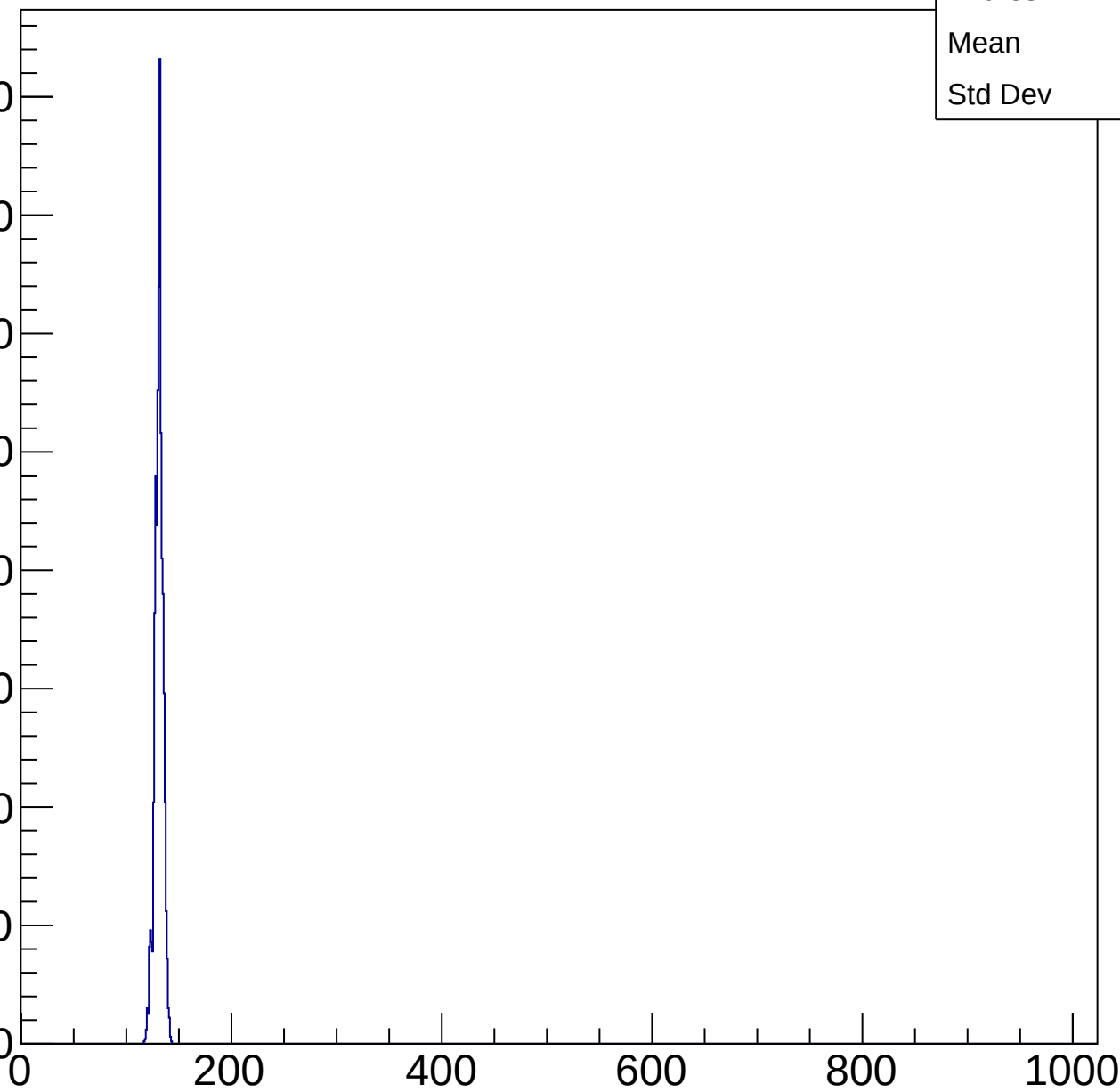
Entries	6000
Mean	8.476
Std Dev	5.413

Pulstrain peak0 ch4 g3-s7-p1-s1-sh

Entries

400
350
300
250
200
150
100
50
0

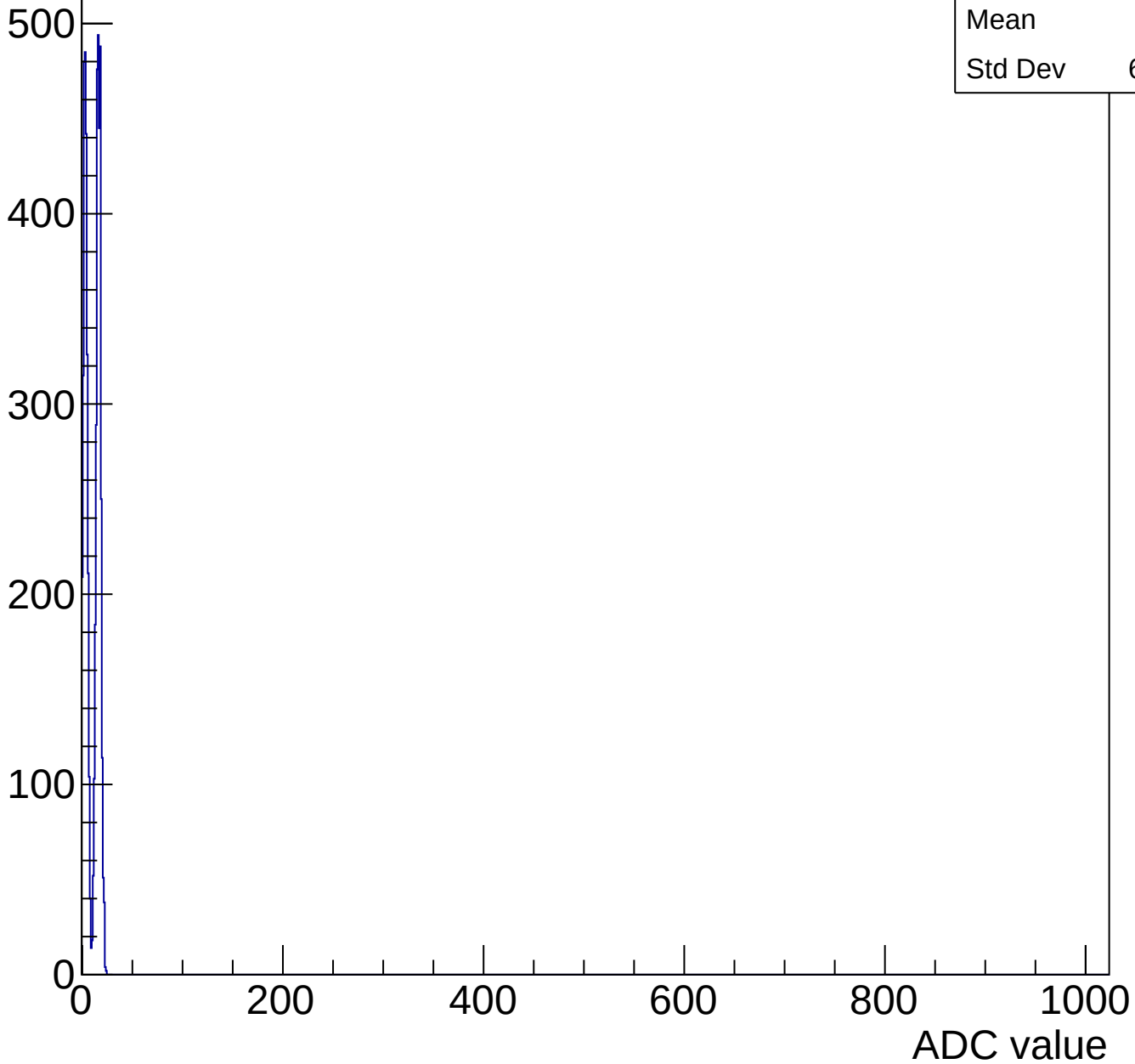
Entries	2988
Mean	131.1
Std Dev	3.87



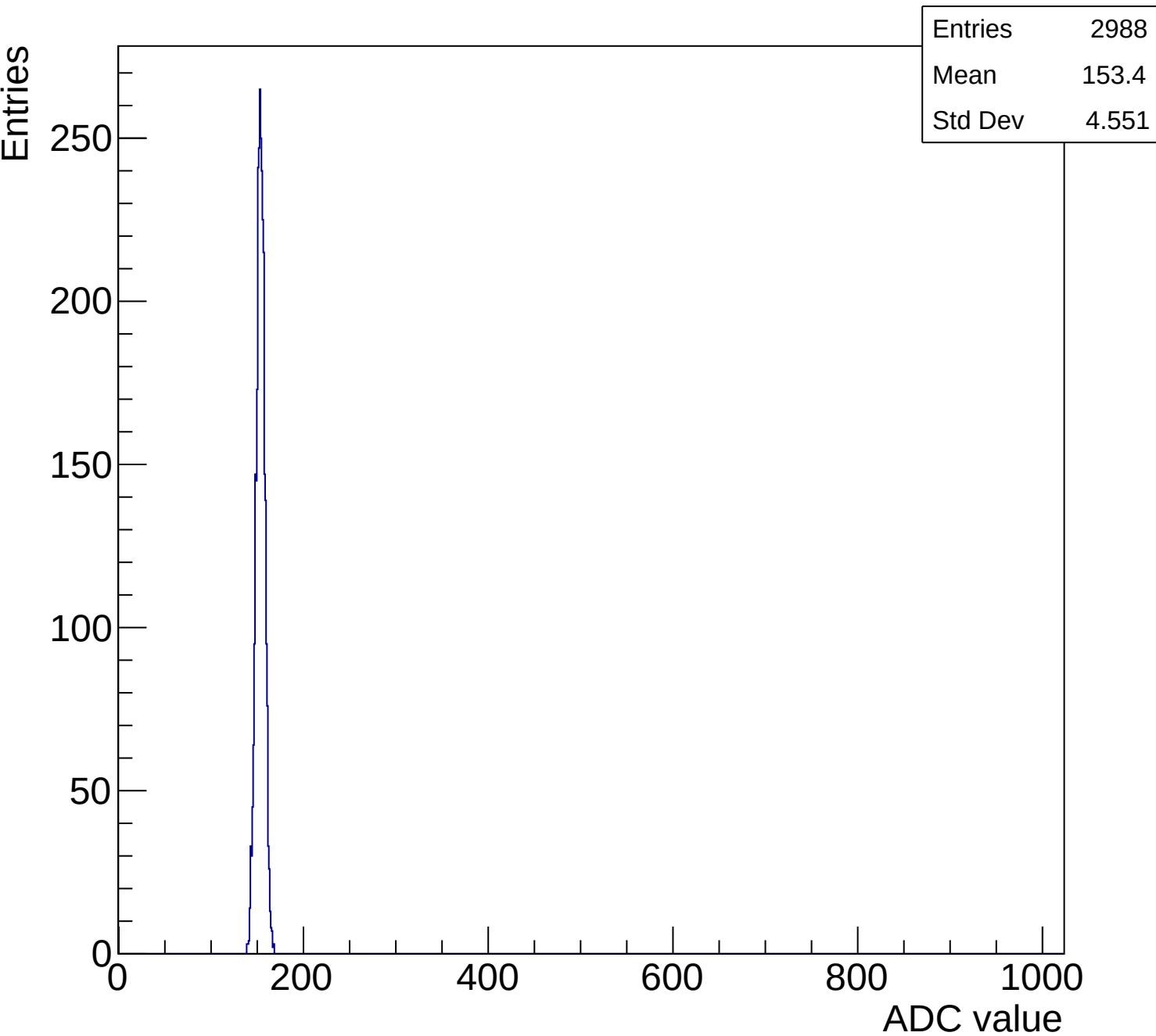
ADC value

Pulstrain peak0 ch5 g3-s7-p1-s1-sh

Entries



Pulstrain peak0 ch6 g3-s7-p1-s1-sh



Pulstrain peak0 ch7 g3-s7-p1-s1-sh

Entries

500

400

300

200

100

0

0 200 400 600 800 1000

ADC value

Entries	6000
Mean	12.17
Std Dev	7.555

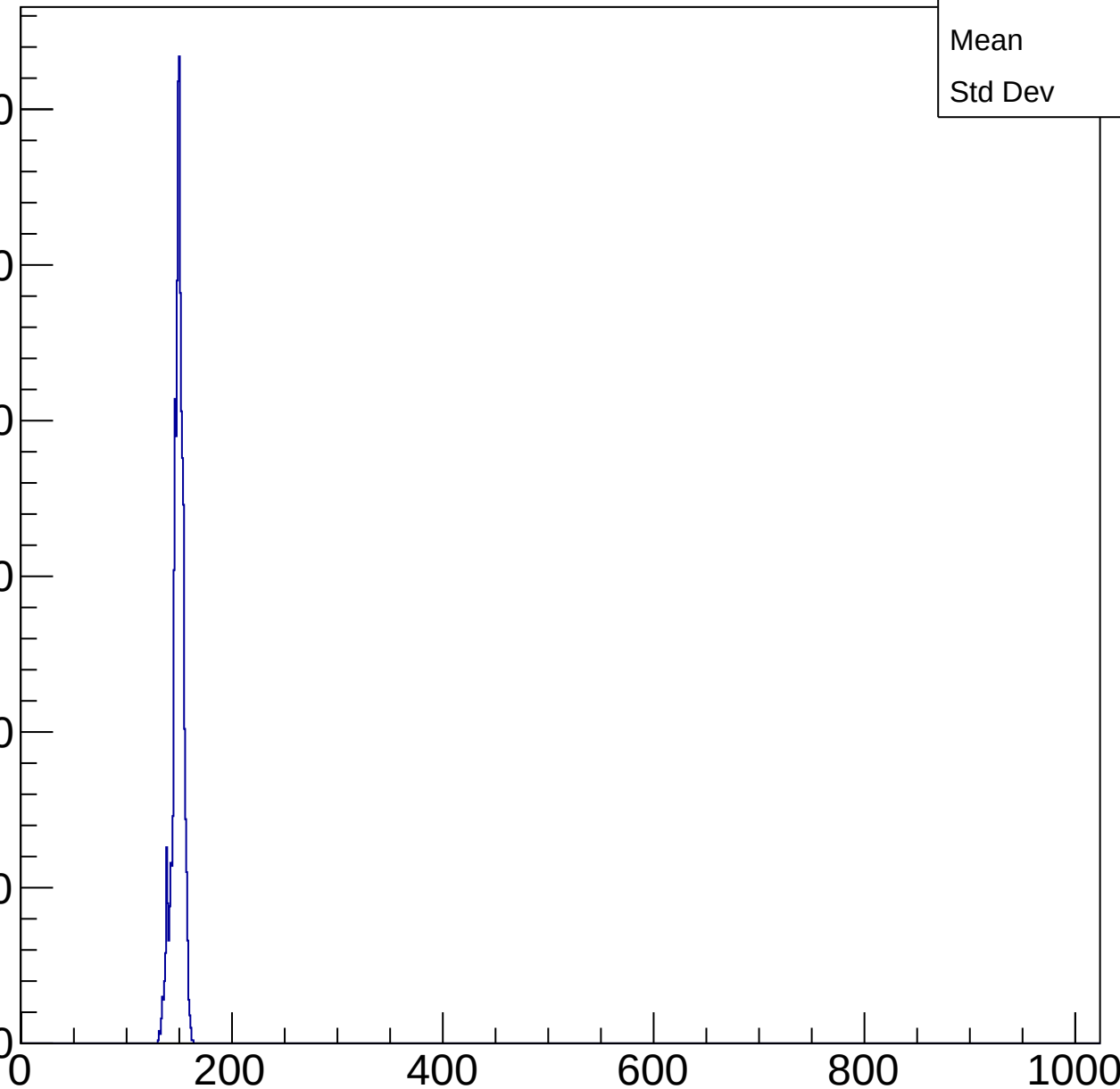
Pulstrain peak0 ch8 g3-s7-p1-s1-sh

Entries

300
250
200
150
100
50
0

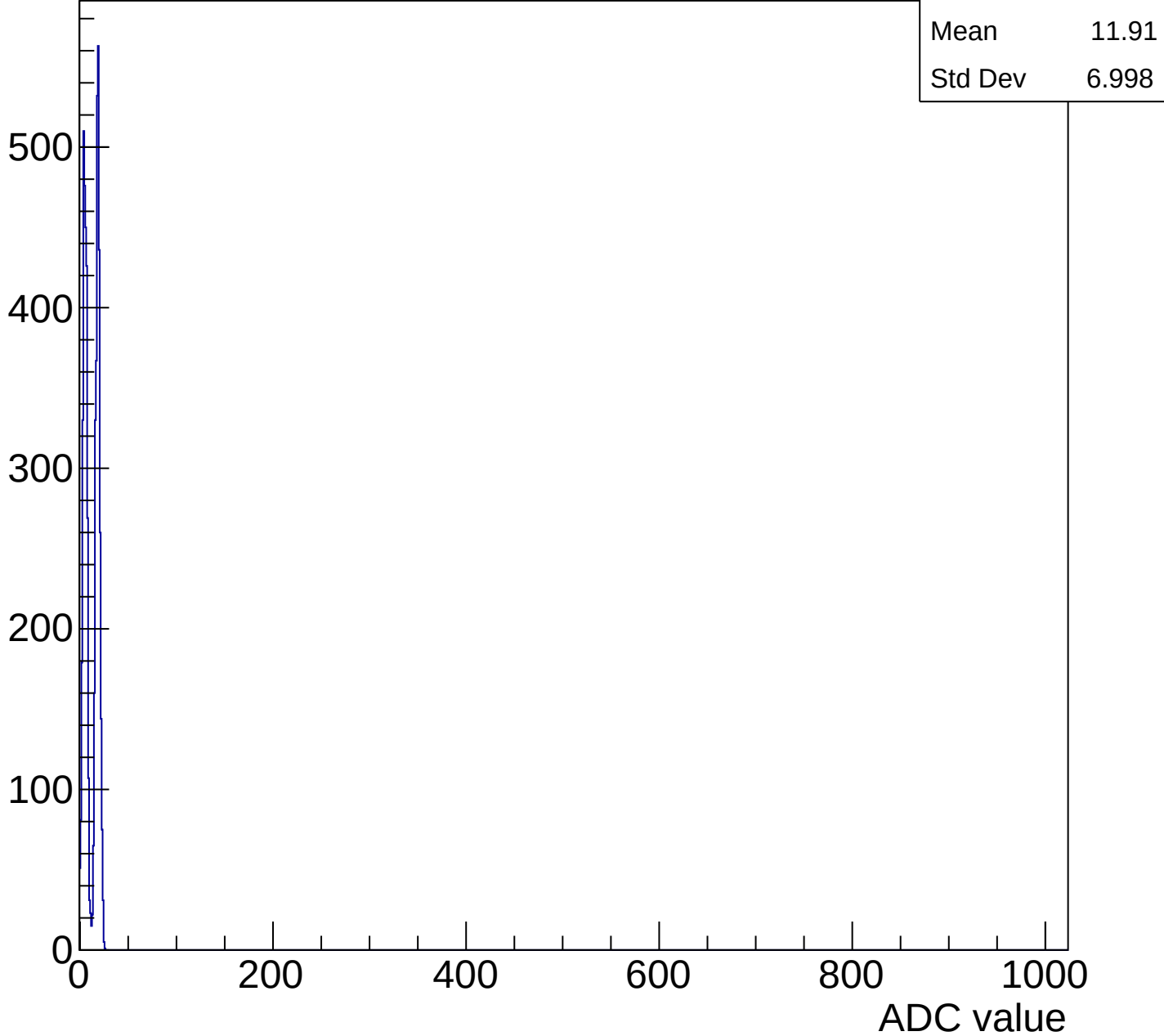
Entries	2988
Mean	148.7
Std Dev	5.011

ADC value



Pulstrain peak0 ch9 g3-s7-p1-s1-sh

Entries



Pulstrain peak0 ch10 g3-s7-p1-s1-sh

Entries

300

250

200

150

100

50

0

0

200

400

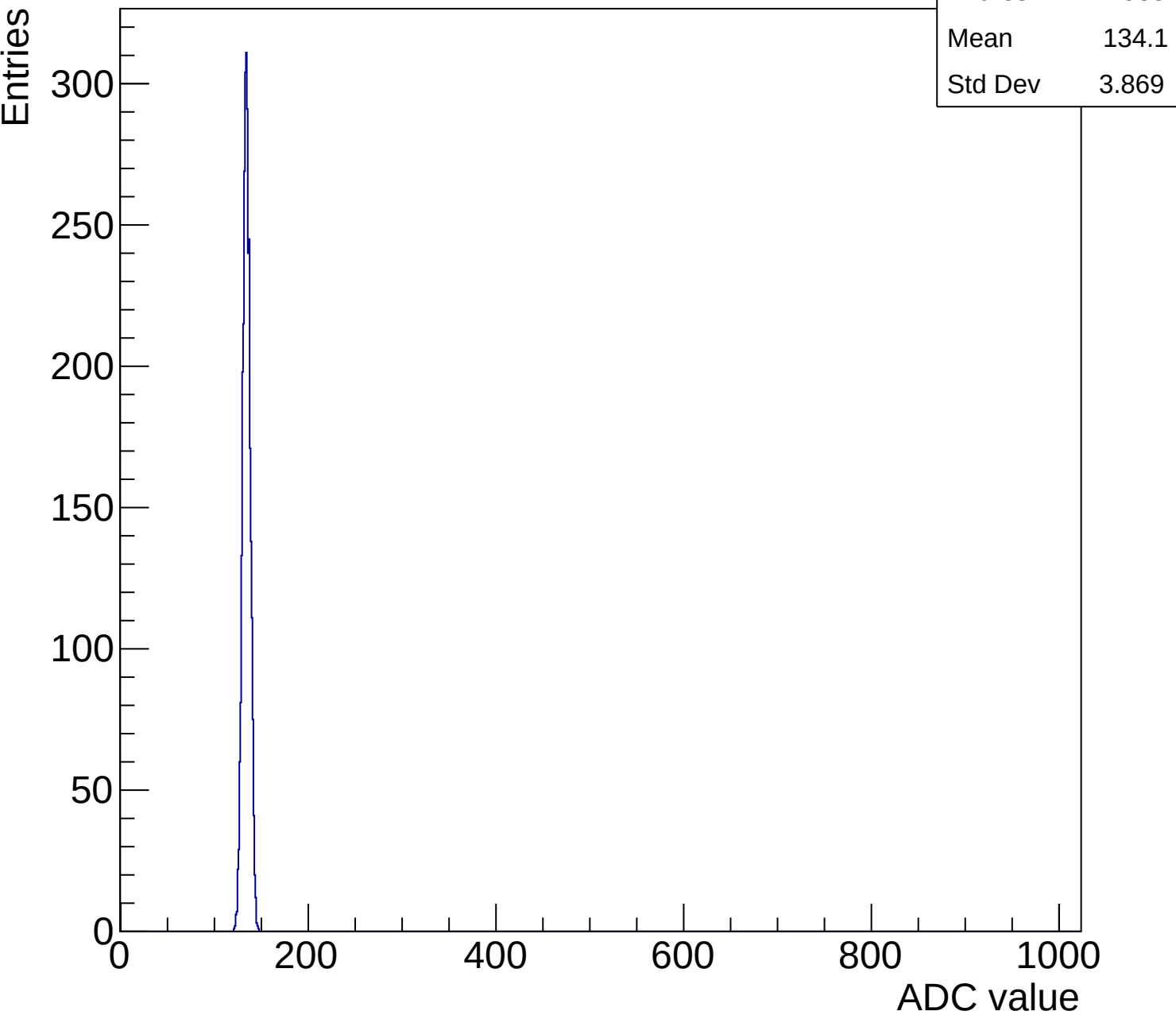
600

800

1000

ADC value

Entries	2988
Mean	134.1
Std Dev	3.869



Pulstrain peak0 ch11 g3-s7-p1-s1-sh

Entries

500

400

300

200

100

0

0 200 400 600 800 1000

ADC value

Entries	6000
Mean	14.22
Std Dev	9.568

Pulstrain peak0 ch12 g3-s7-p1-s1-sh

Entries

300

250

200

150

100

50

0

0

200

400

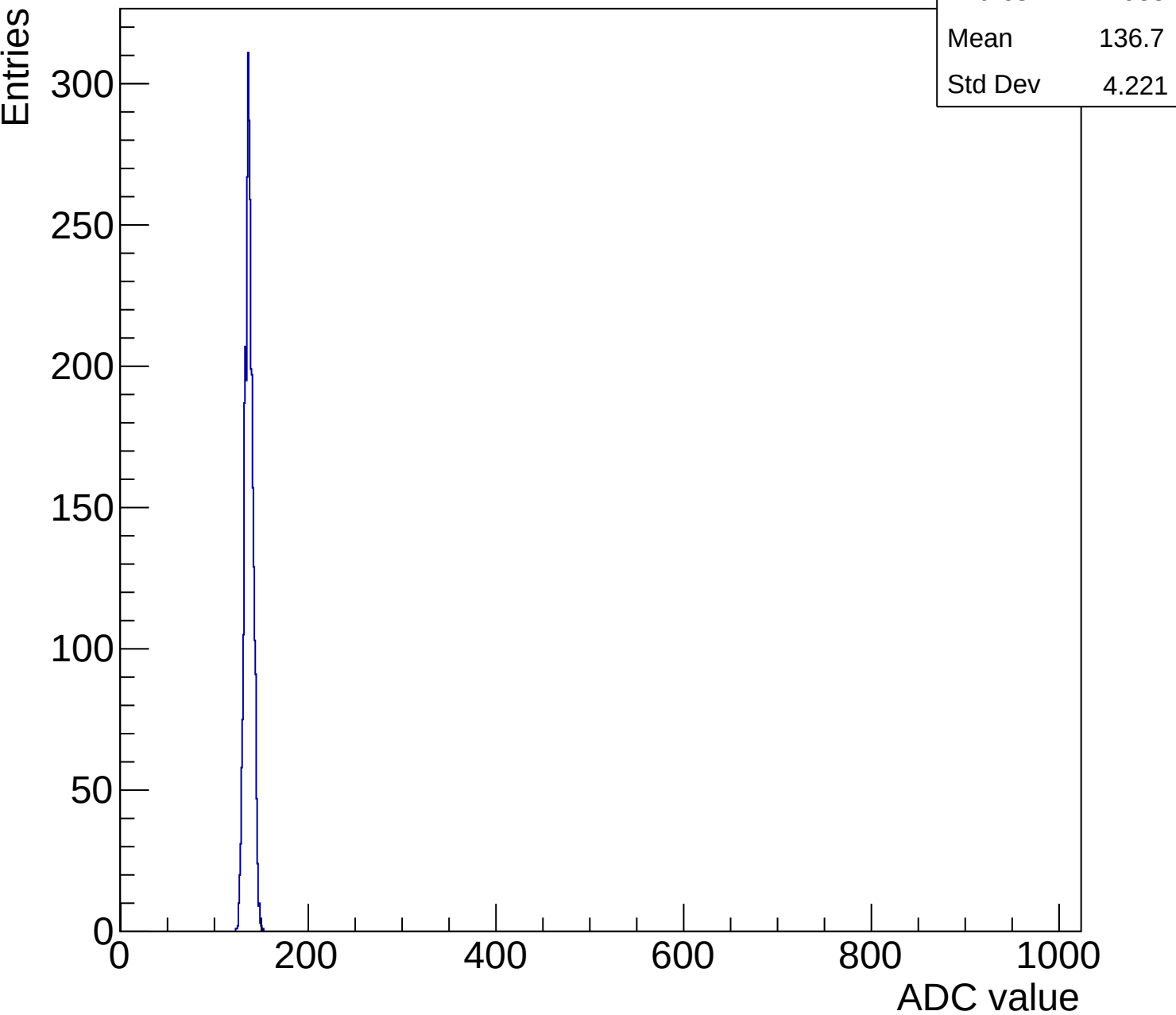
600

800

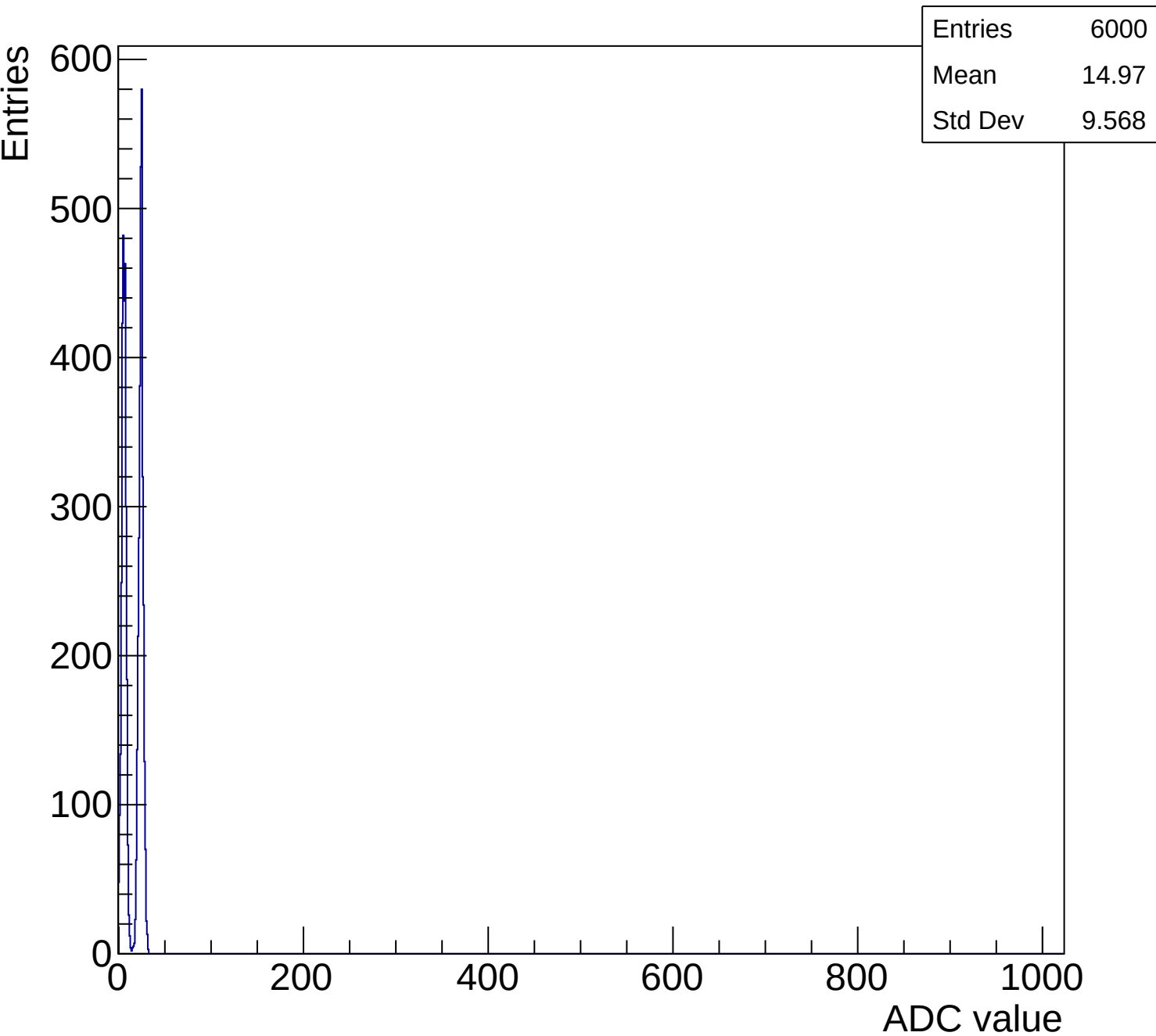
1000

ADC value

Entries	2988
Mean	136.7
Std Dev	4.221



Pulstrain peak0 ch13 g3-s7-p1-s1-sh



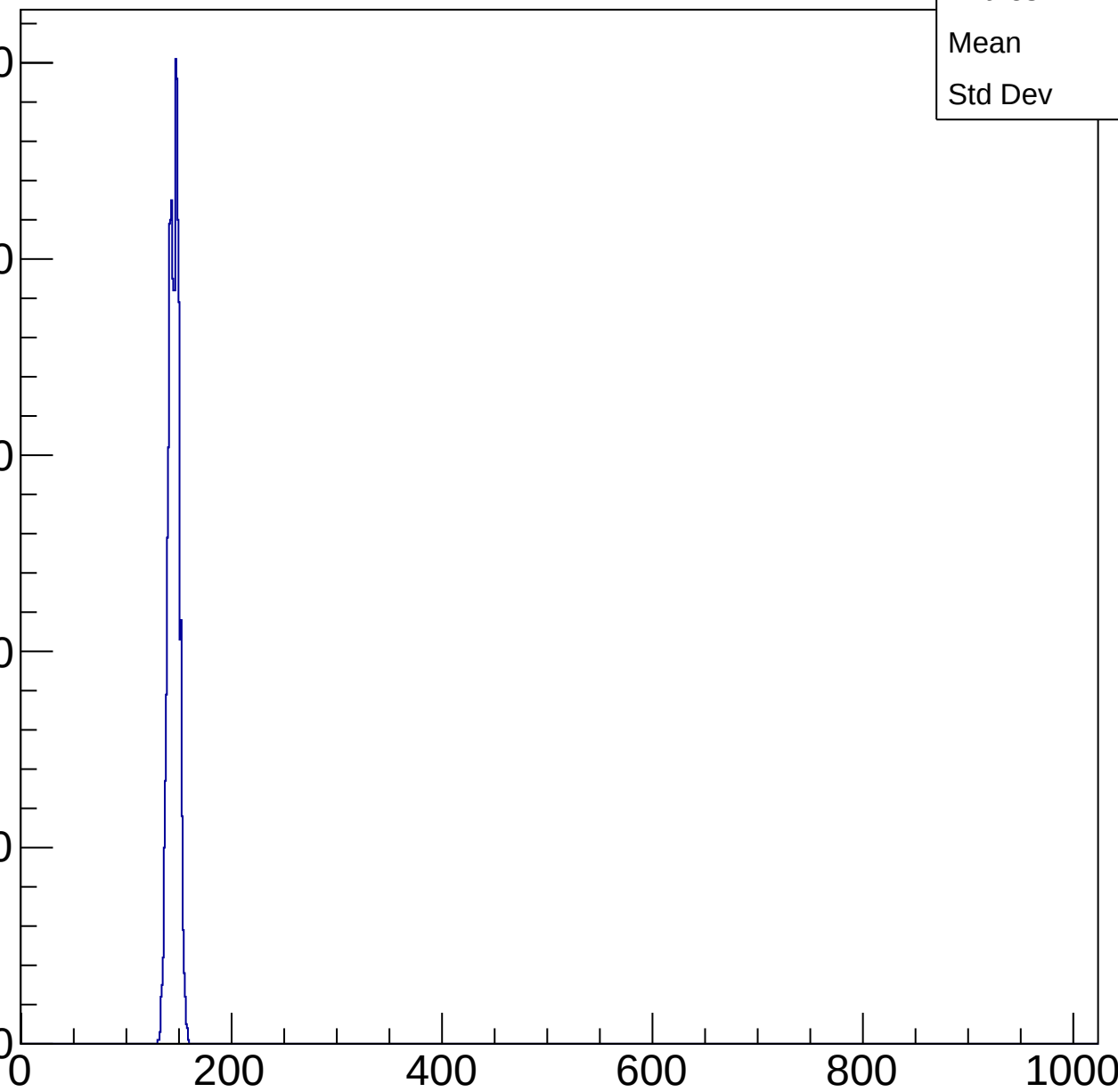
Pulstrain peak0 ch14 g3-s7-p1-s1-sh

Entries

250
200
150
100
50
0

Entries	2988
Mean	145
Std Dev	4.682

ADC value



Pulstrain peak0 ch15 g3-s7-p1-s1-sh

Entries

