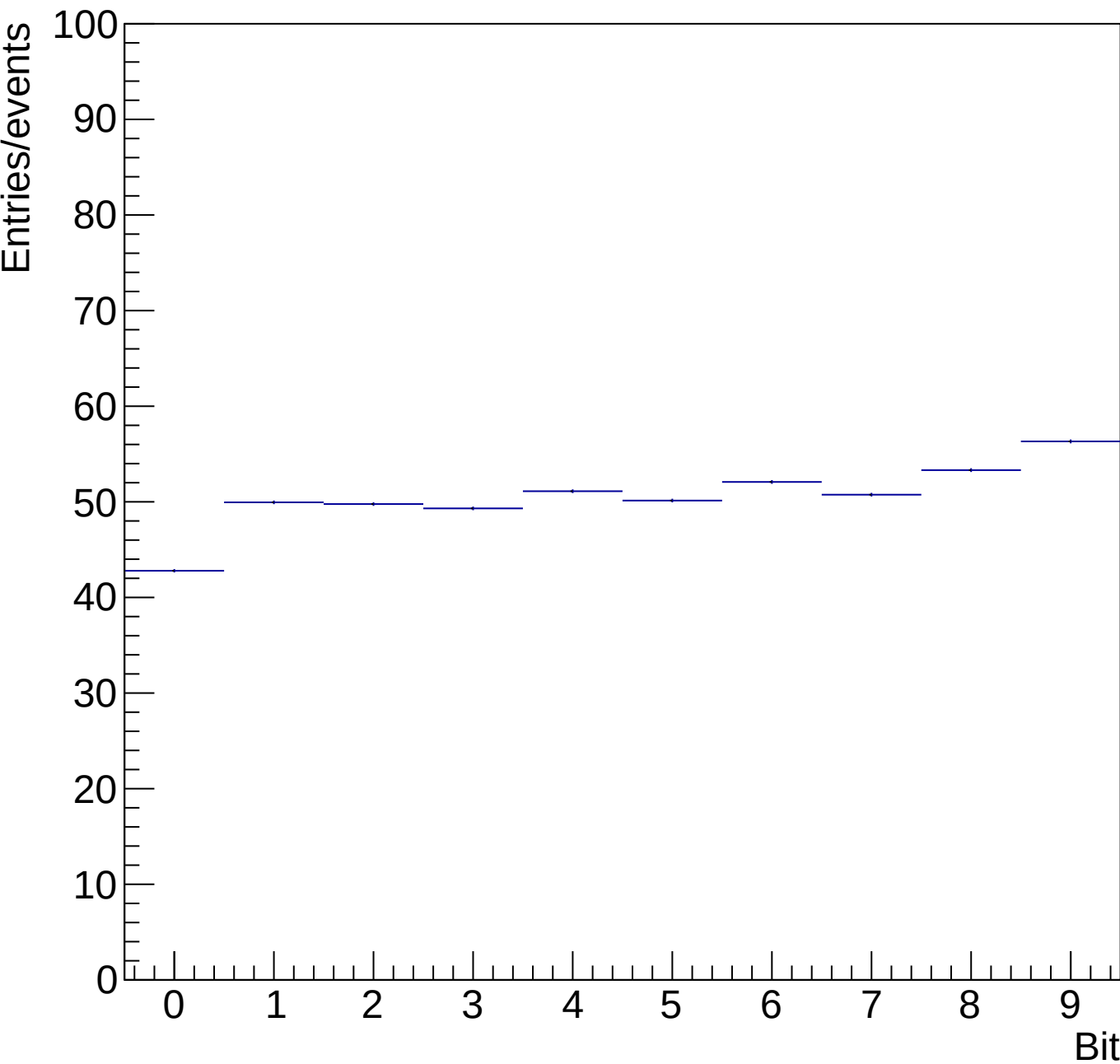
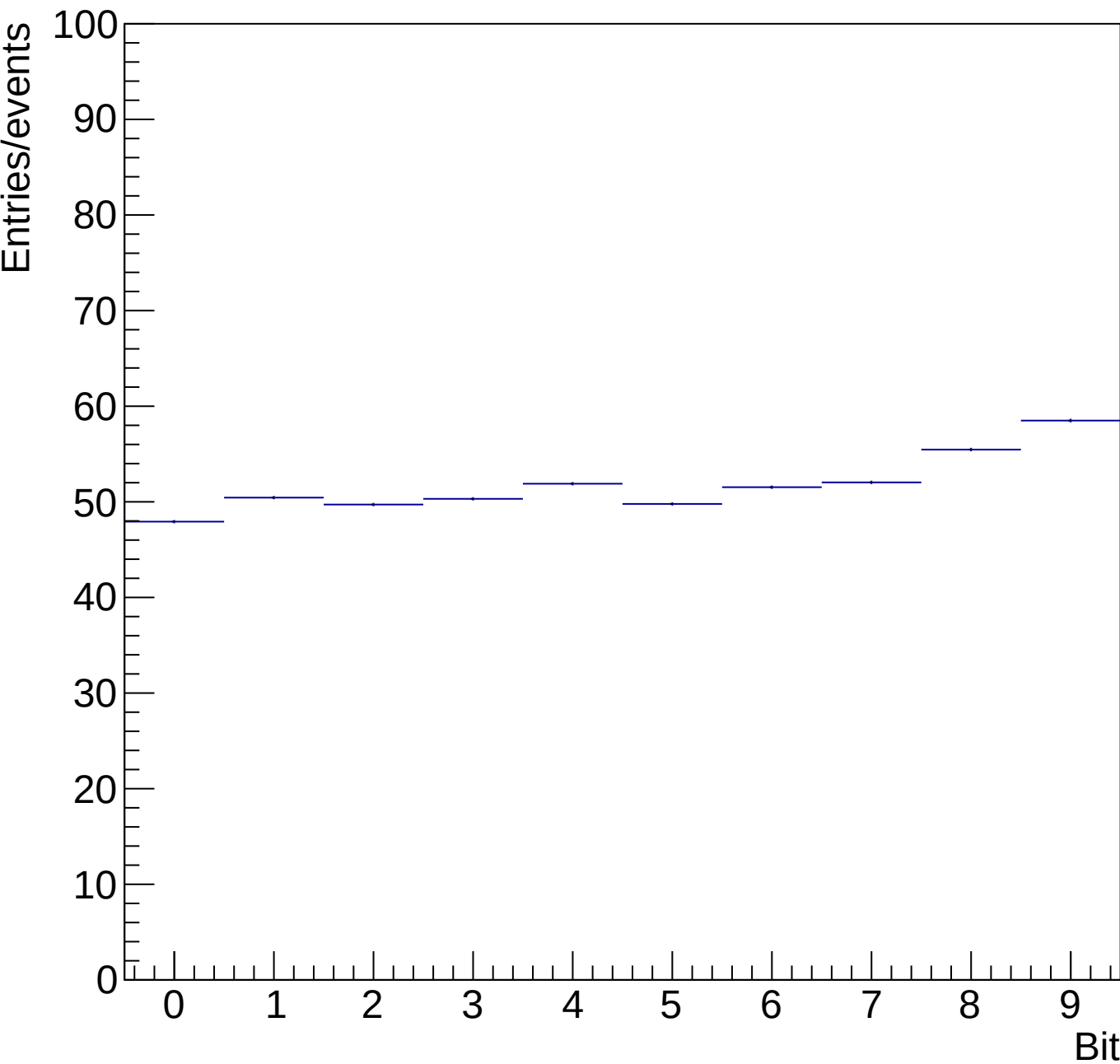


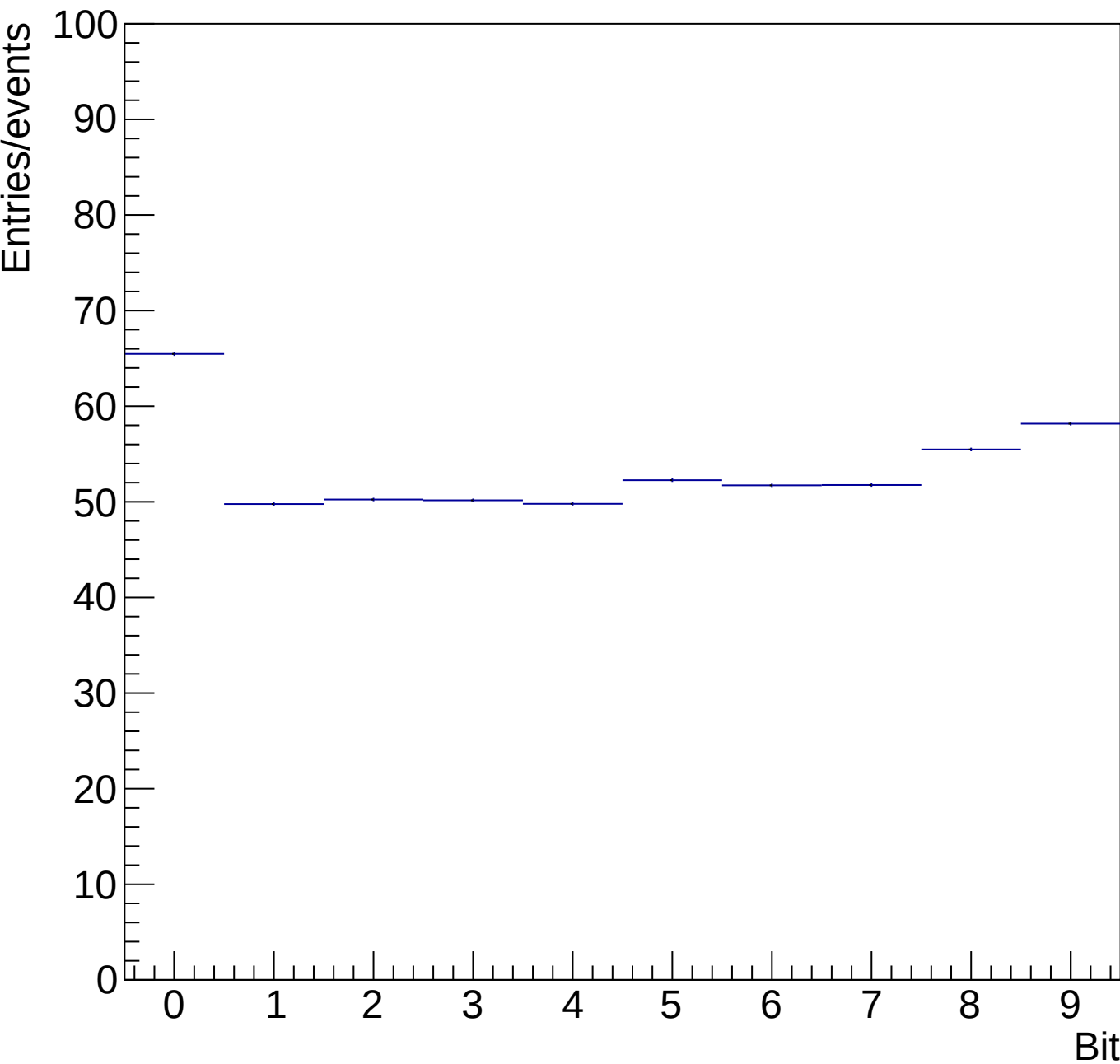
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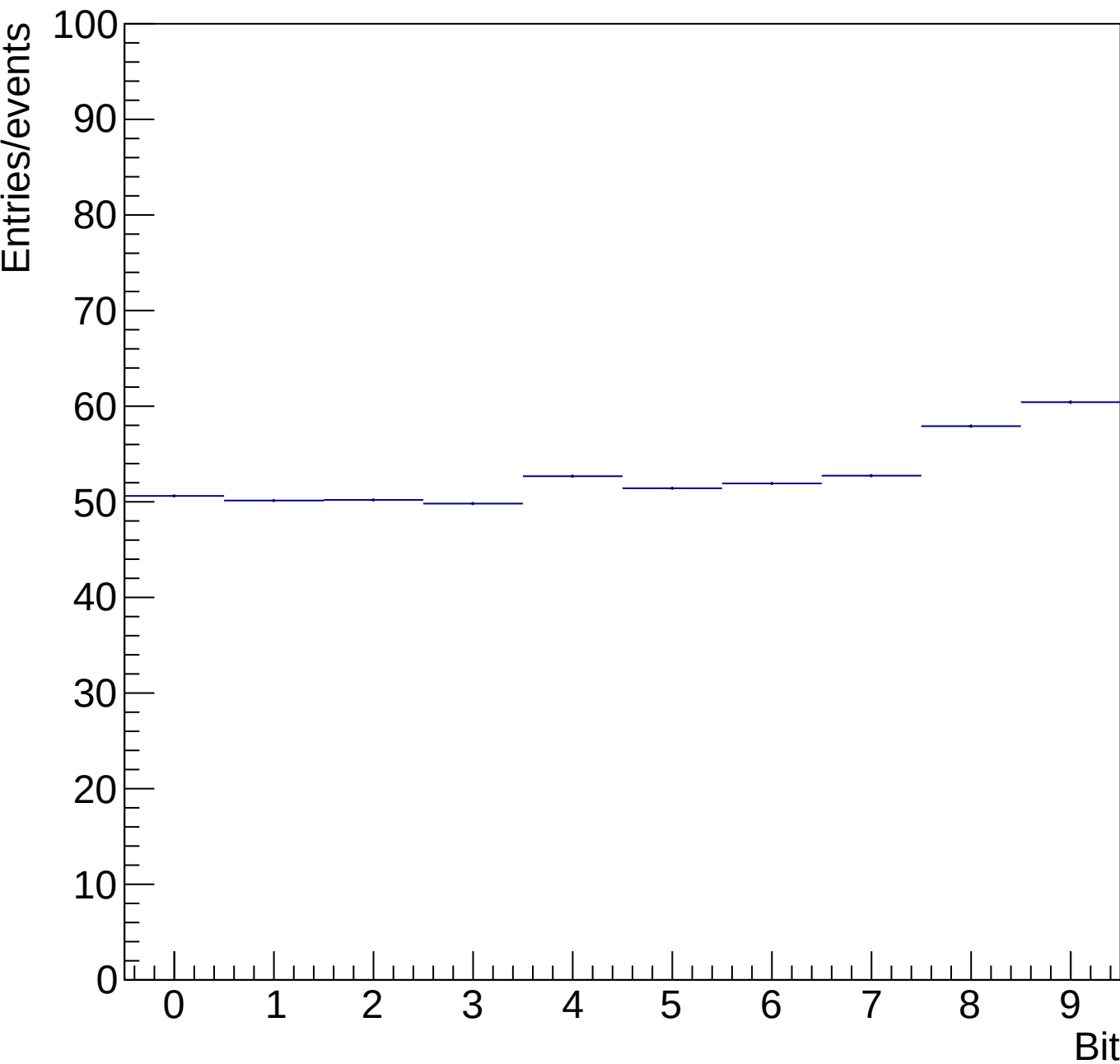
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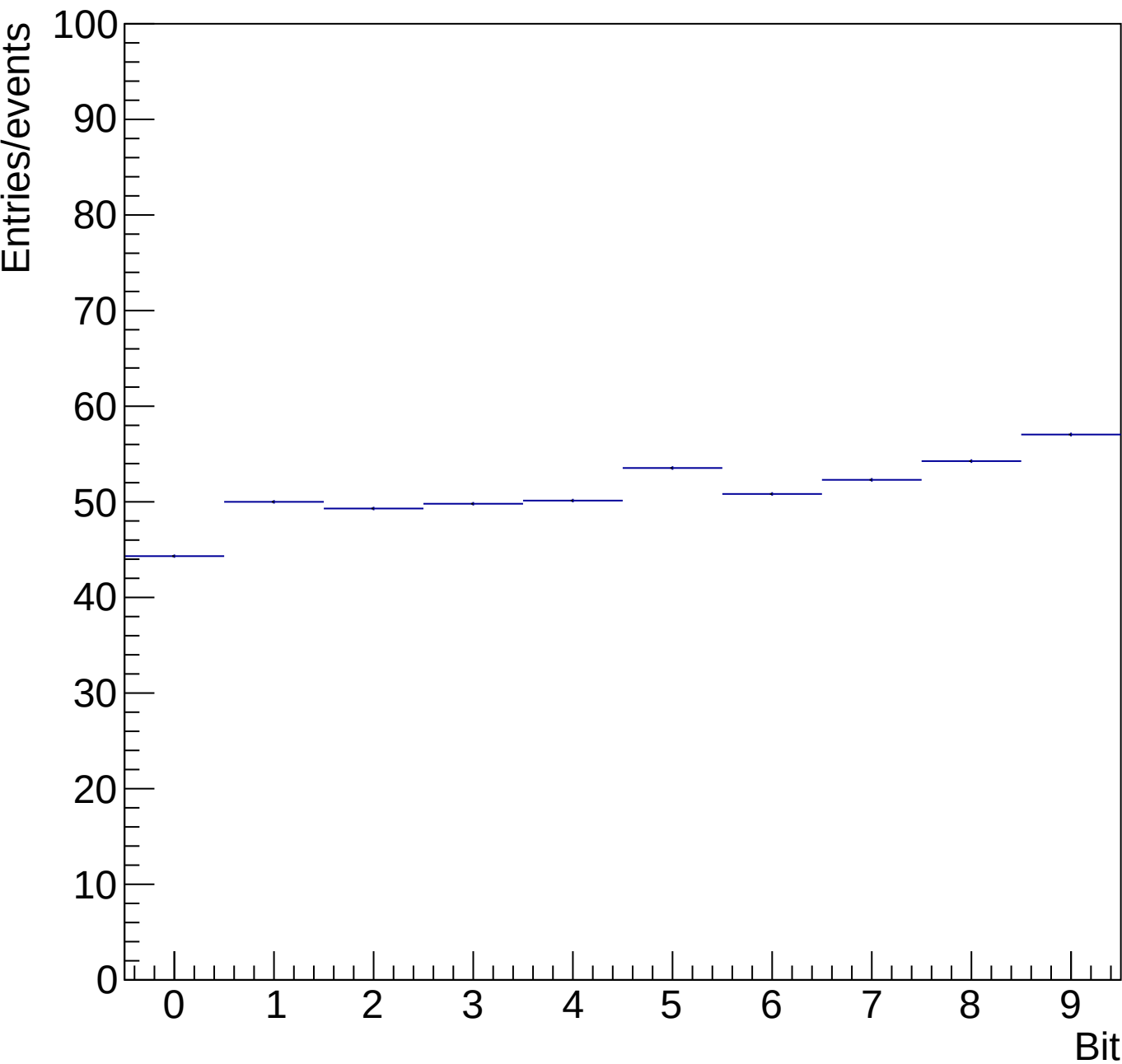
Bits ch 2 chip 7 g3-s0-p1-s3



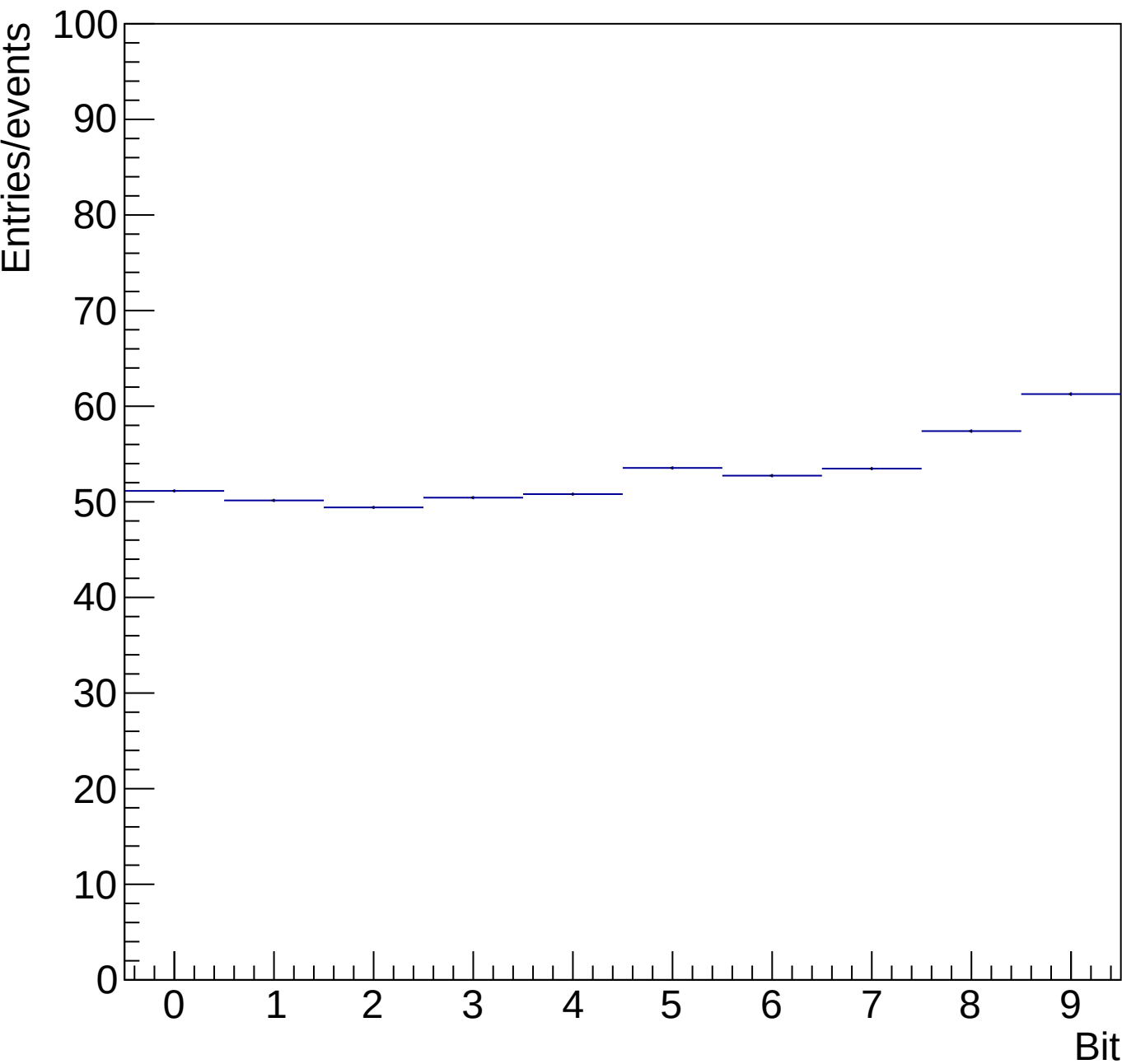
Bits ch 3 chip 7 g3-s0-p1-s3



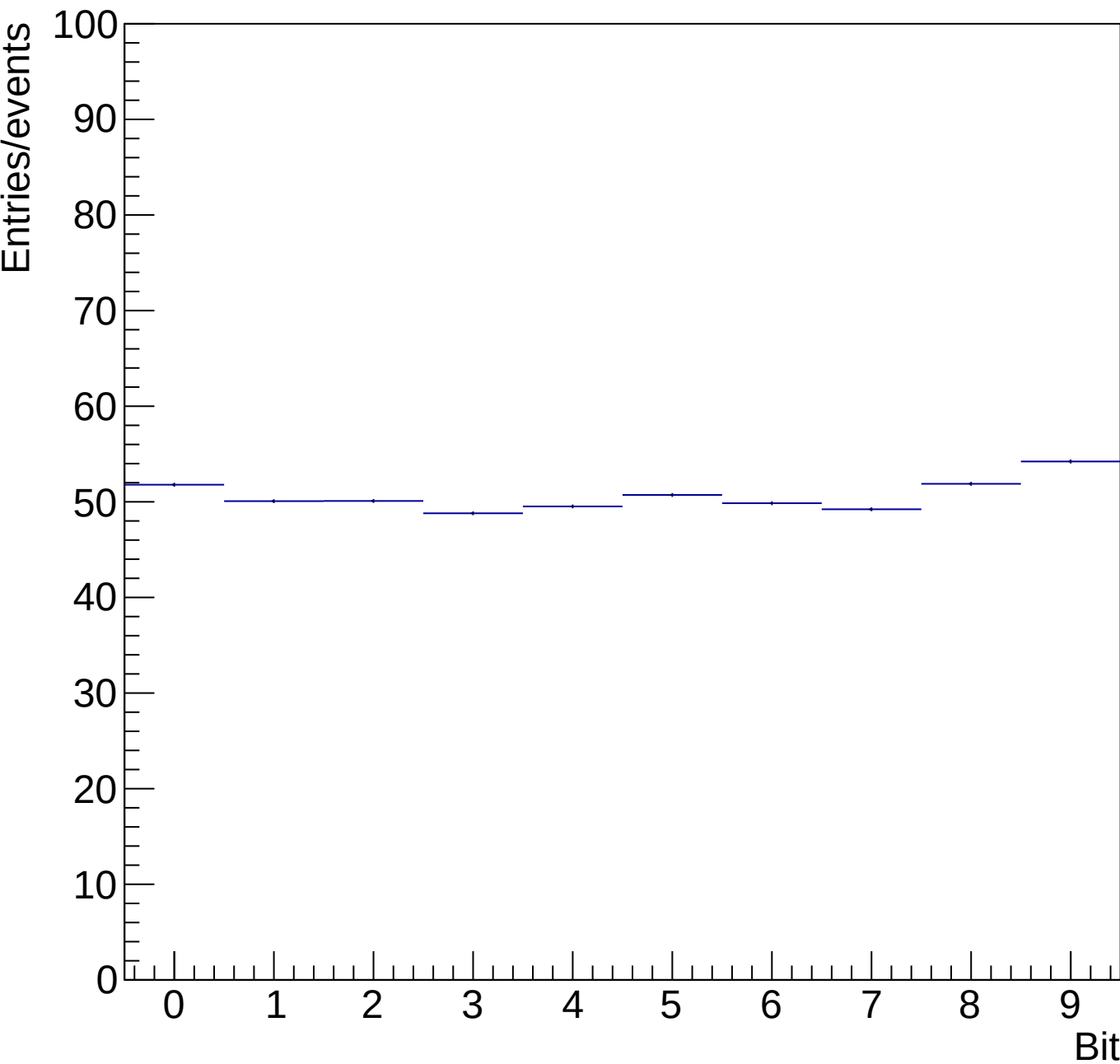
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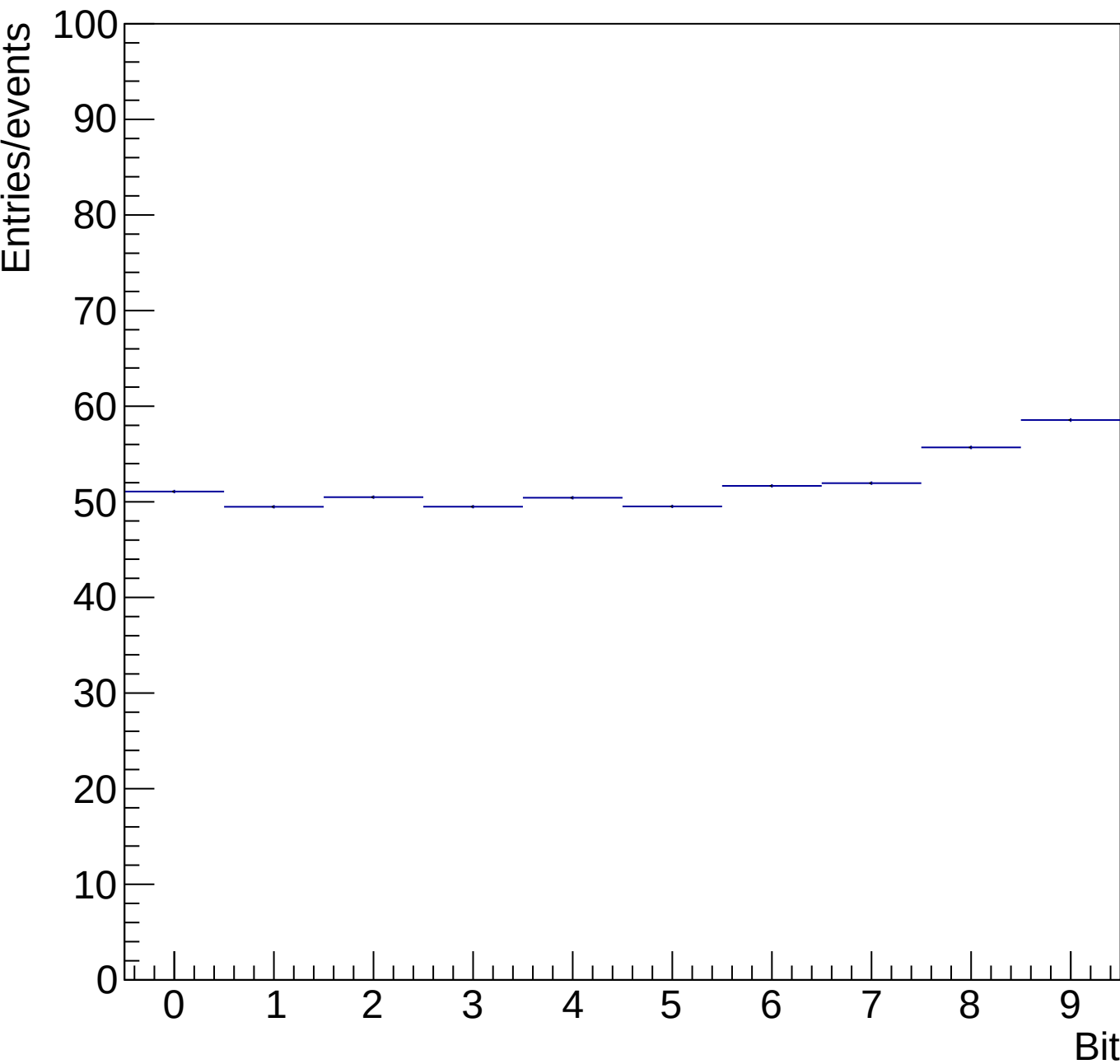
Bits ch 5 chip 7 g3-s0-p1-s3



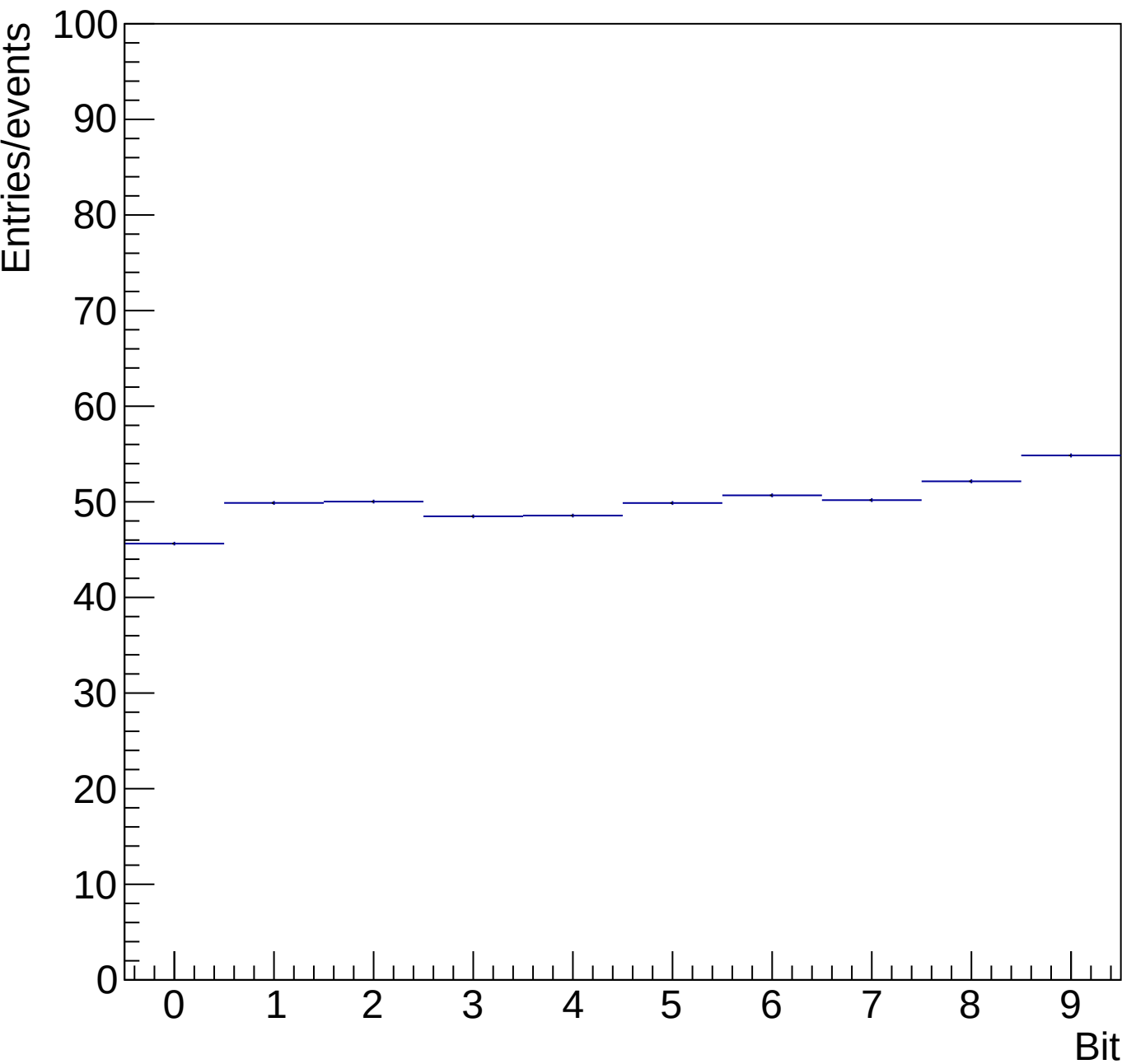
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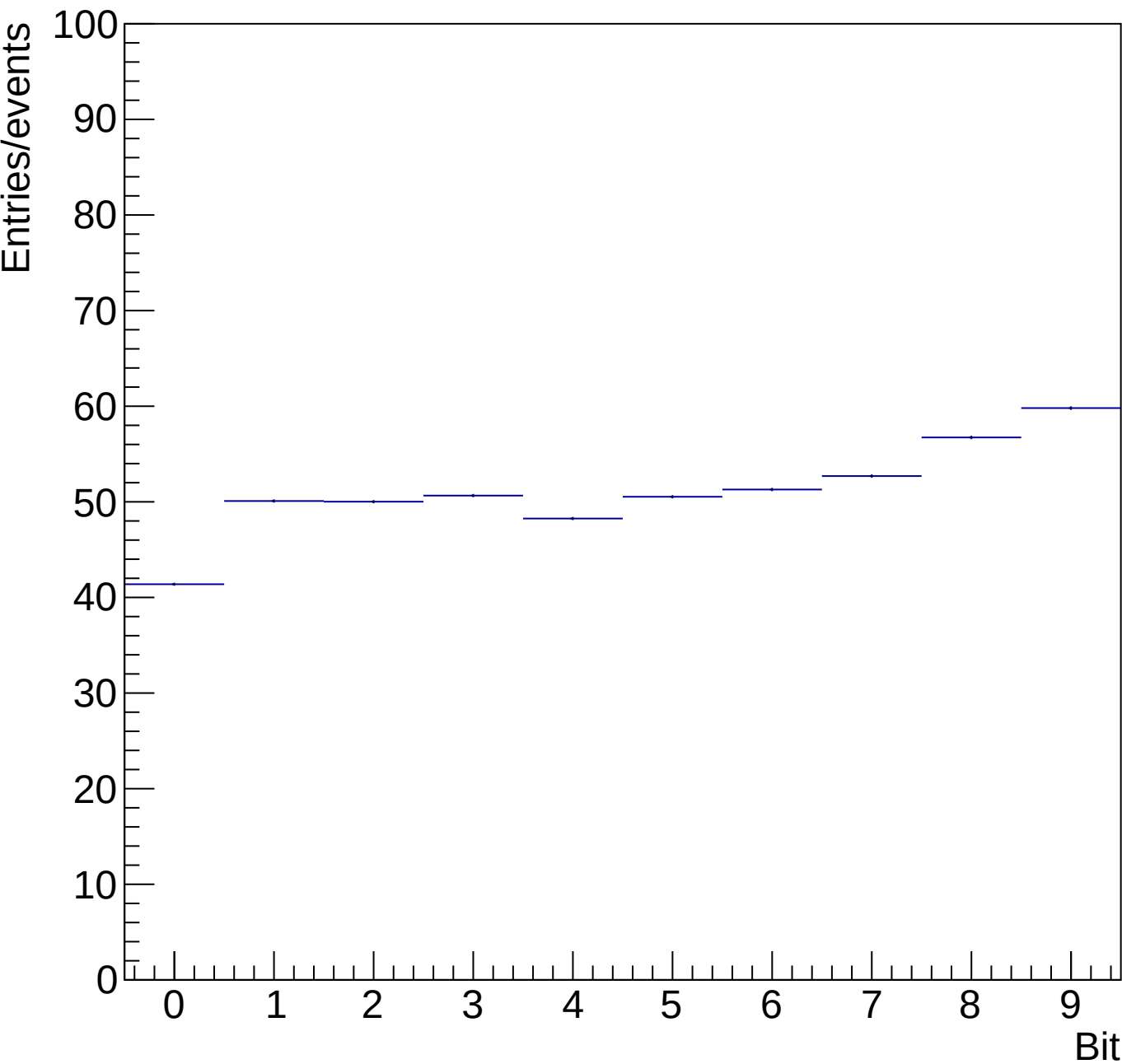
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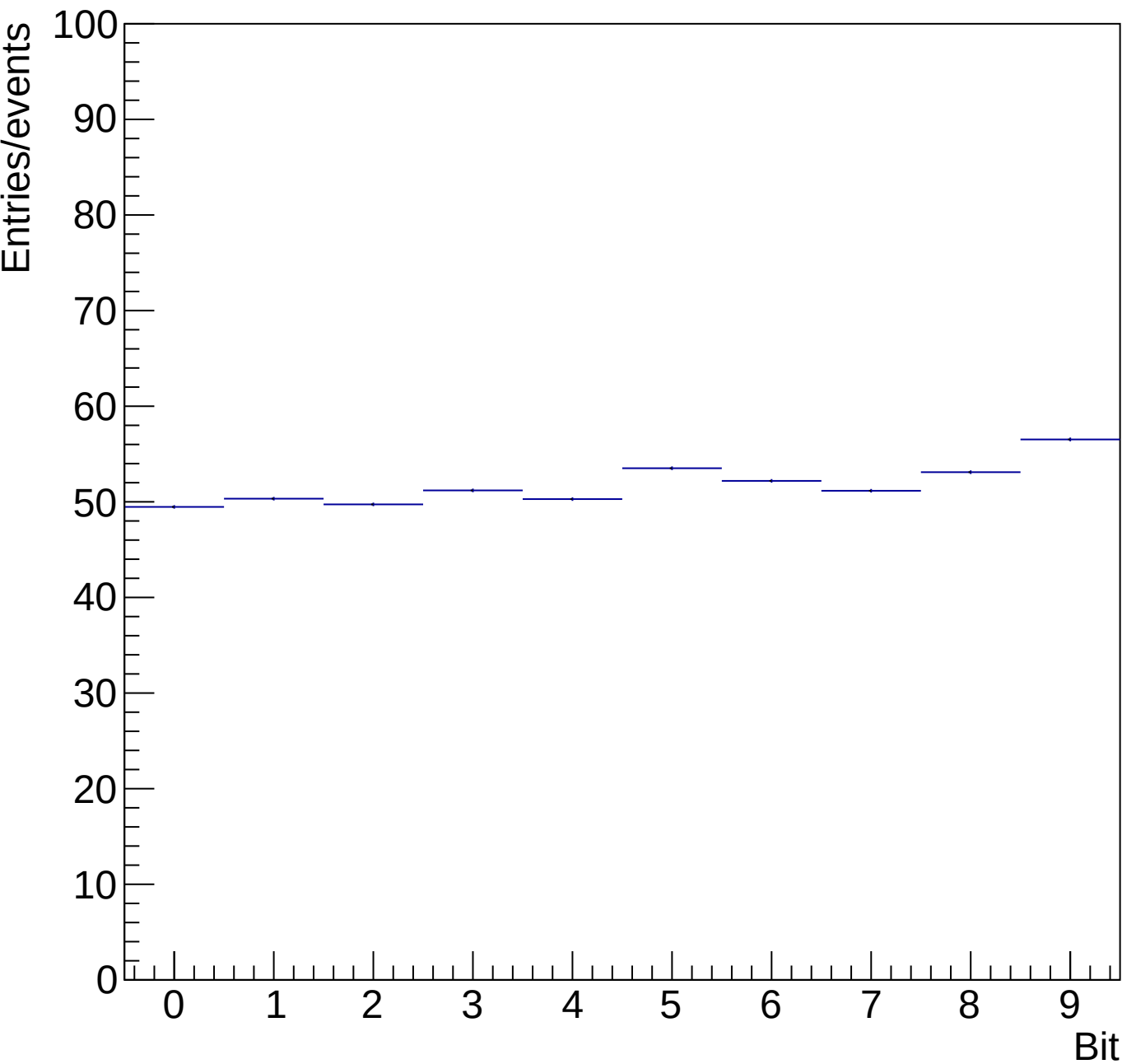
Bits ch 8 chip 7 g3-s0-p1-s3



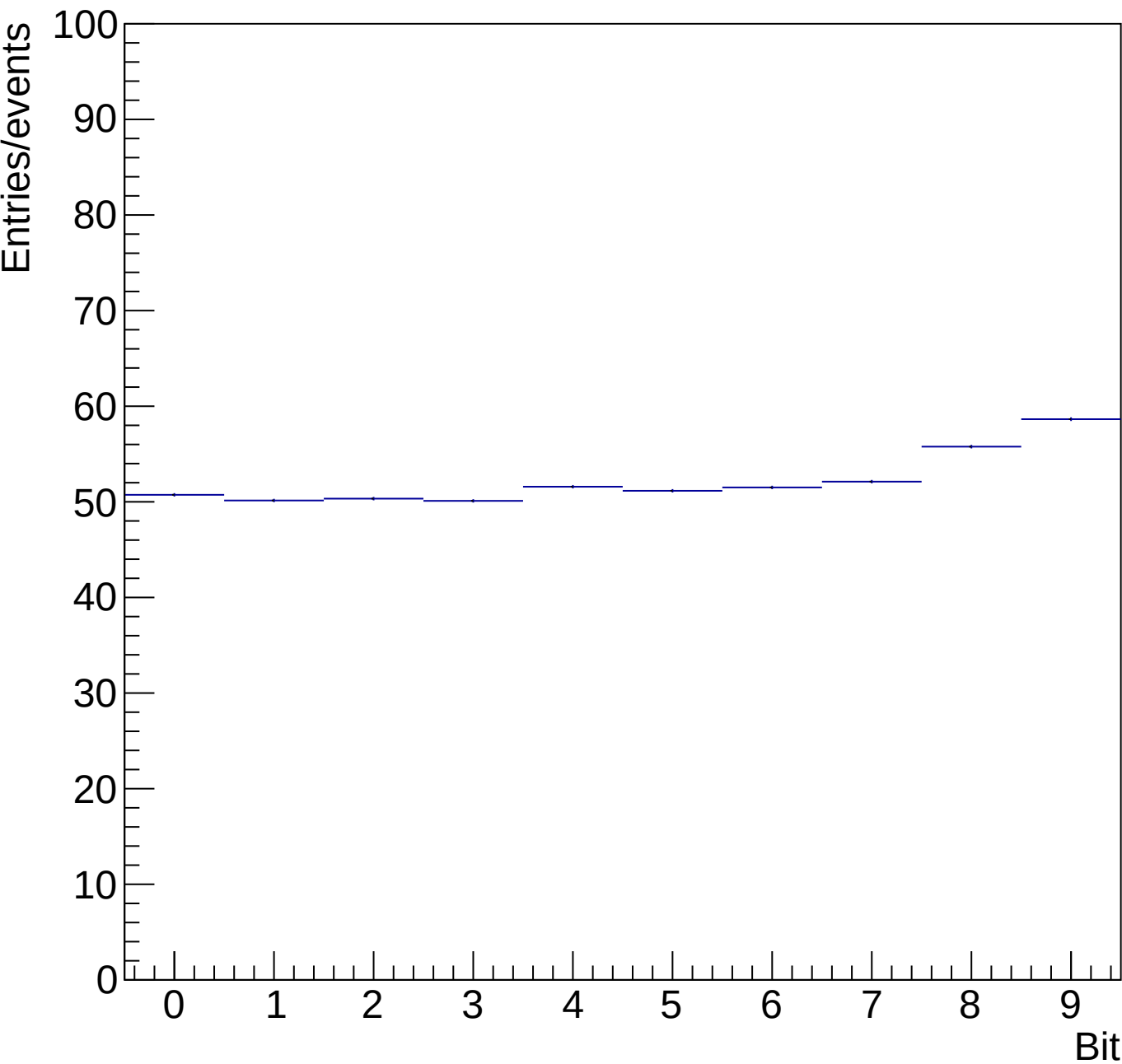
Bits ch 9 chip 7 g3-s0-p1-s3



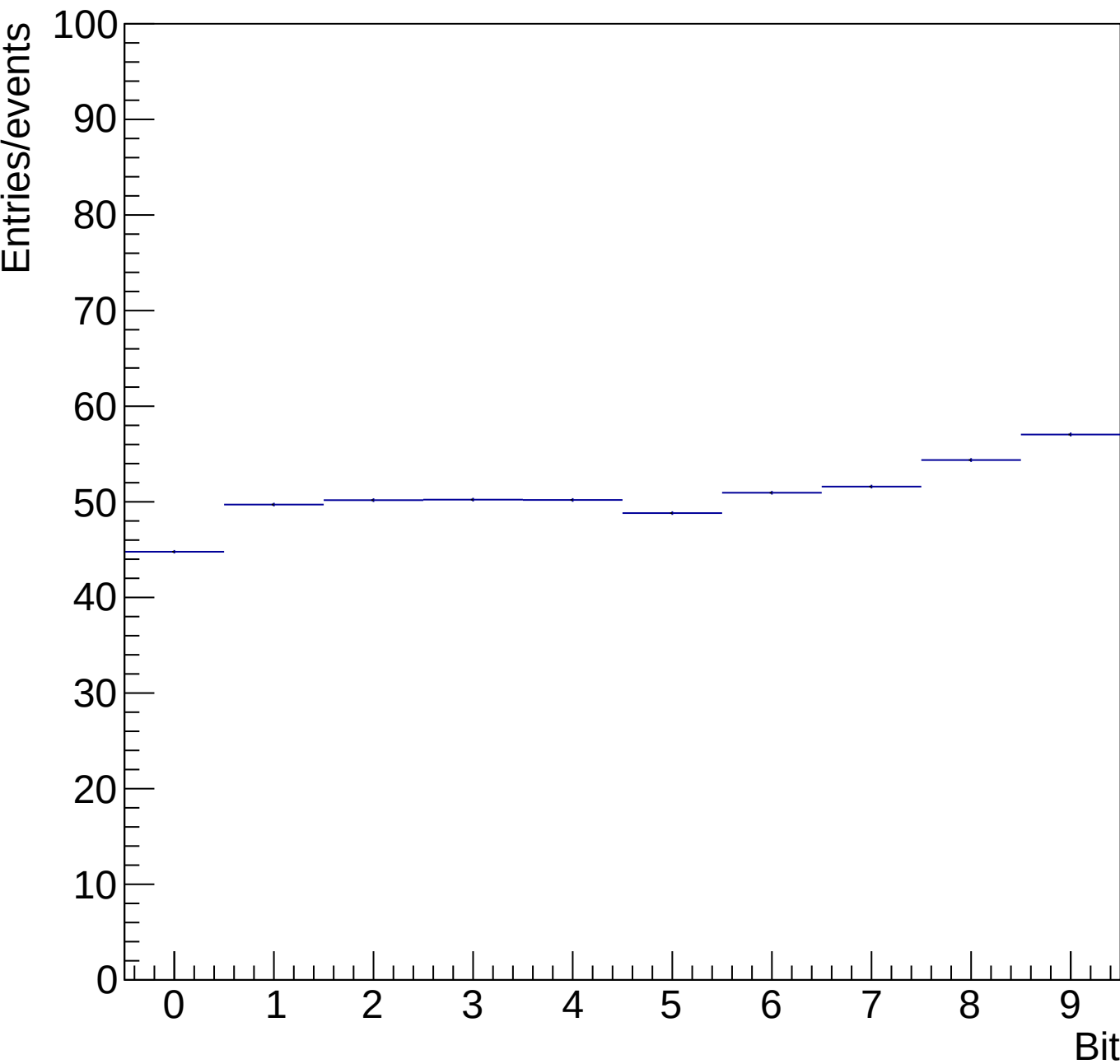
Bits ch 10 chip 7 g3-s0-p1-s3



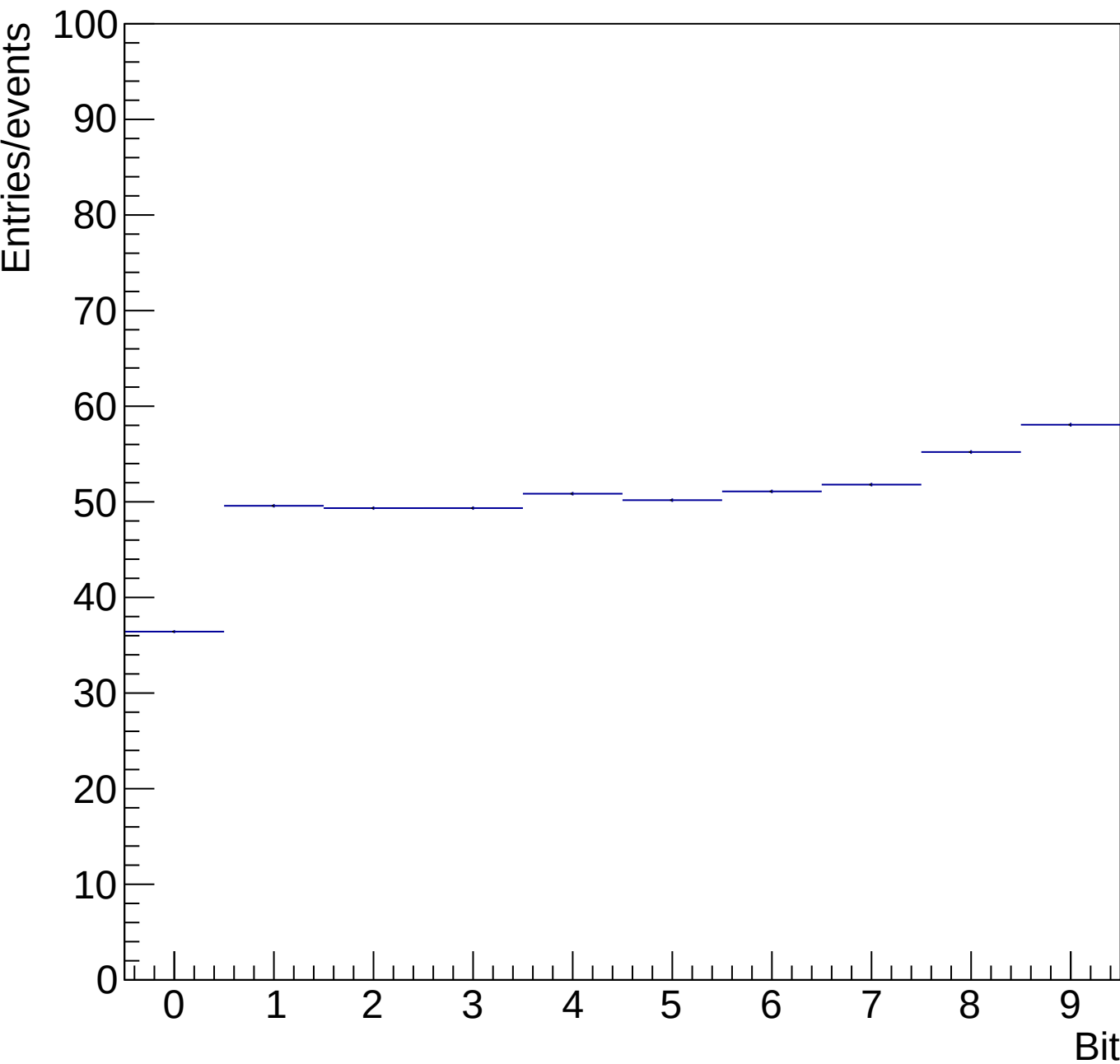
Bits ch 11 chip 7 g3-s0-p1-s3



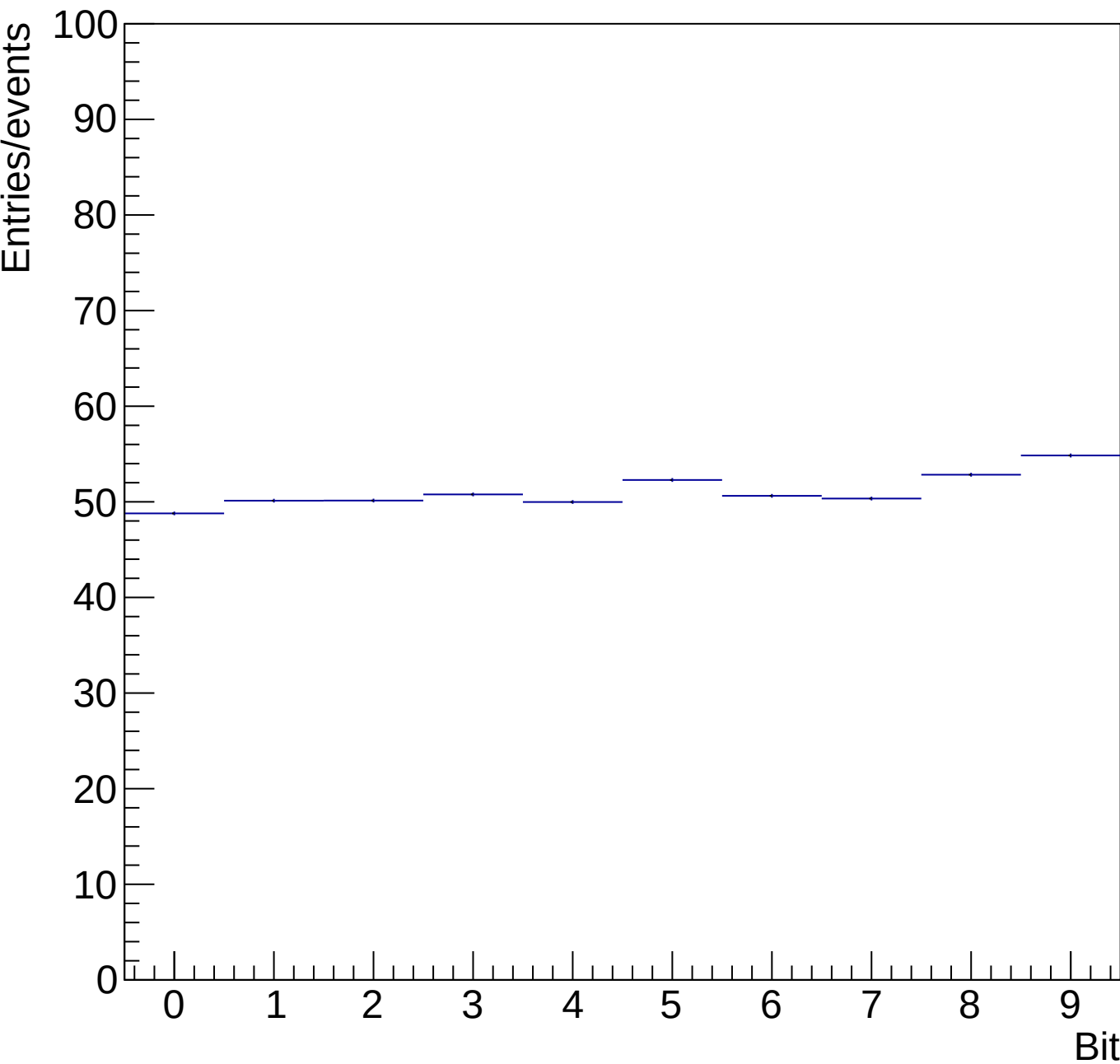
Bits ch 12 chip 7 g3-s0-p1-s3



Bits ch 13 chip 7 g3-s0-p1-s3



Bits ch 14 chip 7 g3-s0-p1-s3



Bits ch 15 chip 7 g3-s0-p1-s3

