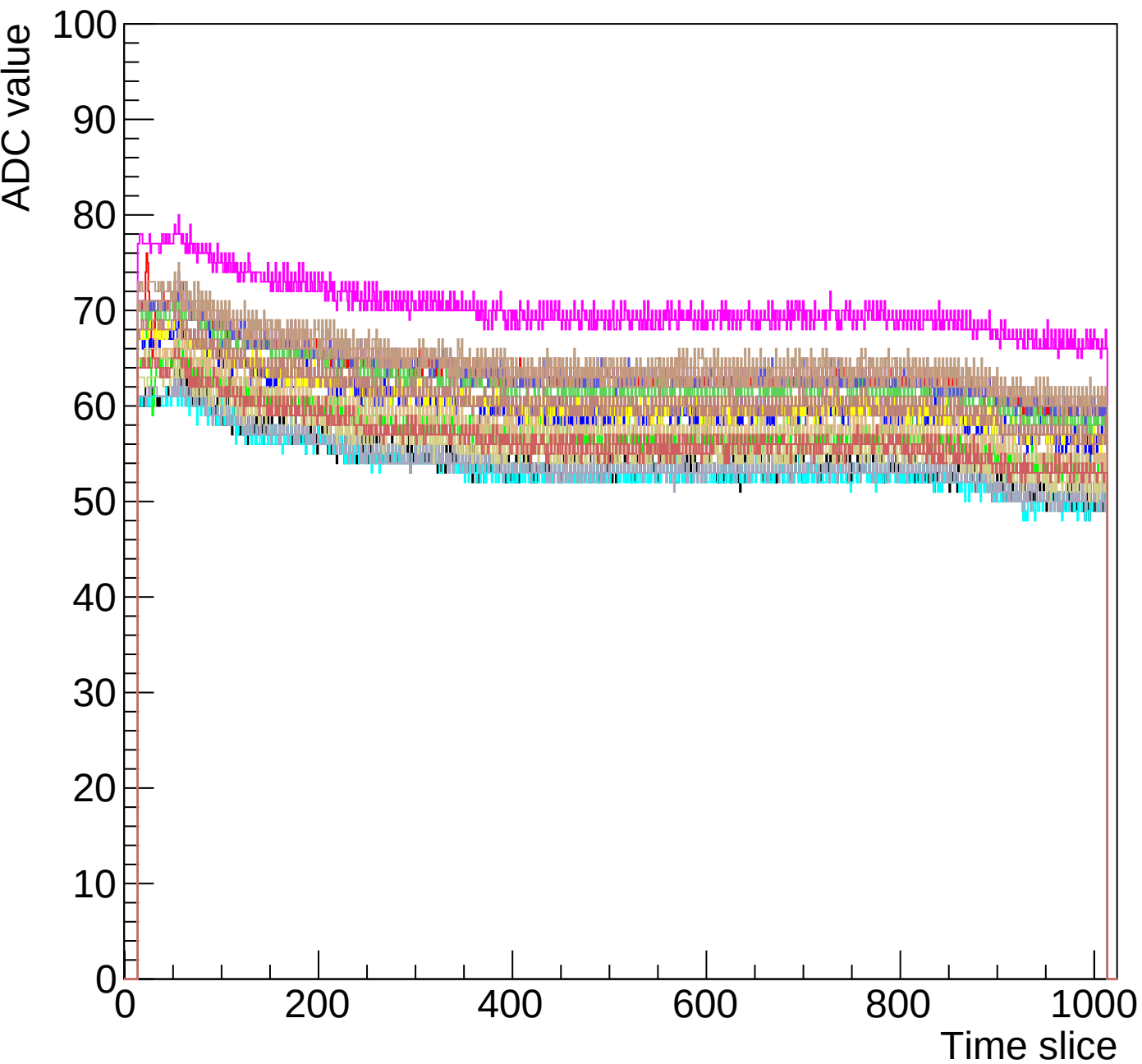
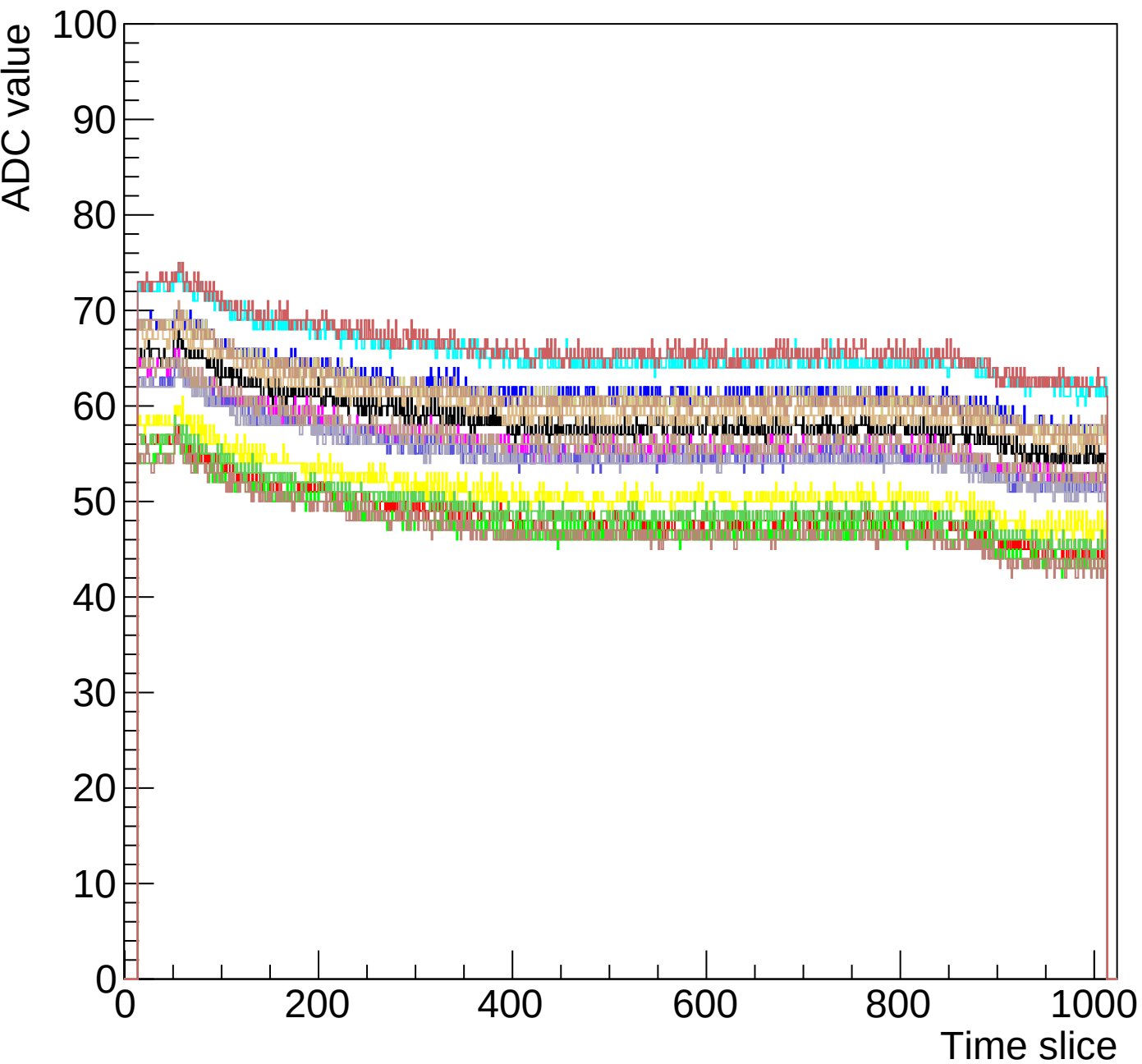


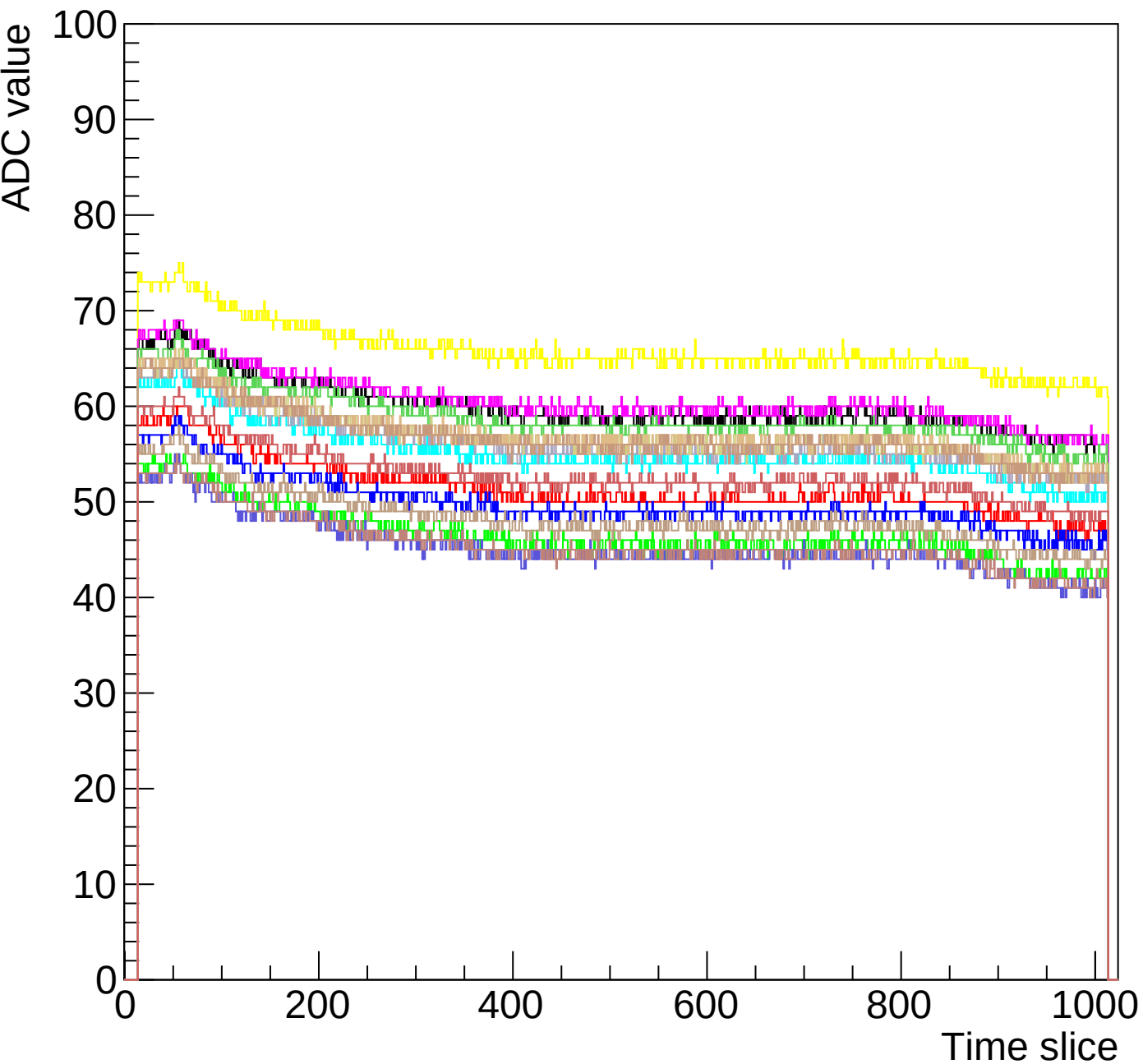
Run 110 event 12 mcm 0 chip 0 test



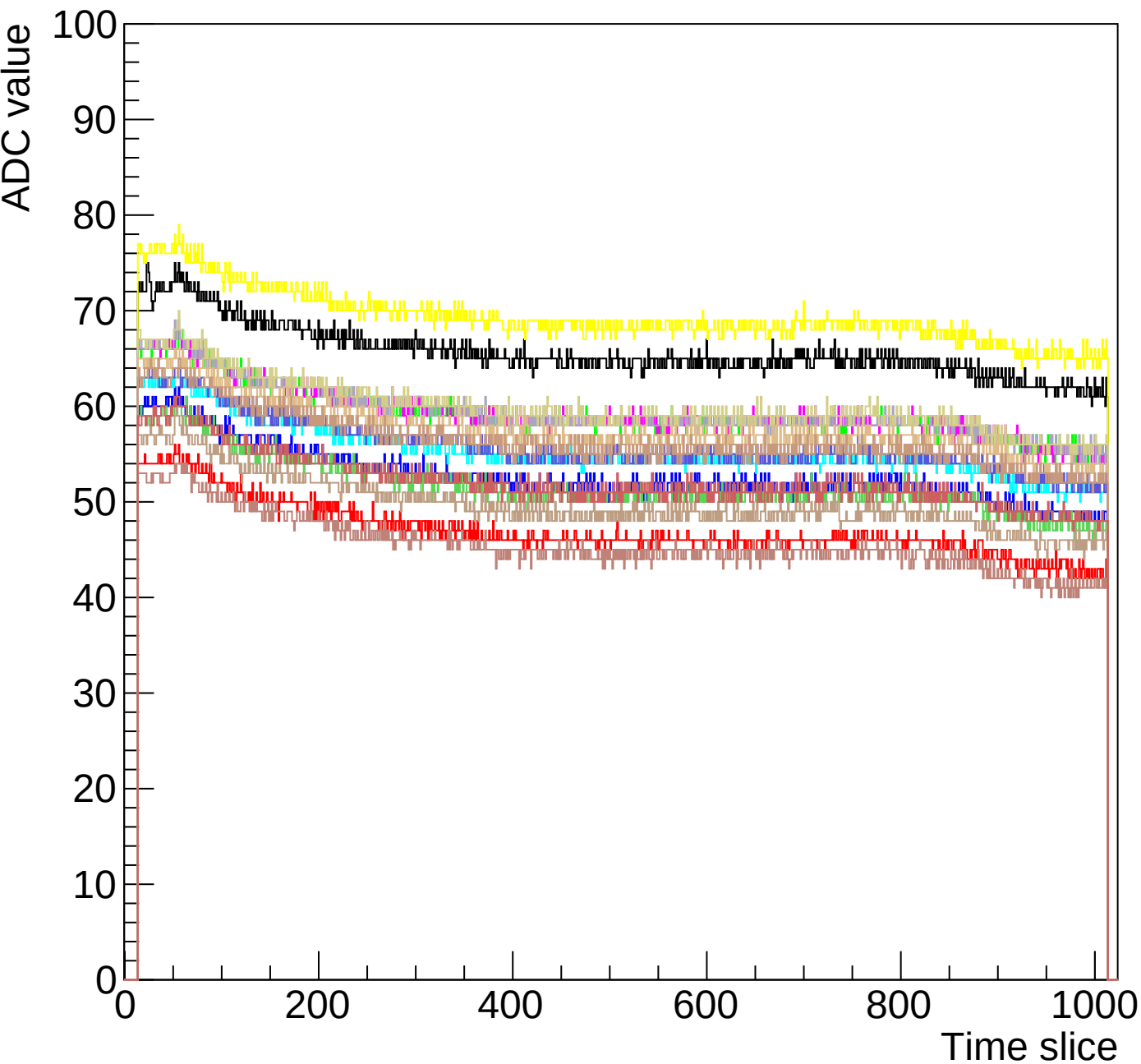
Run 110 event 12 mcm 0 chip 1 test



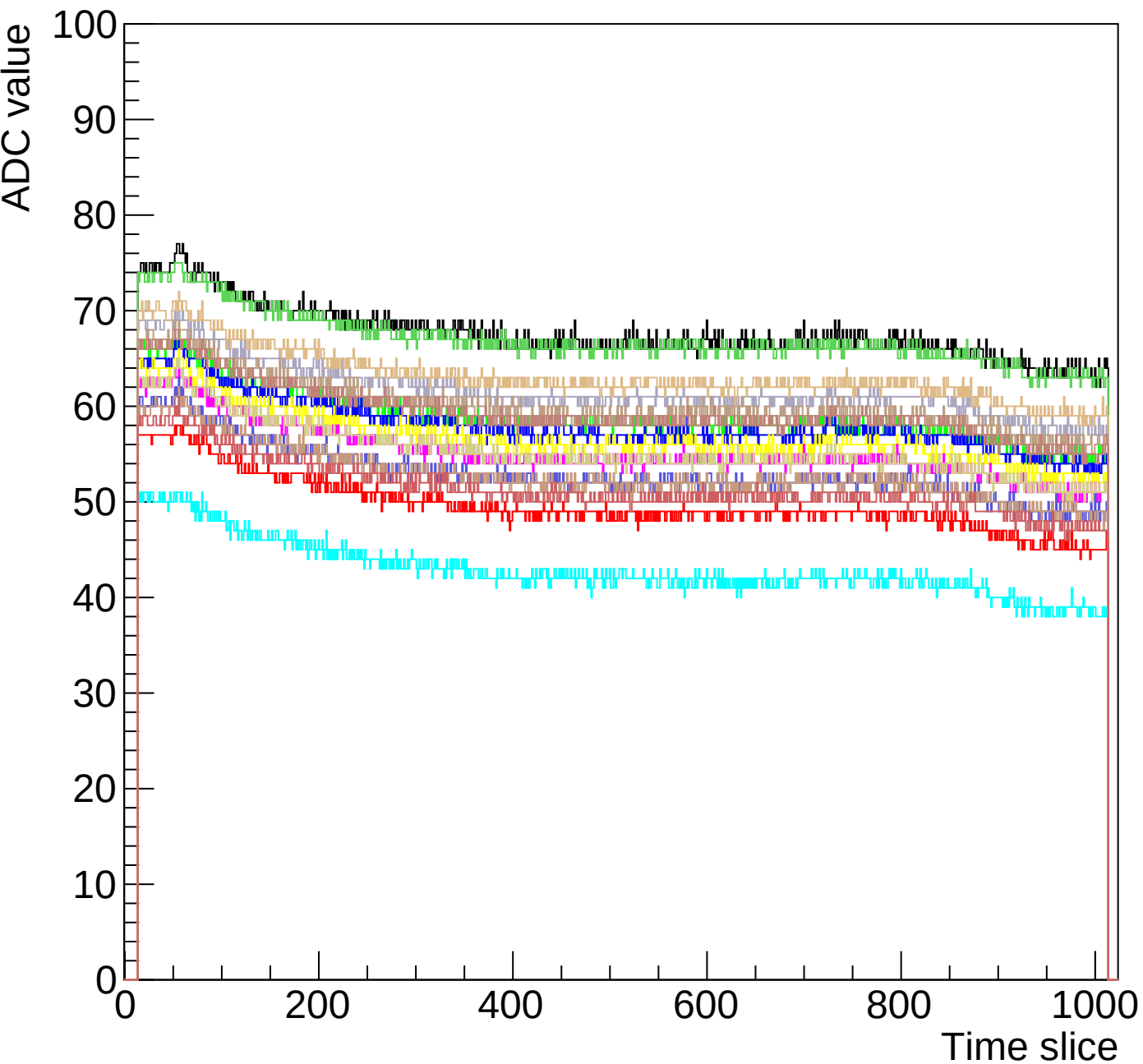
Run 110 event 12 mcm 0 chip 2 test



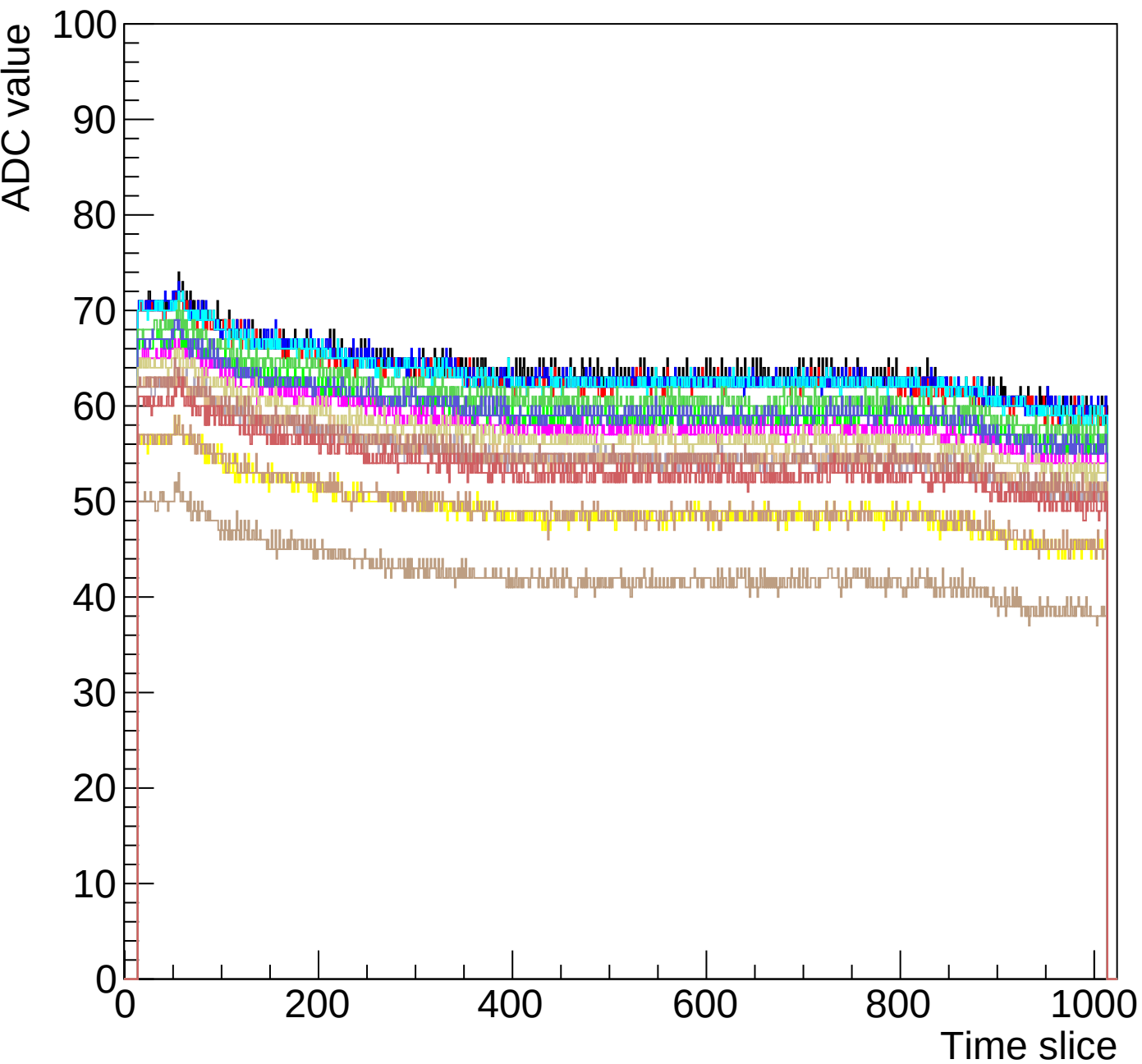
Run 110 event 12 mcm 0 chip 3 test



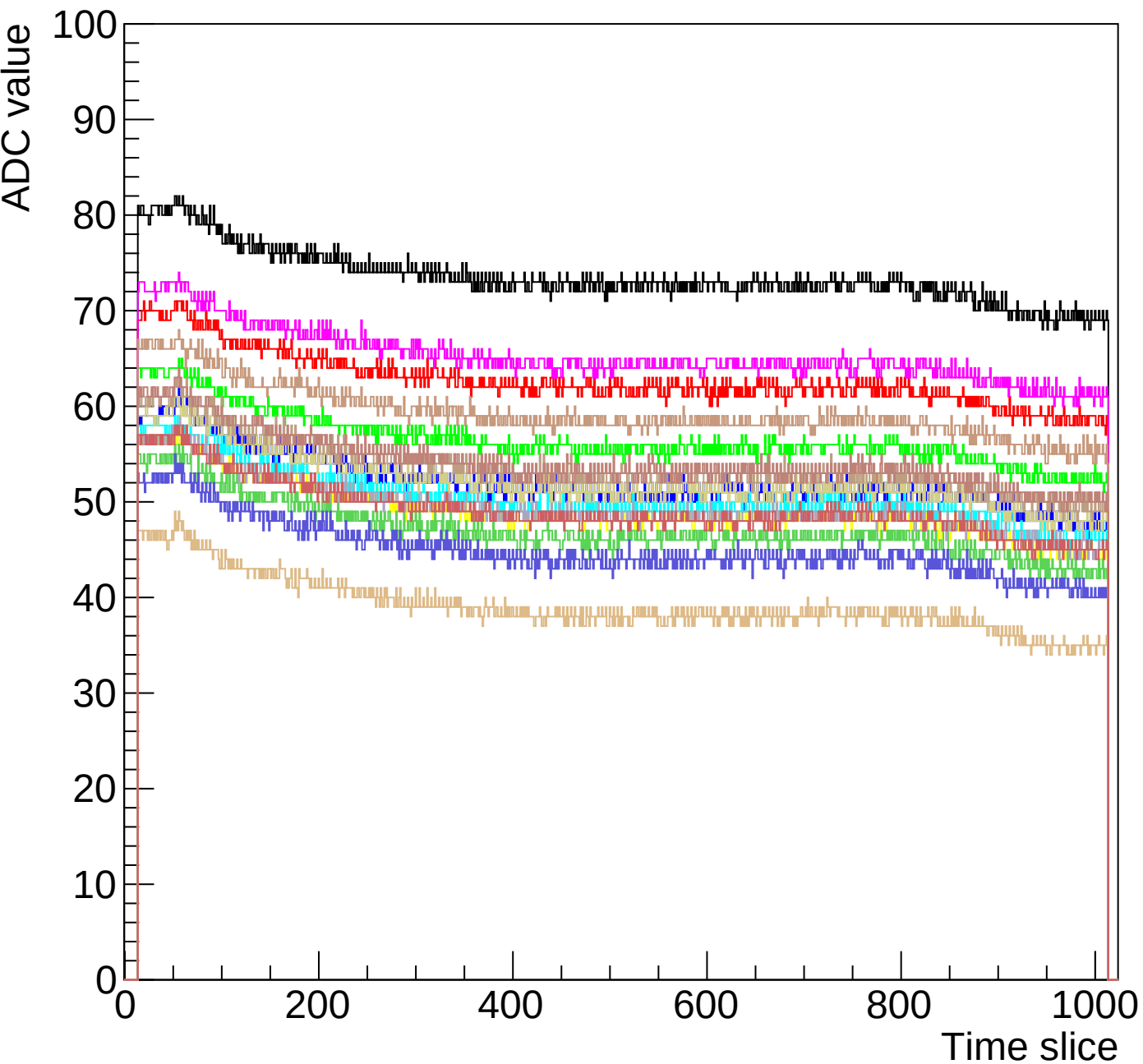
Run 110 event 12 mcm 0 chip 4 test



Run 110 event 12 mcm 0 chip 5 test



Run 110 event 12 mcm 0 chip 6 test



Run 110 event 12 mcm 0 chip 7 test

