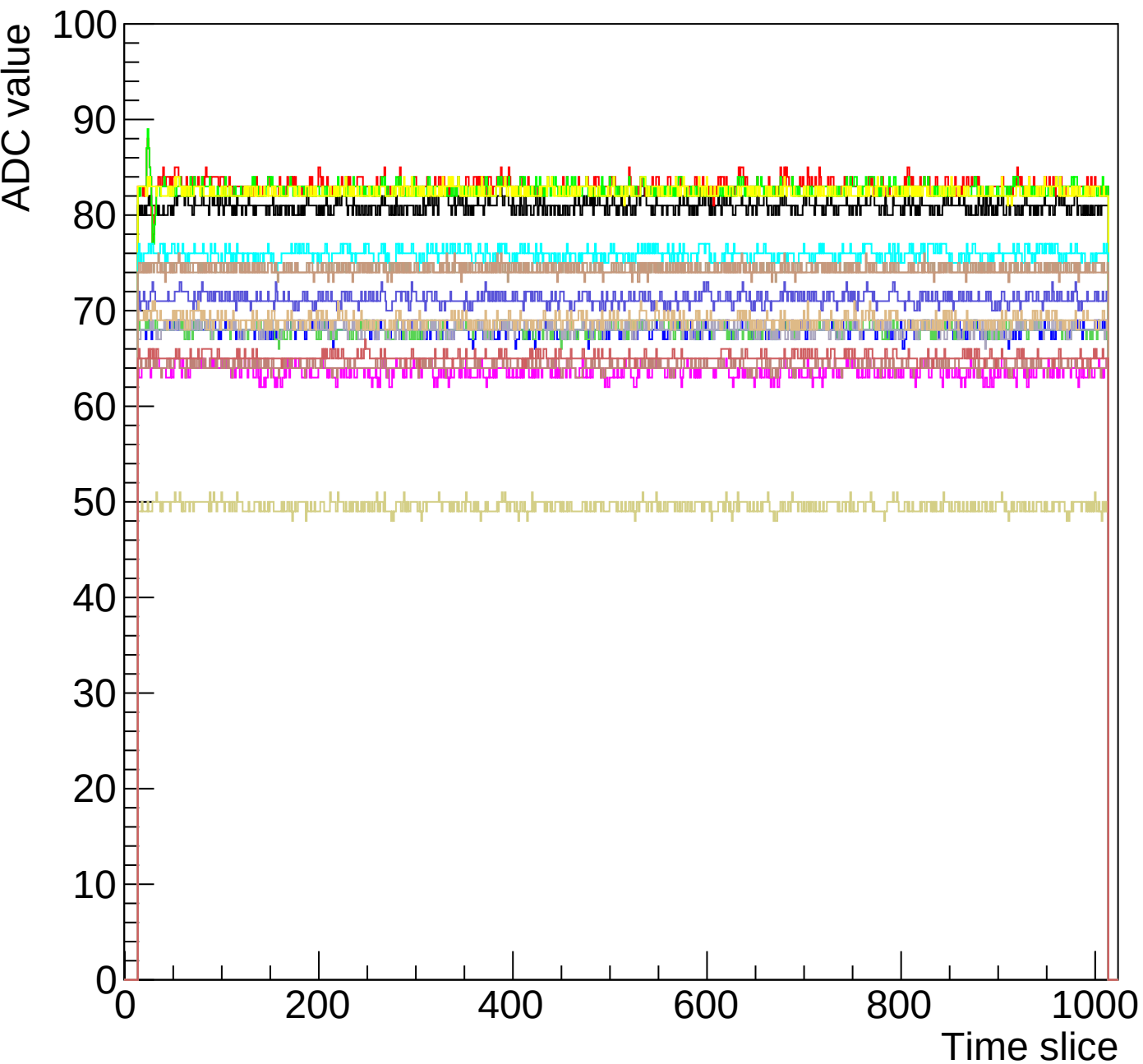
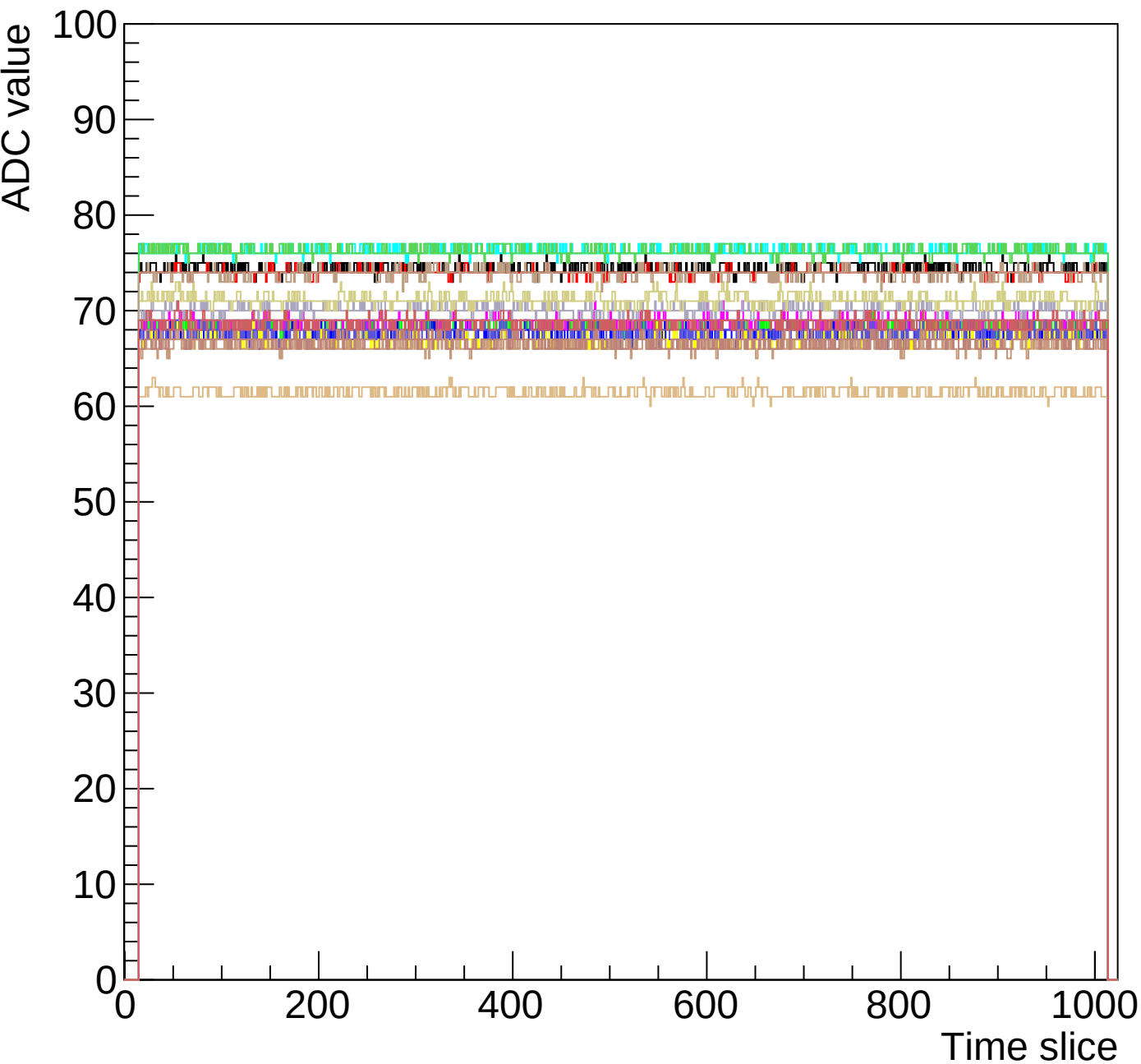


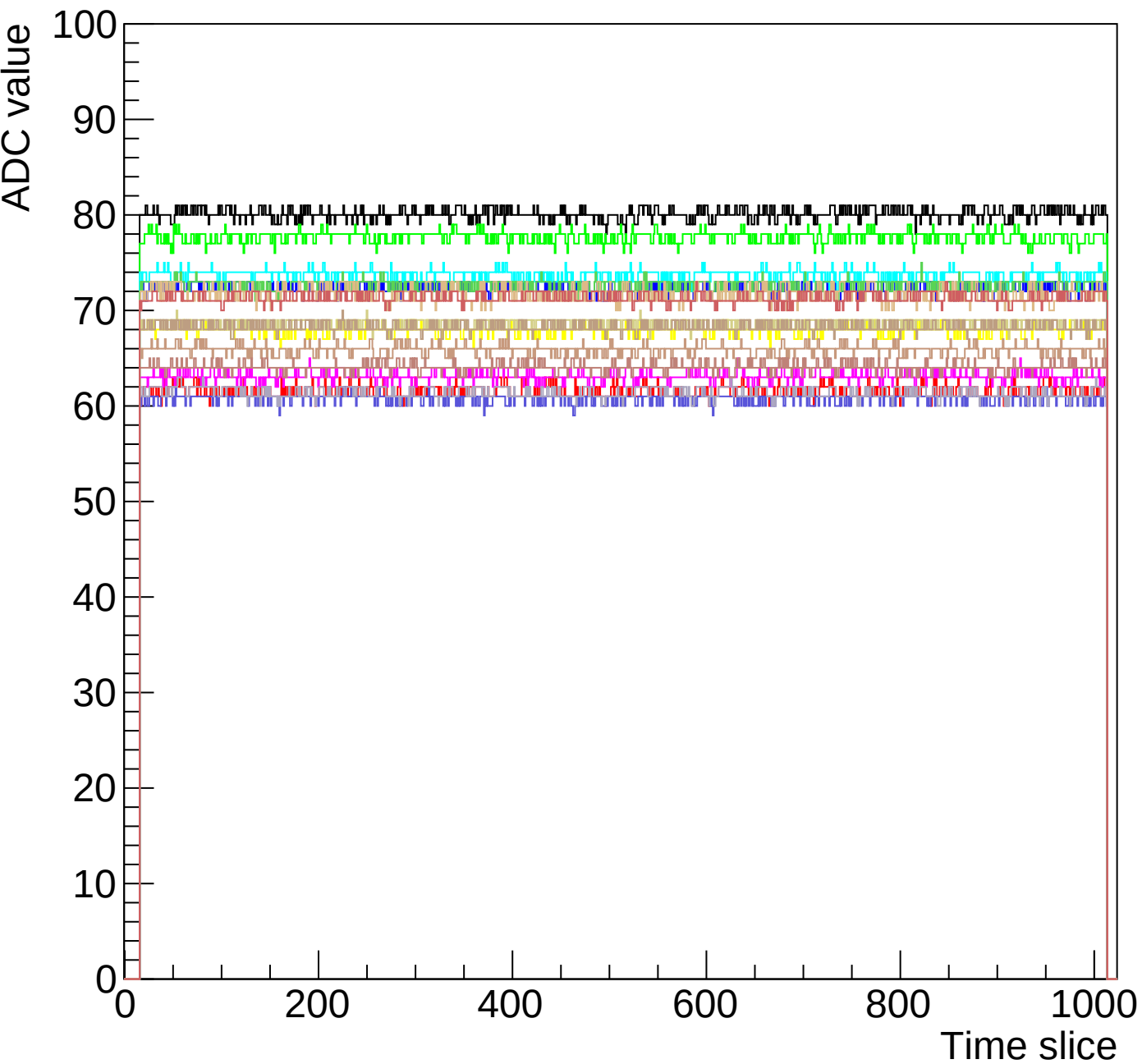
Run 121 event 14 mcm 0 chip 0 35ohm



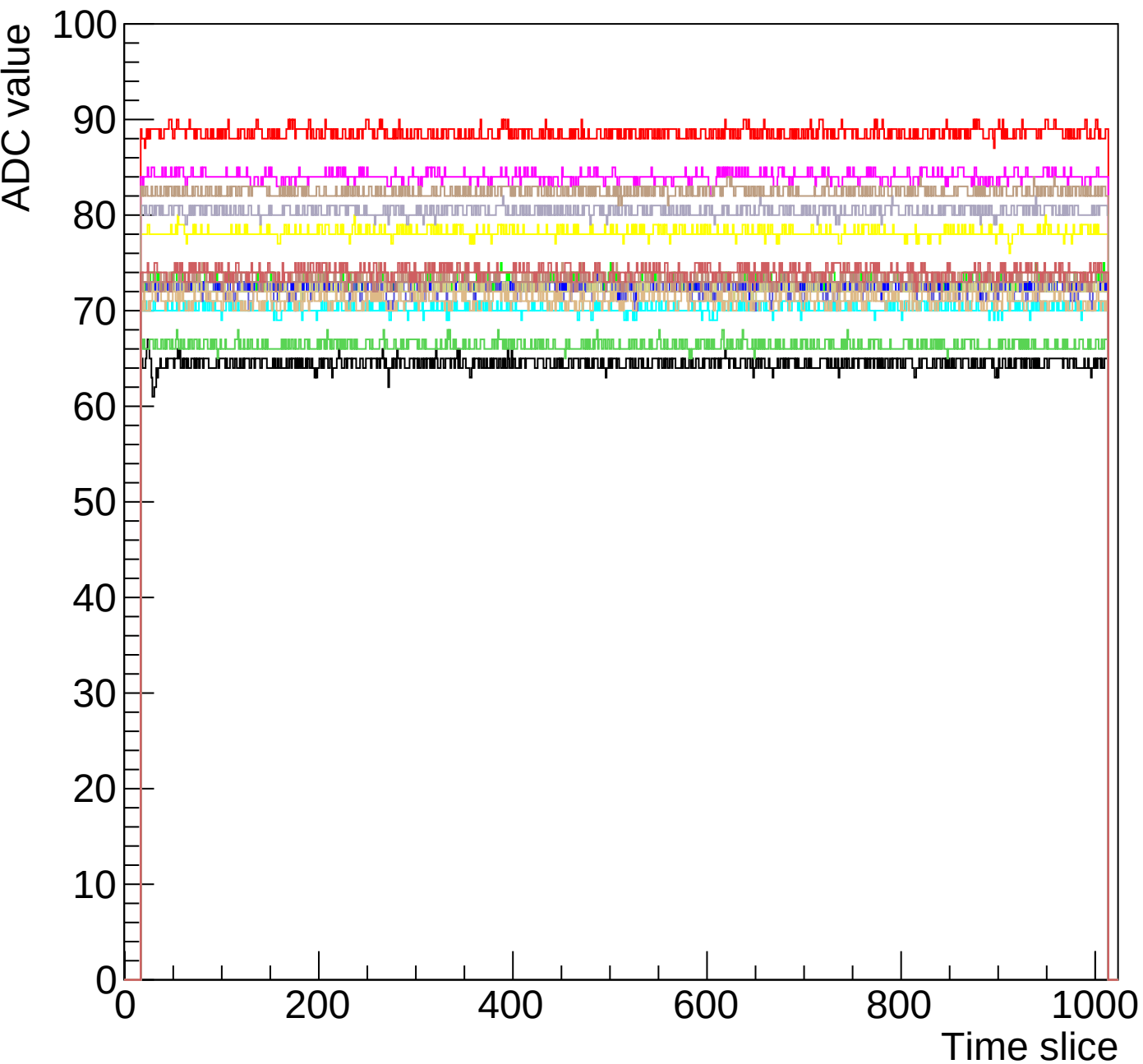
Run 121 event 14 mcm 0 chip 1 35ohm



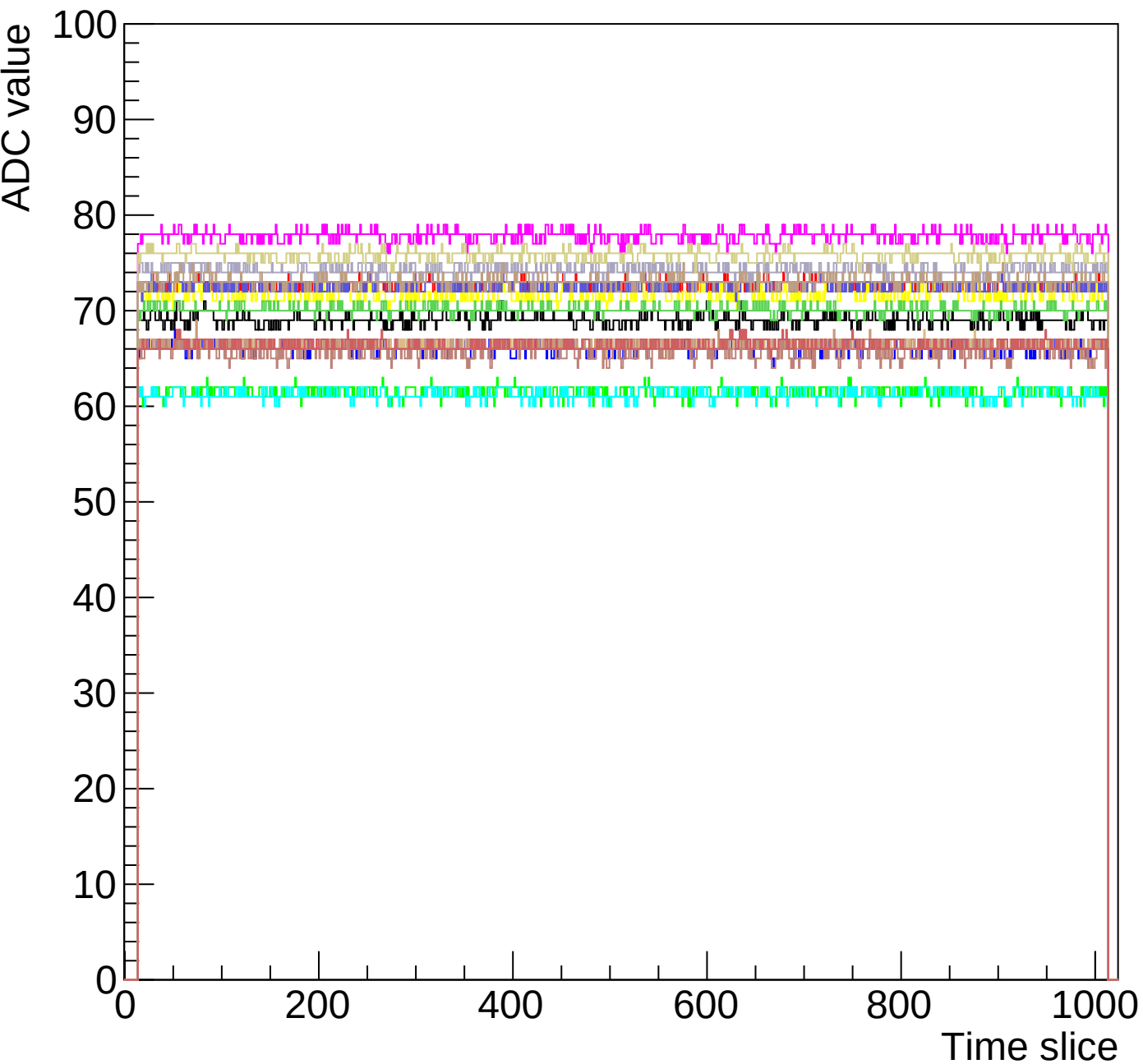
Run 121 event 14 mcm 0 chip 2 35ohm



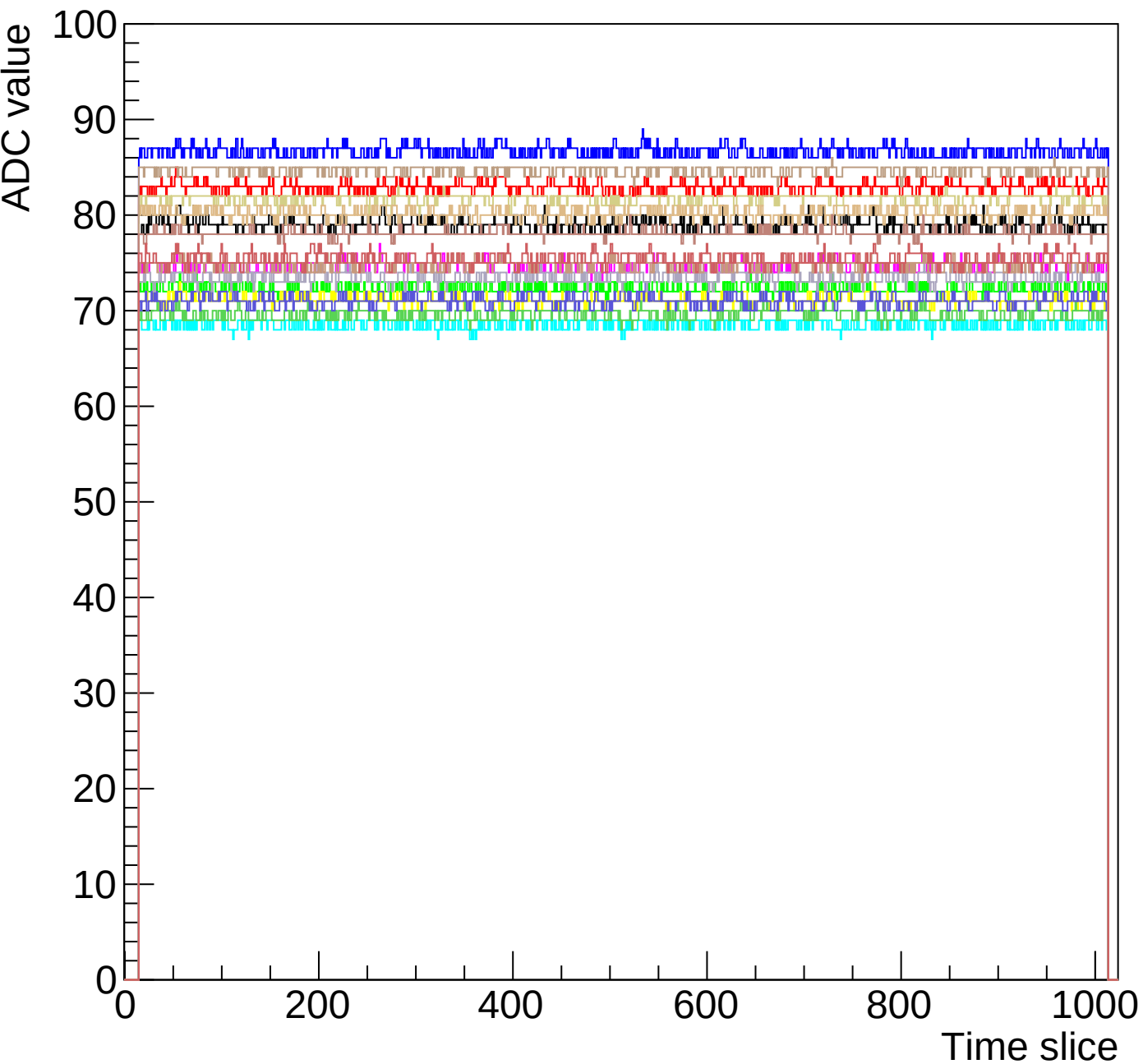
Run 121 event 14 mcm 0 chip 3 35ohm



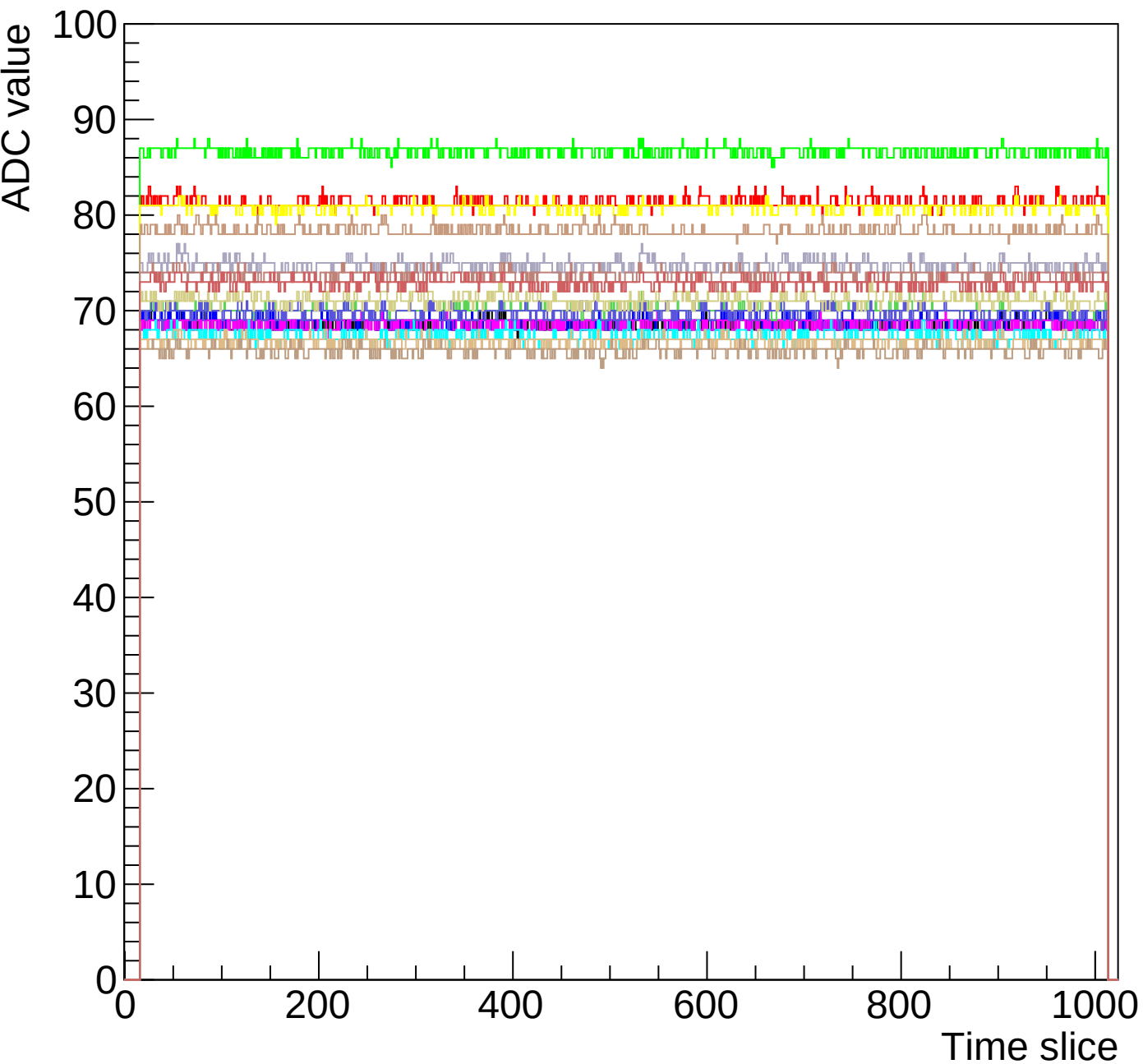
Run 121 event 14 mcm 0 chip 4 35ohm



Run 121 event 14 mcm 0 chip 5 35ohm



Run 121 event 14 mcm 0 chip 6 35ohm



Run 121 event 14 mcm 0 chip 7 35ohm

