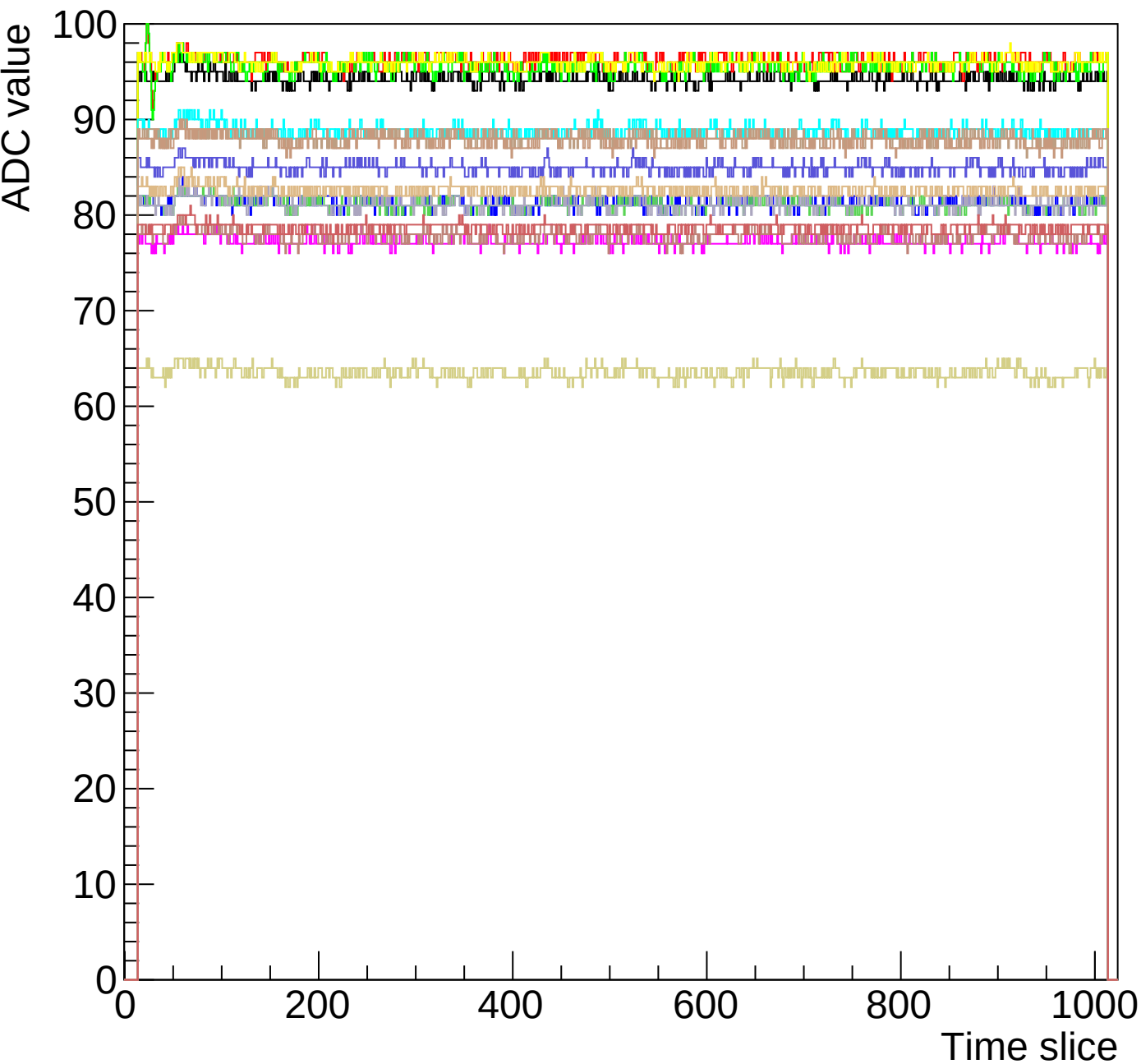
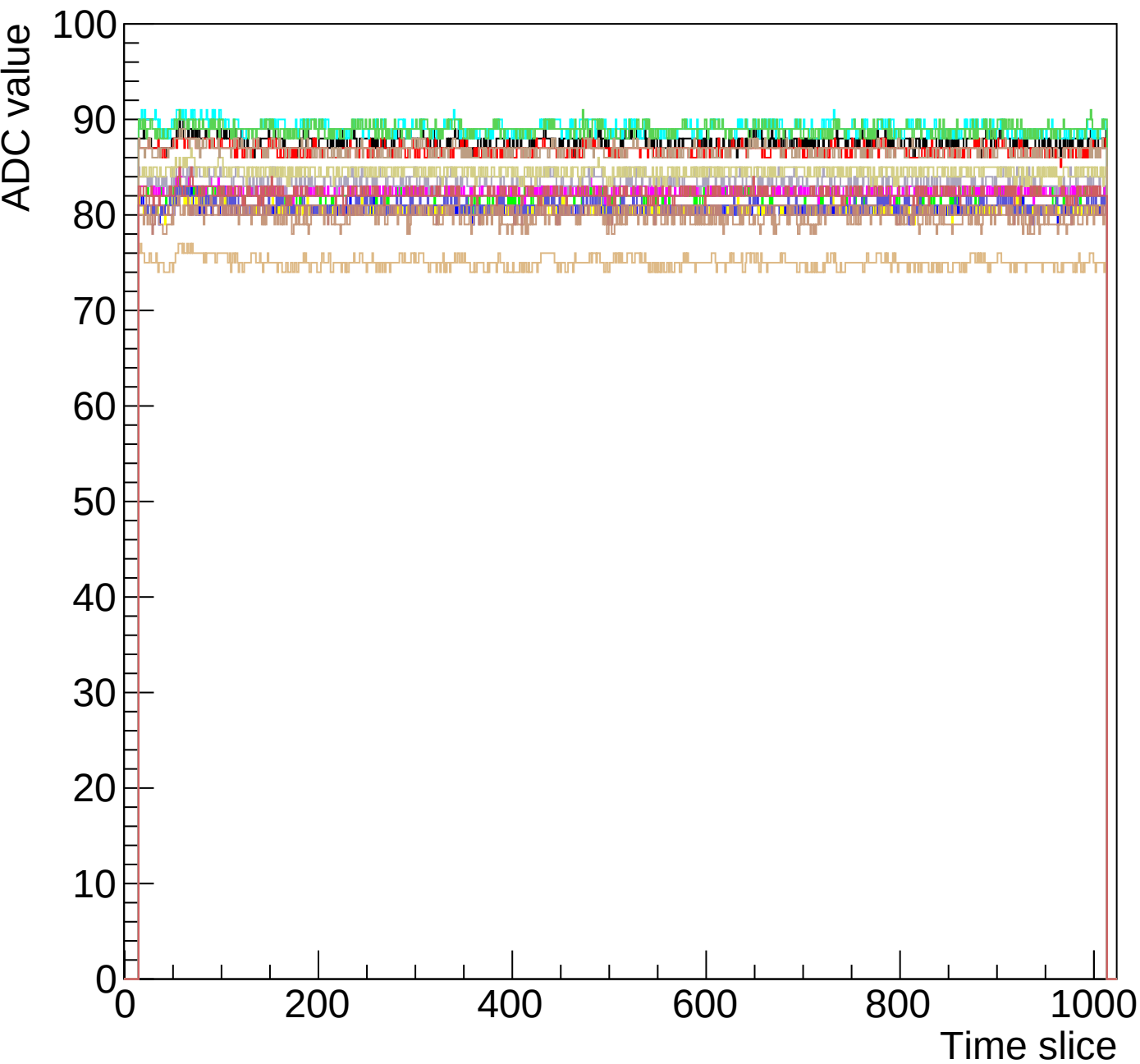


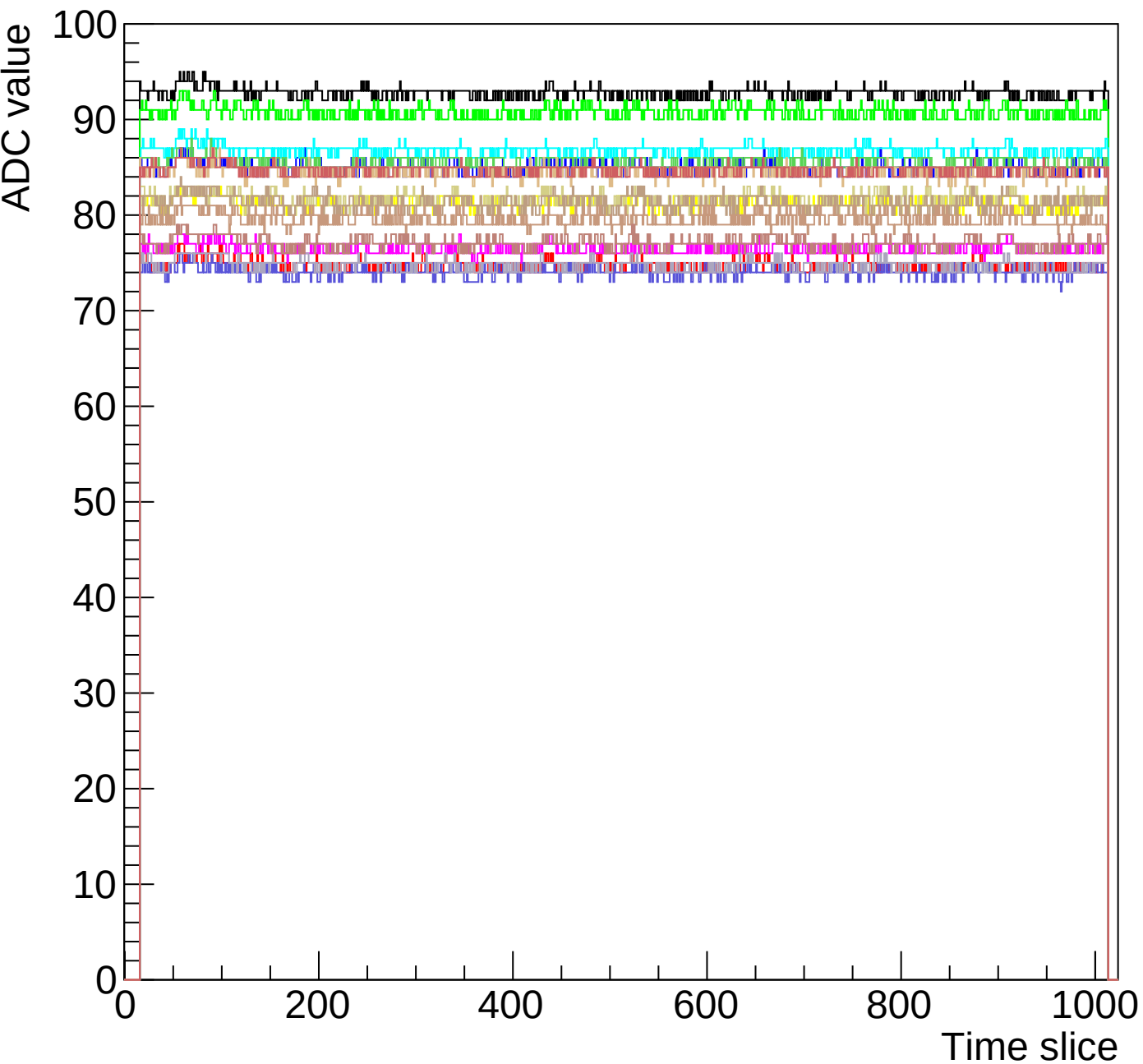
Run 122 event 14 mcm 0 chip 0 18ohm



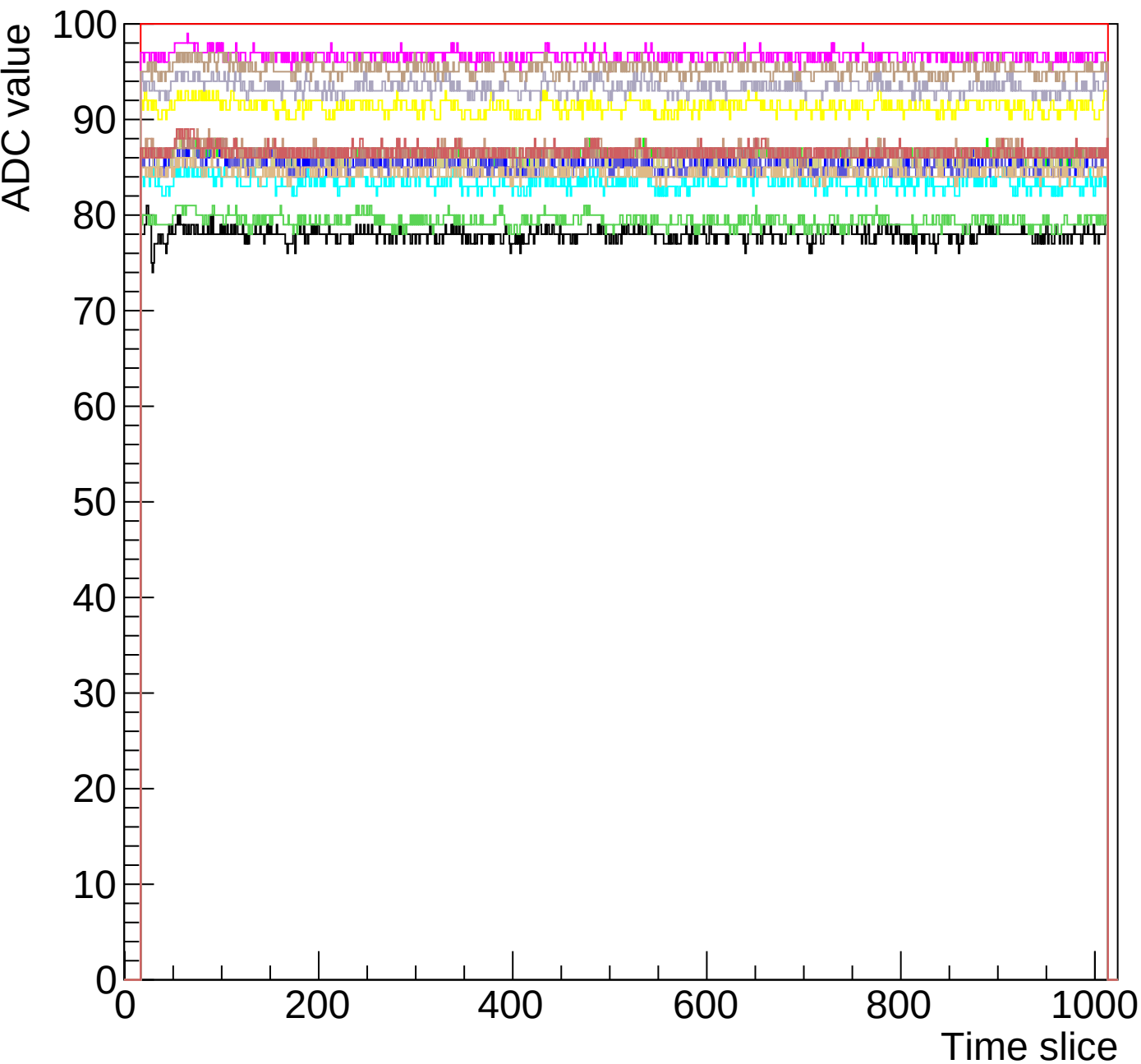
Run 122 event 14 mcm 0 chip 1 18ohm



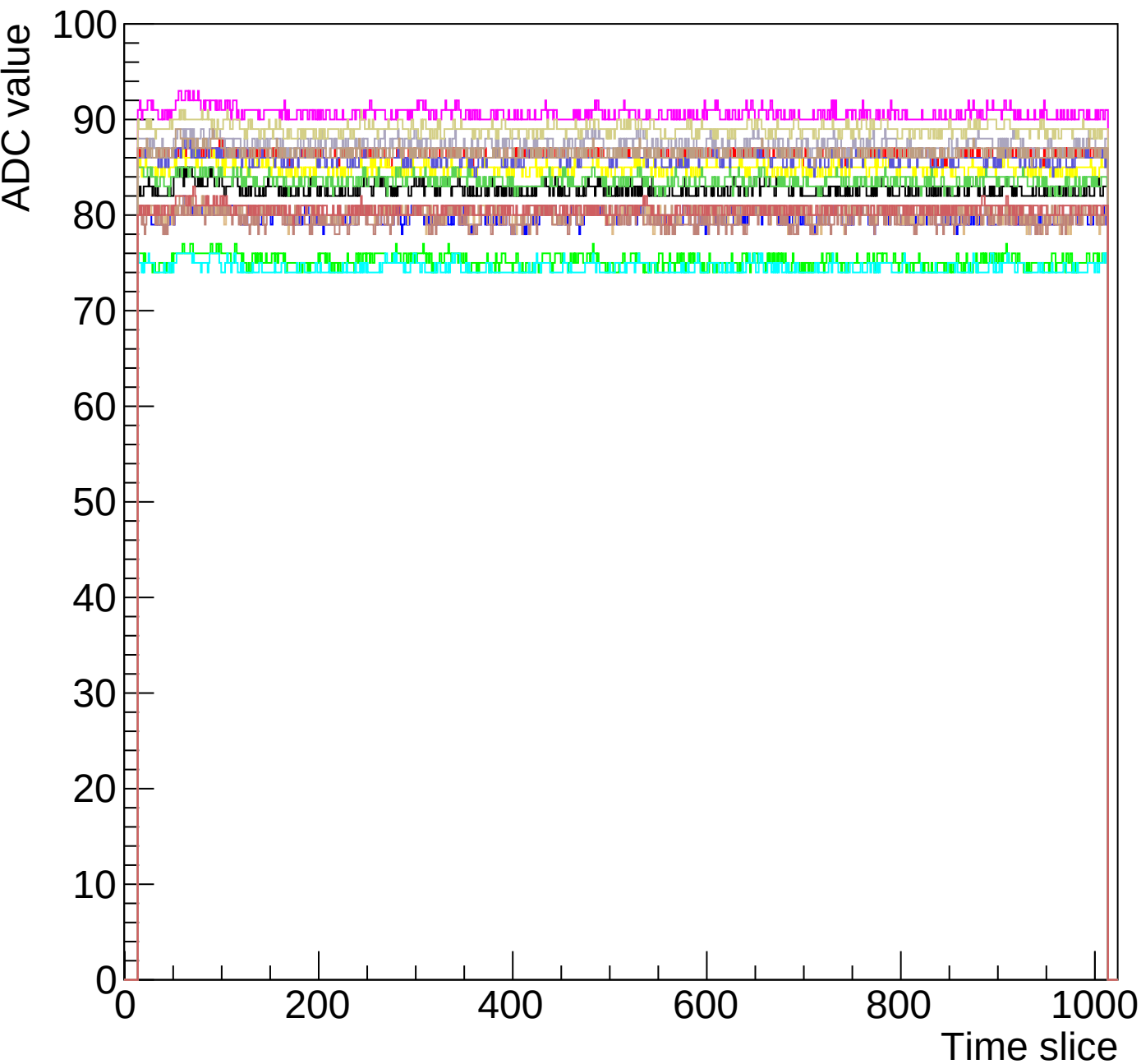
Run 122 event 14 mcm 0 chip 2 18ohm



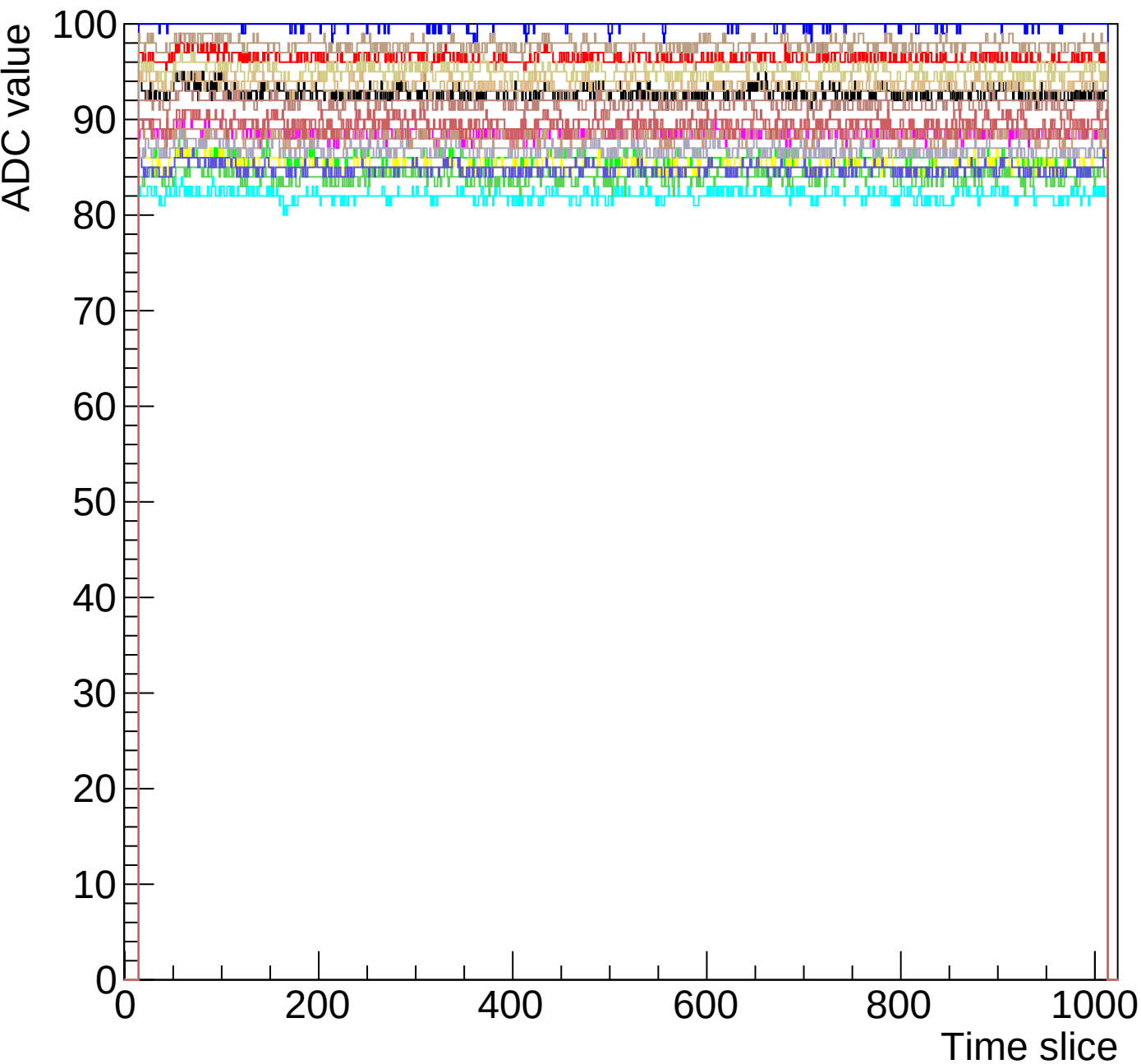
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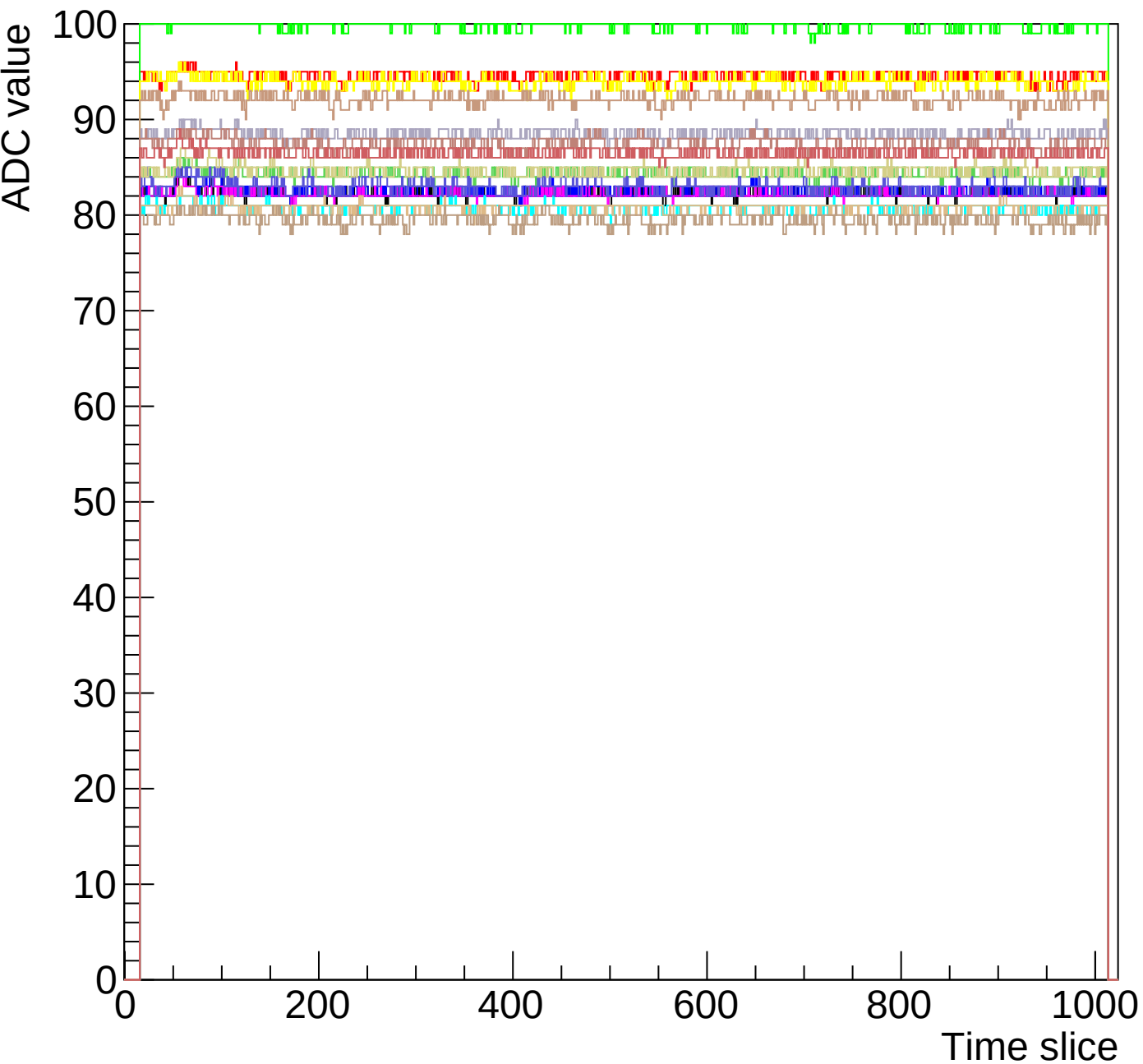
Run 122 event 14 mcm 0 chip 4 18ohm



Run 122 event 14 mcm 0 chip 5 18ohm



Run 122 event 14 mcm 0 chip 6 18ohm



Run 122 event 14 mcm 0 chip 7 18ohm

