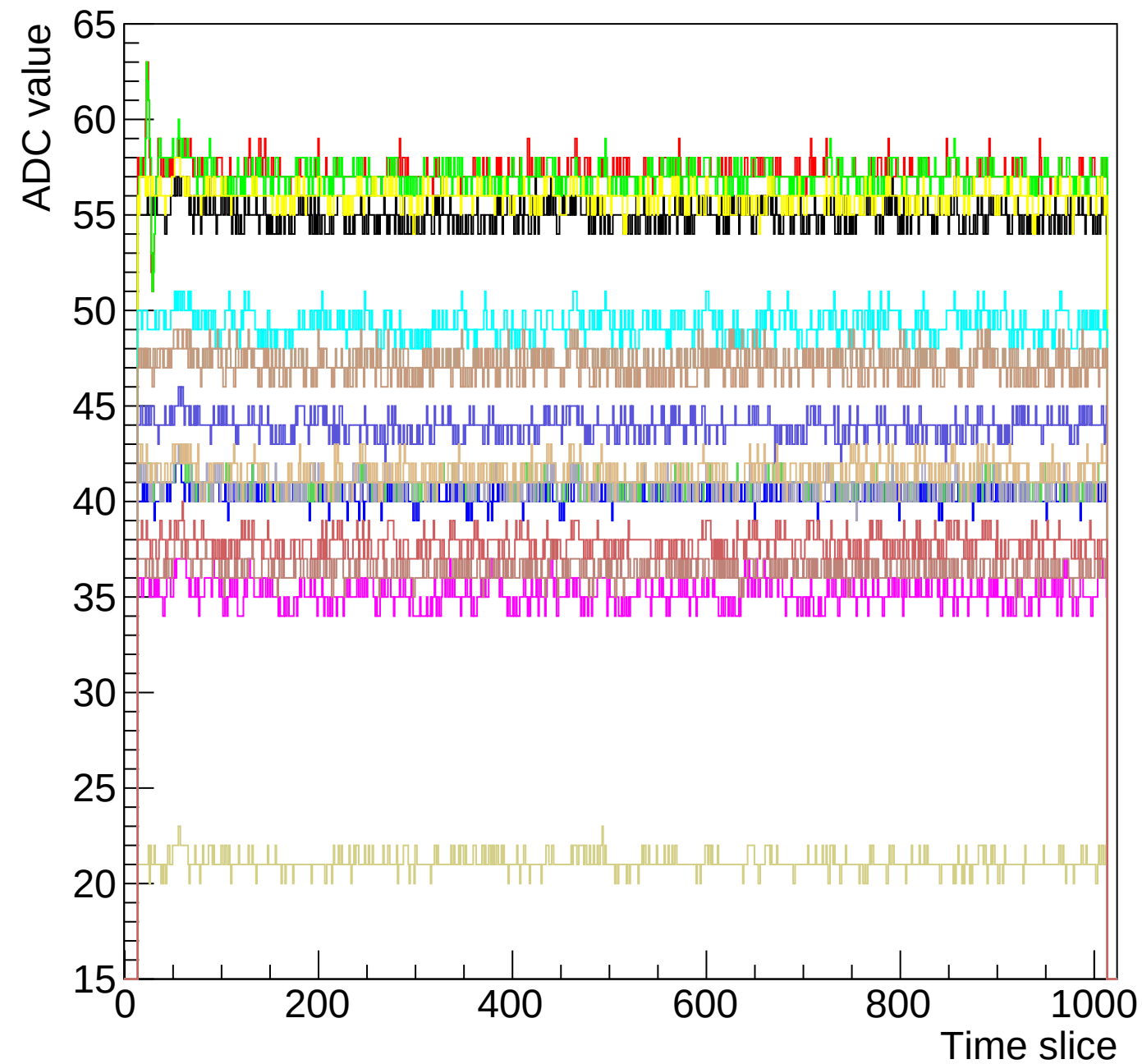
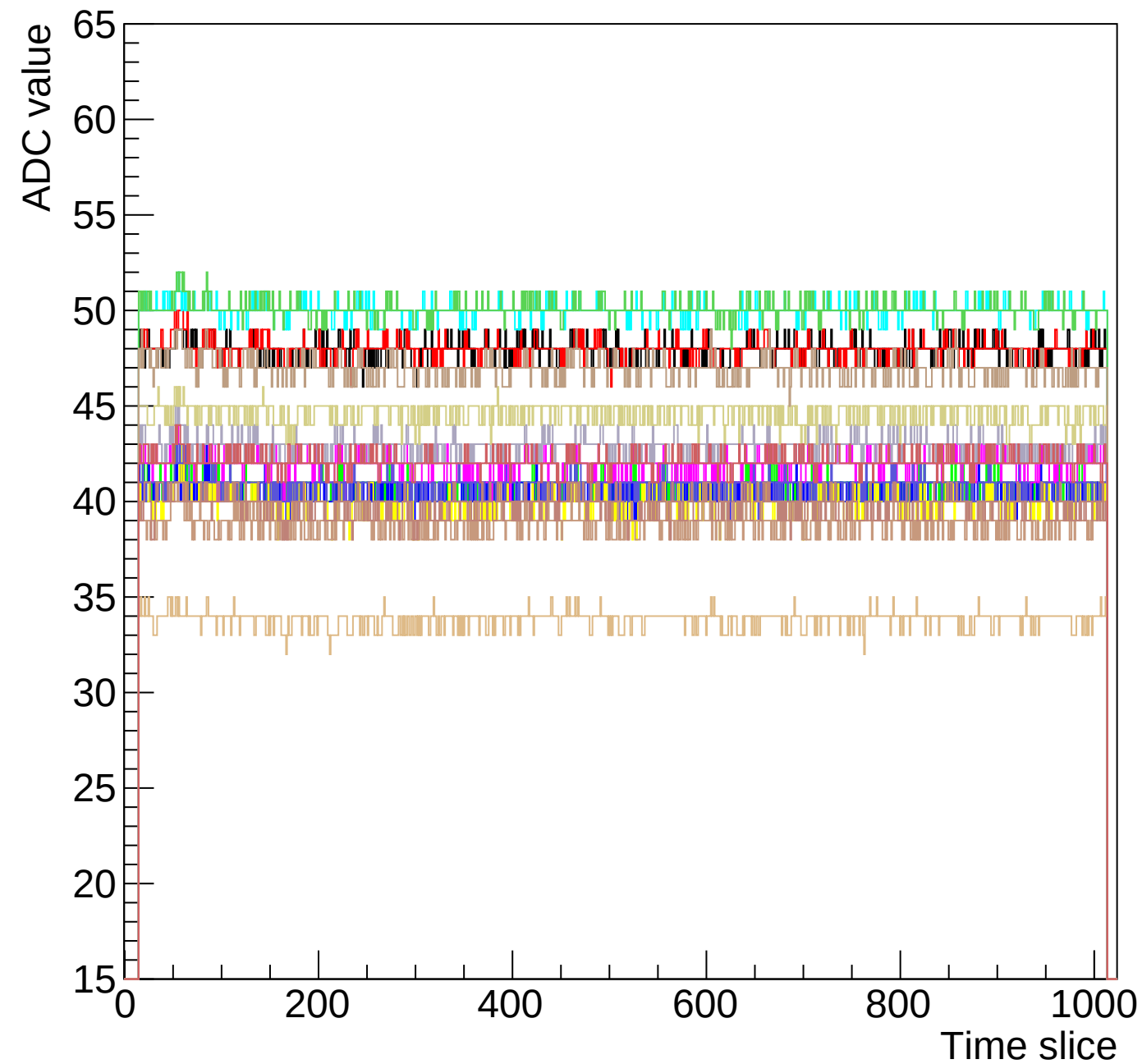


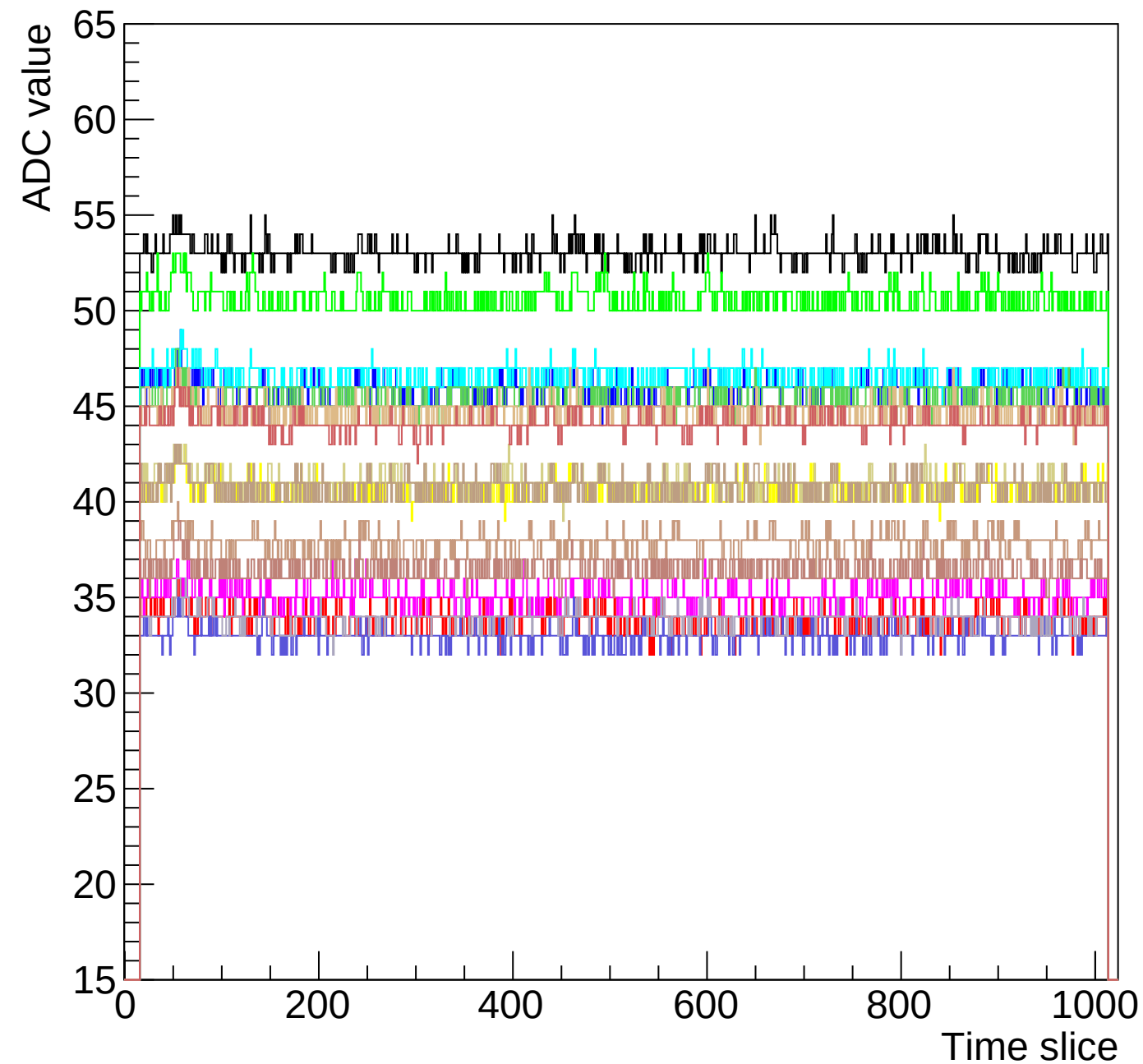
Run 120 event 4 mcm 0 chip 0 58ohm



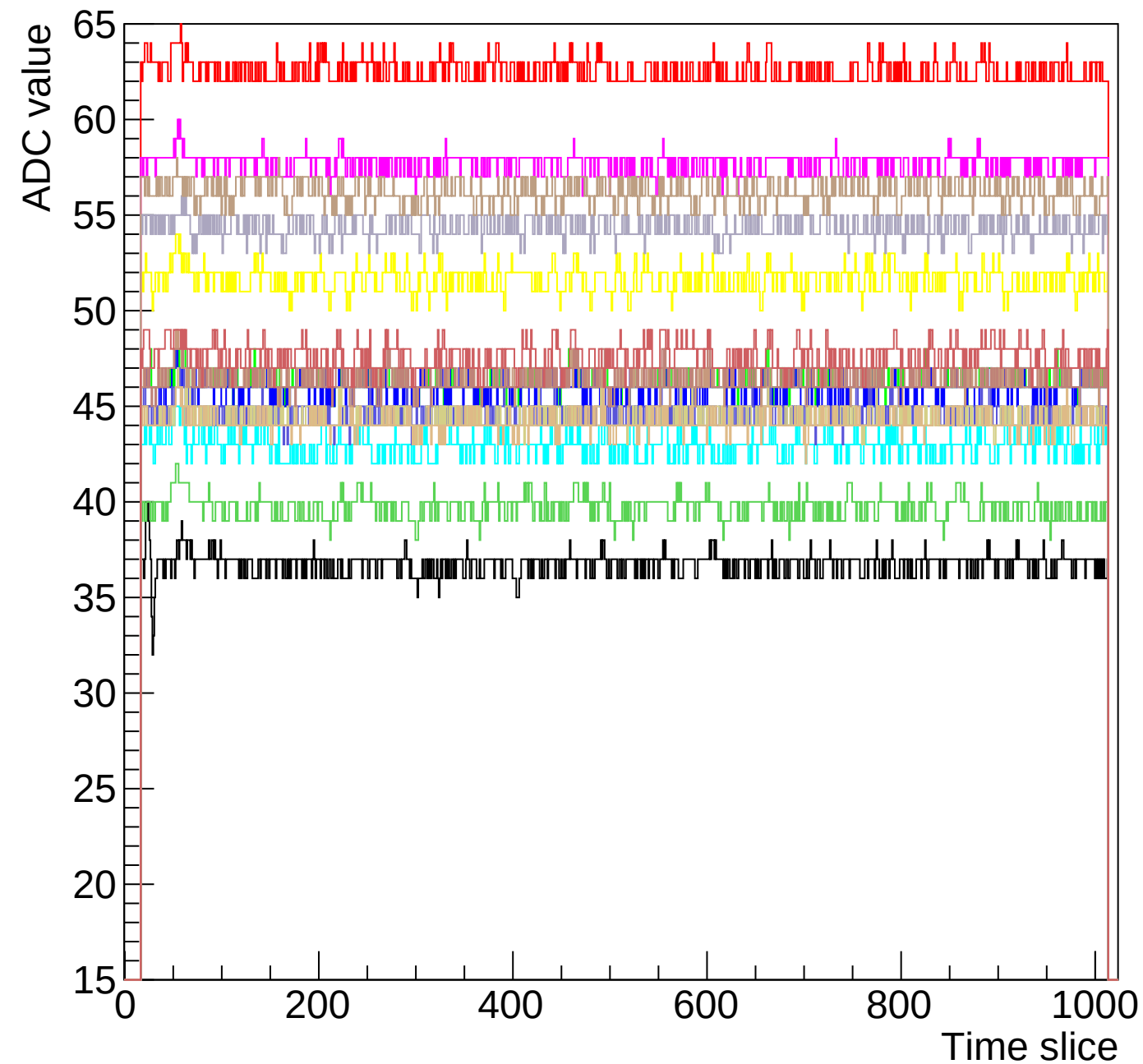
Run 120 event 4 mcm 0 chip 1 58ohm



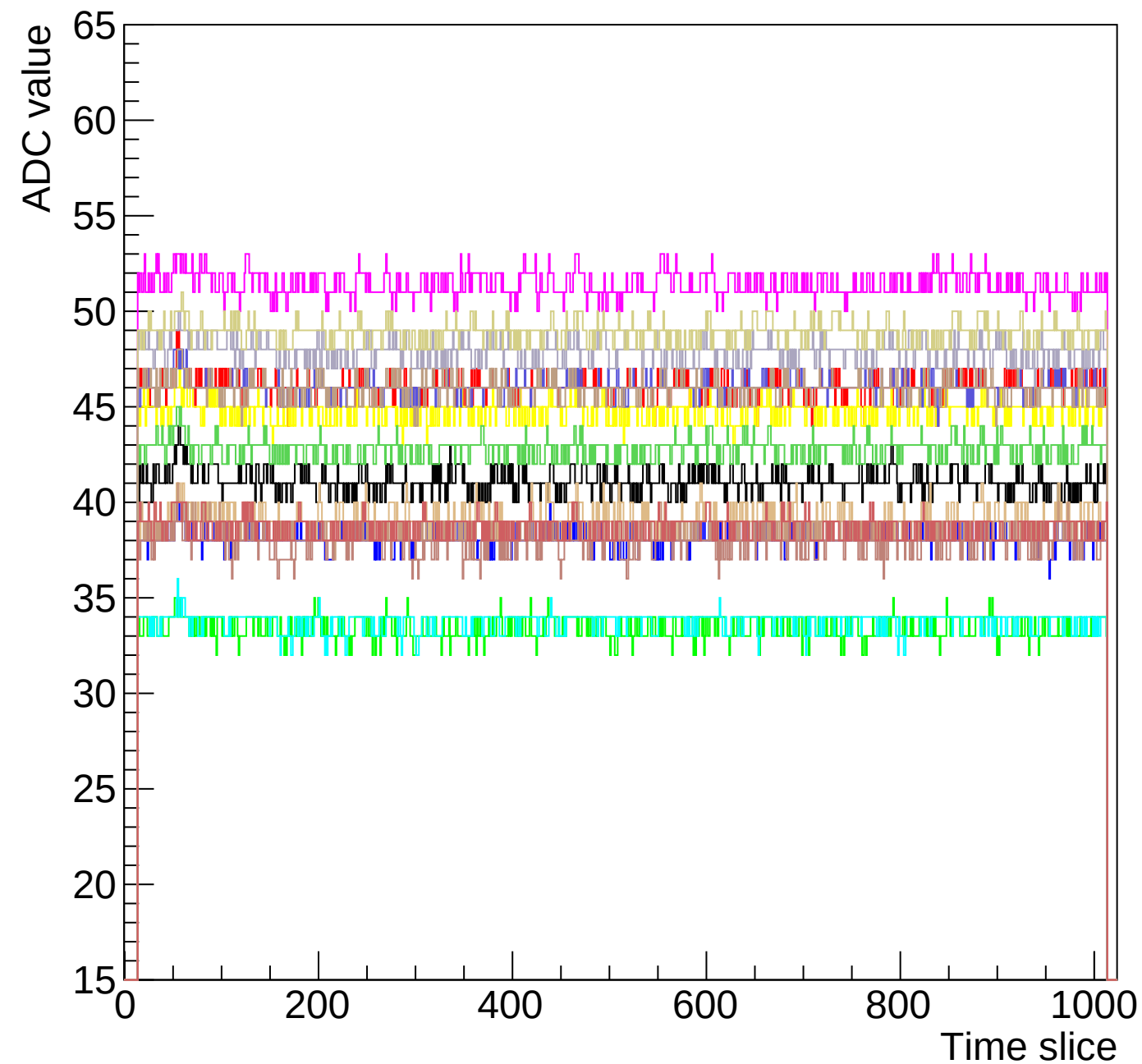
Run 120 event 4 mcm 0 chip 2 58ohm



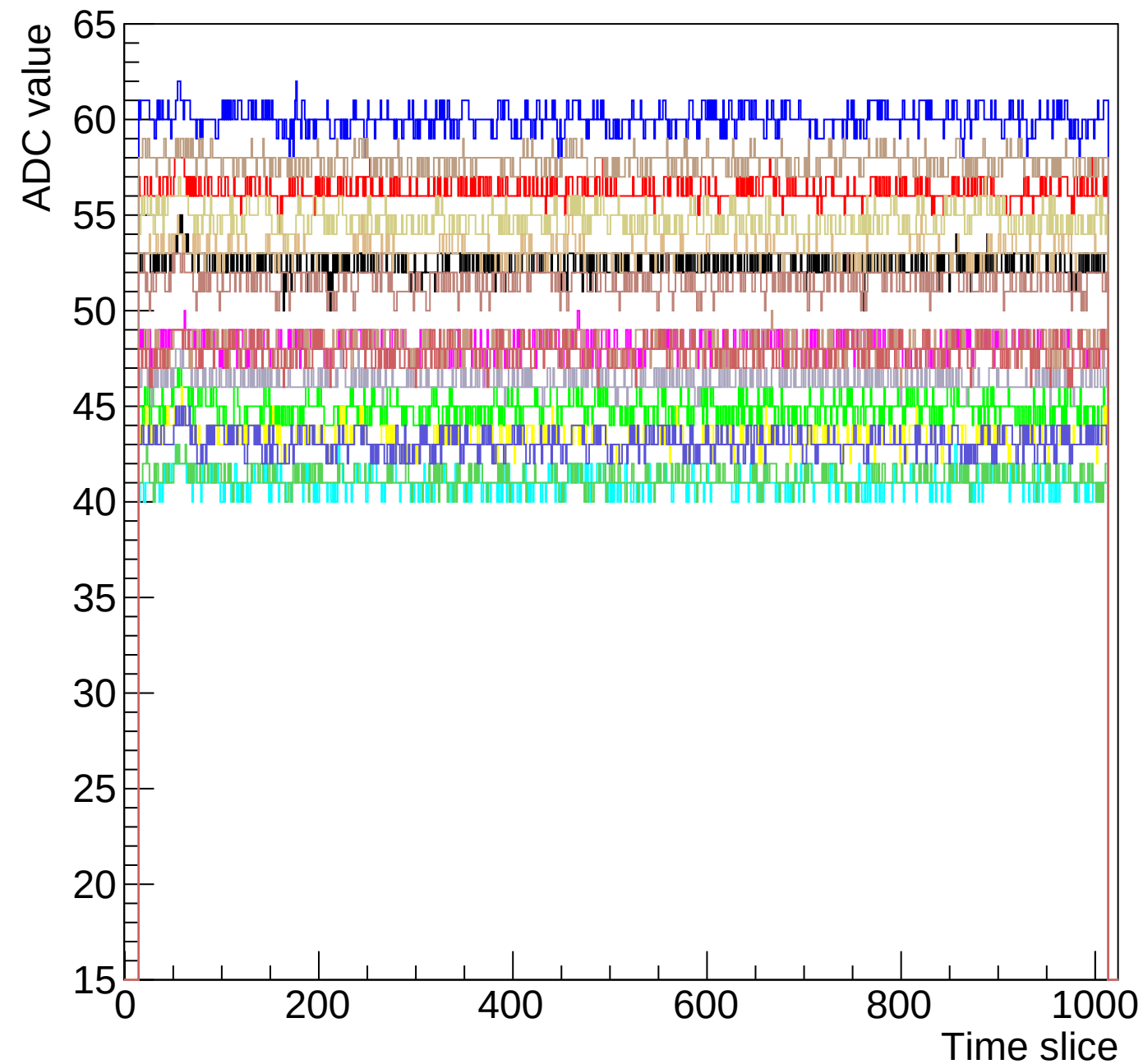
Run 120 event 4 mcm 0 chip 3 58ohm



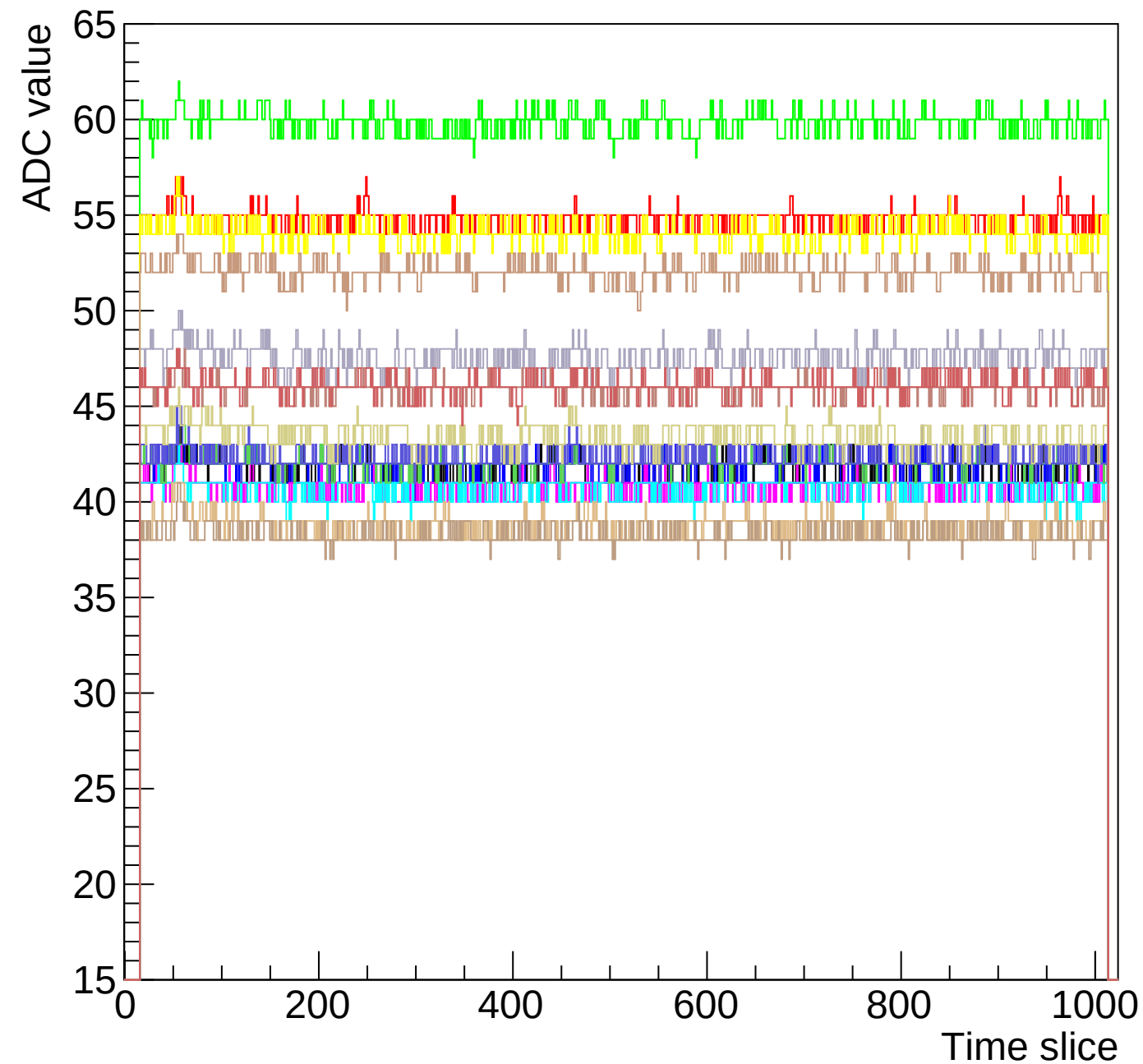
Run 120 event 4 mcm 0 chip 4 58ohm



Run 120 event 4 mcm 0 chip 5 58ohm



Run 120 event 4 mcm 0 chip 6 58ohm



Run 120 event 4 mcm 0 chip 7 58ohm

