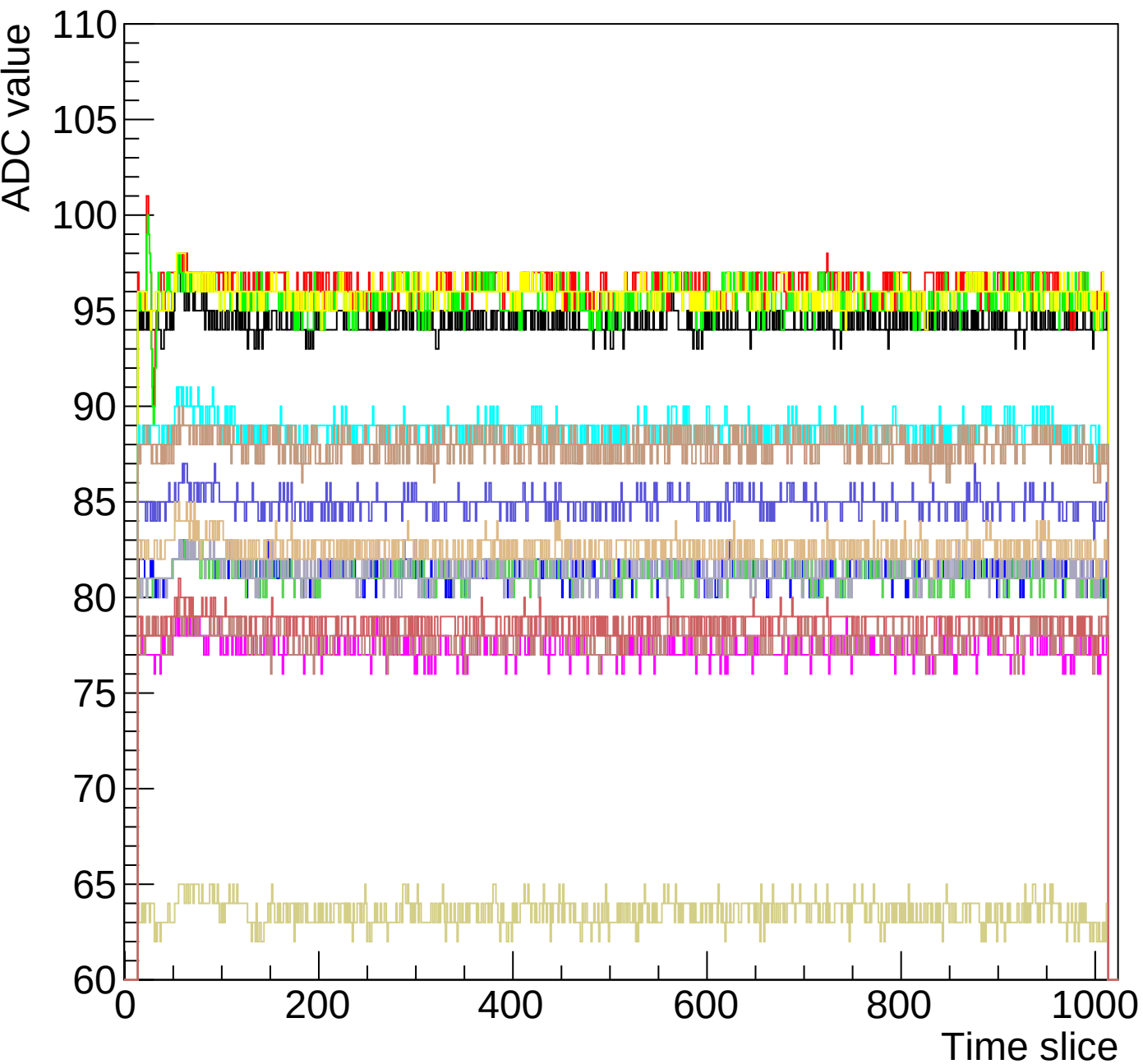
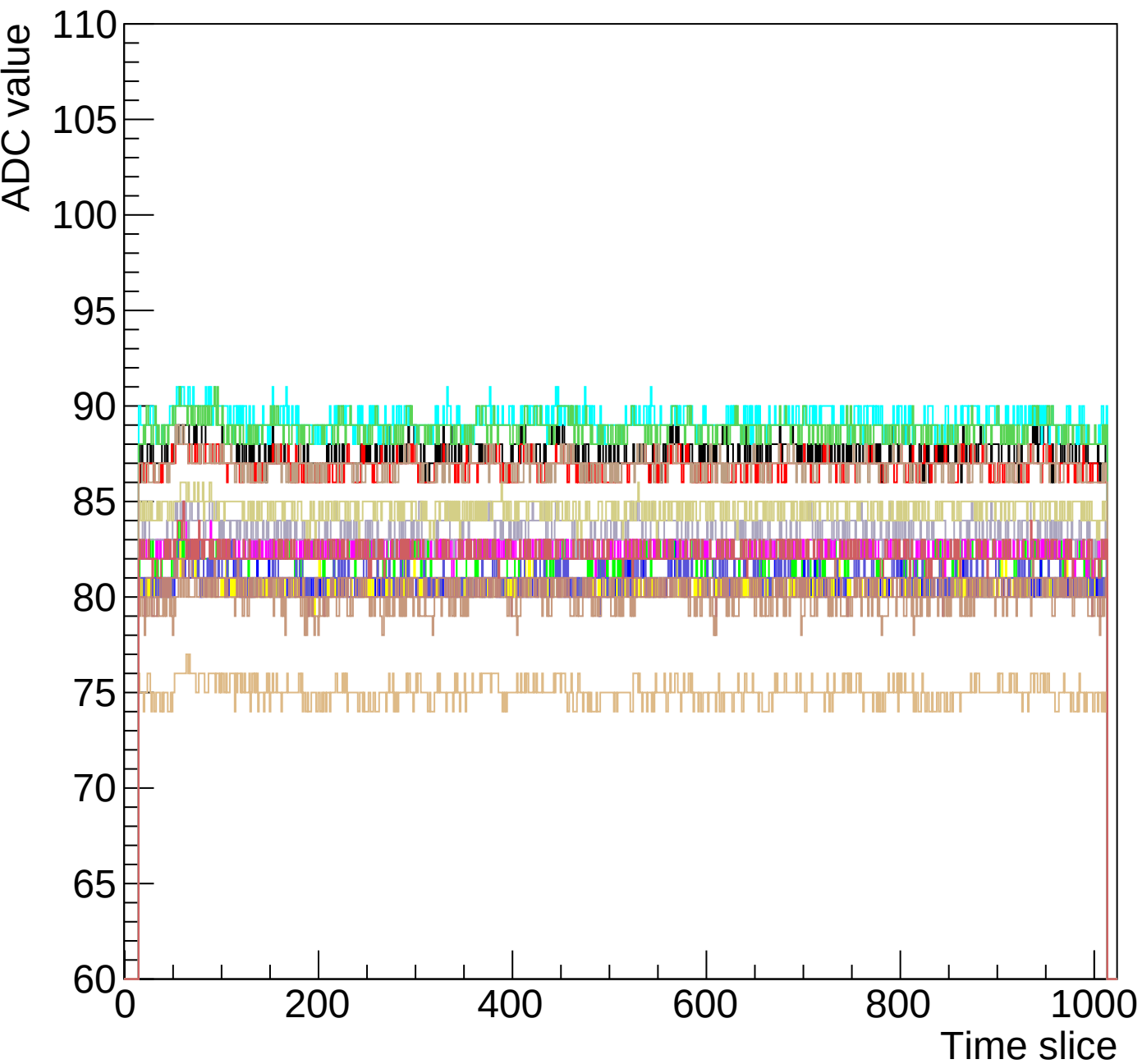


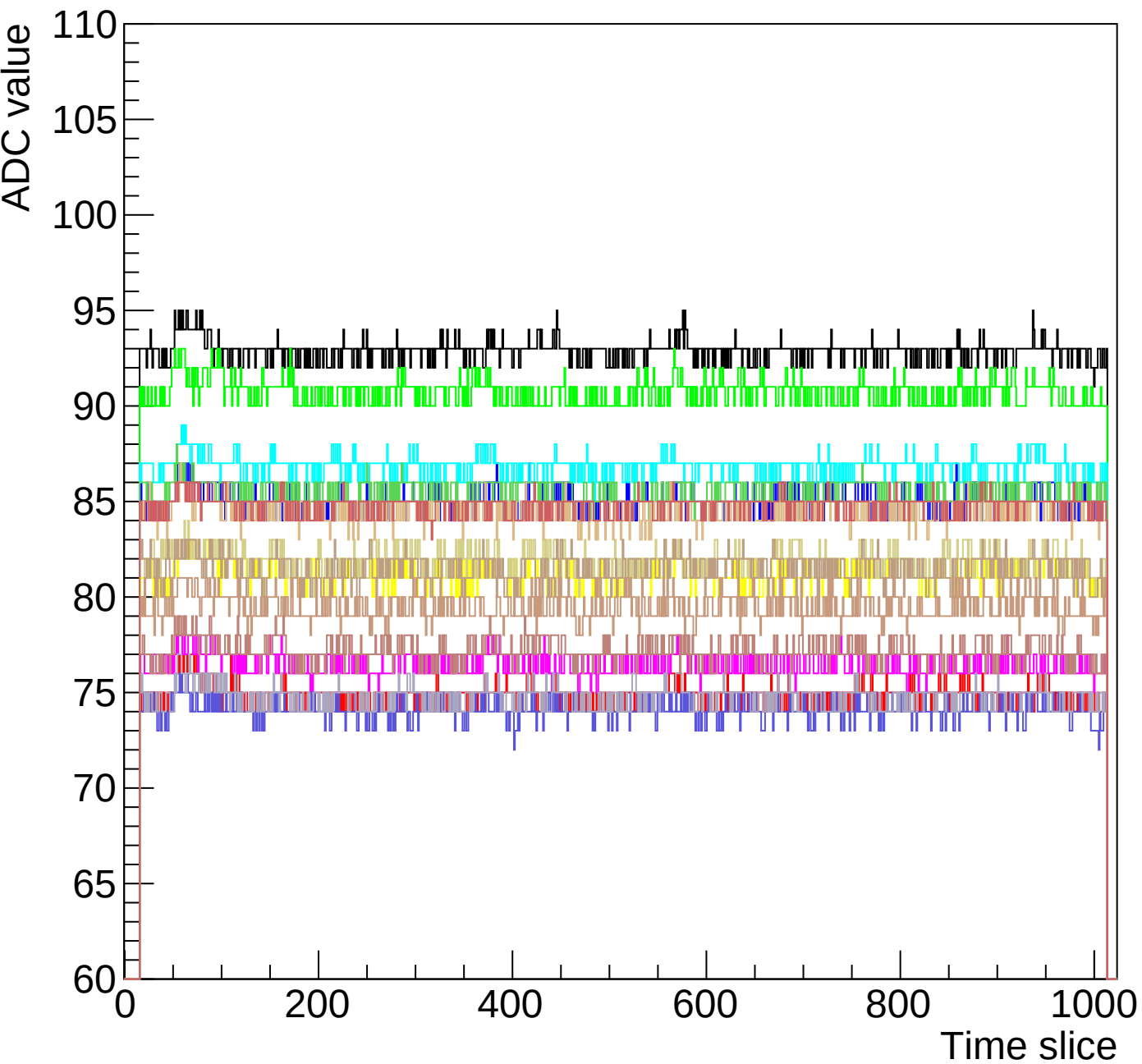
Run 122 event 4 mcm 0 chip 0 18ohm



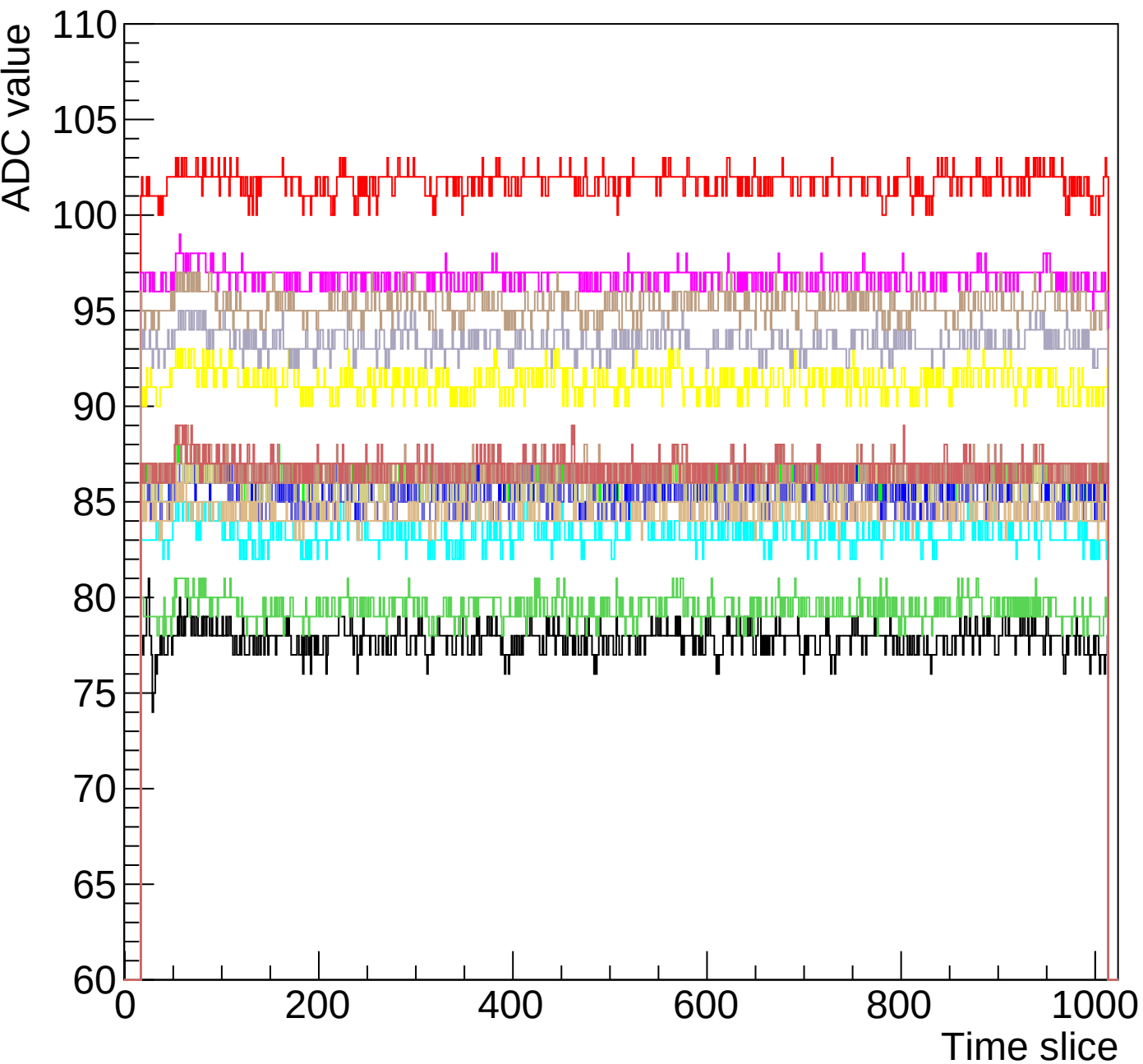
Run 122 event 4 mcm 0 chip 1 18ohm



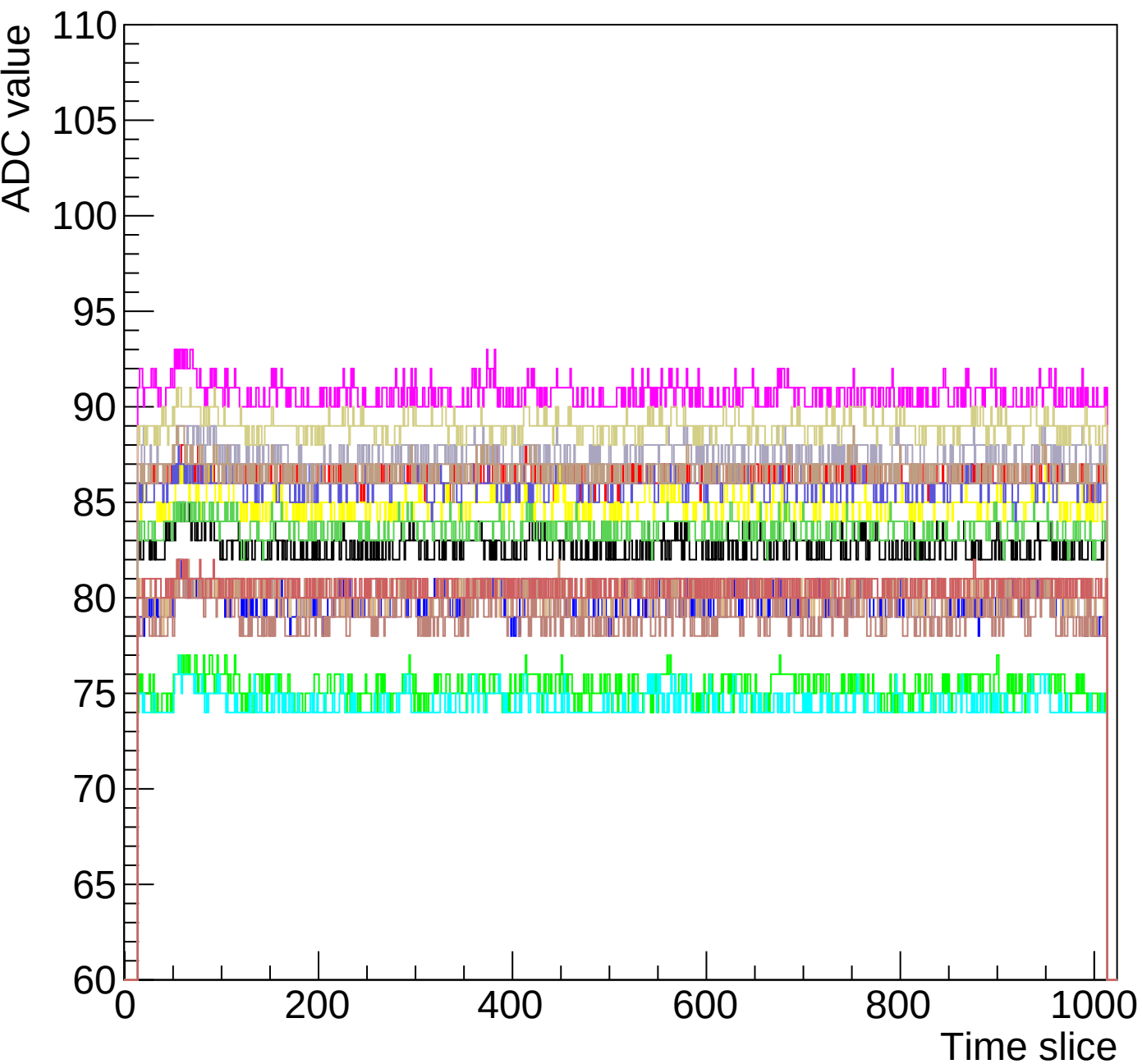
Run 122 event 4 mcm 0 chip 2 18ohm



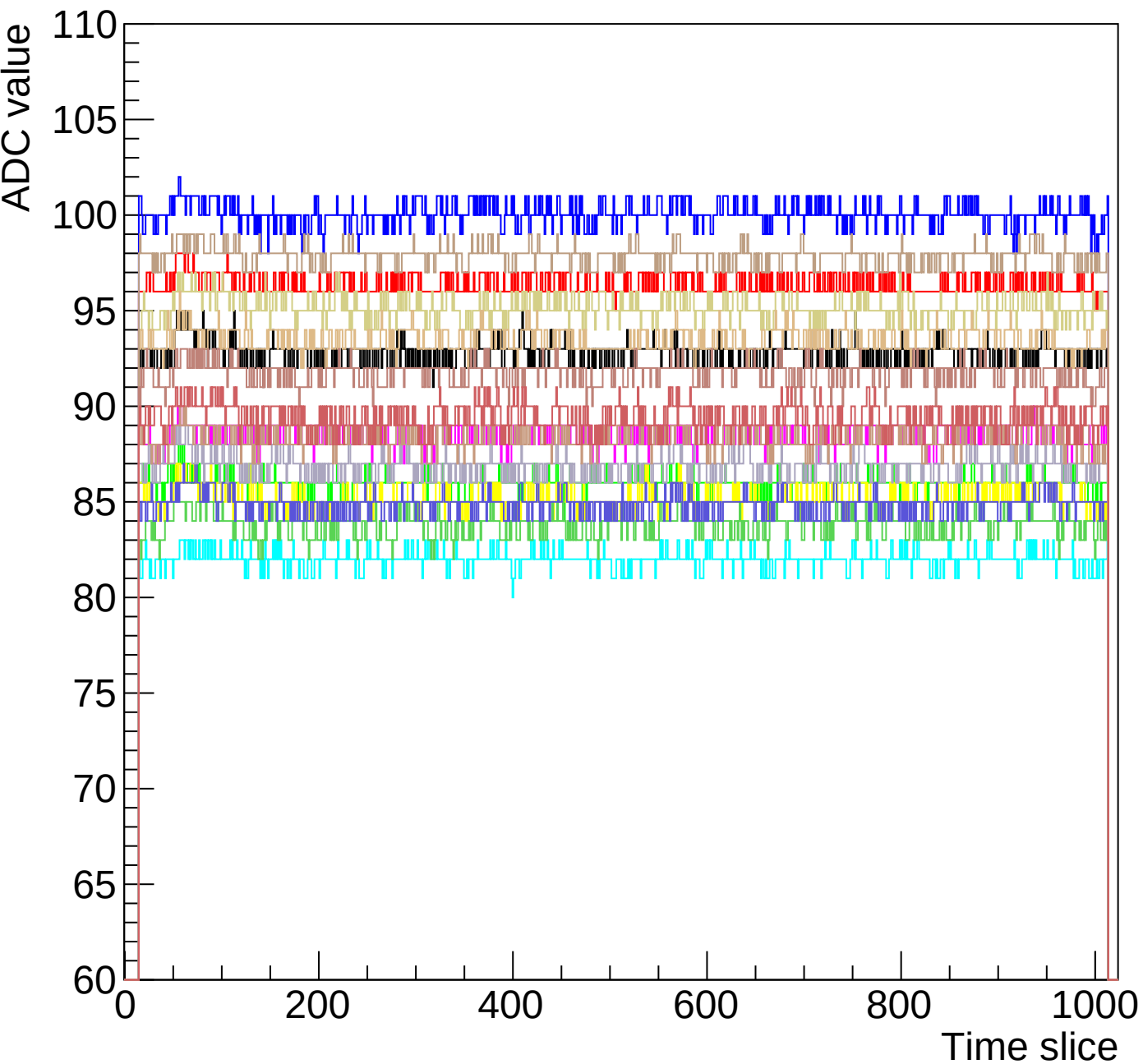
Run 122 event 4 mcm 0 chip 3 18ohm



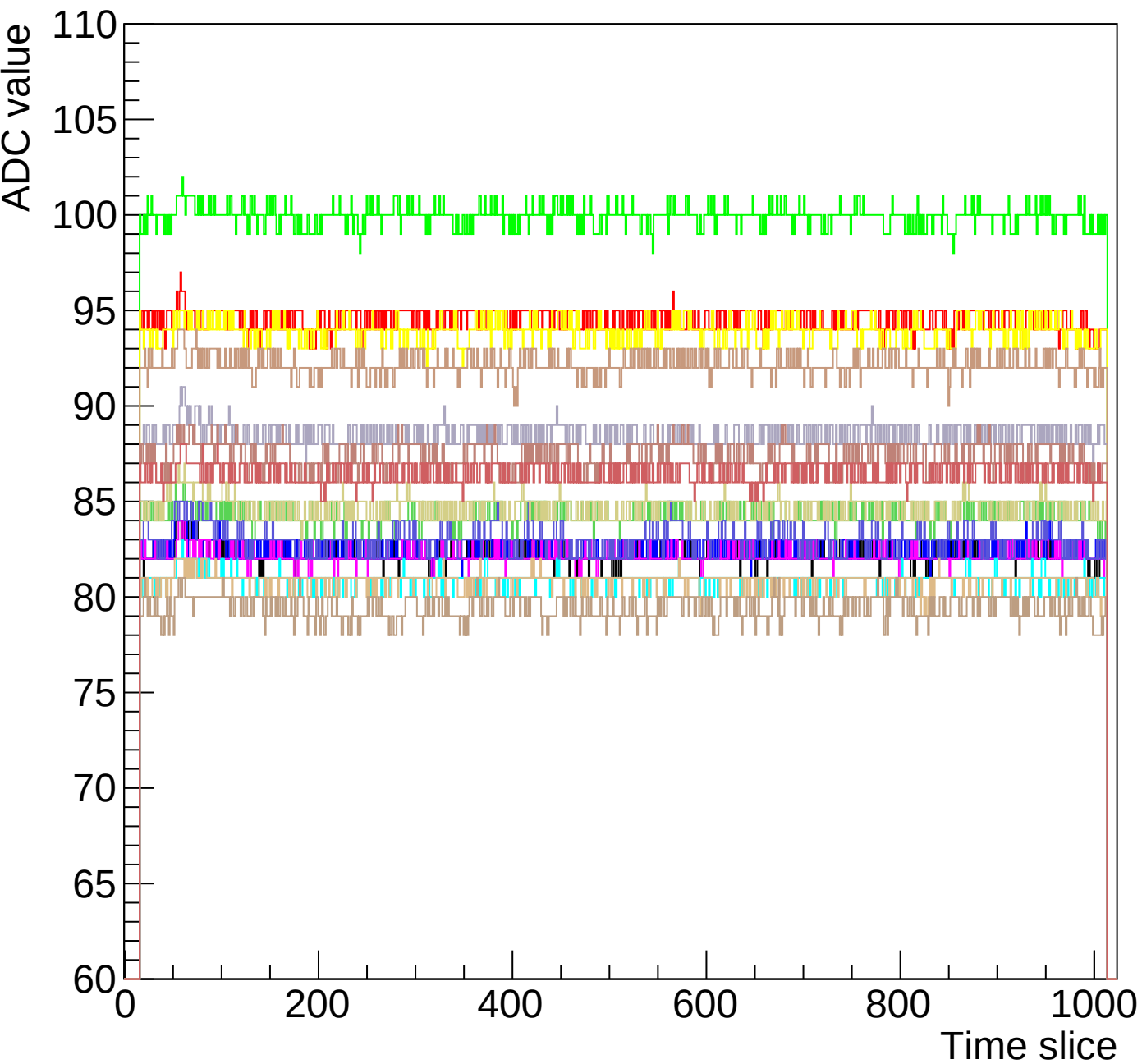
Run 122 event 4 mcm 0 chip 4 18ohm



Run 122 event 4 mcm 0 chip 5 18ohm



Run 122 event 4 mcm 0 chip 6 18ohm



Run 122 event 4 mcm 0 chip 7 18ohm

