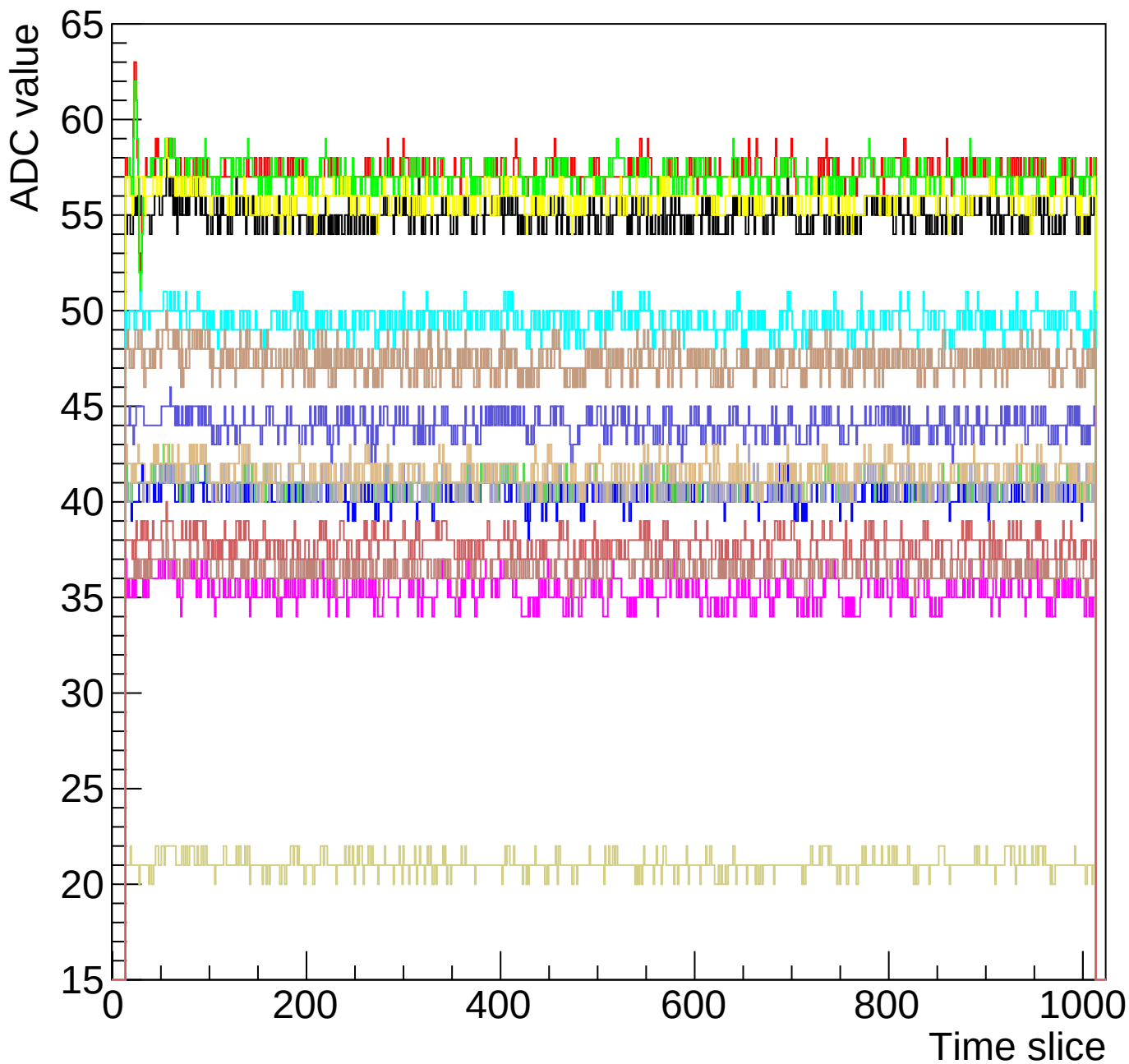
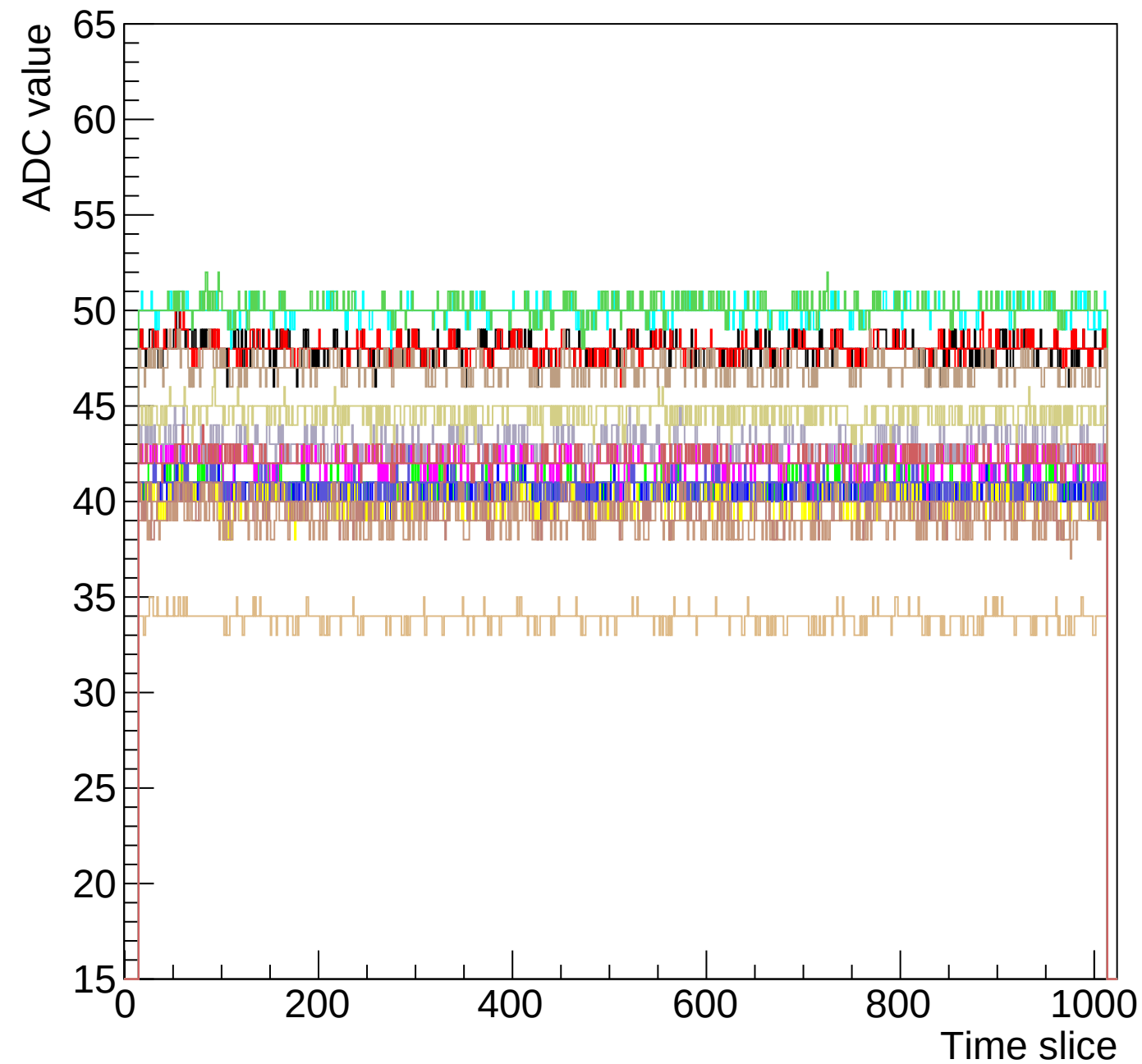


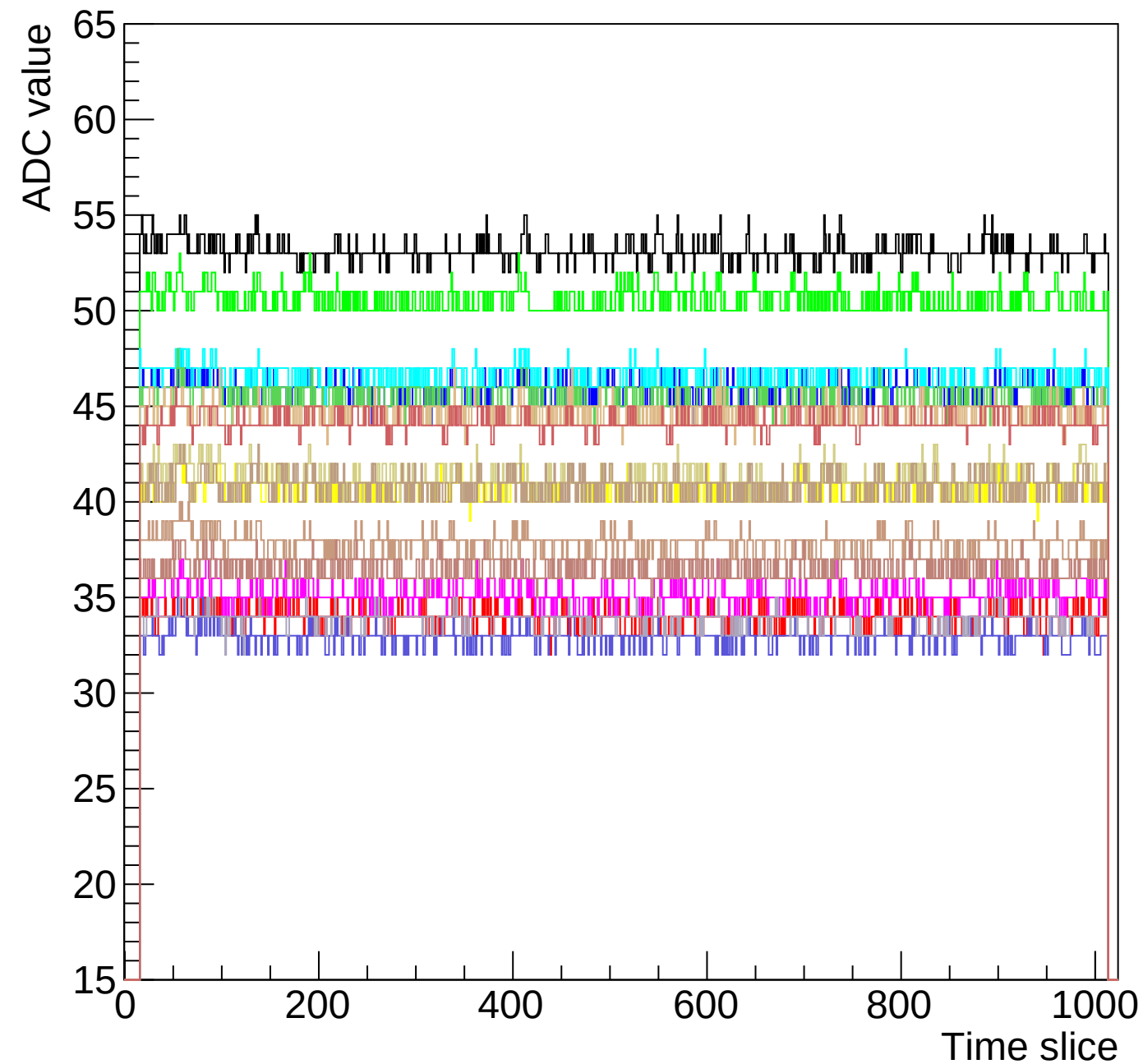
Run 120 event 5 mcm 0 chip 0 58ohm



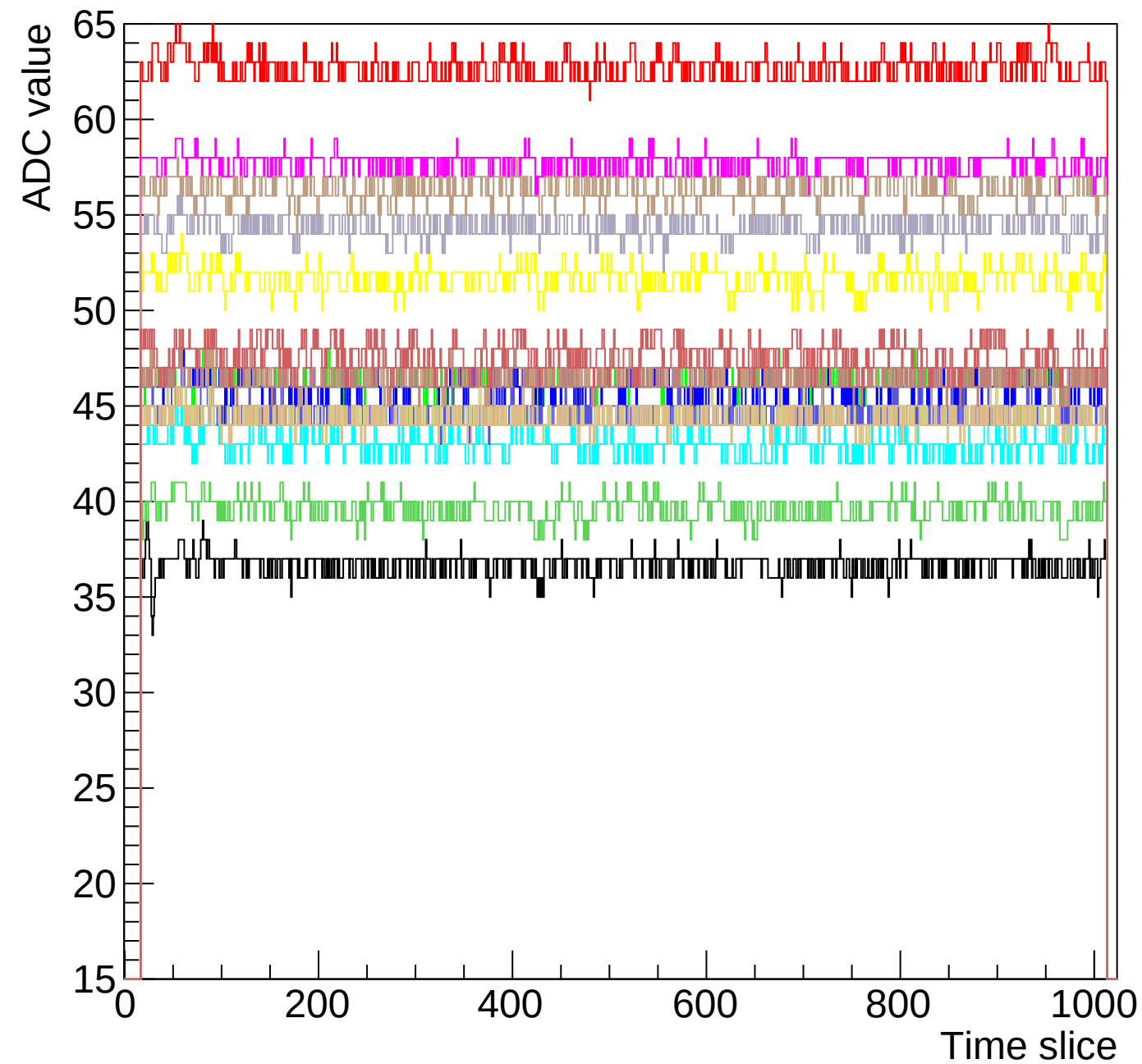
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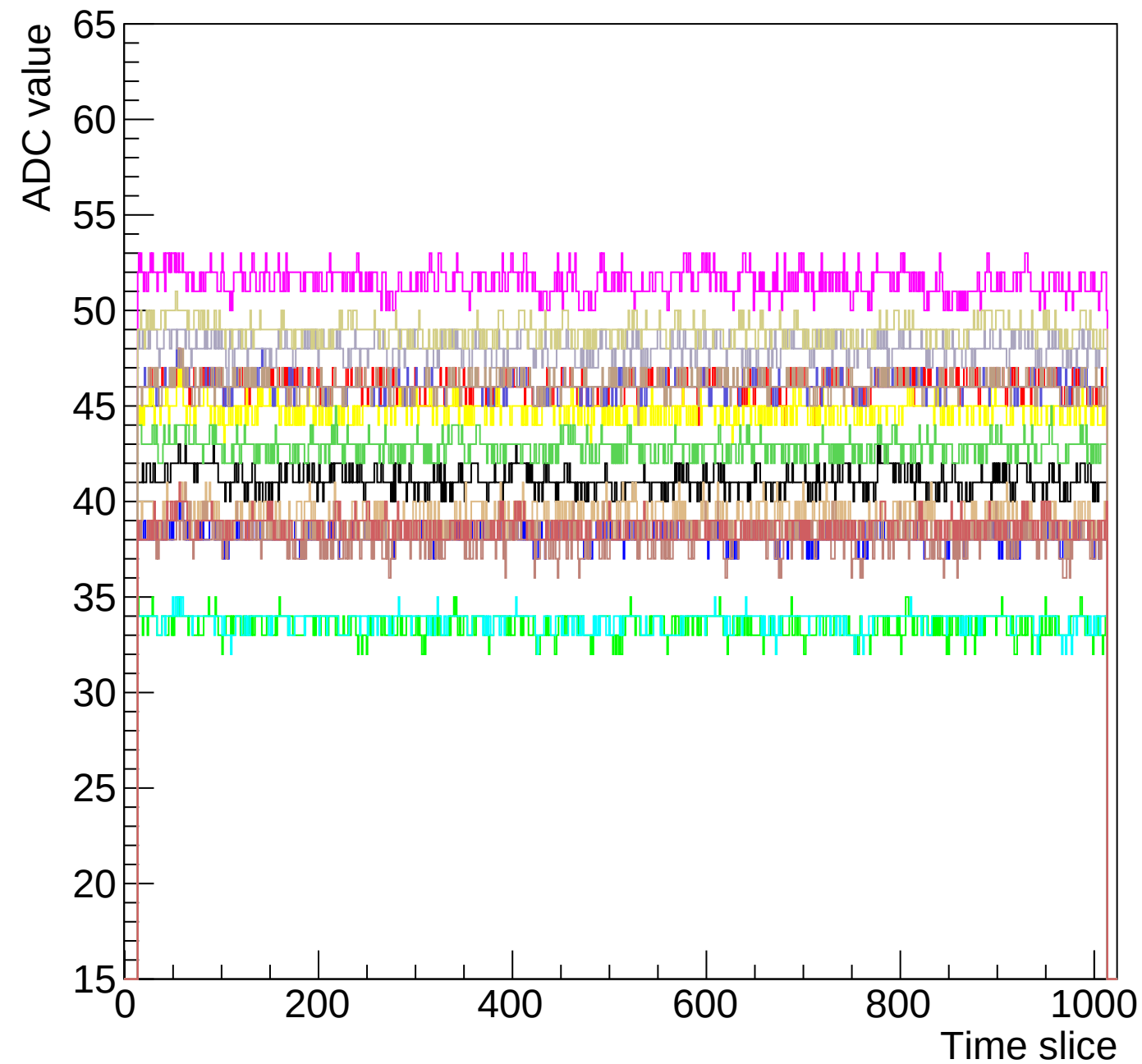
Run 120 event 5 mcm 0 chip 2 58ohm



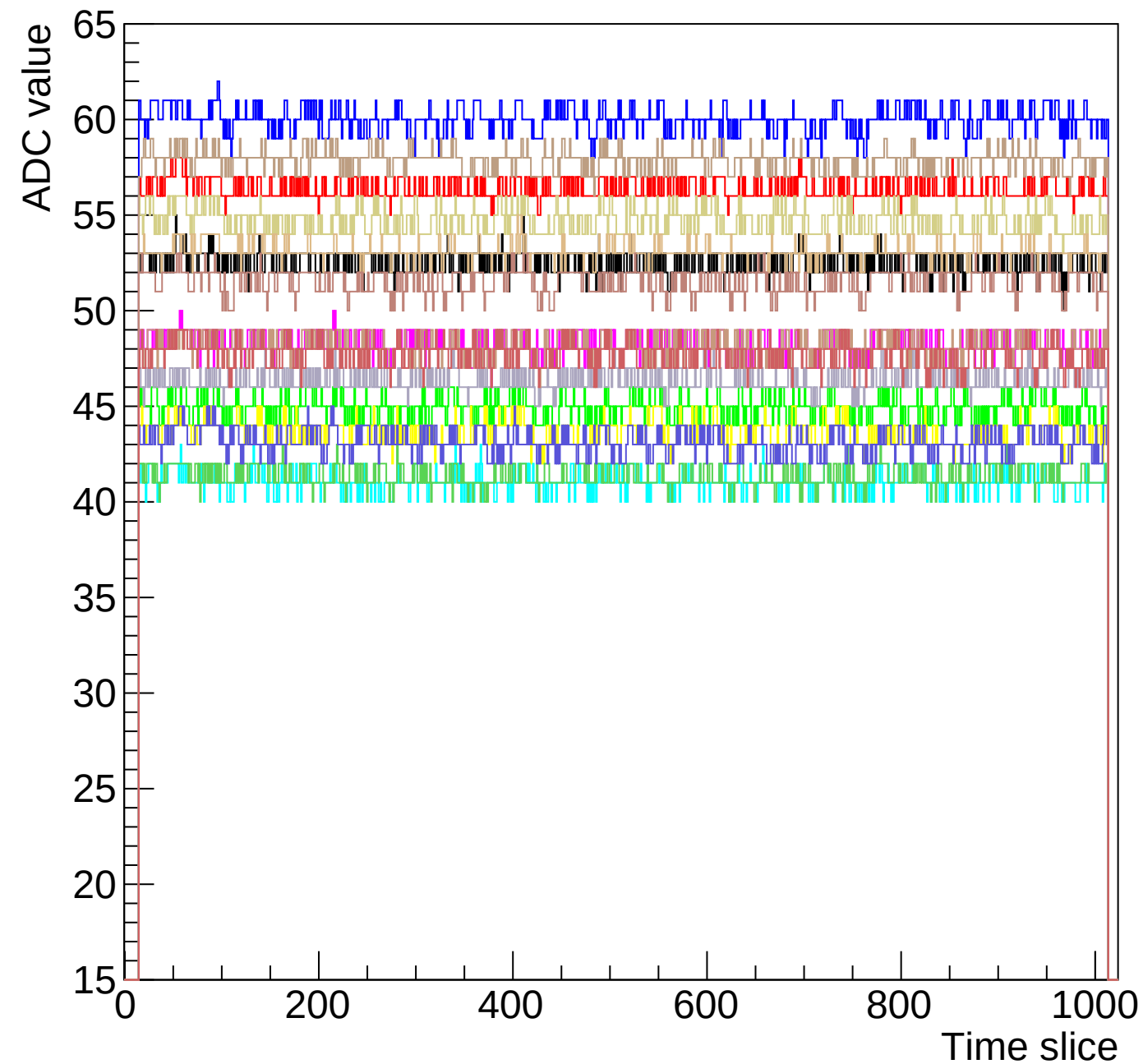
Run 120 event 5 mcm 0 chip 3 58ohm



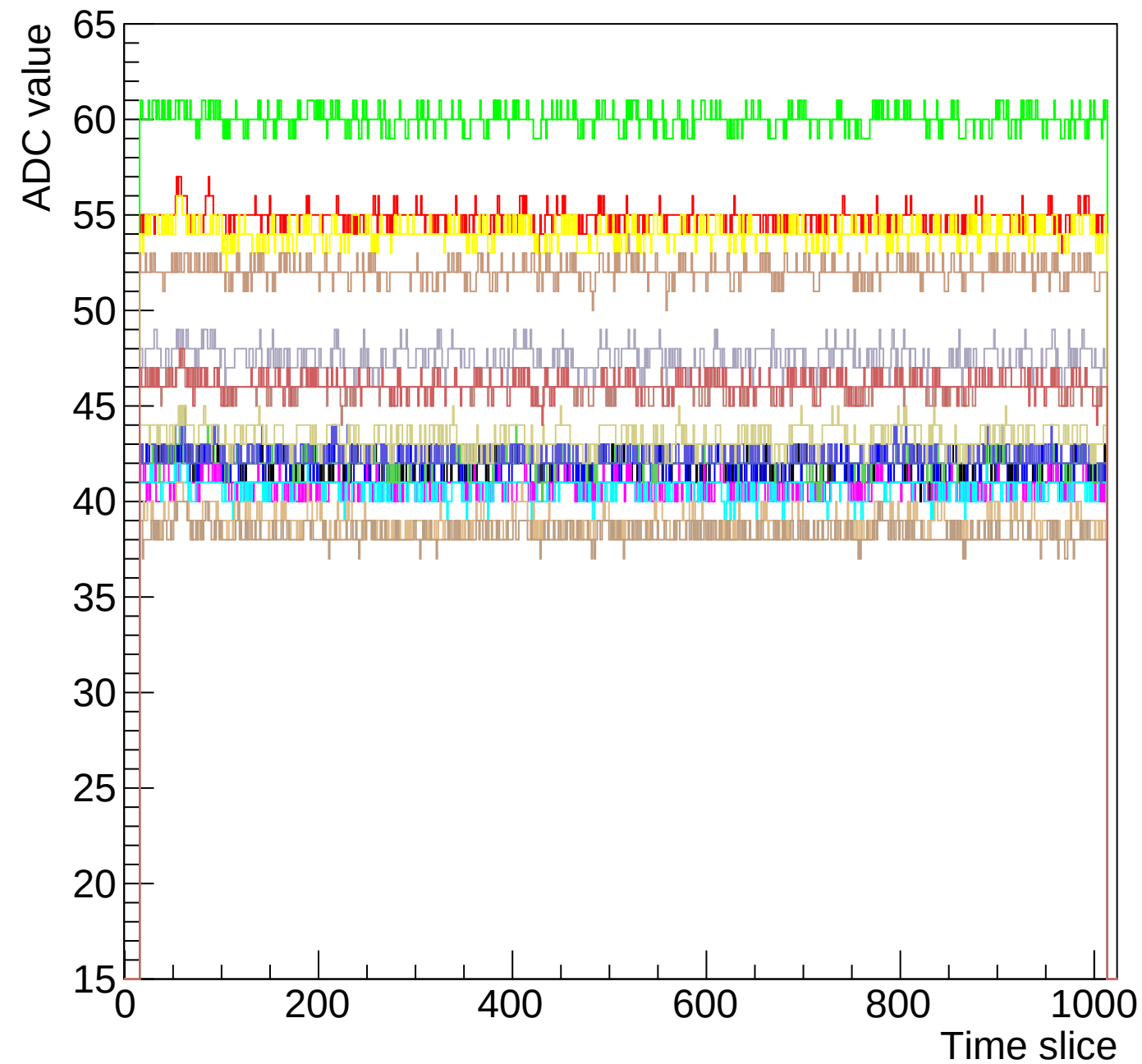
Run 120 event 5 mcm 0 chip 4 58ohm



Run 120 event 5 mcm 0 chip 5 58ohm



Run 120 event 5 mcm 0 chip 6 58ohm



Run 120 event 5 mcm 0 chip 7 58ohm

