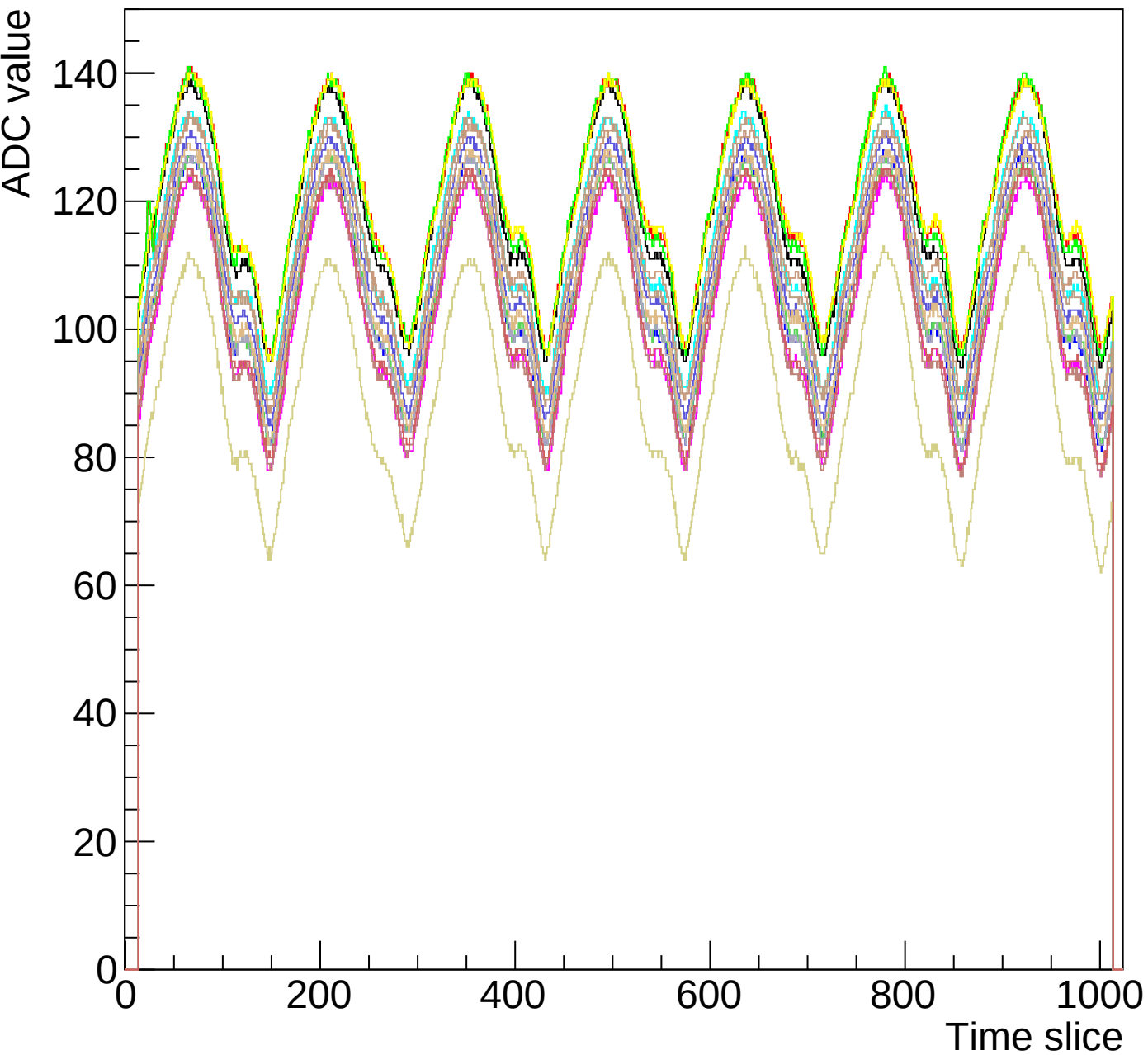
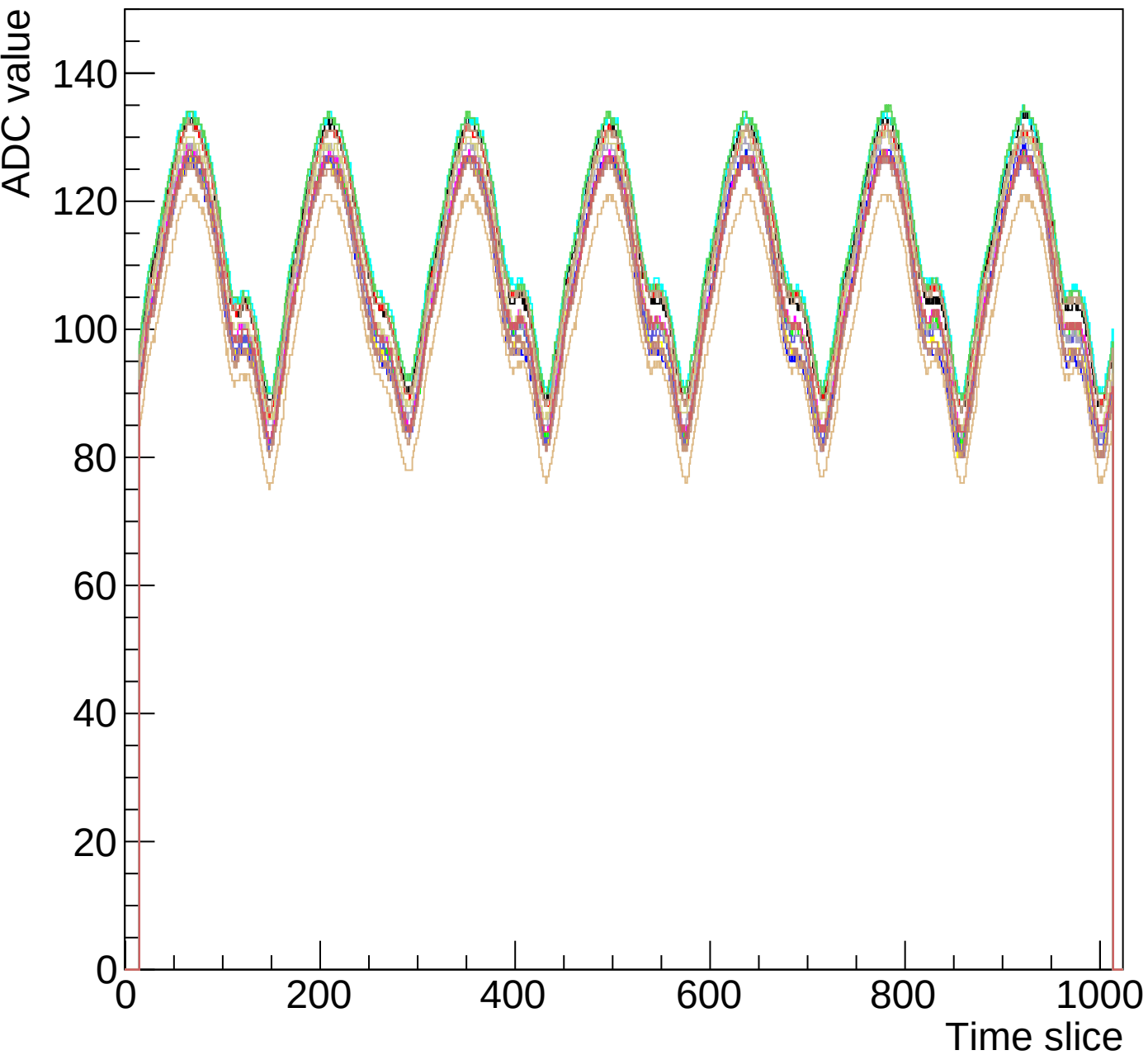


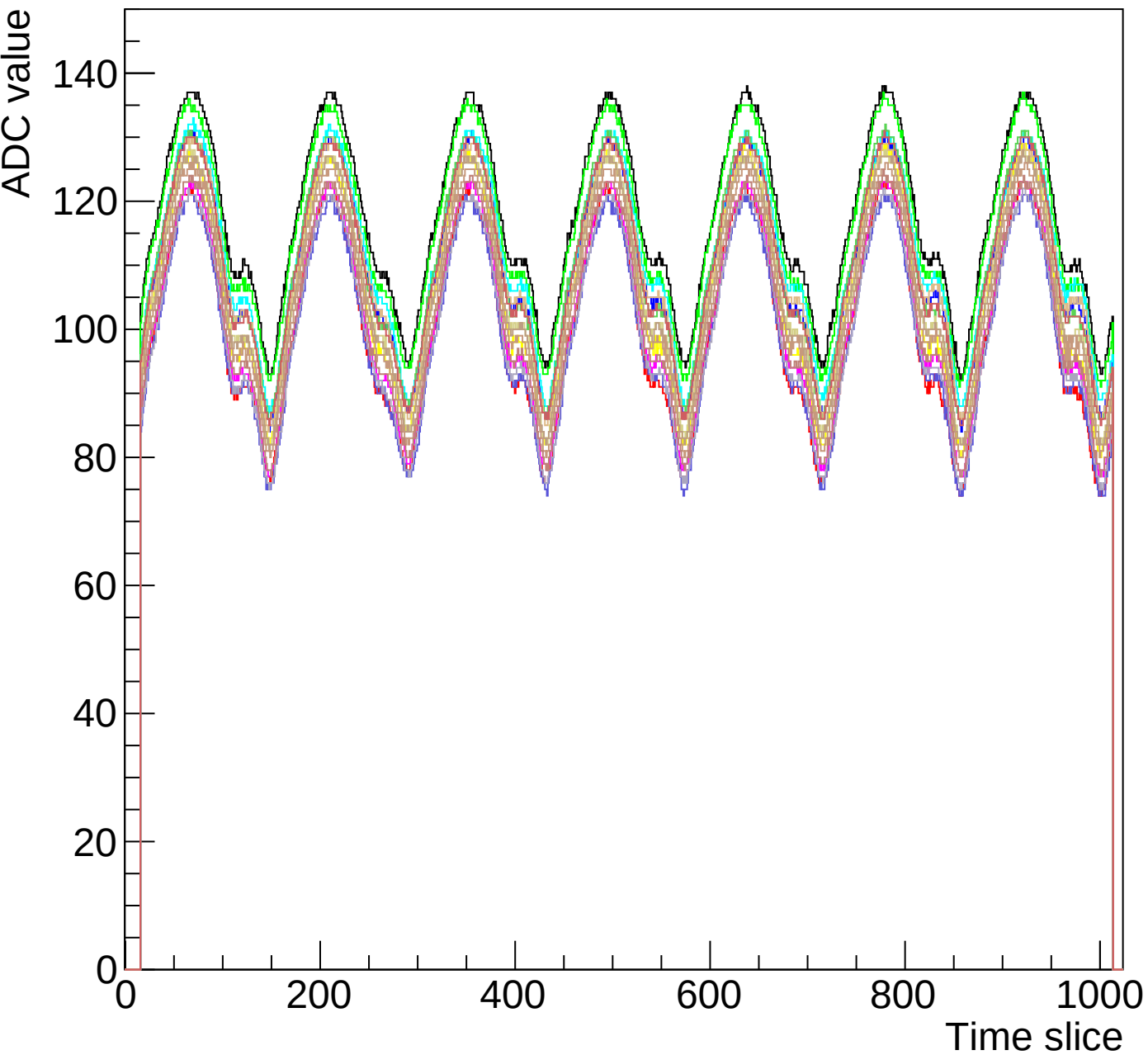
Run 123 event 6 mcm 0 chip 0 0ohm



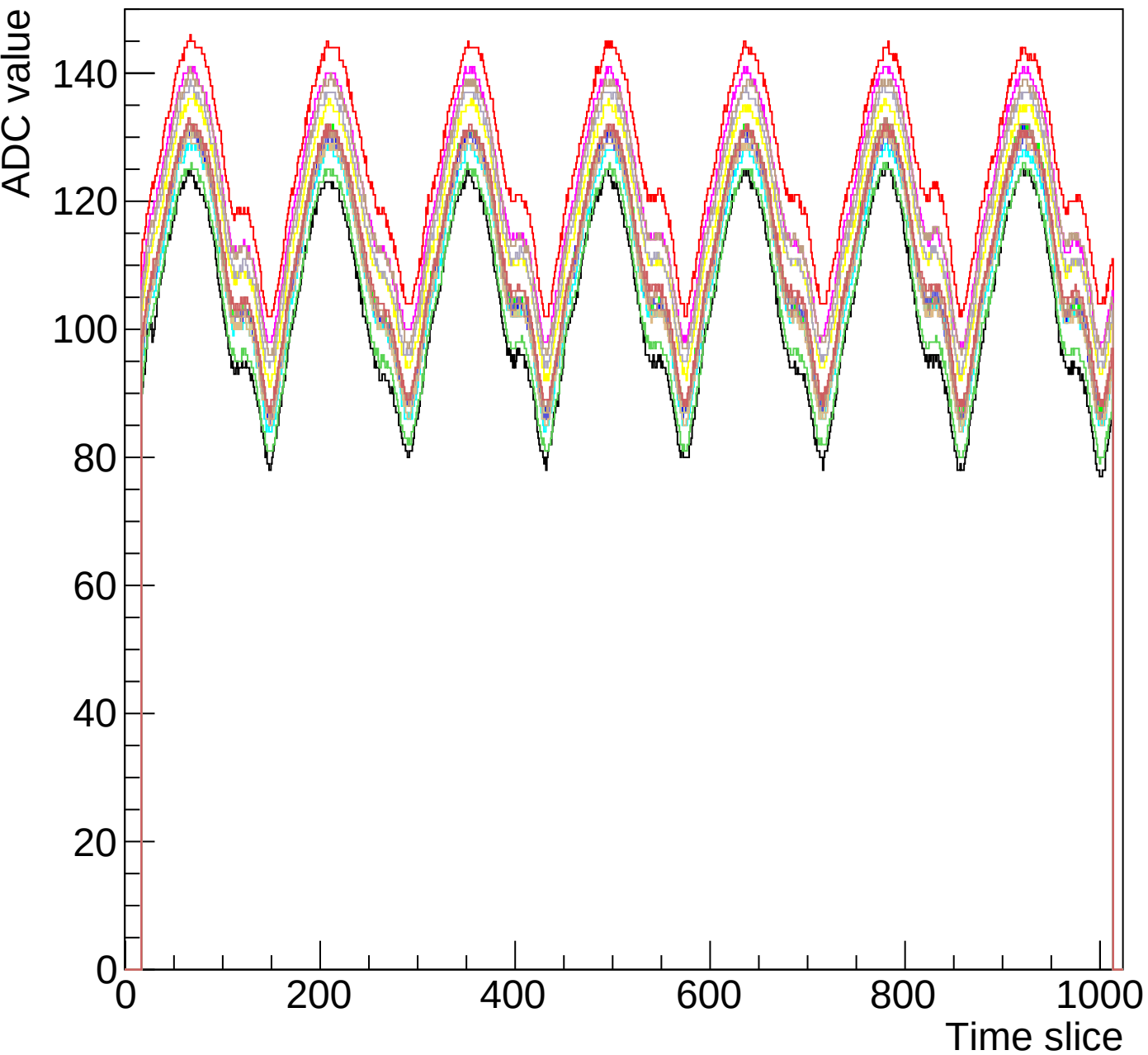
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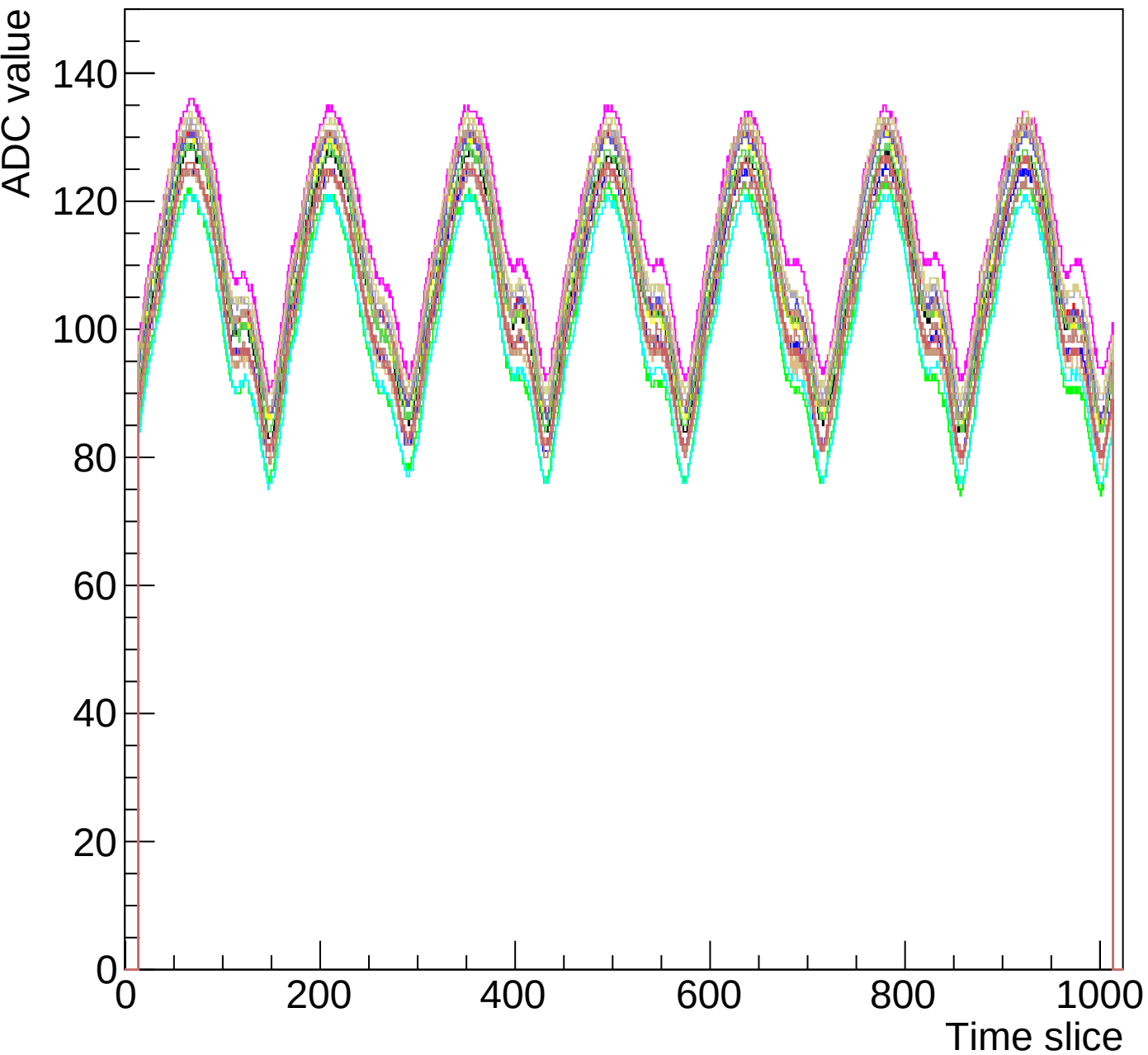
Run 123 event 6 mcm 0 chip 2 0ohm



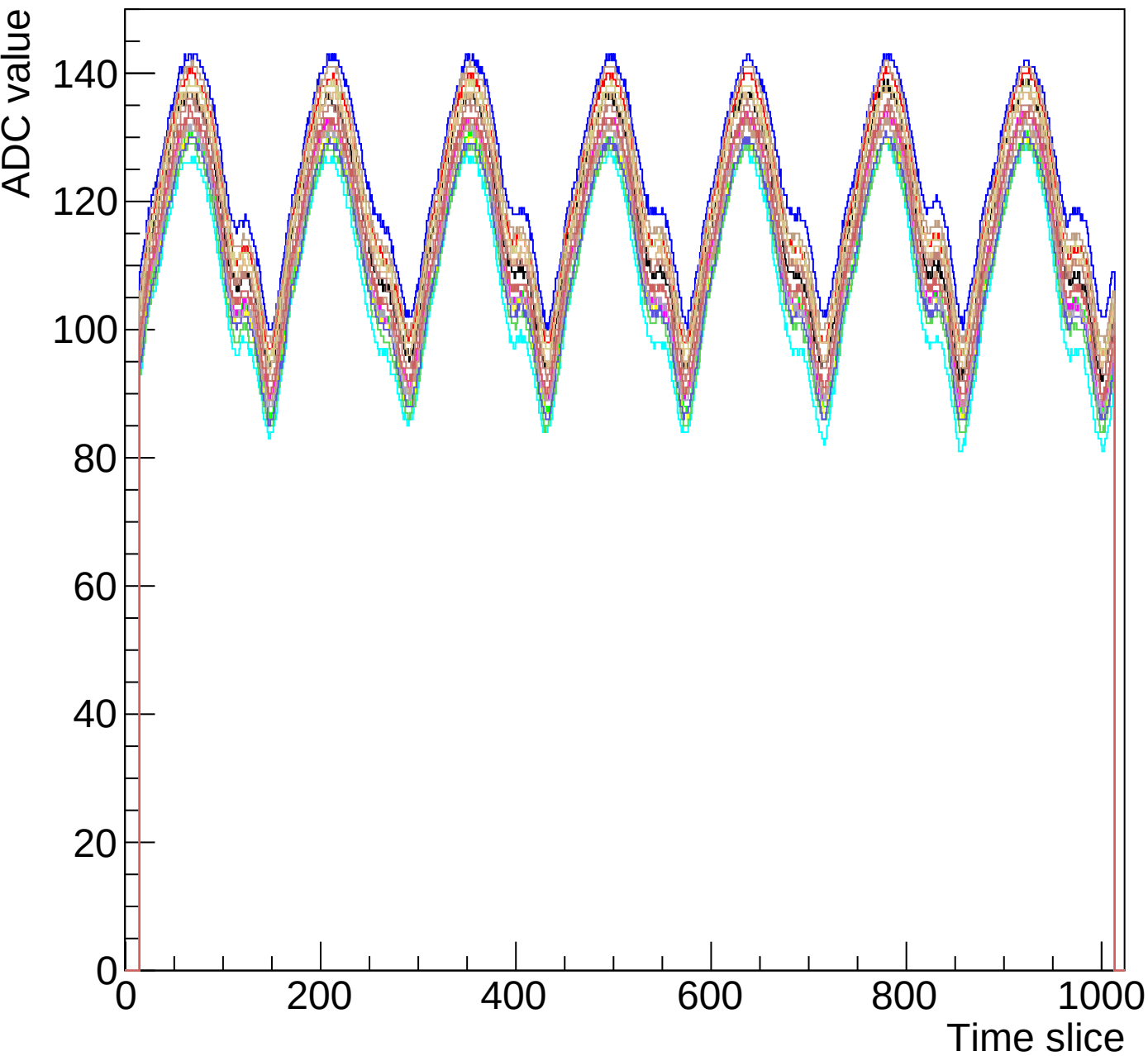
Run 123 event 6 mcm 0 chip 3 0ohm



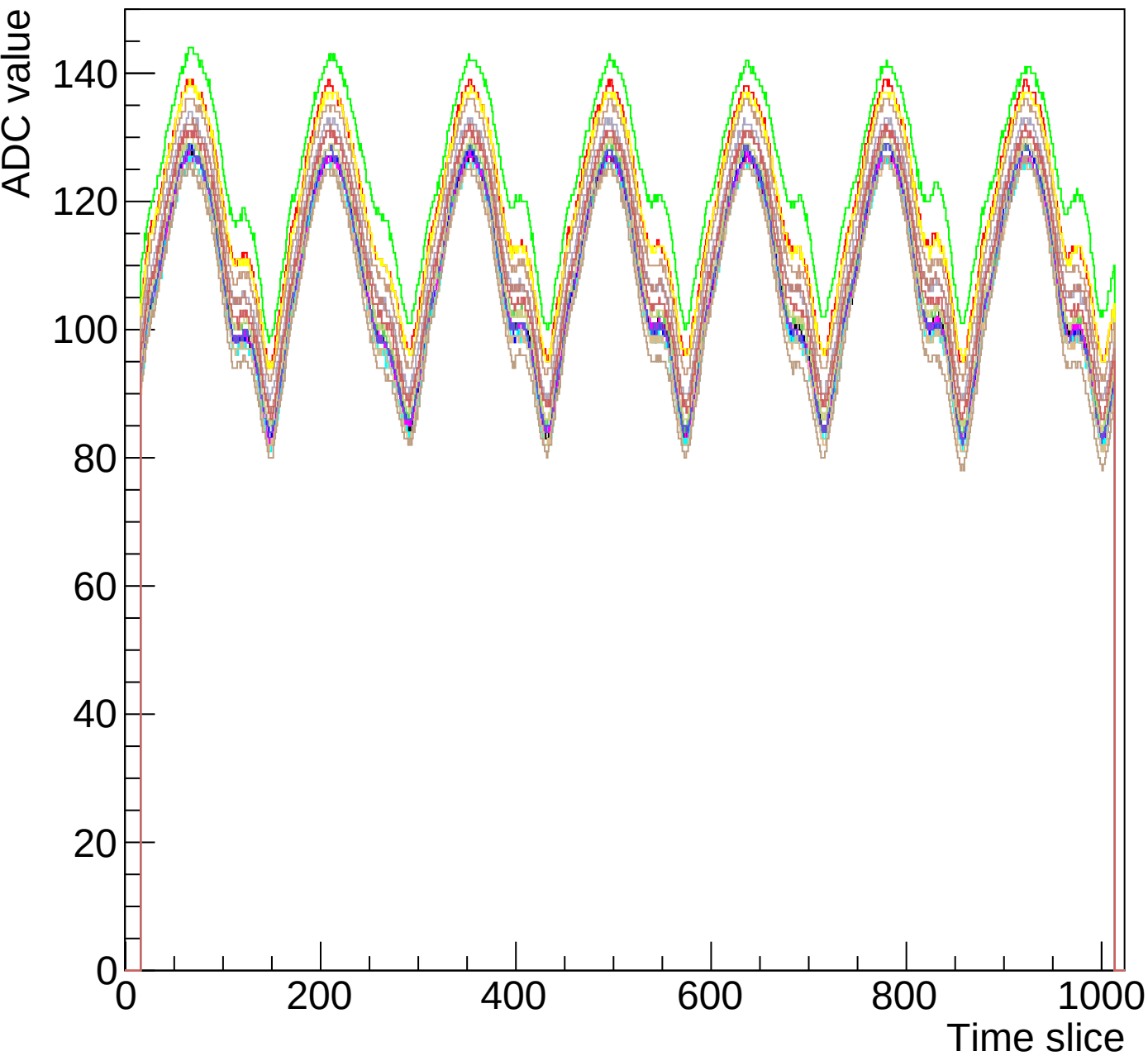
Run 123 event 6 mcm 0 chip 4 0ohm



Run 123 event 6 mcm 0 chip 5 0ohm



Run 123 event 6 mcm 0 chip 6 0ohm



Run 123 event 6 mcm 0 chip 7 0ohm

