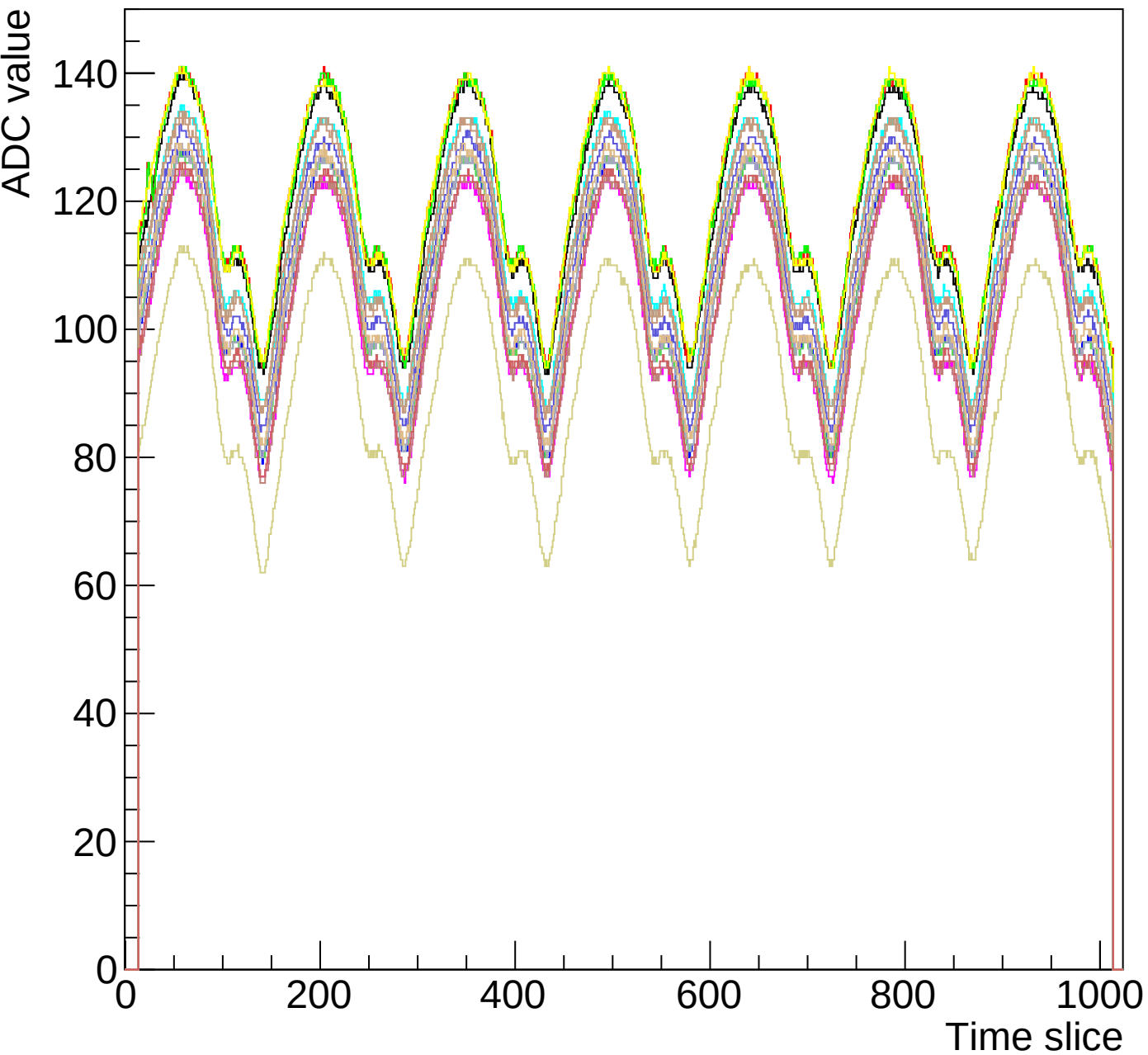
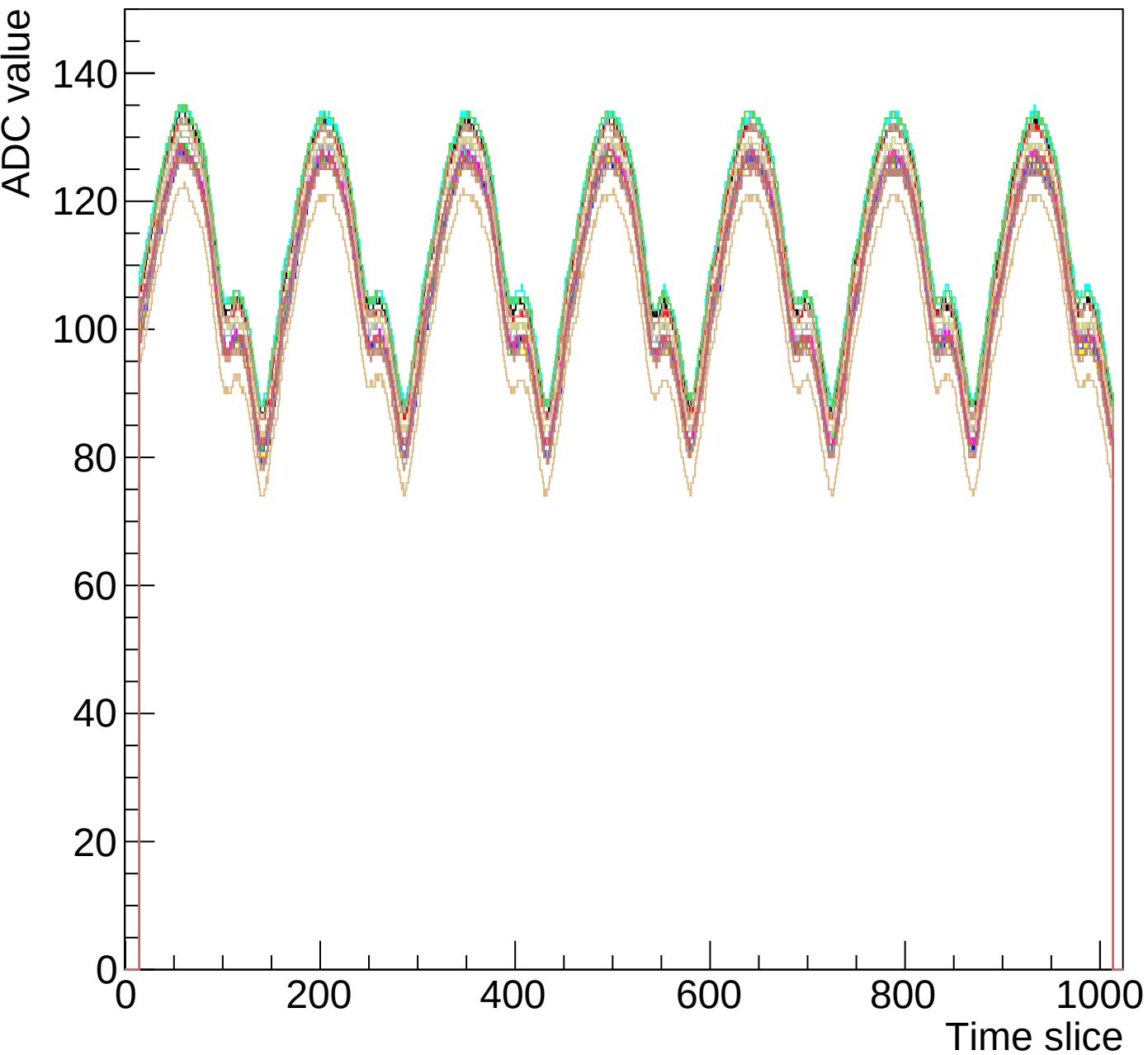


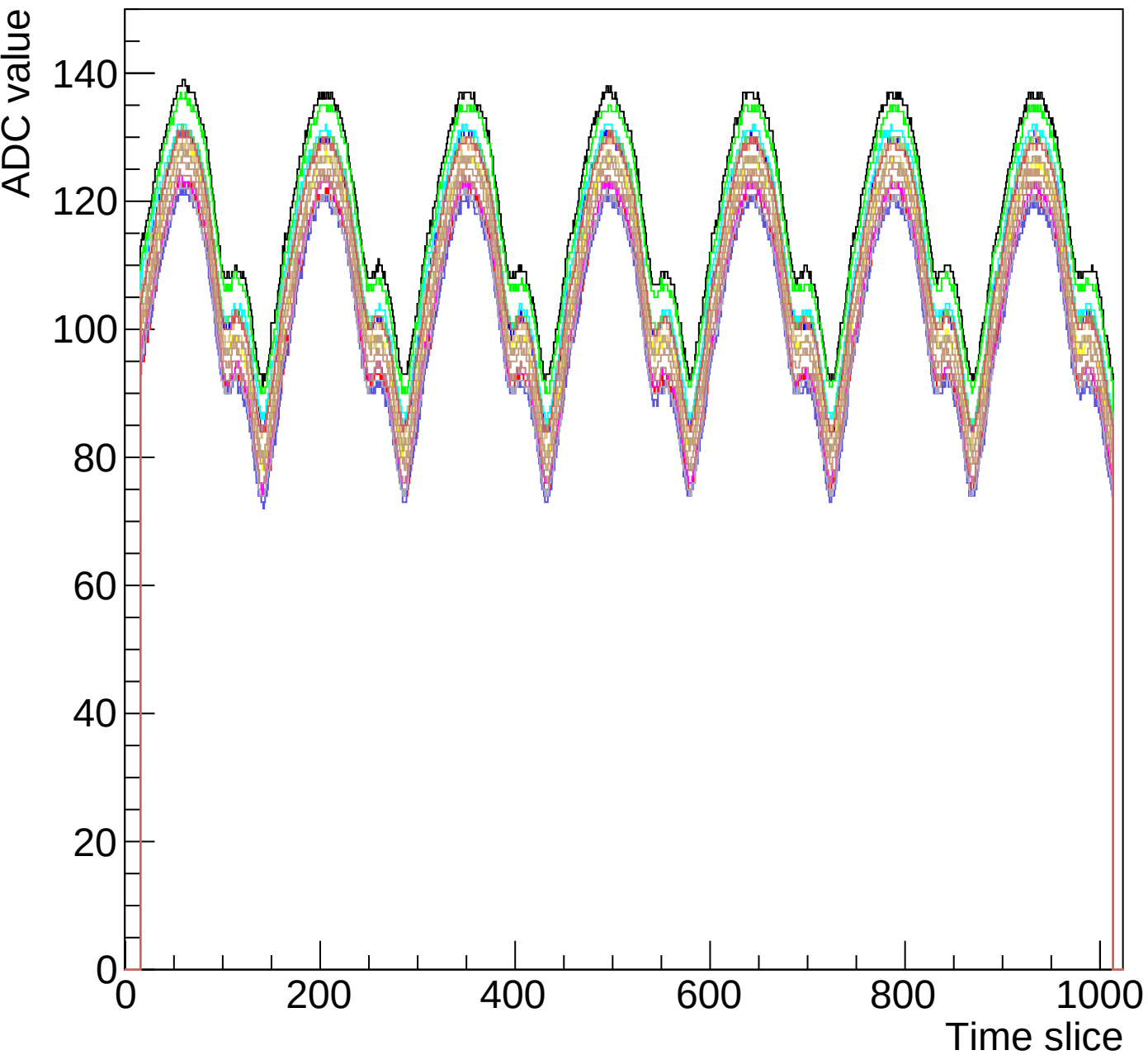
Run 123 event 9 mcm 0 chip 0 0ohm



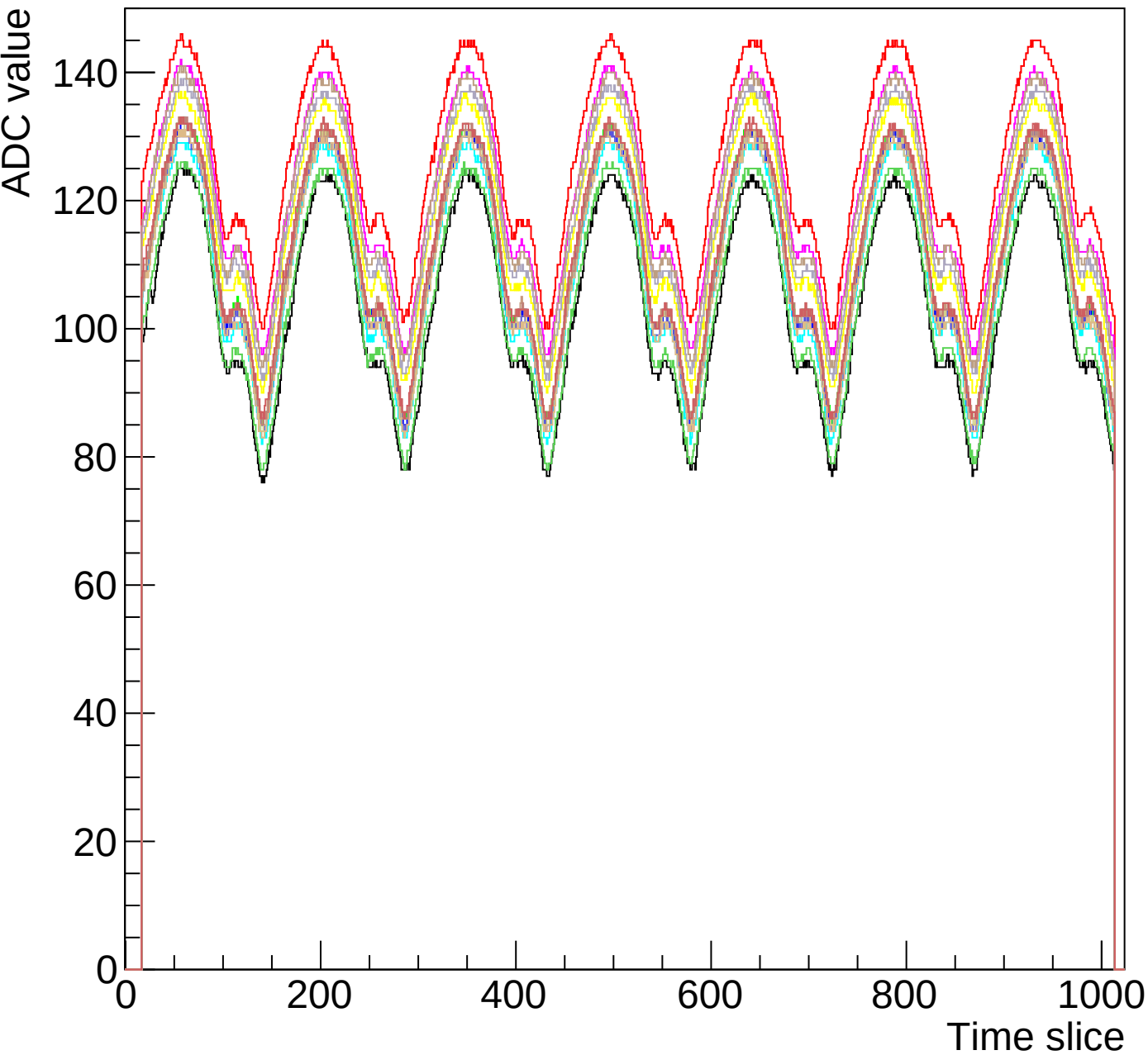
Run 123 event 9 mcm 0 chip 1 0ohm



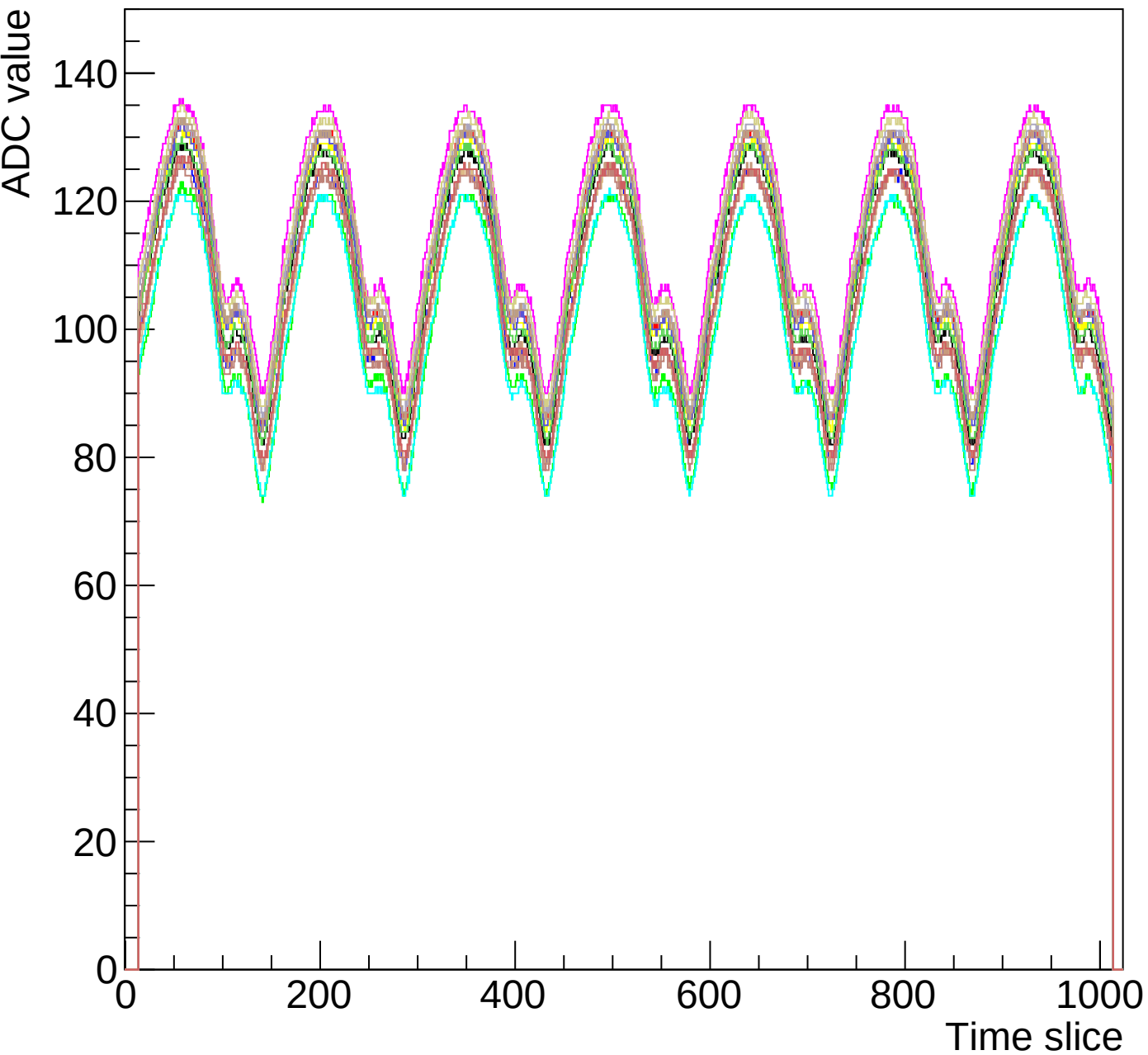
Run 123 event 9 mcm 0 chip 2 0ohm



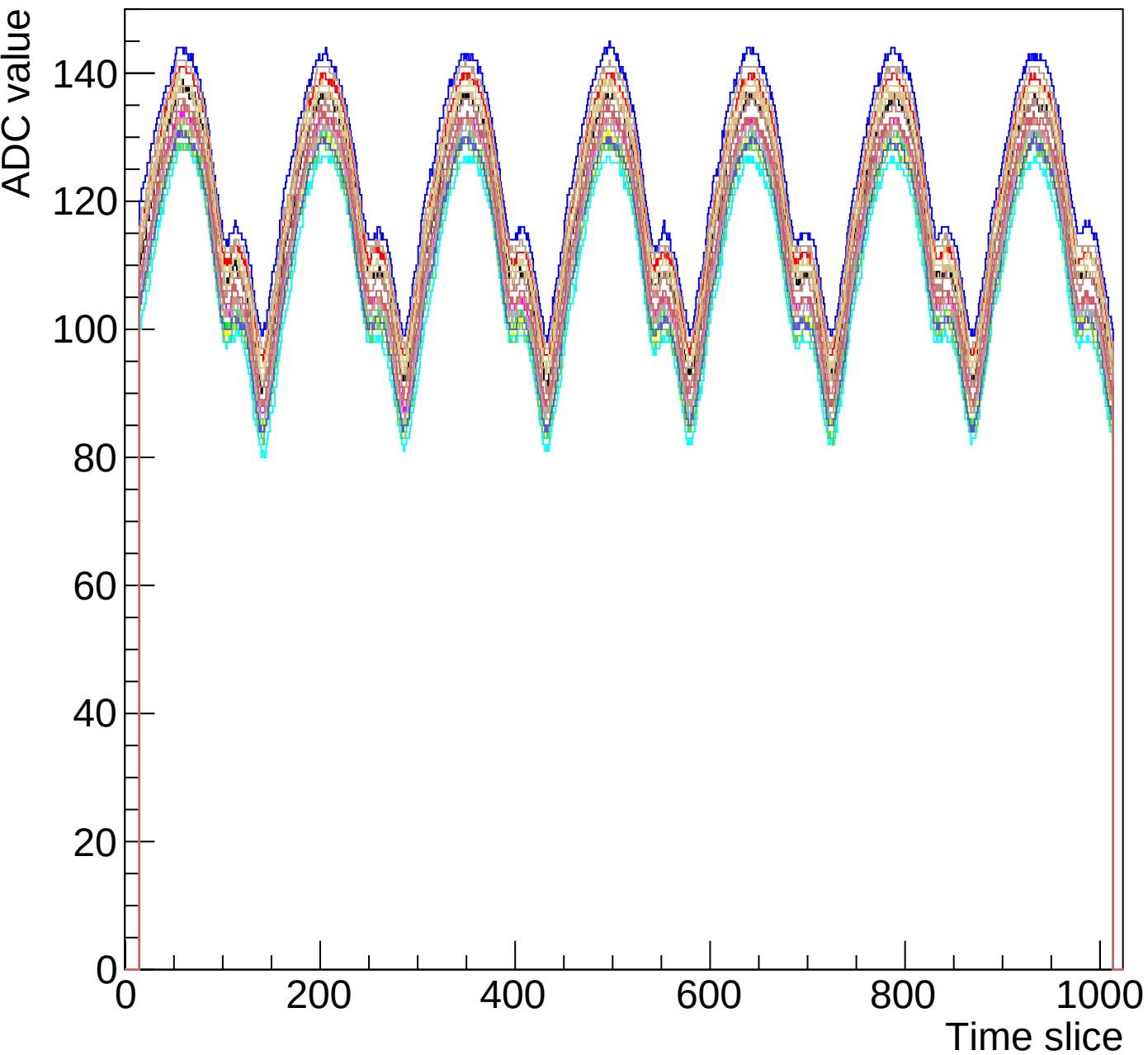
Run 123 event 9 mcm 0 chip 3 0ohm



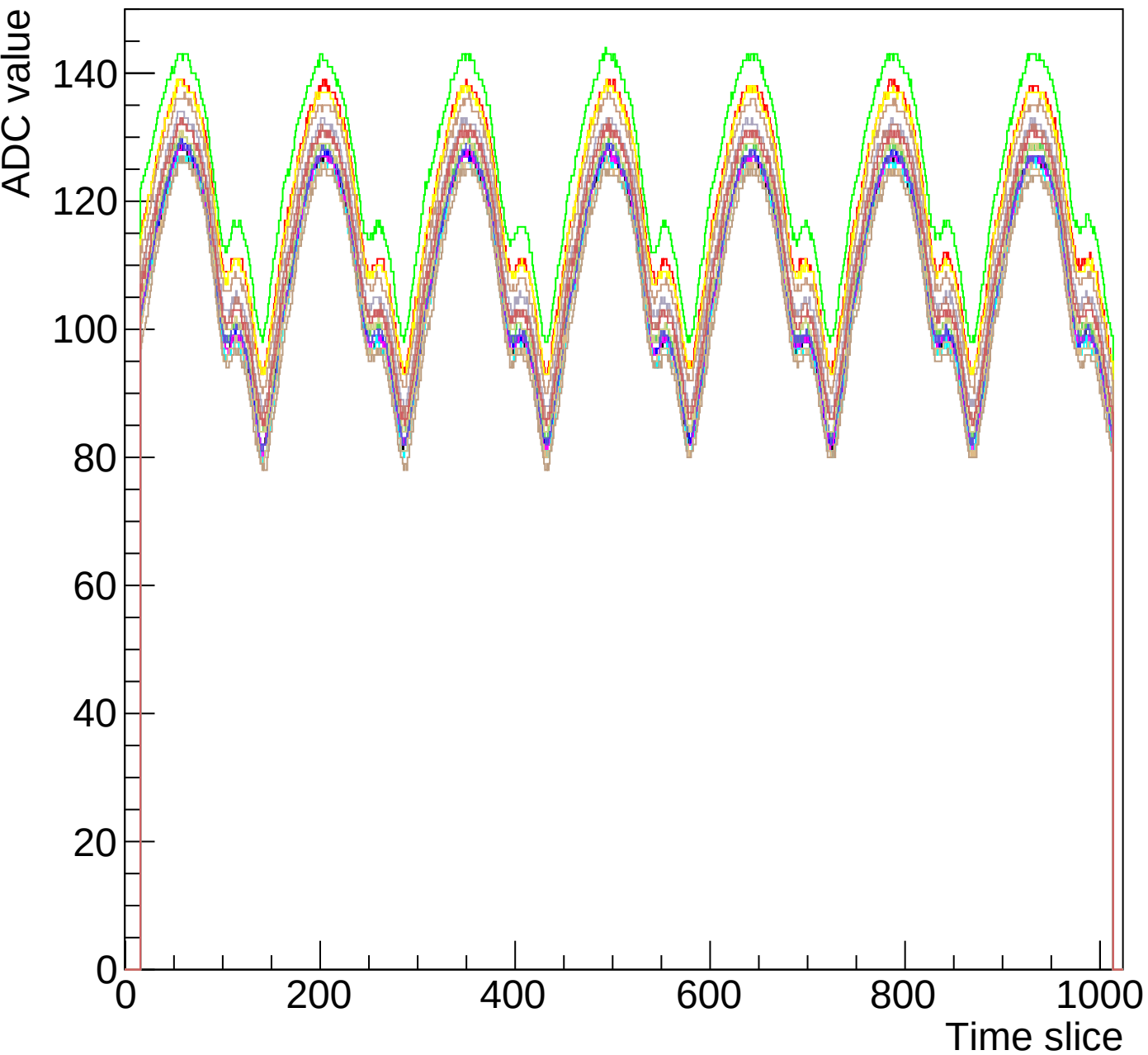
Run 123 event 9 mcm 0 chip 4 0ohm



Run 123 event 9 mcm 0 chip 5 0ohm



Run 123 event 9 mcm 0 chip 6 0ohm



Run 123 event 9 mcm 0 chip 7 0ohm

