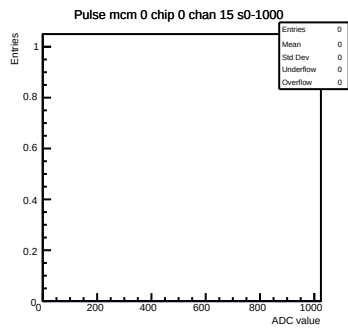
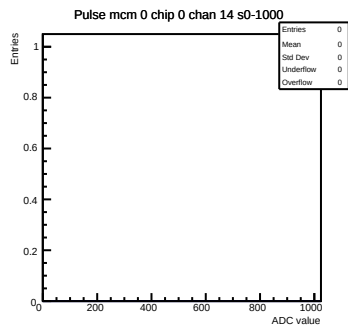
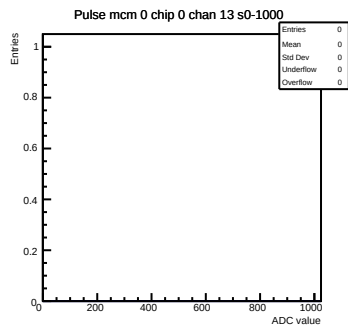
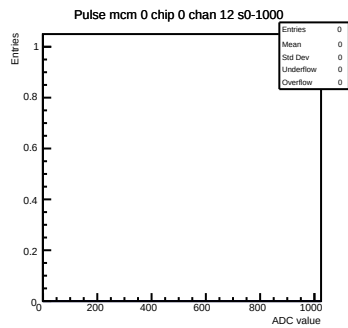
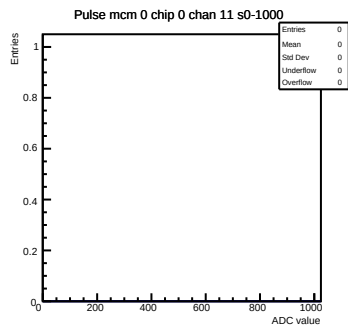
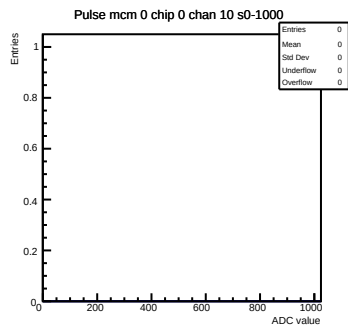
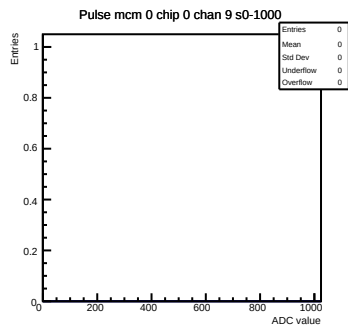
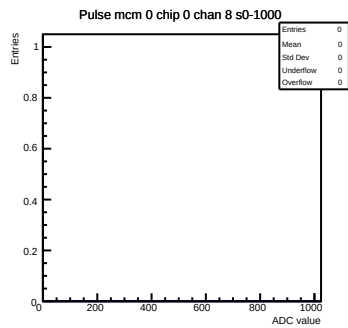
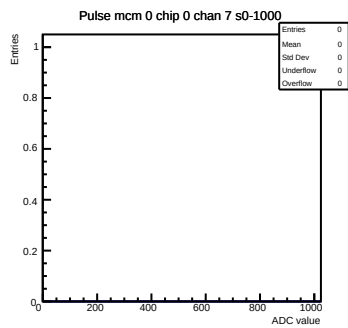
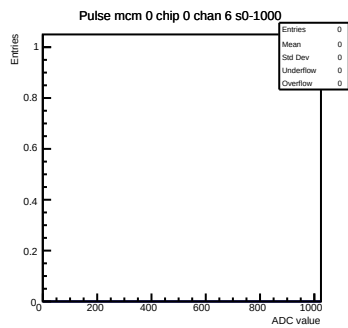
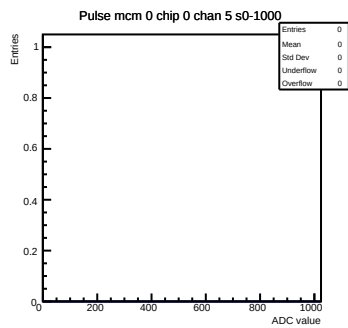
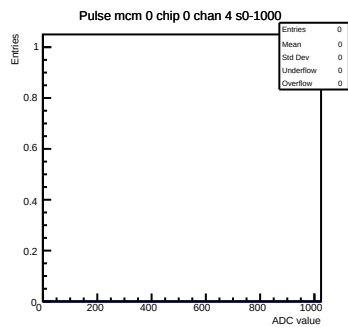
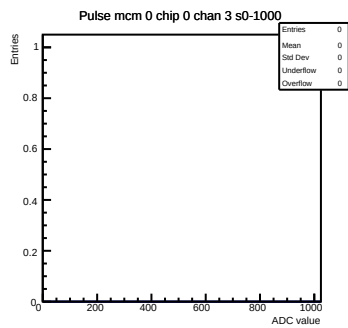
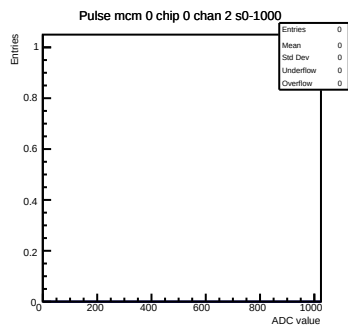
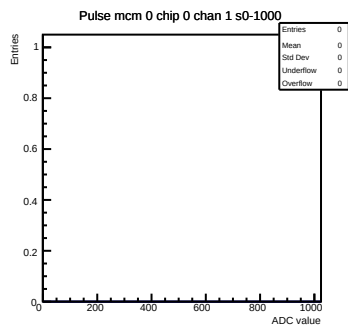
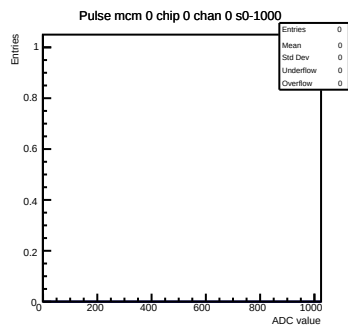
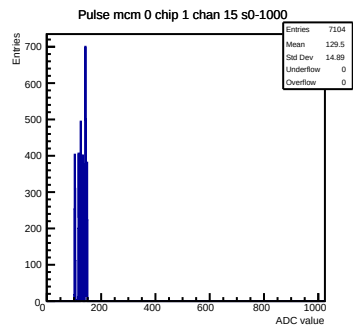
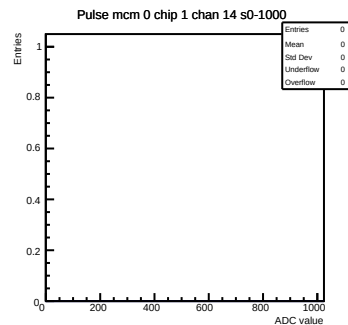
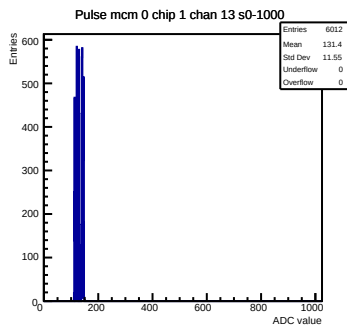
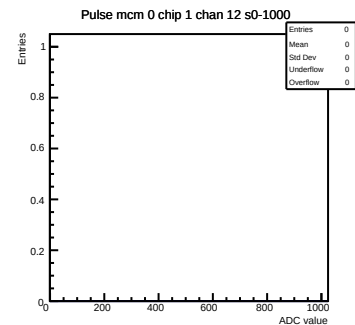
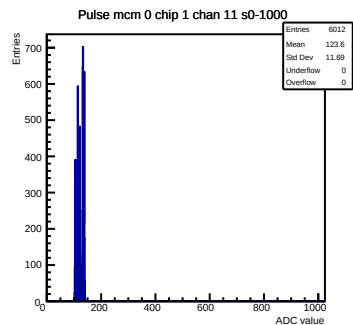
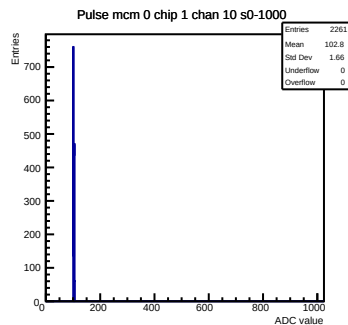
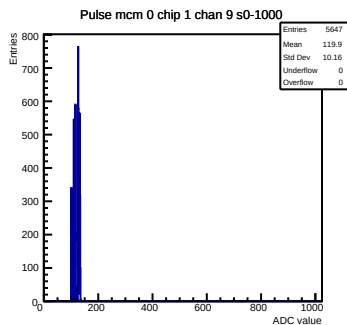
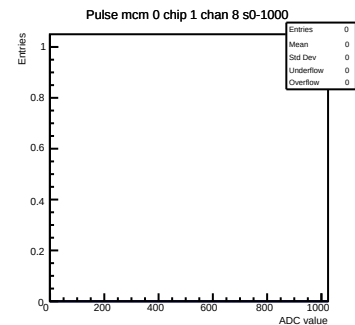
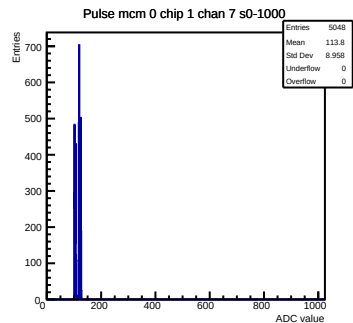
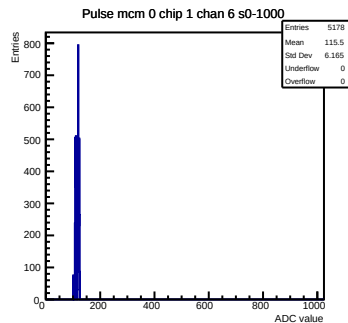
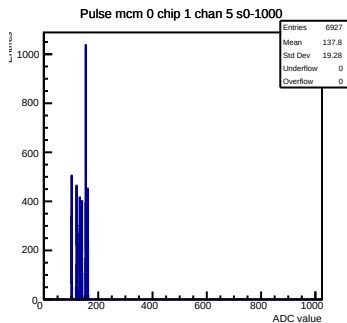
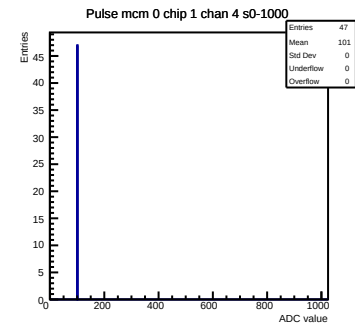
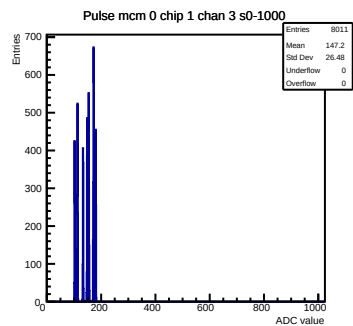
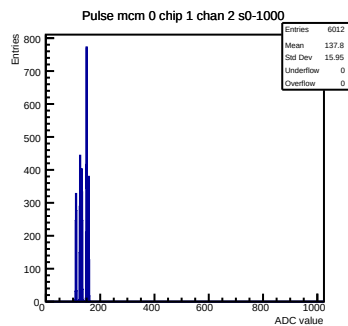
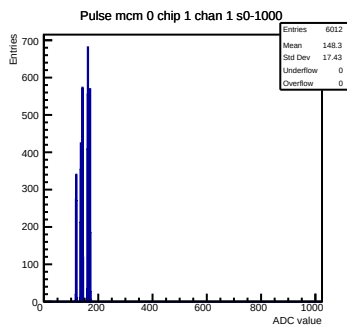
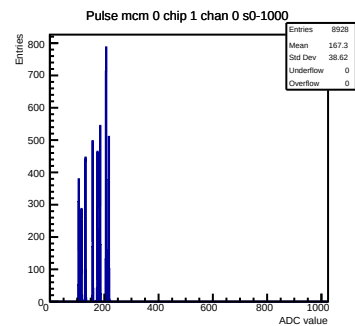
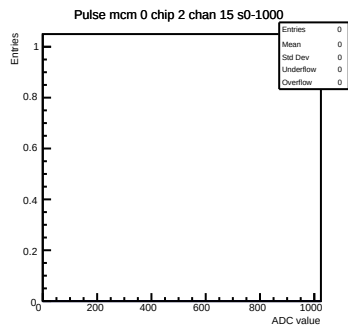
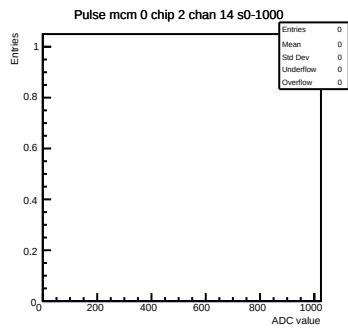
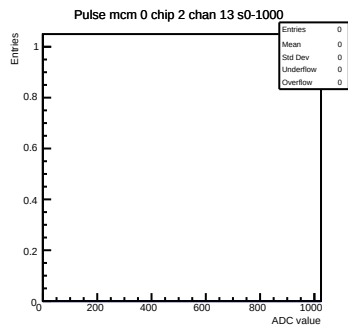
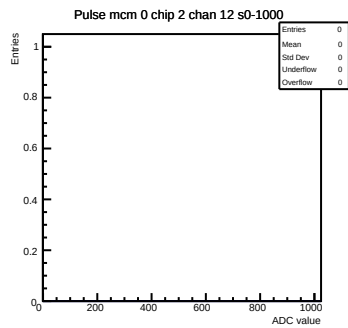
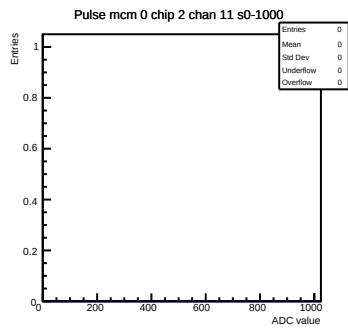
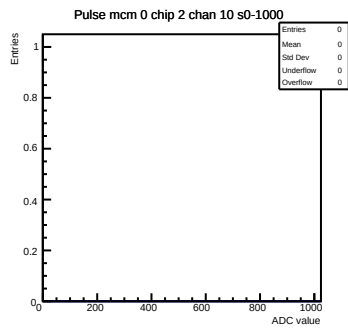
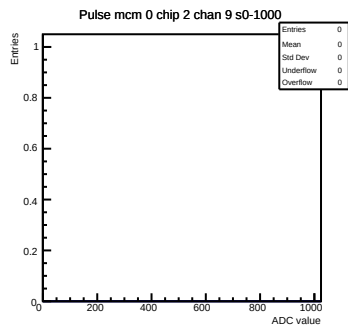
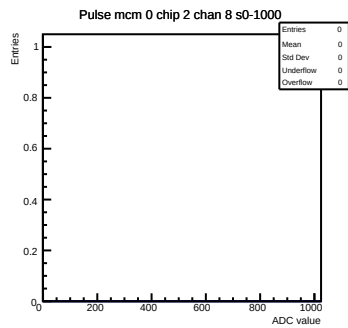
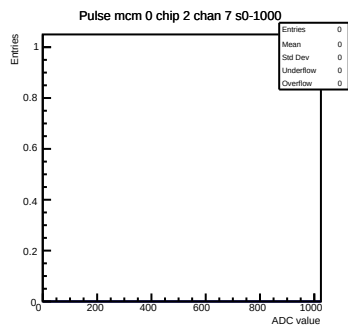
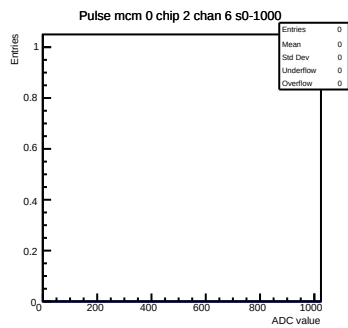
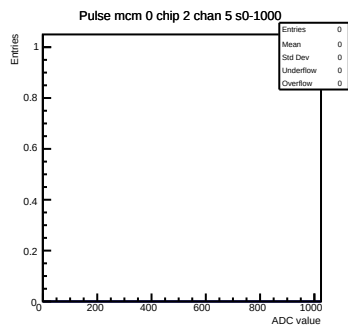
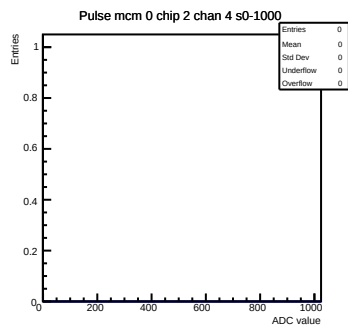
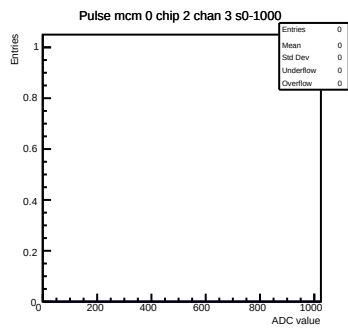
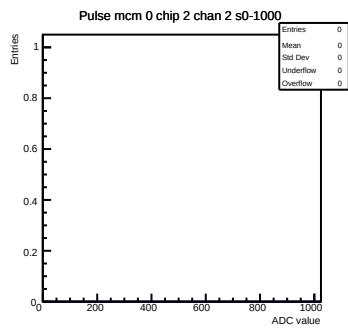
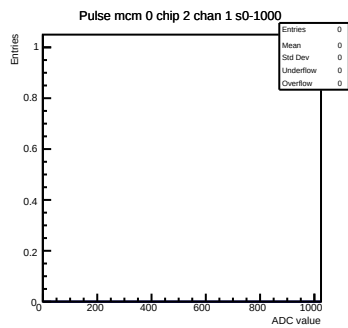
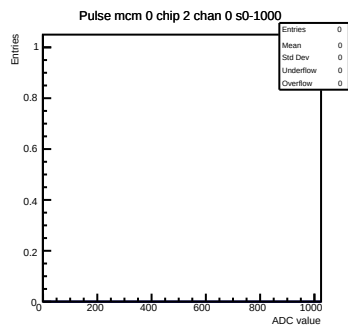
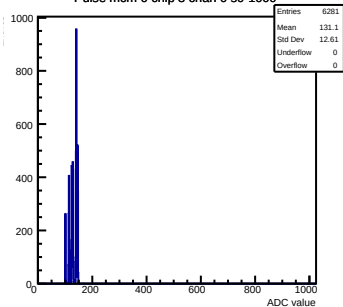




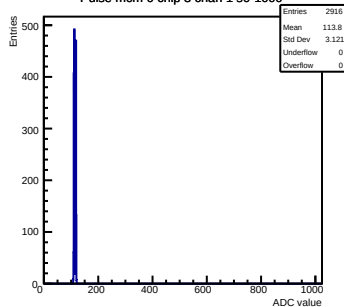




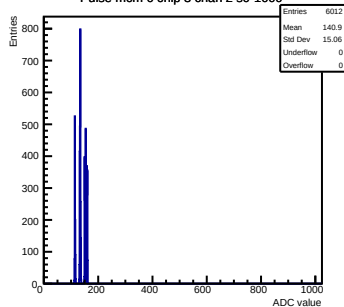
Pulse mcm 0 chip 3 chan 0 s0-1000



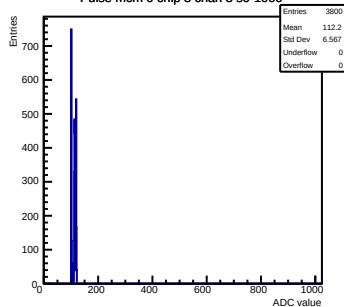
Pulse mcm 0 chip 3 chan 1 s0-1000



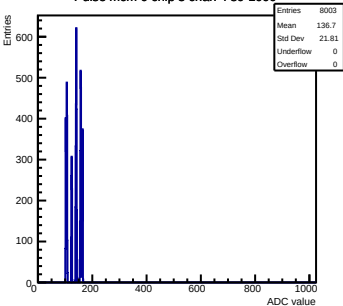
Pulse mcm 0 chip 3 chan 2 s0-1000



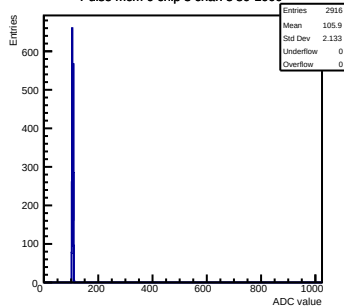
Pulse mcm 0 chip 3 chan 3 s0-1000



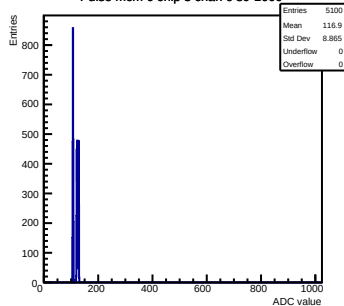
Pulse mcm 0 chip 3 chan 4 s0-1000



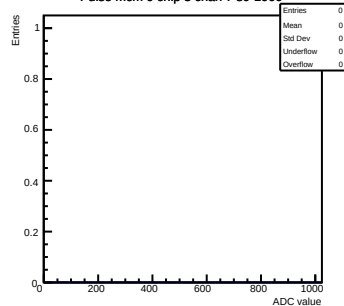
Pulse mcm 0 chip 3 chan 5 s0-1000



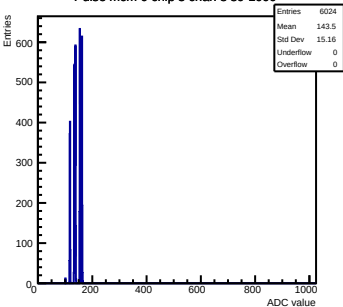
Pulse mcm 0 chip 3 chan 6 s0-1000



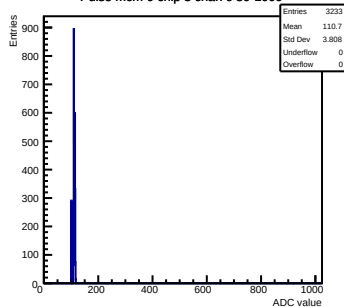
Pulse mcm 0 chip 3 chan 7 s0-1000



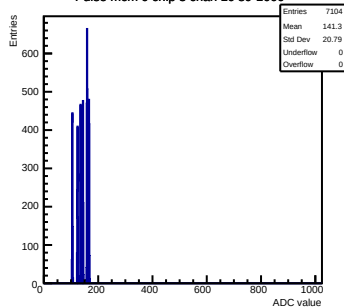
Pulse mcm 0 chip 3 chan 8 s0-1000



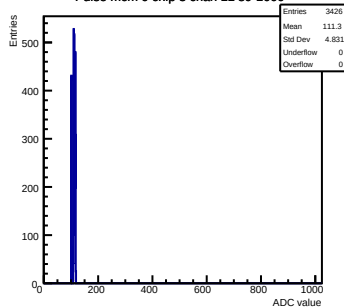
Pulse mcm 0 chip 3 chan 9 s0-1000



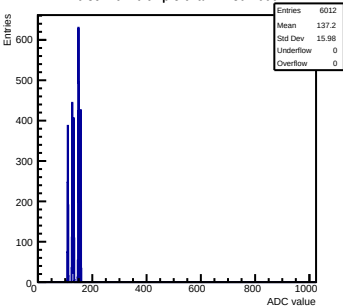
Pulse mcm 0 chip 3 chan 10 s0-1000



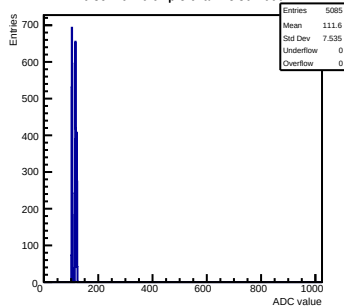
Pulse mcm 0 chip 3 chan 11 s0-1000



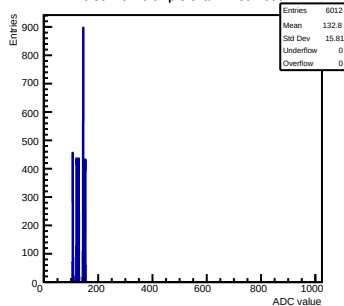
Pulse mcm 0 chip 3 chan 12 s0-1000



Pulse mcm 0 chip 3 chan 13 s0-1000



Pulse mcm 0 chip 3 chan 14 s0-1000



Pulse mcm 0 chip 3 chan 15 s0-1000

