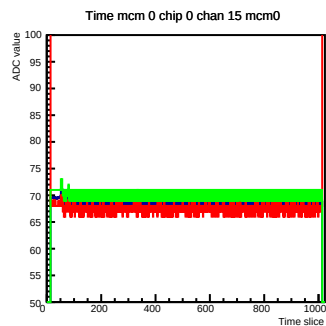
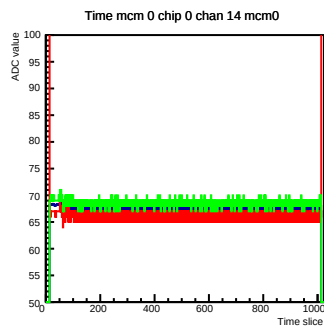
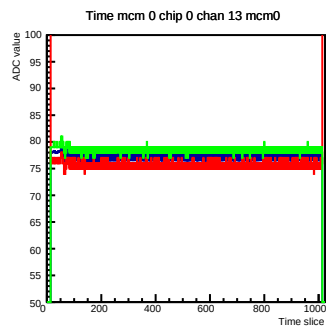
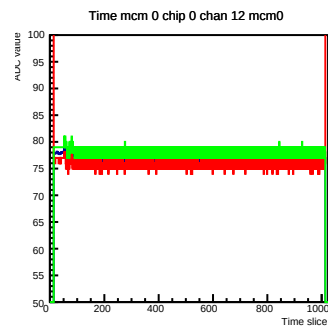
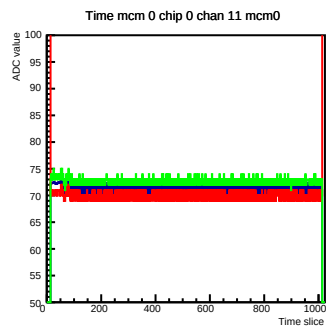
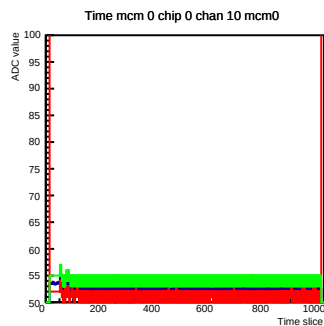
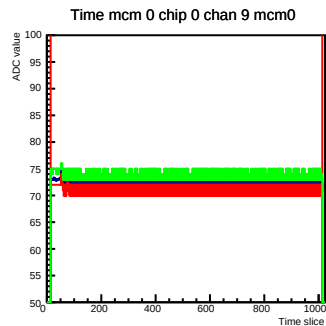
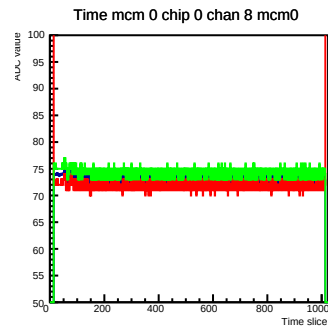
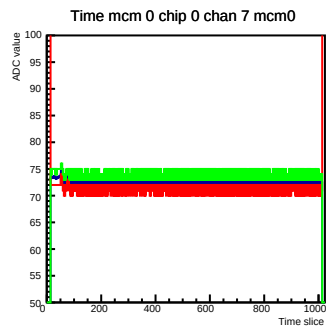
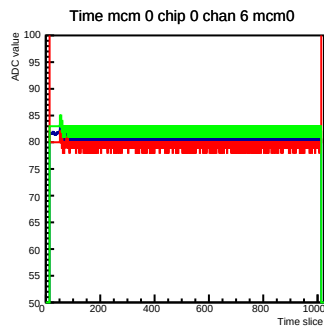
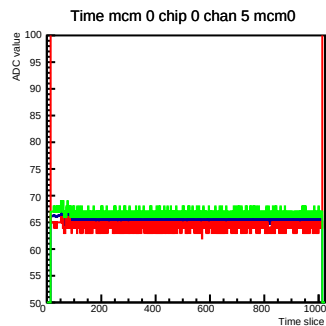
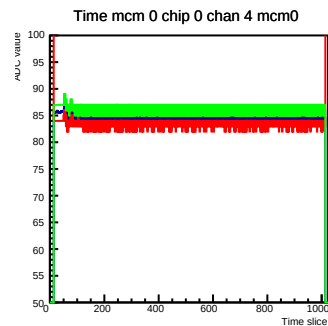
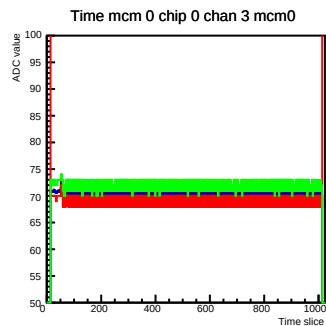
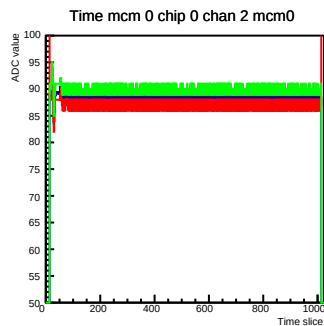
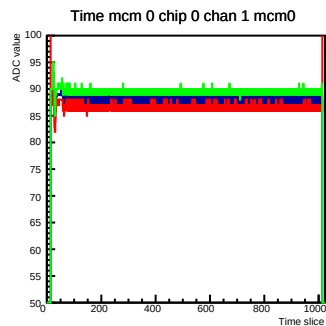
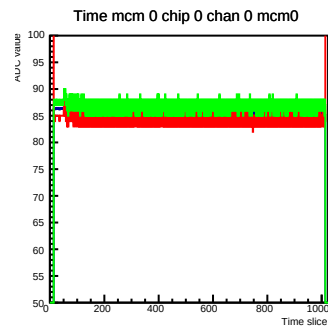
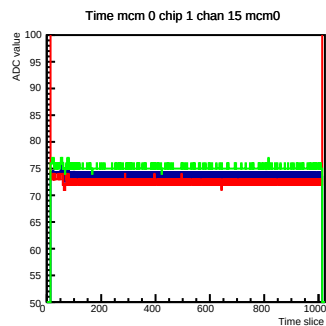
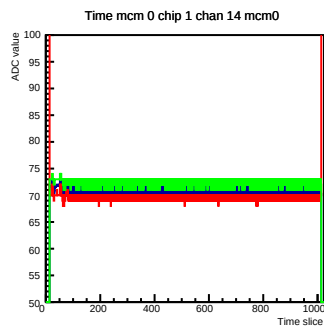
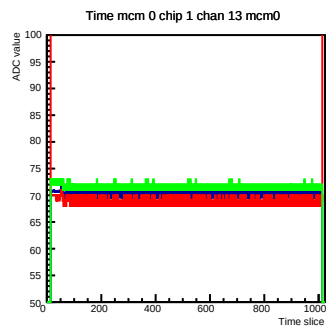
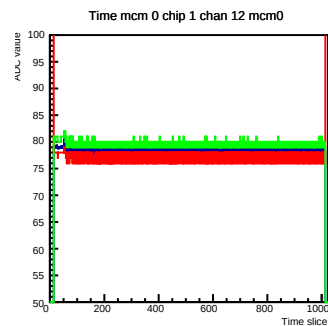
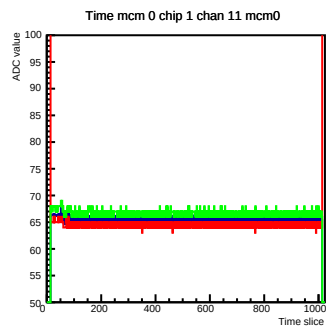
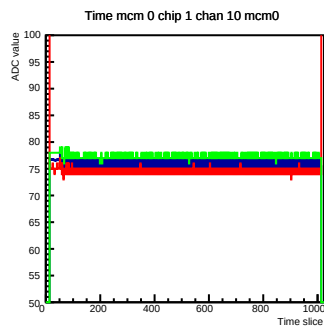
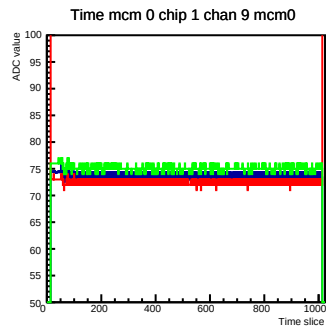
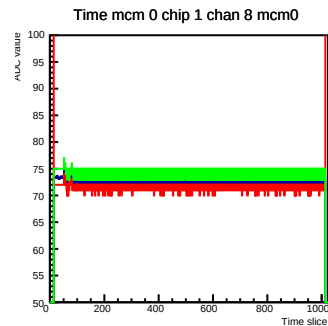
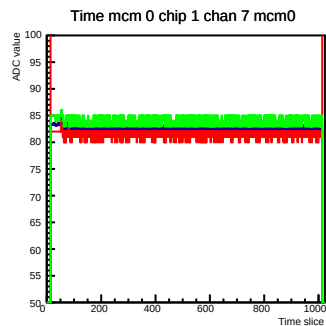
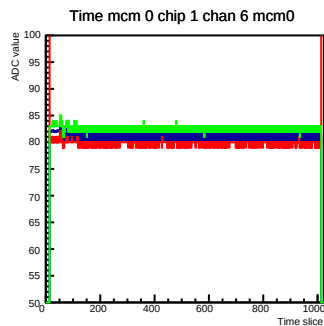
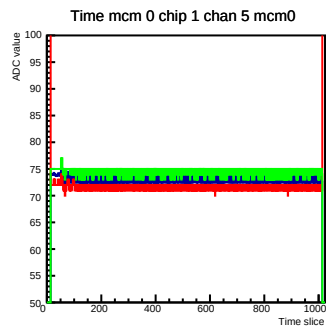
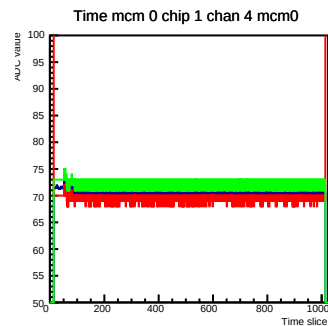
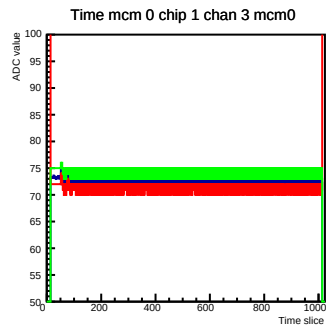
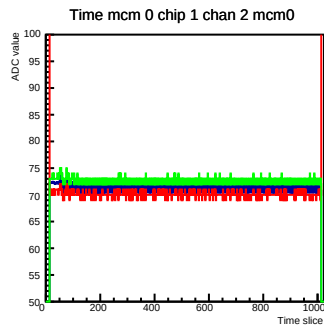
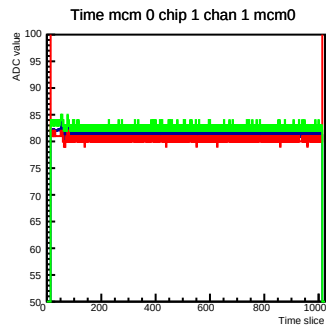
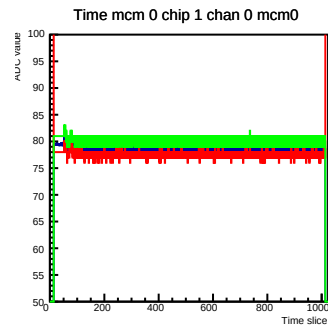
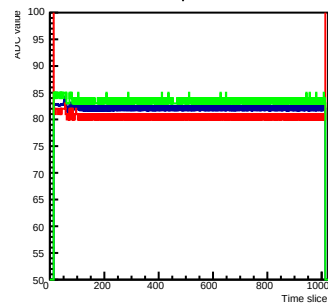


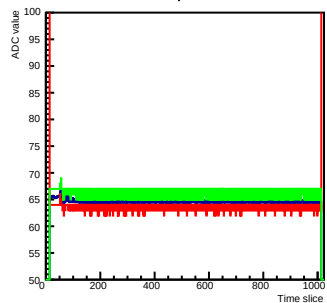




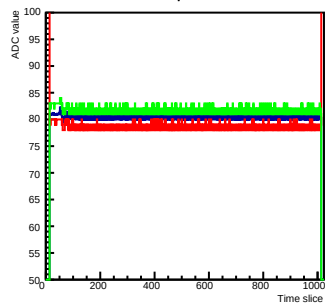
Time mcm 0 chip 2 chan 0 mcm0



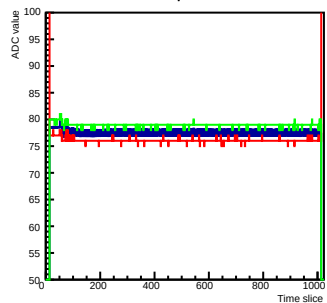
Time mcm 0 chip 2 chan 1 mcm0



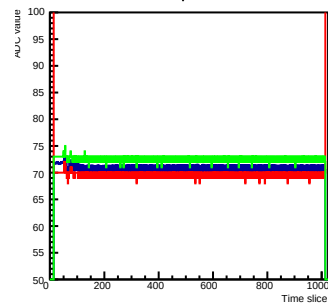
Time mcm 0 chip 2 chan 2 mcm0



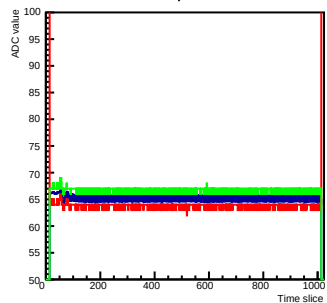
Time mcm 0 chip 2 chan 3 mcm0



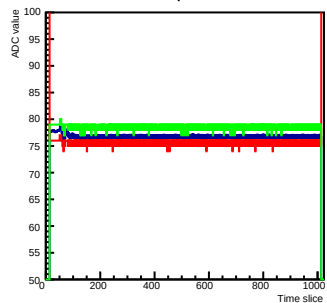
Time mcm 0 chip 2 chan 4 mcm0



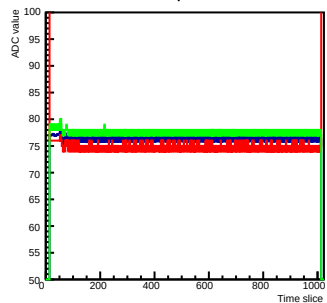
Time mcm 0 chip 2 chan 5 mcm0



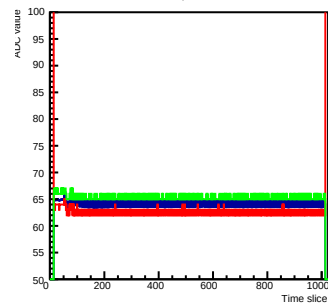
Time mcm 0 chip 2 chan 6 mcm0



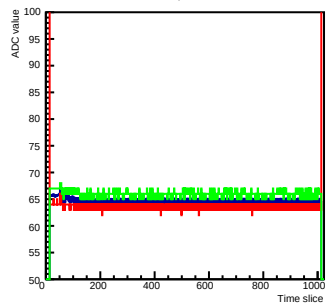
Time mcm 0 chip 2 chan 7 mcm0



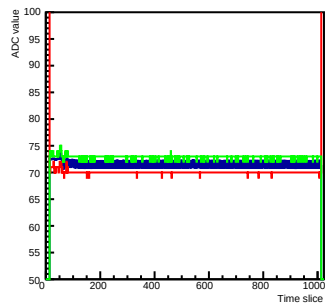
Time mcm 0 chip 2 chan 8 mcm0



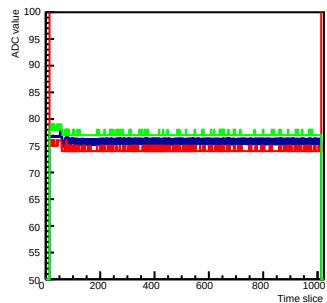
Time mcm 0 chip 2 chan 9 mcm0



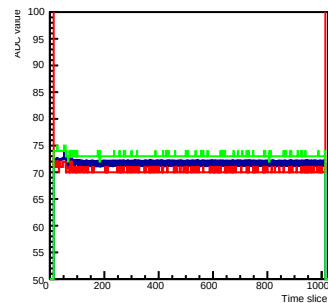
Time mcm 0 chip 2 chan 10 mcm0



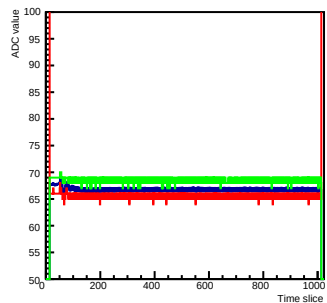
Time mcm 0 chip 2 chan 11 mcm0



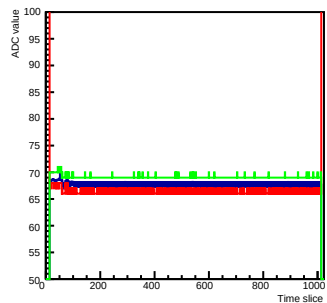
Time mcm 0 chip 2 chan 12 mcm0



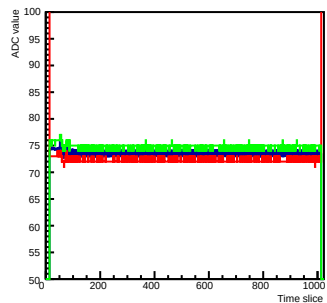
Time mcm 0 chip 2 chan 13 mcm0

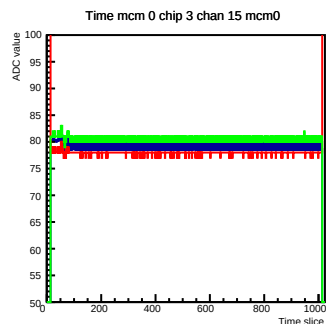
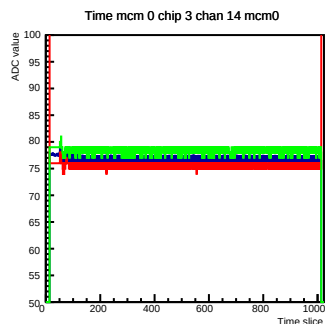
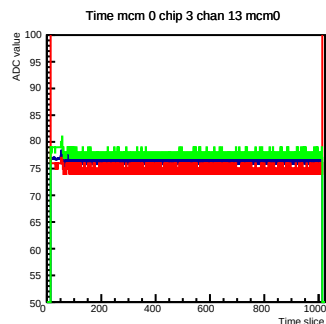
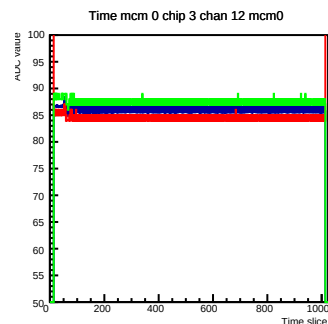
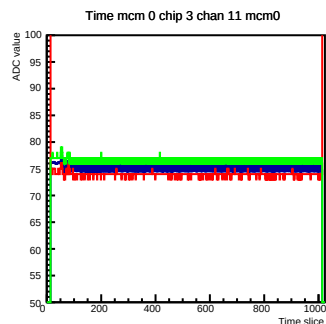
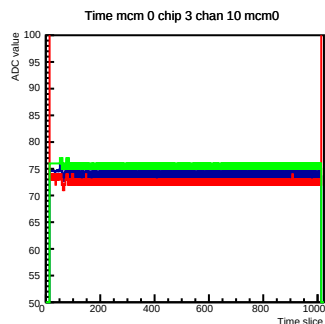
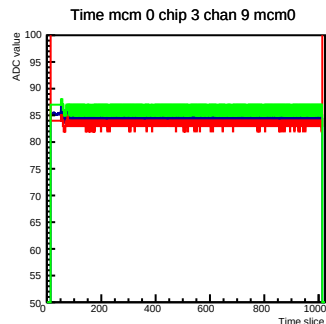
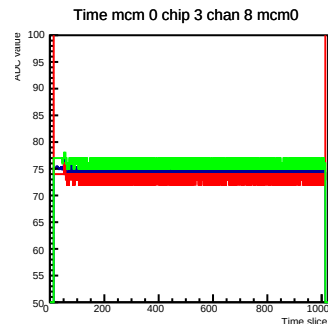
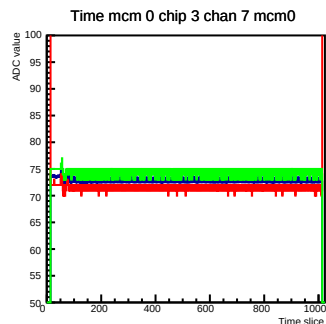
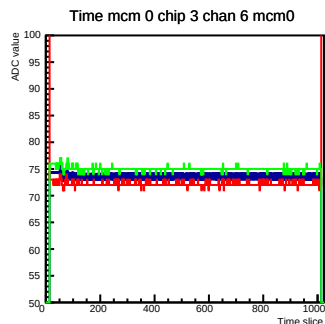
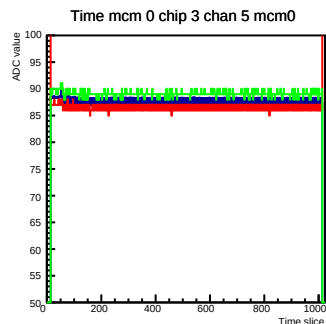
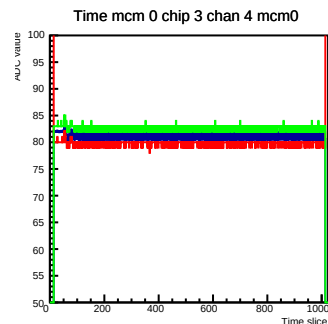
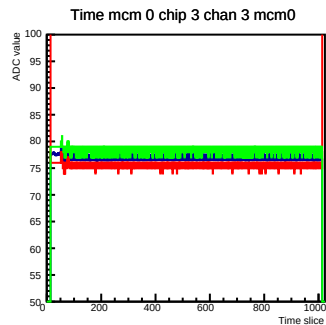
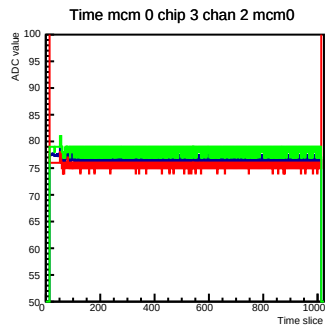
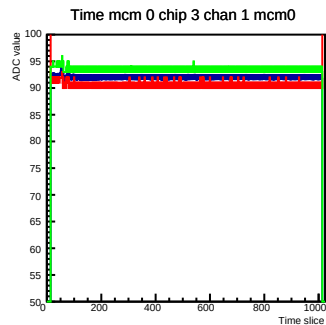
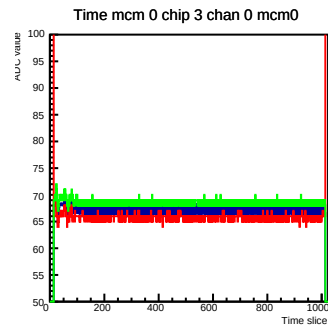


Time mcm 0 chip 2 chan 14 mcm0

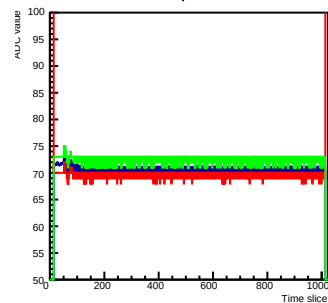


Time mcm 0 chip 2 chan 15 mcm0

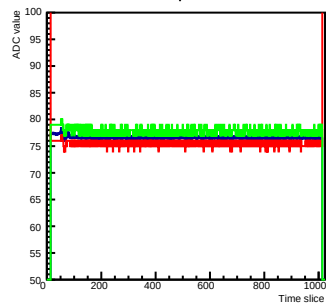




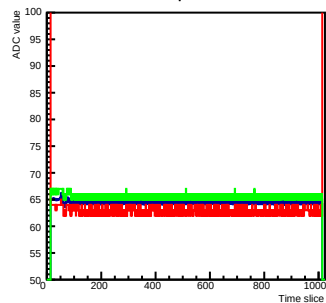
Time mcm 0 chip 4 chan 0 mcm0



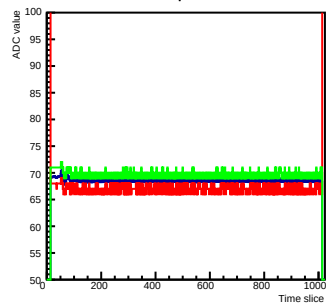
Time mcm 0 chip 4 chan 1 mcm0



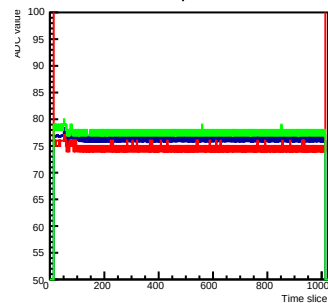
Time mcm 0 chip 4 chan 2 mcm0



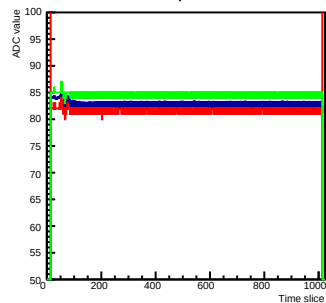
Time mcm 0 chip 4 chan 3 mcm0



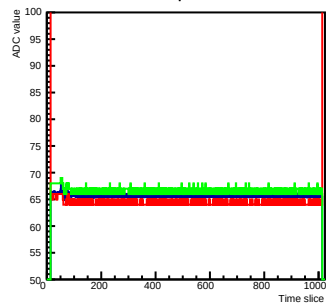
Time mcm 0 chip 4 chan 4 mcm0



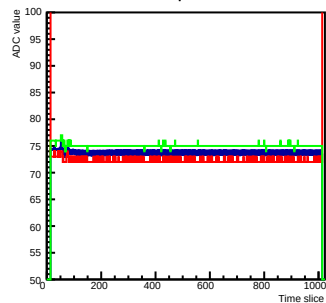
Time mcm 0 chip 4 chan 5 mcm0



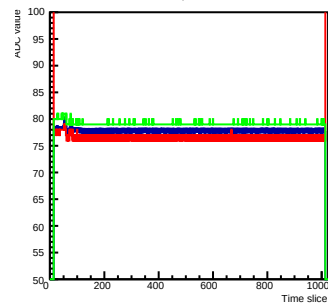
Time mcm 0 chip 4 chan 6 mcm0



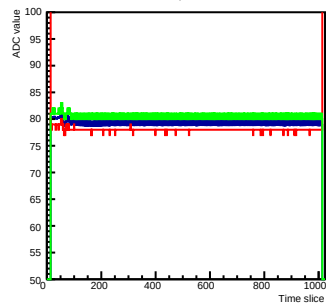
Time mcm 0 chip 4 chan 7 mcm0



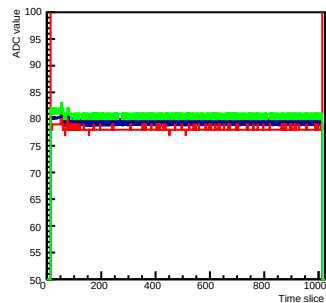
Time mcm 0 chip 4 chan 8 mcm0



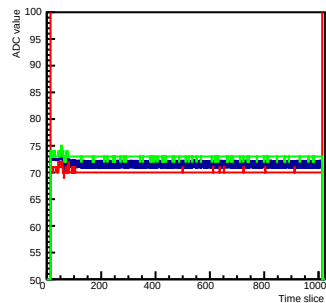
Time mcm 0 chip 4 chan 9 mcm0



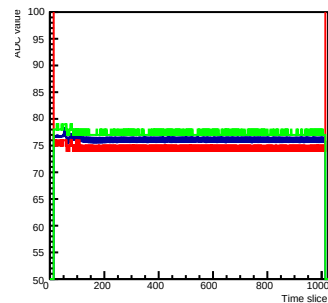
Time mcm 0 chip 4 chan 10 mcm0



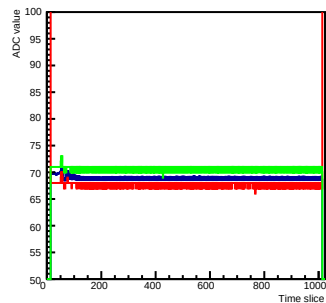
Time mcm 0 chip 4 chan 11 mcm0



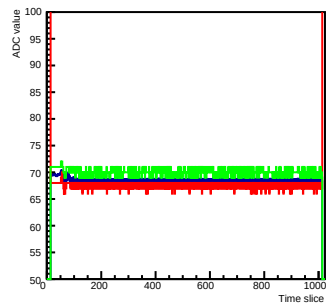
Time mcm 0 chip 4 chan 12 mcm0



Time mcm 0 chip 4 chan 13 mcm0



Time mcm 0 chip 4 chan 14 mcm0



Time mcm 0 chip 4 chan 15 mcm0

