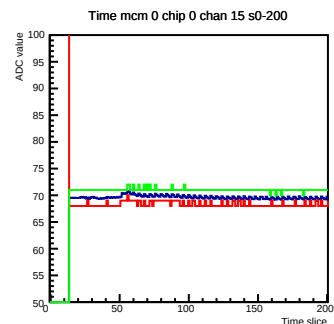
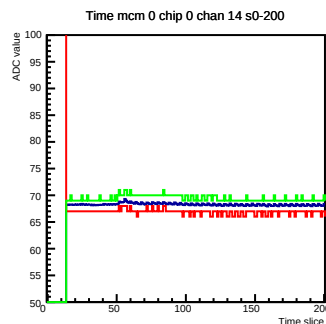
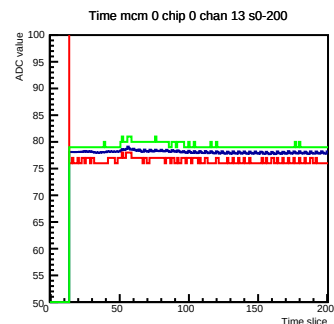
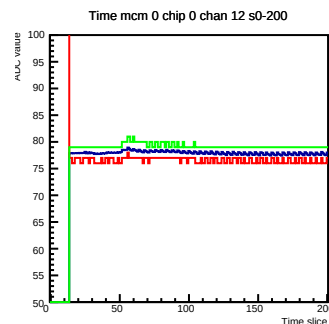
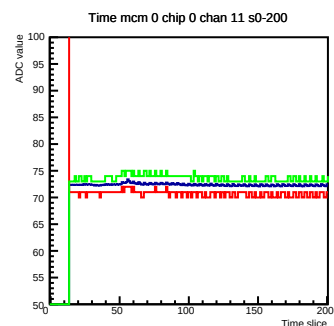
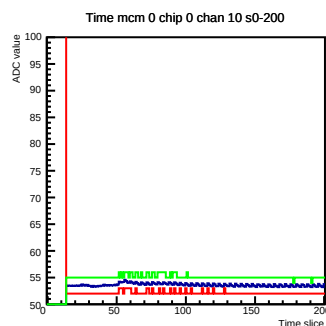
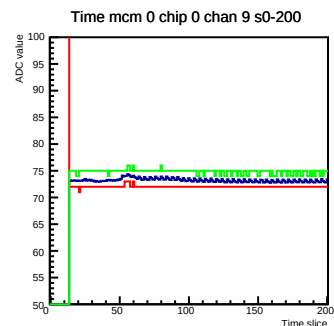
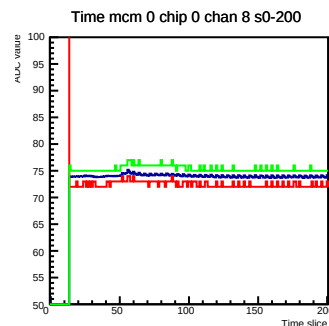
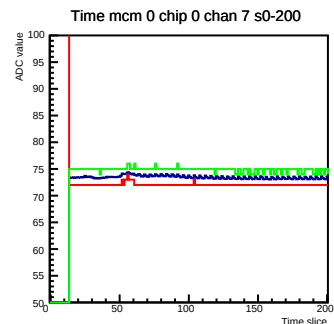
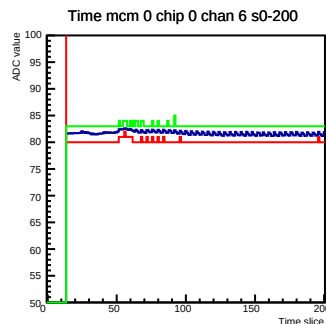
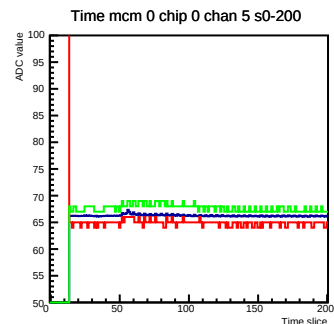
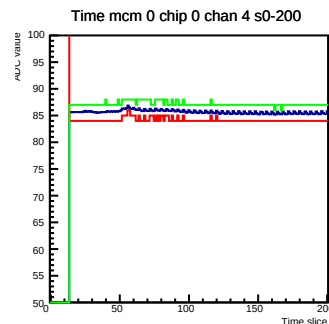
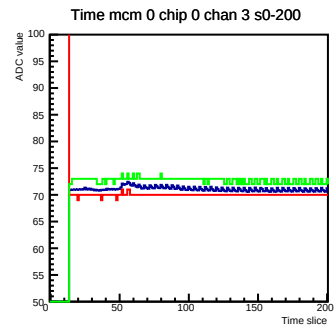
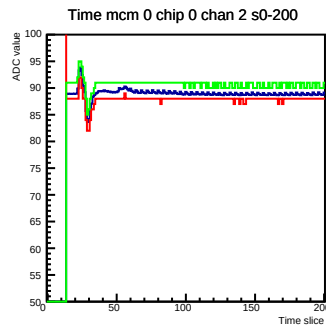
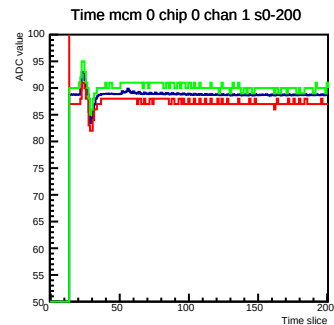
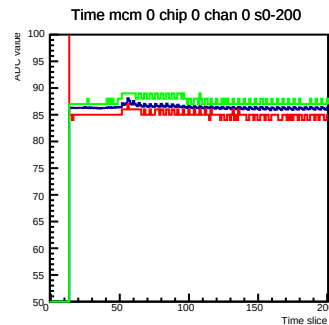
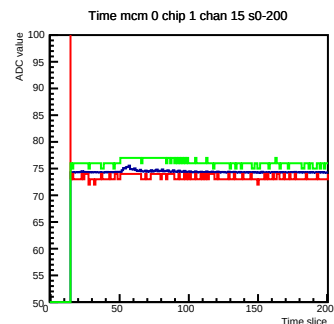
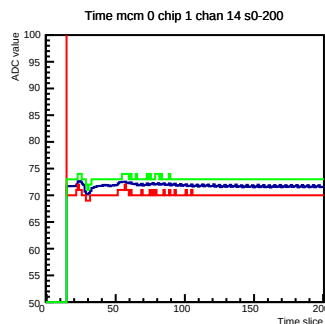
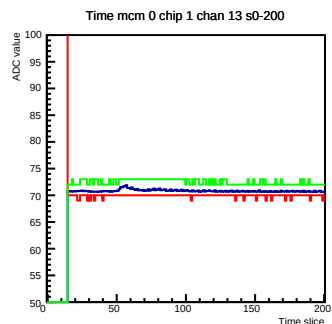
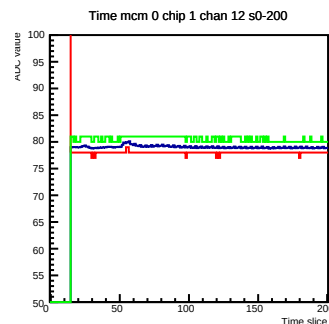
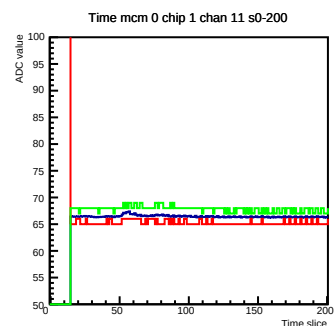
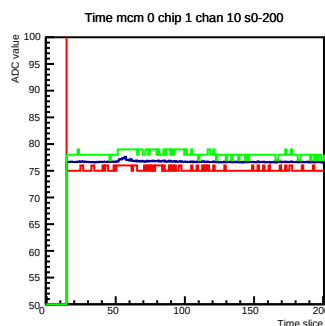
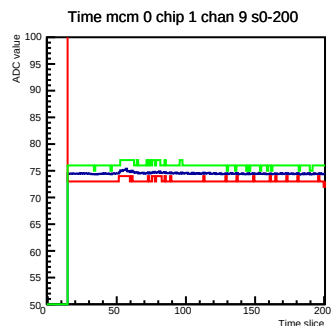
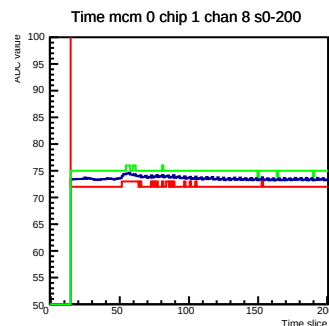
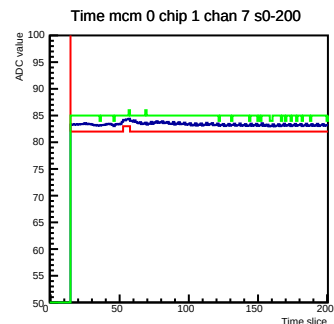
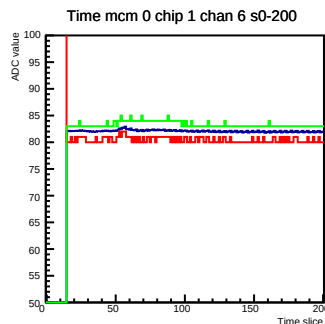
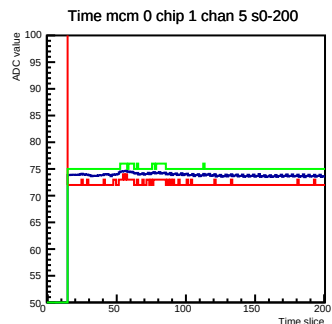
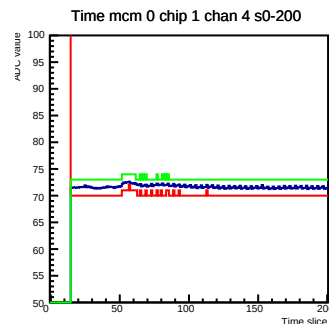
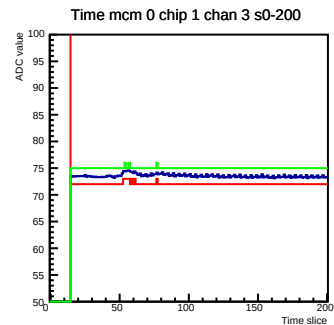
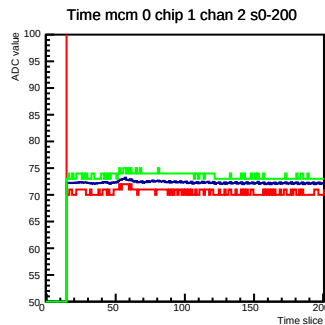
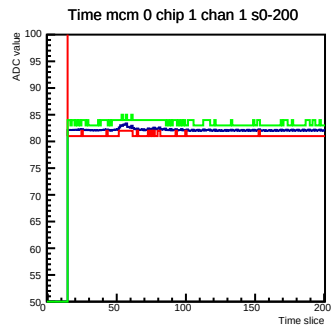
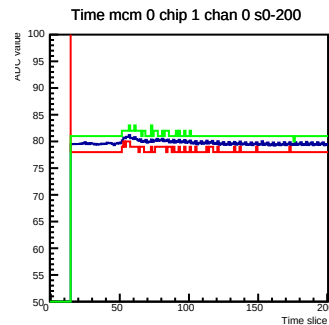
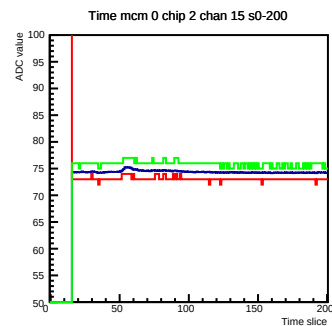
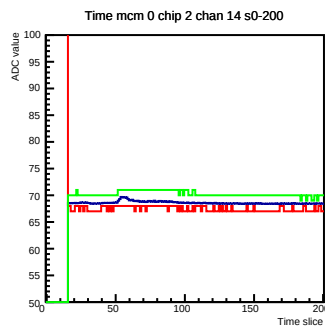
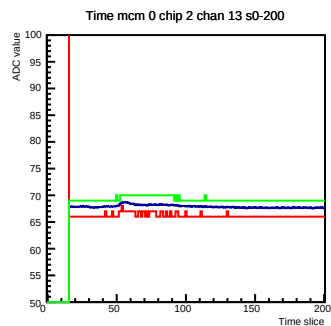
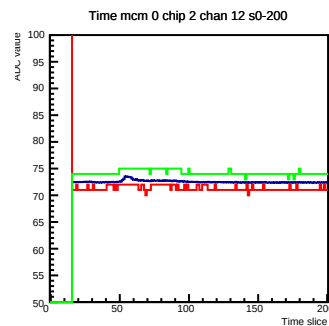
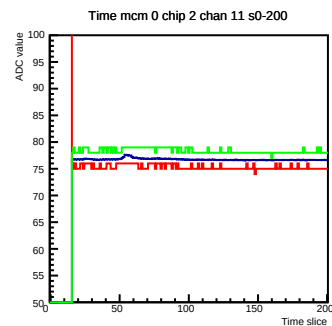
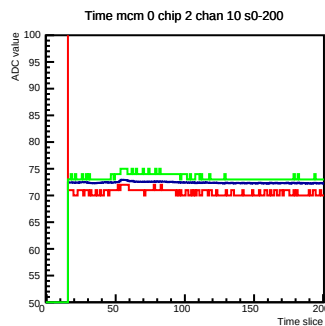
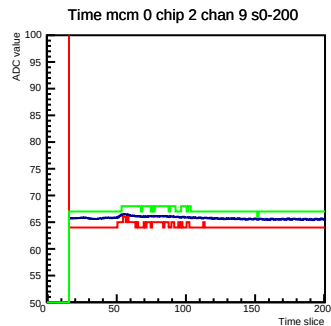
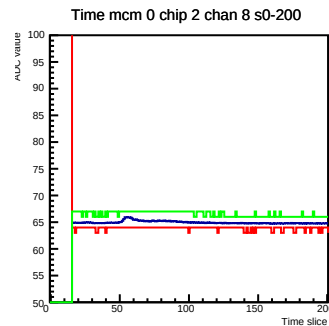
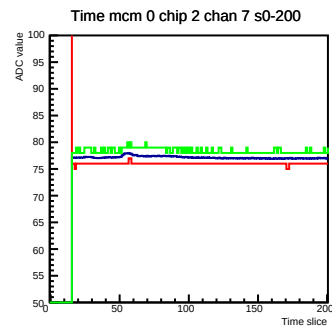
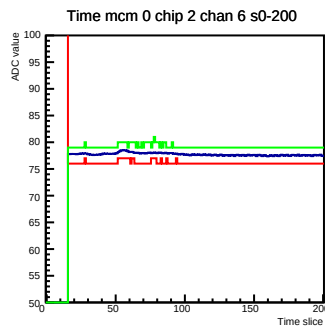
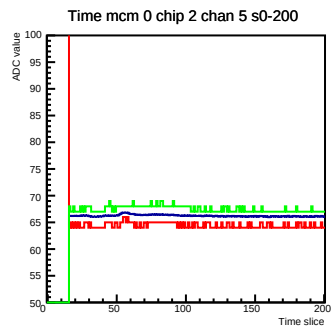
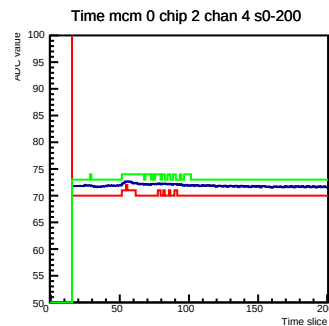
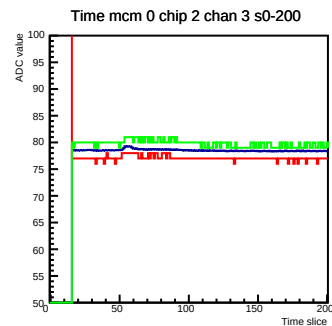
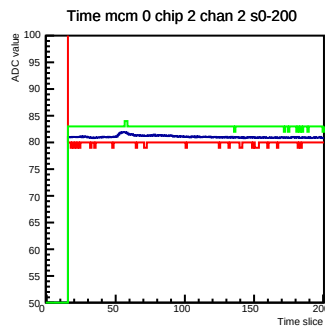
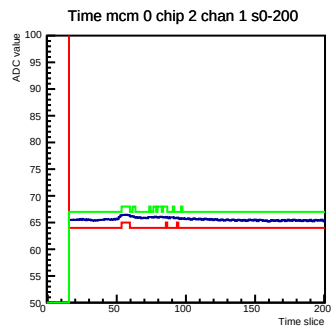
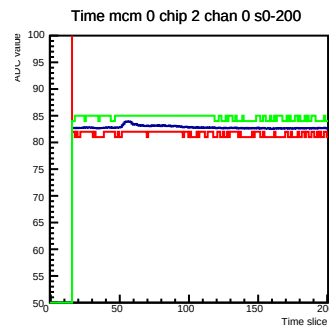
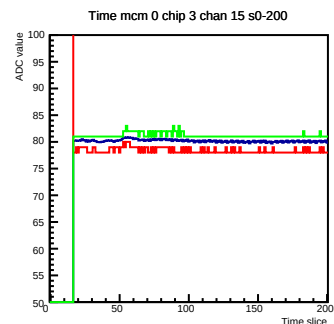
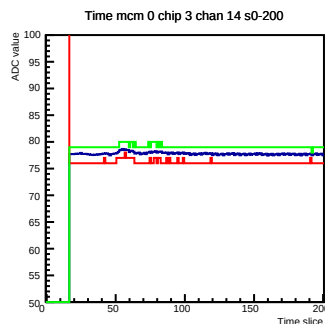
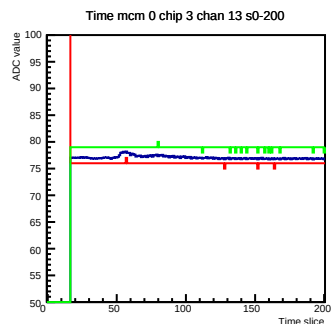
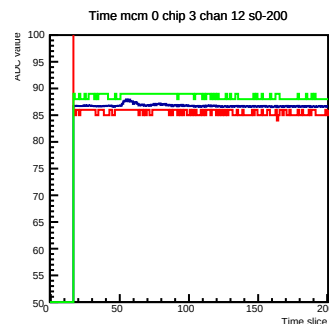
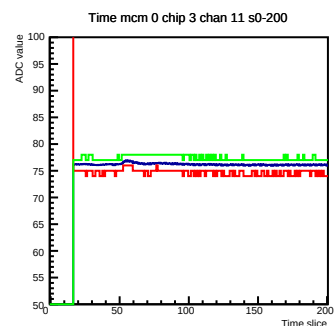
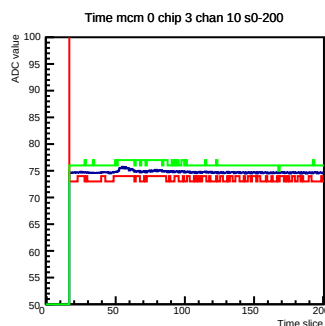
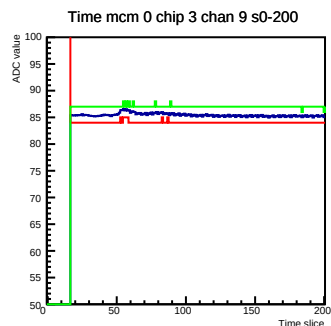
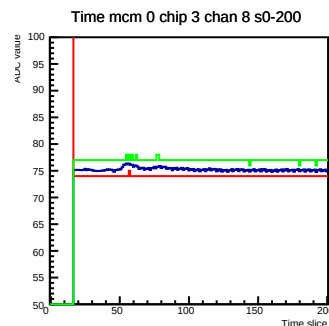
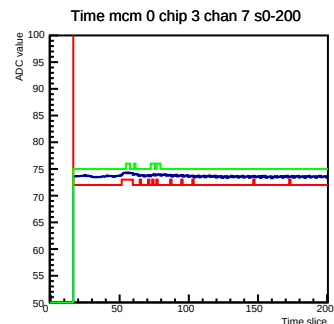
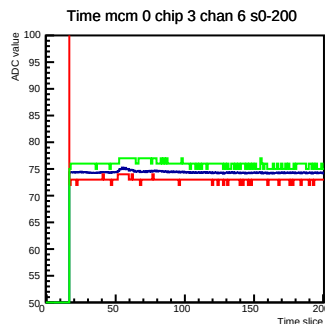
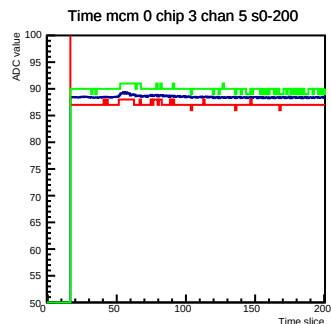
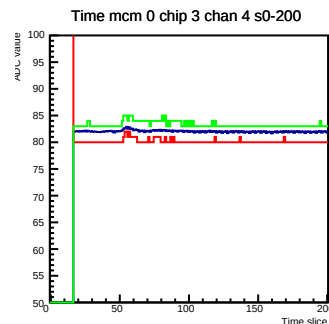
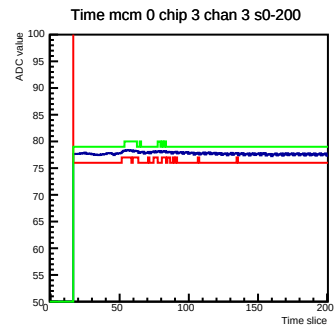
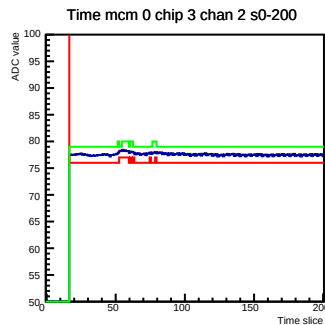
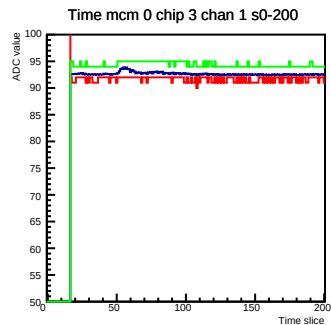
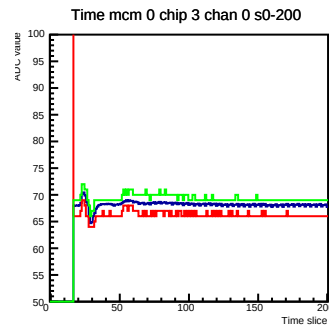


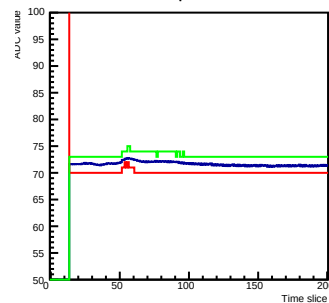




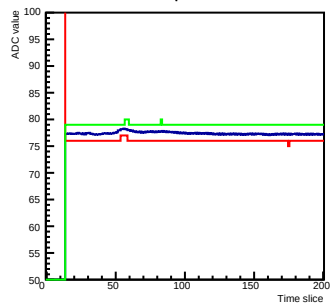




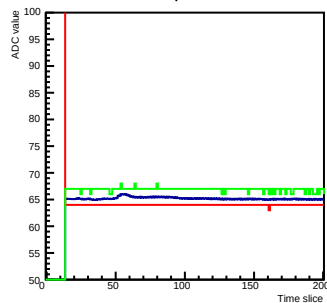
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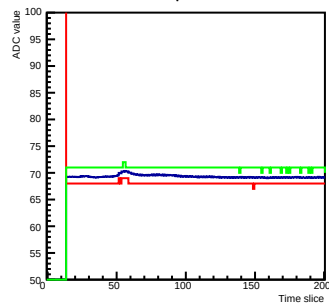
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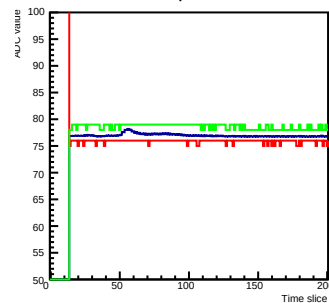
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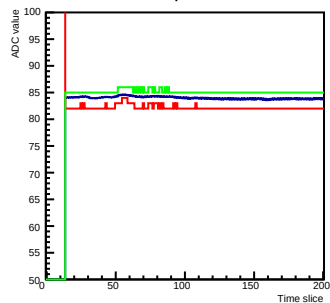
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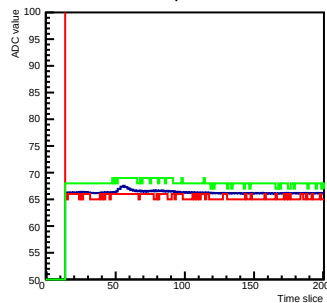
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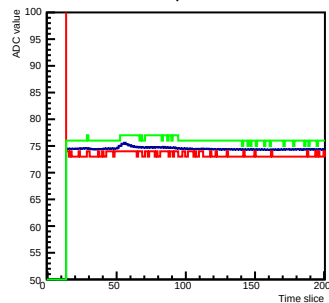
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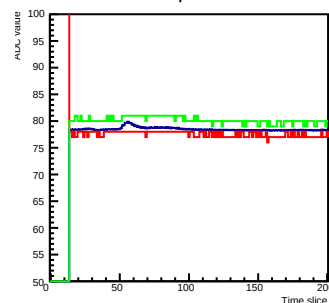
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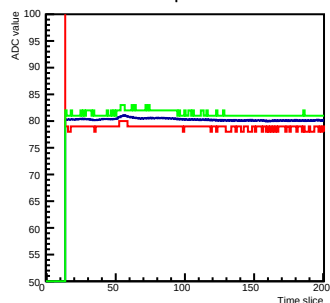
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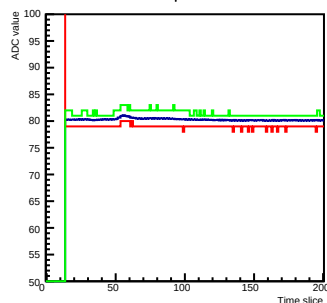
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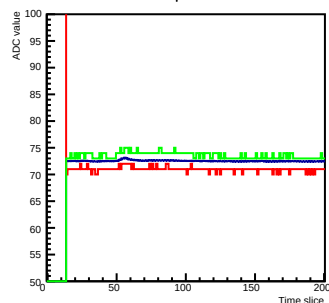
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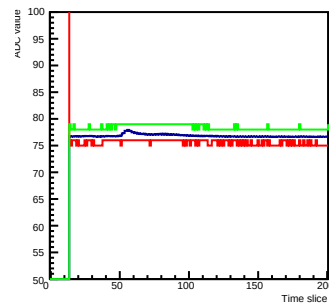
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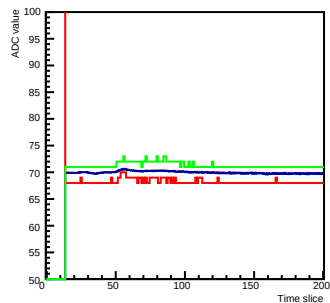
Time mcm 0 chip 4 chan 11 s0-200



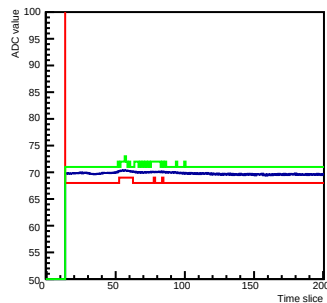
Time mcm 0 chip 4 chan 12 s0-200



Time mcm 0 chip 4 chan 13 s0-200



Time mcm 0 chip 4 chan 14 s0-200



Time mcm 0 chip 4 chan 15 s0-200

