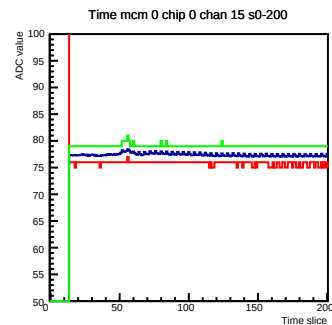
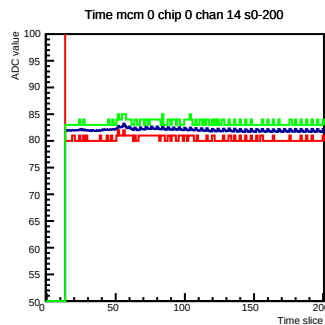
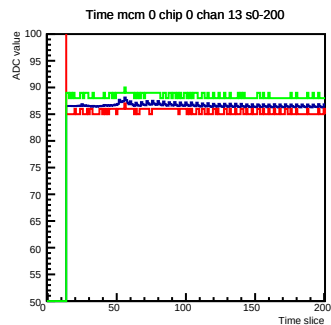
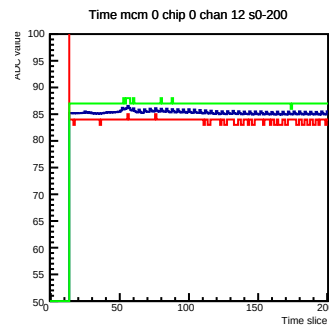
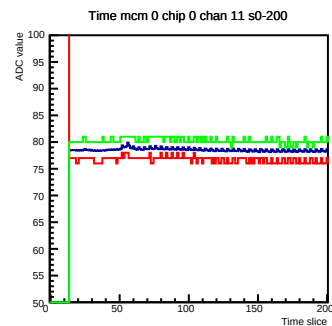
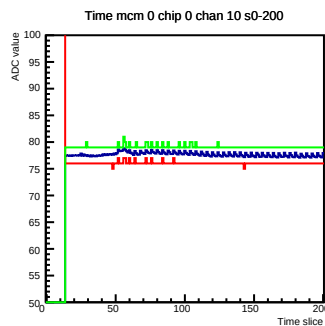
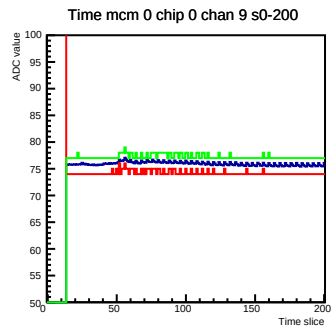
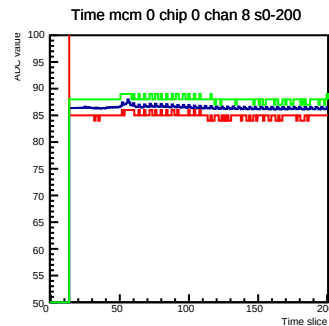
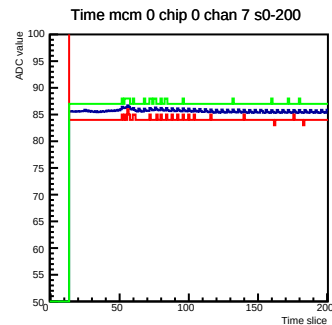
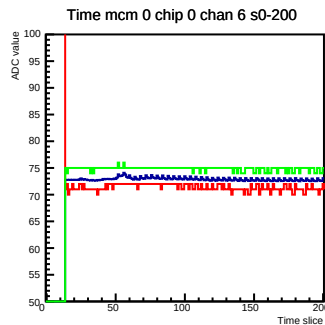
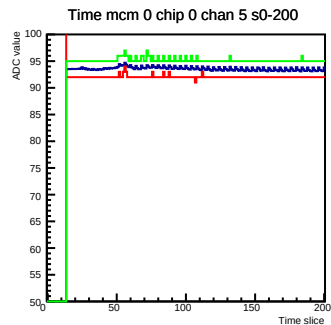
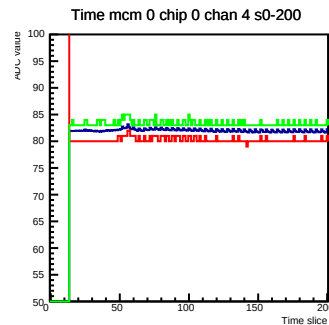
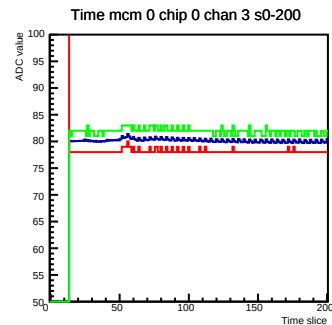
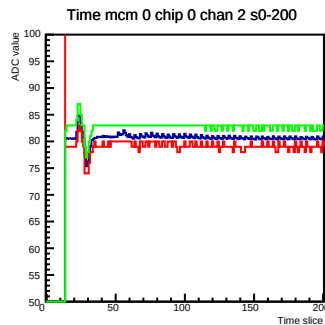
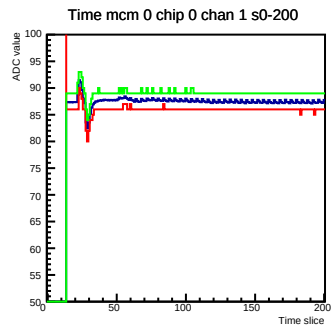
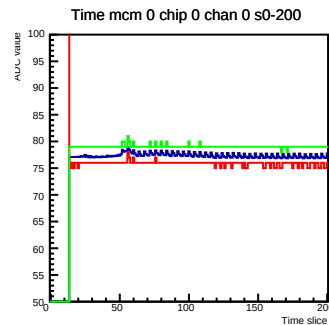
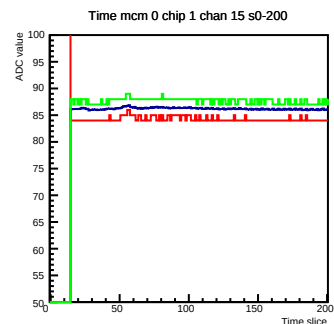
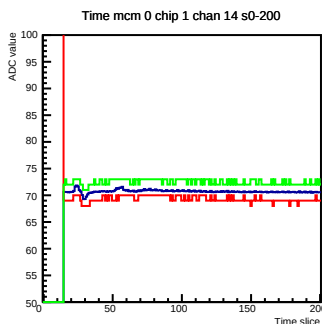
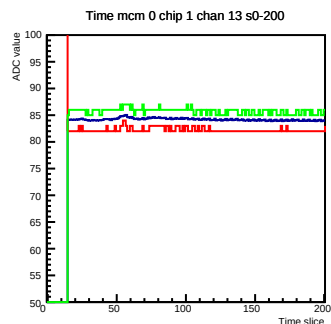
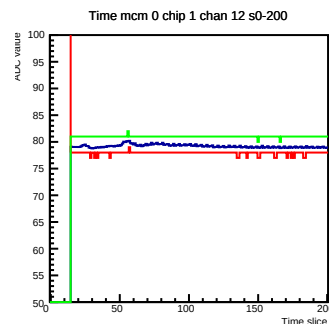
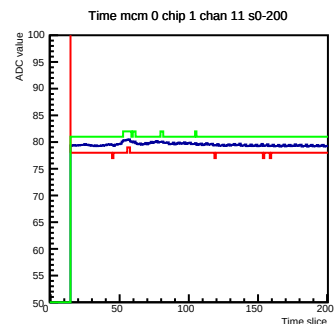
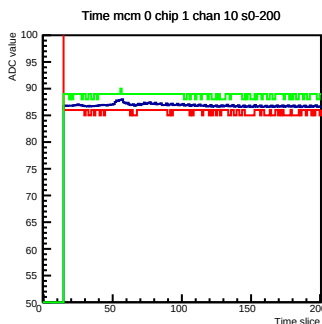
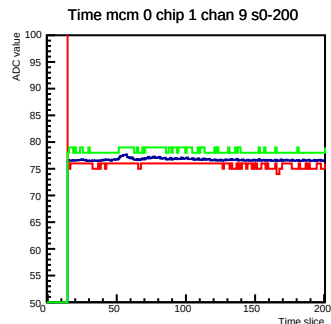
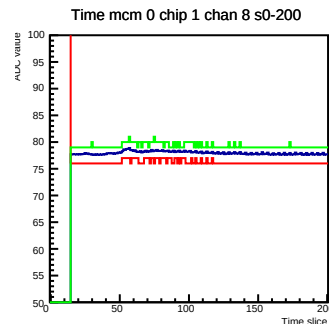
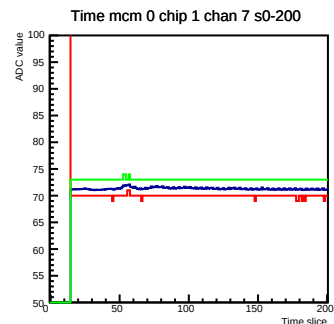
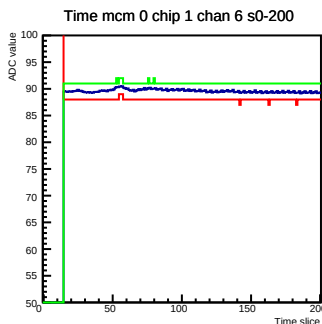
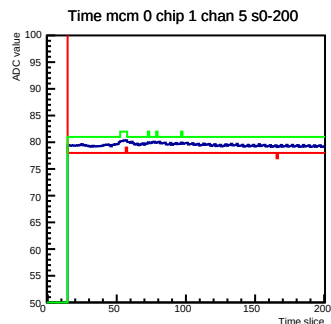
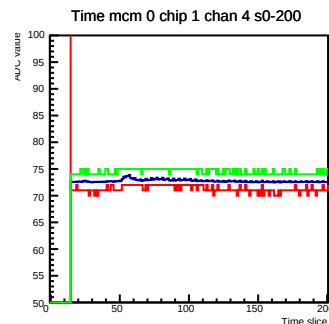
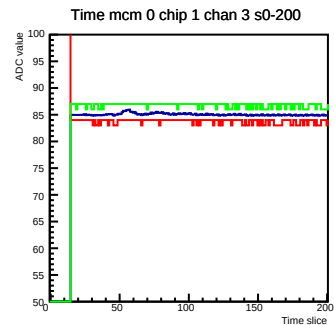
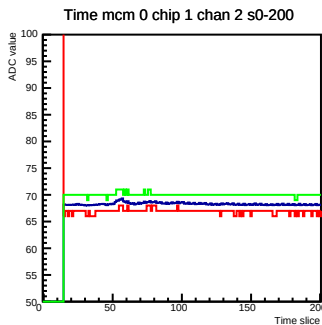
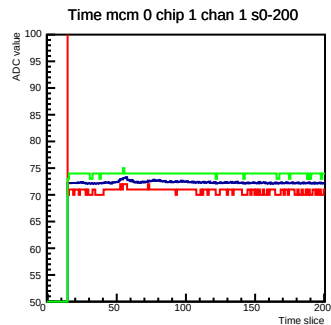
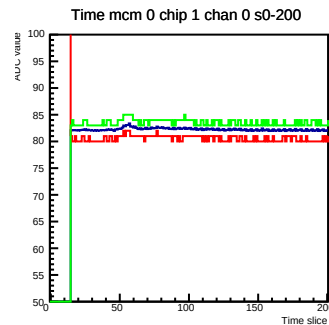
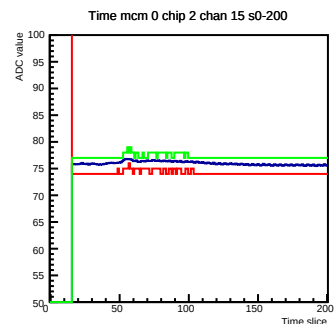
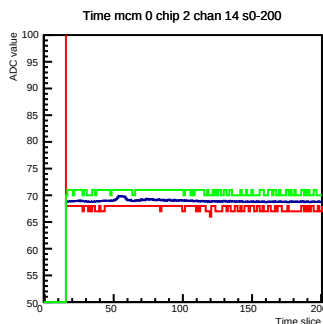
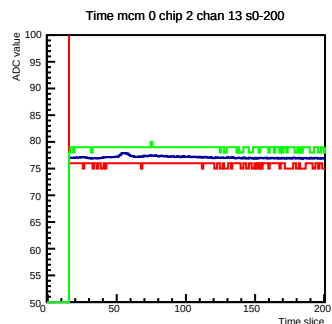
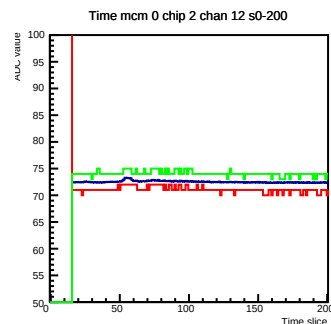
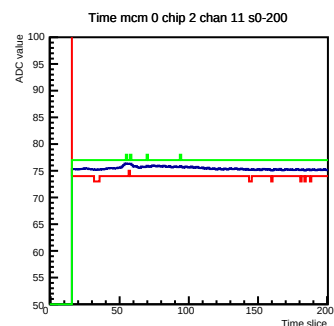
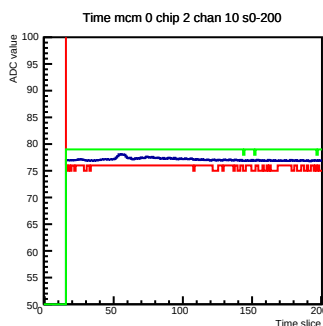
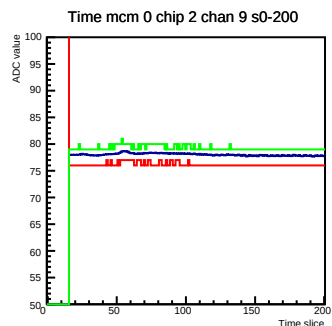
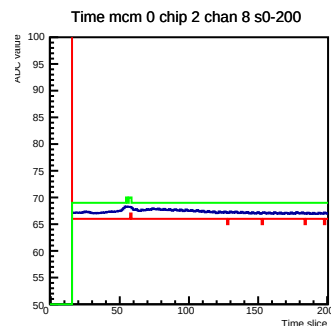
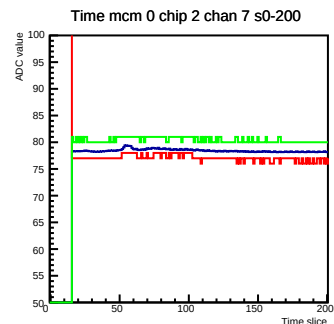
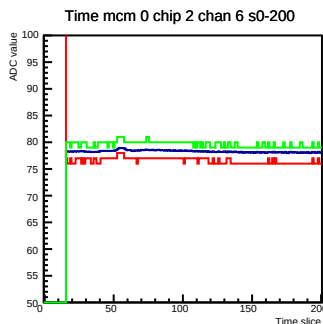
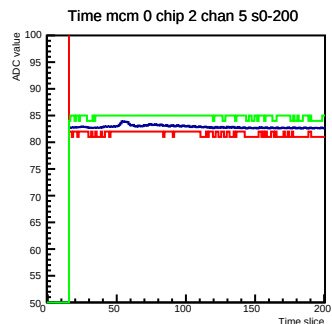
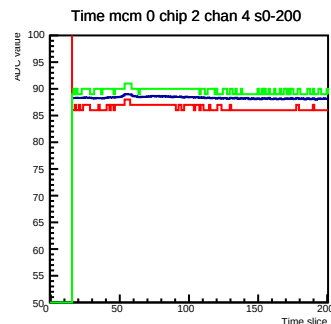
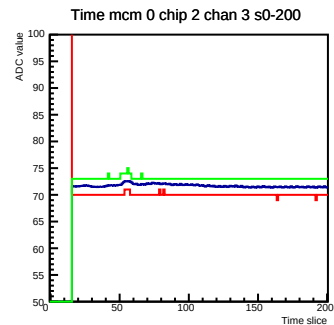
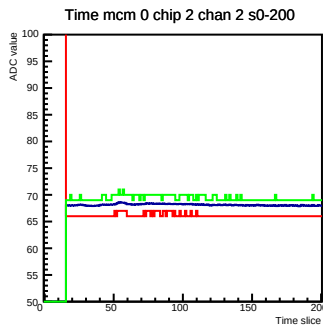
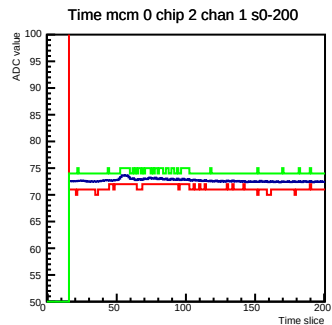
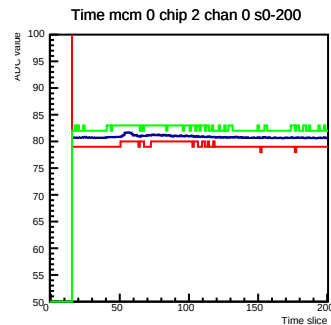
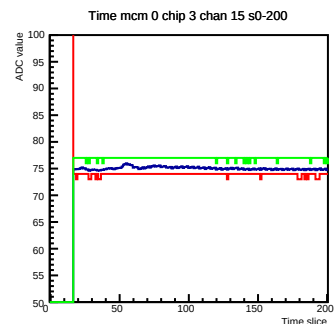
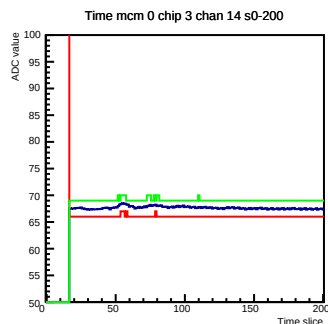
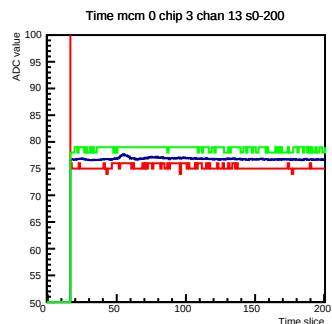
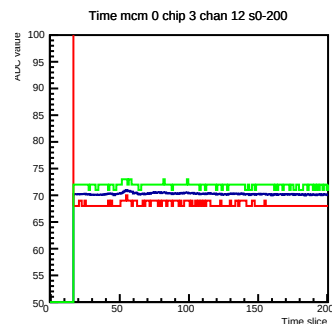
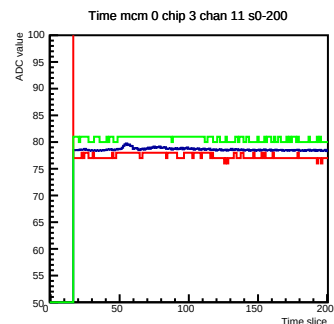
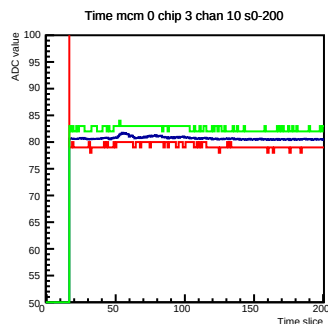
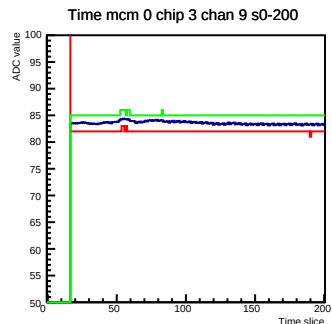
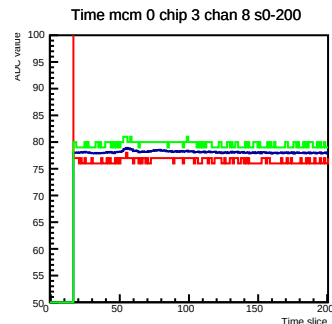
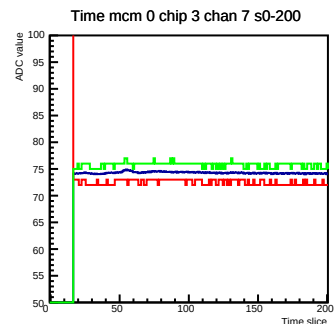
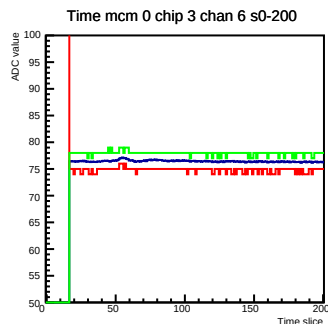
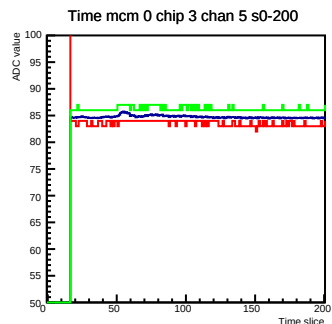
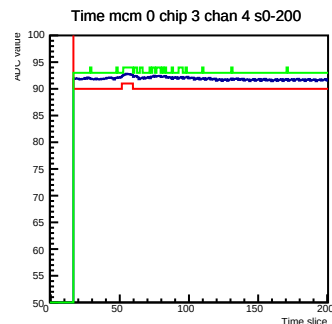
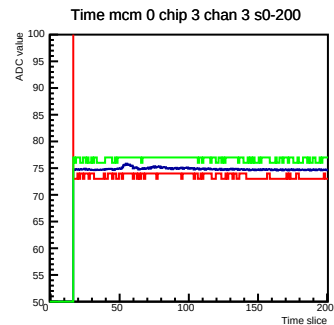
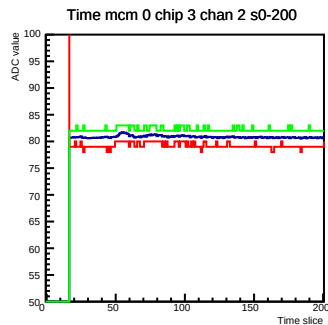
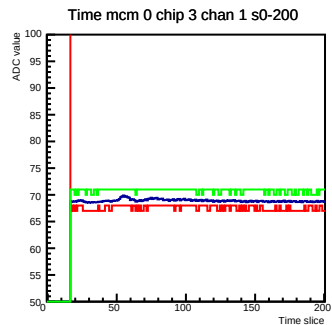
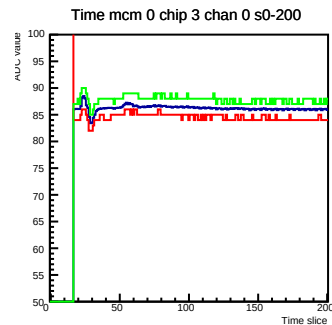
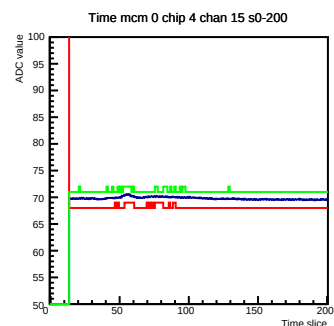
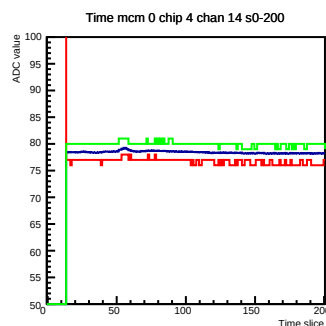
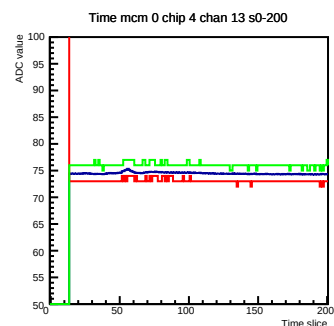
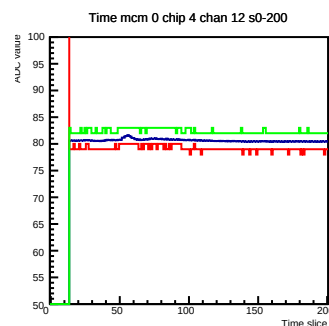
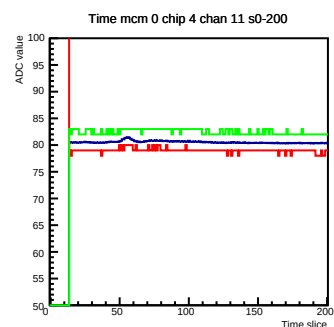
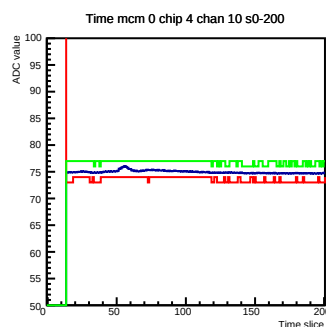
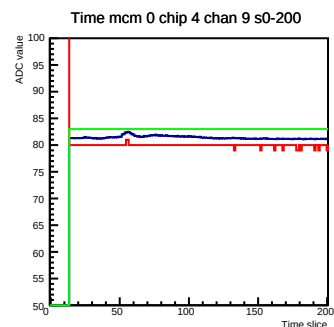
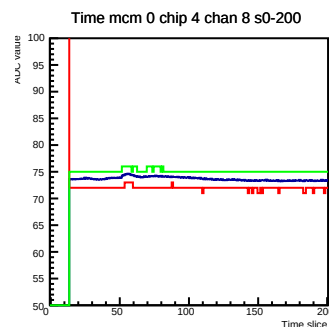
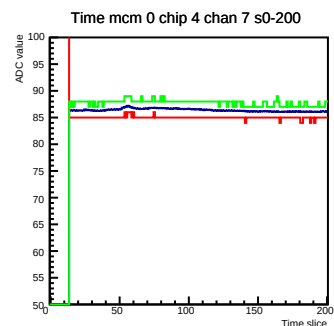
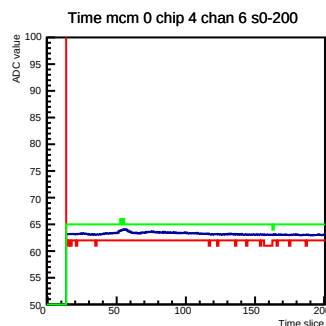
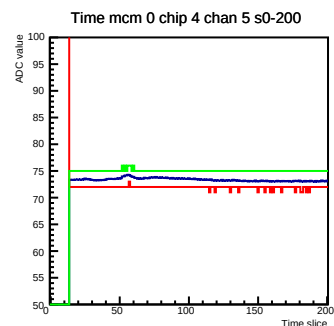
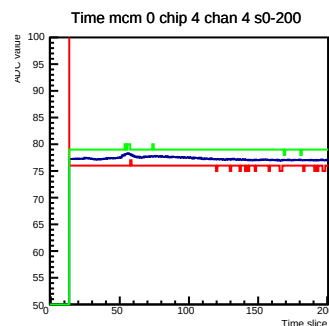
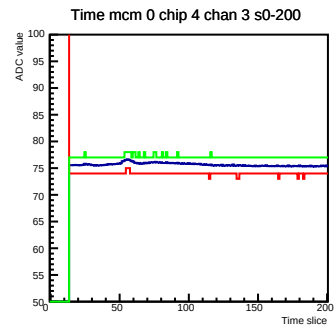
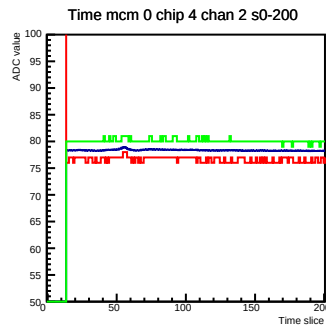
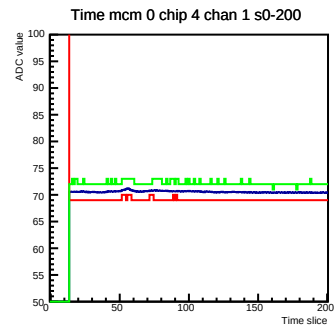
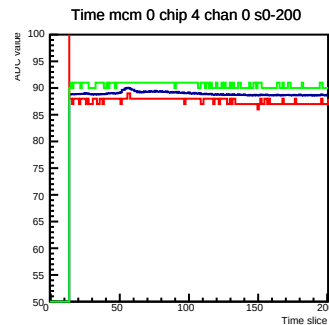


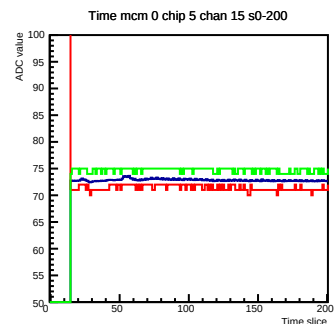
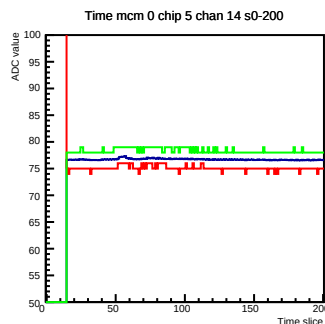
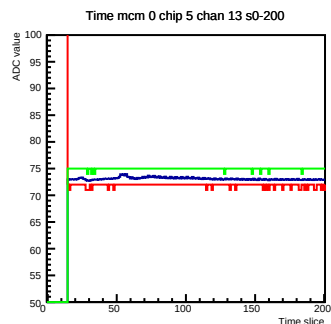
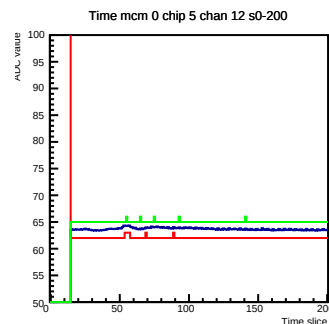
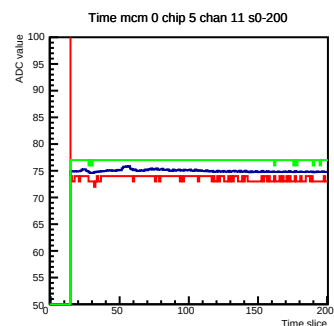
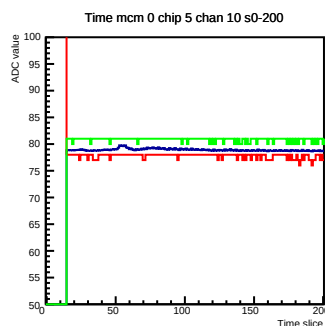
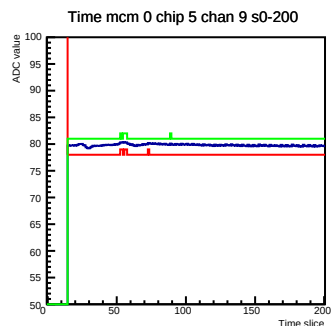
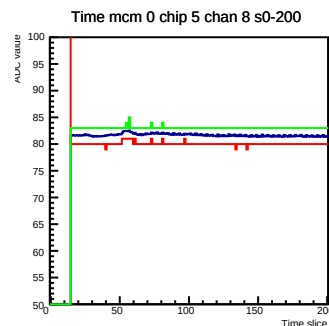
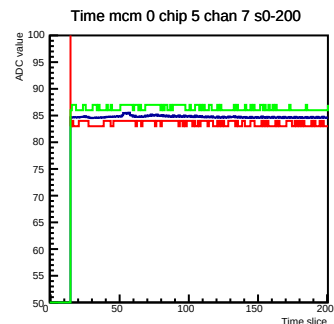
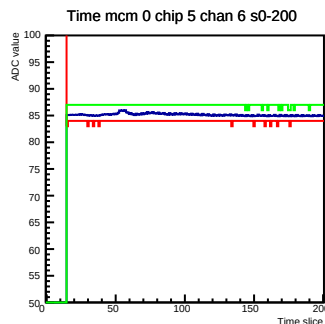
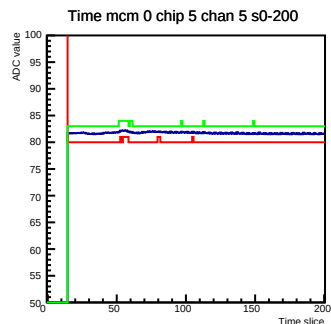
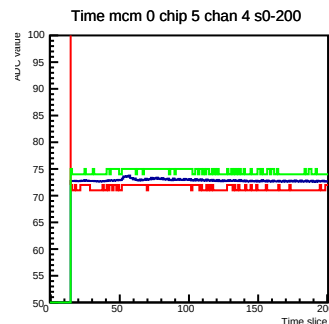
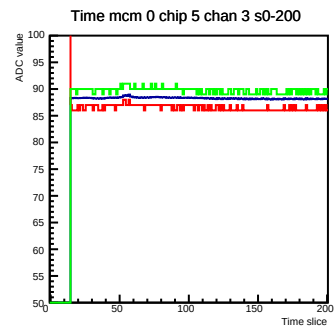
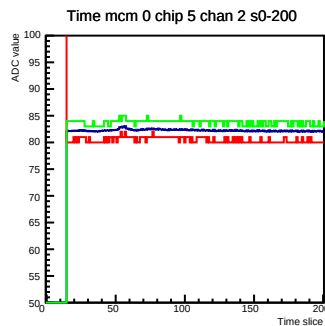
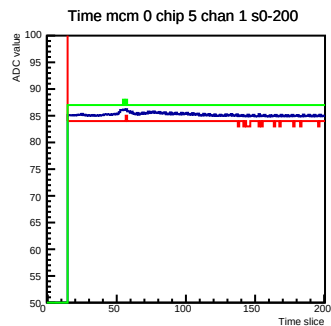
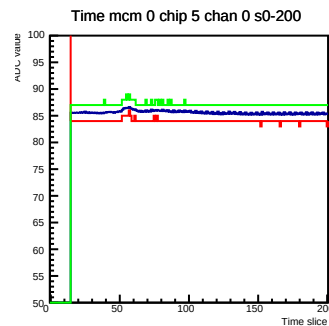




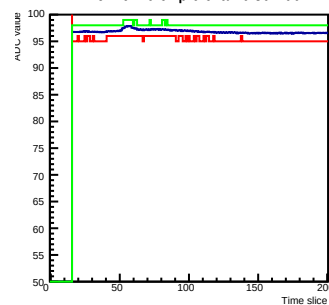




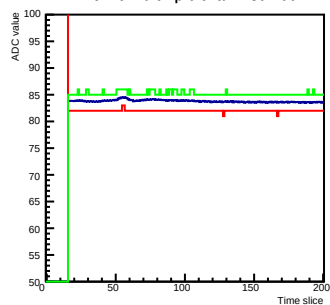




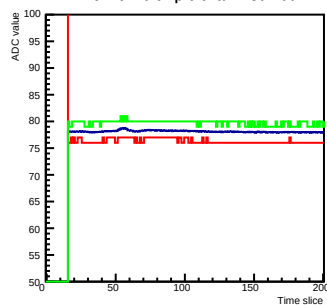
Time mcm 0 chip 6 chan 0 s0-200



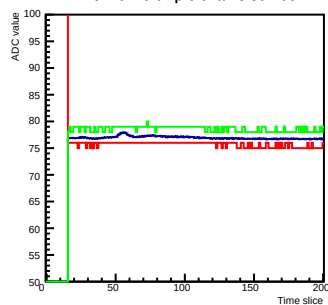
Time mcm 0 chip 6 chan 1 s0-200



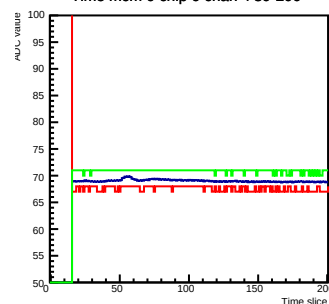
Time mcm 0 chip 6 chan 2 s0-200



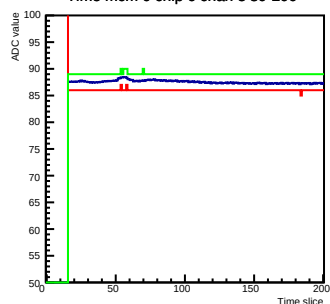
Time mcm 0 chip 6 chan 3 s0-200



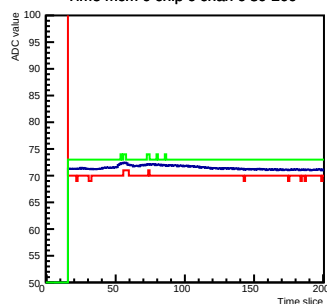
Time mcm 0 chip 6 chan 4 s0-200



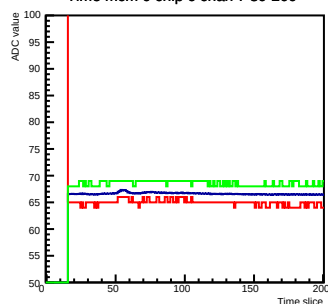
Time mcm 0 chip 6 chan 5 s0-200



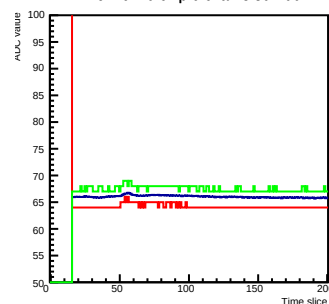
Time mcm 0 chip 6 chan 6 s0-200



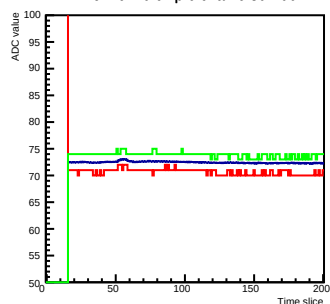
Time mcm 0 chip 6 chan 7 s0-200



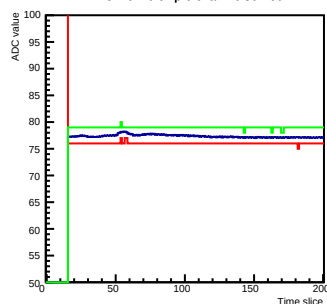
Time mcm 0 chip 6 chan 8 s0-200



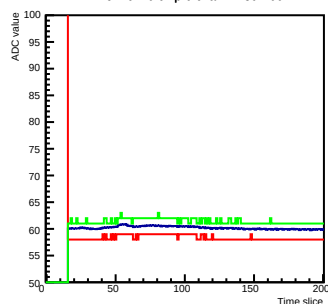
Time mcm 0 chip 6 chan 9 s0-200



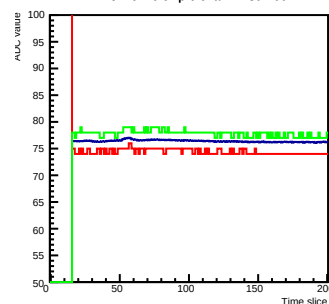
Time mcm 0 chip 6 chan 10 s0-200



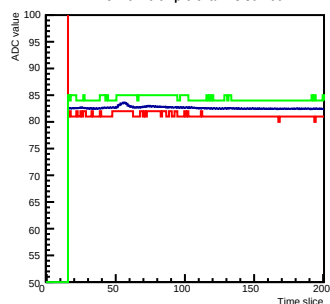
Time mcm 0 chip 6 chan 11 s0-200



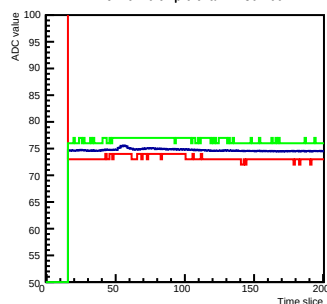
Time mcm 0 chip 6 chan 12 s0-200



Time mcm 0 chip 6 chan 13 s0-200



Time mcm 0 chip 6 chan 14 s0-200



Time mcm 0 chip 6 chan 15 s0-200

