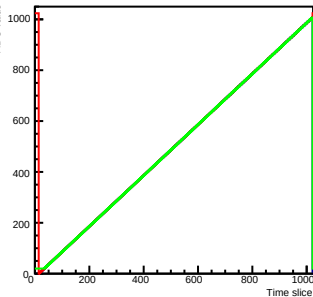
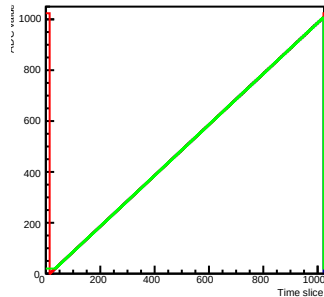


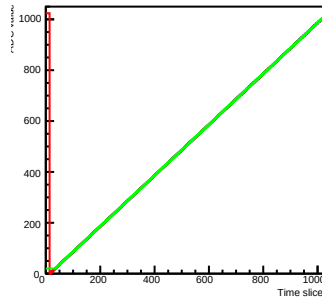
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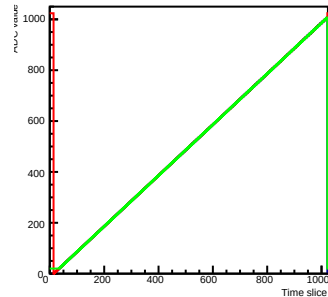
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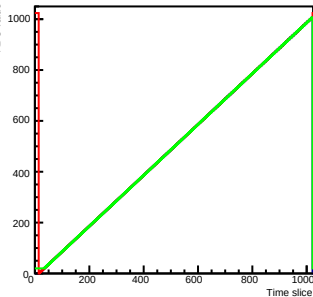
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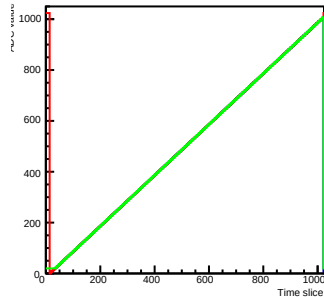
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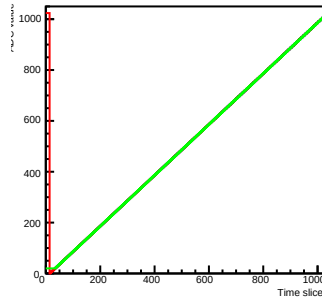
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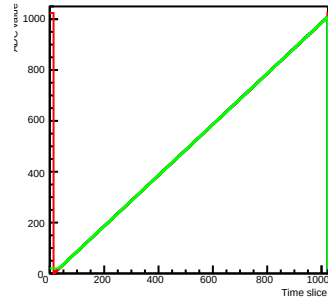
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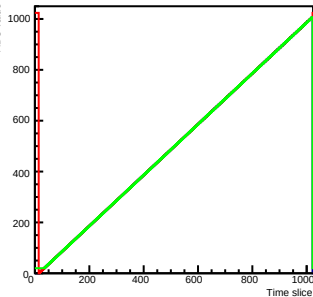
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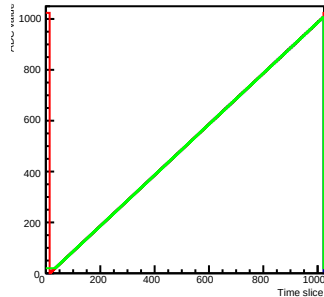
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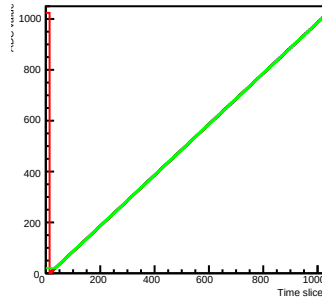
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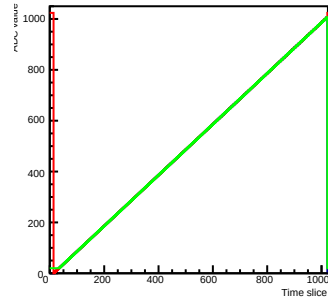
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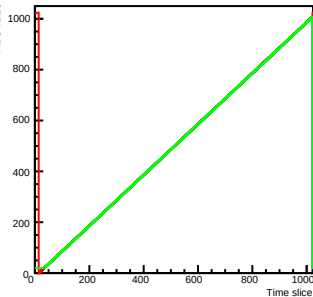
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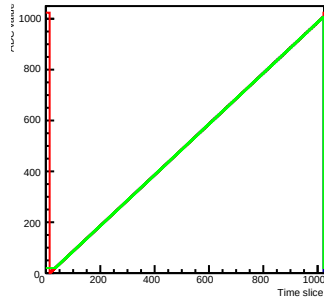
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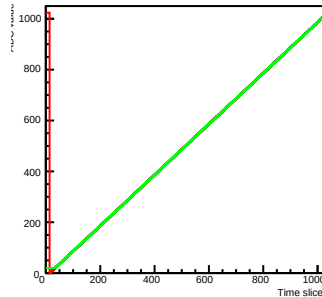
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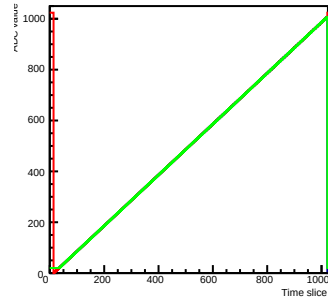
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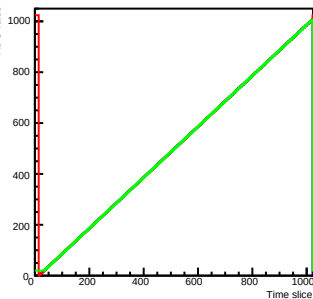
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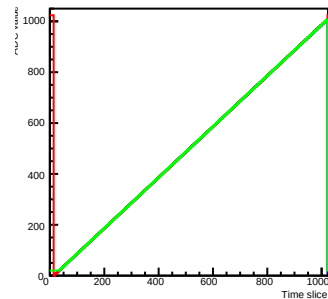
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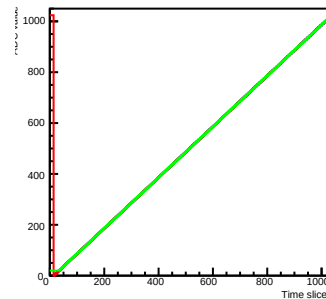
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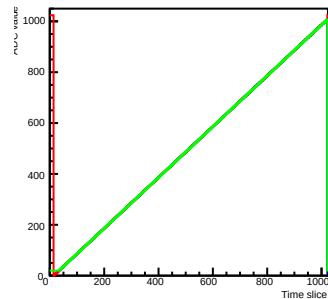
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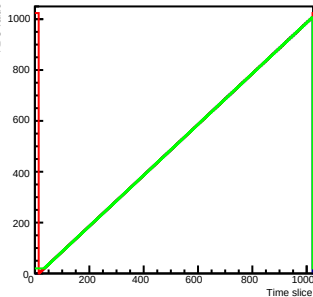
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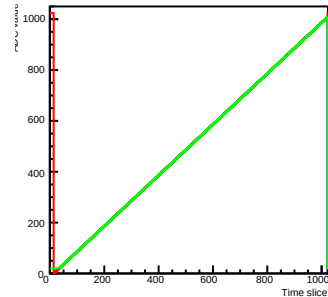
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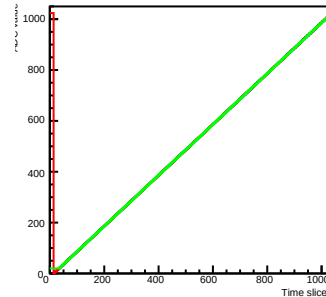
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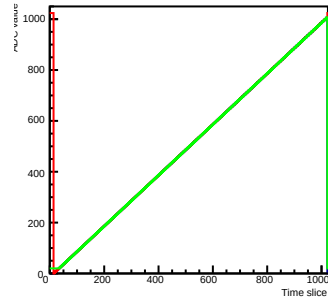
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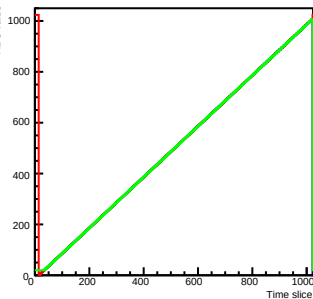
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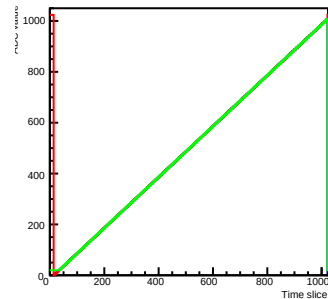
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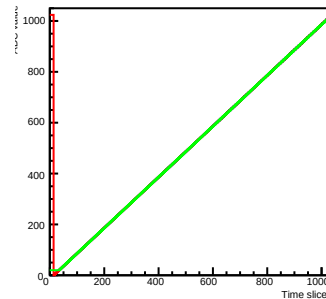
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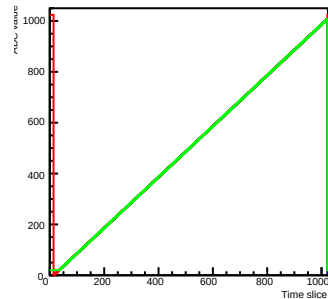
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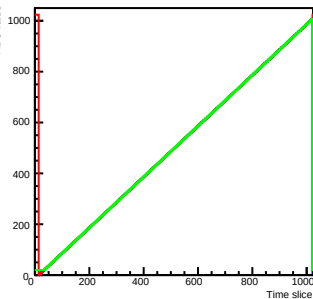
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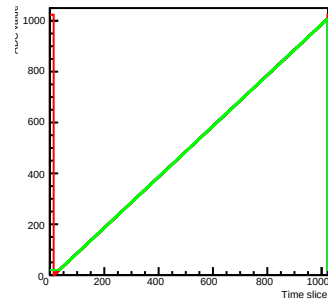
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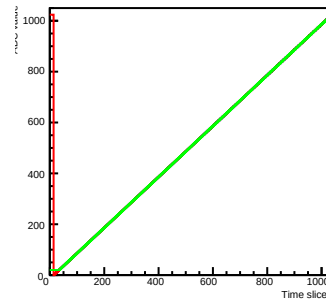
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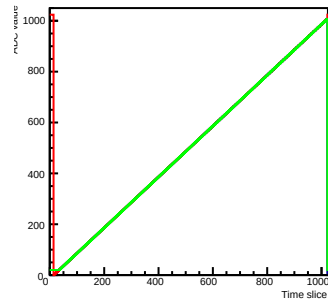
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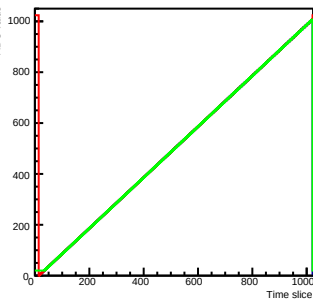
mcm 0 chip 1 chan 14 ramp



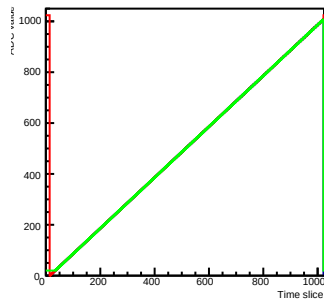
mcm 0 chip 1 chan 15 ramp



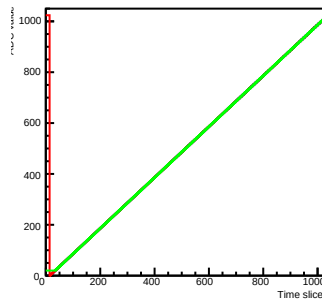
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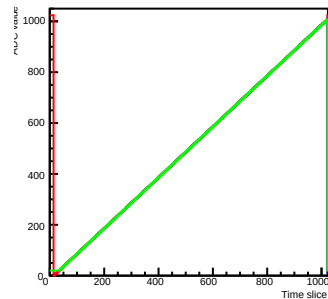
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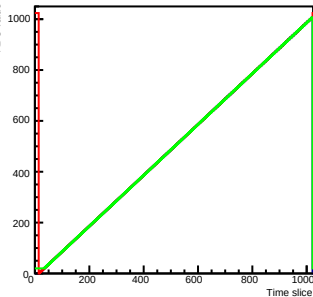
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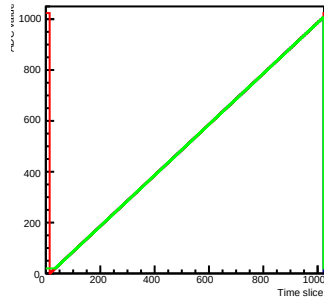
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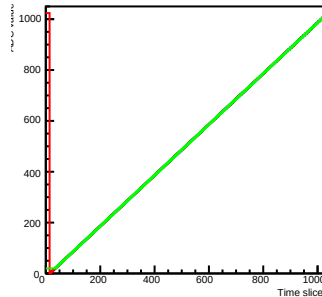
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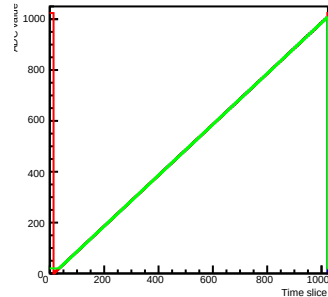
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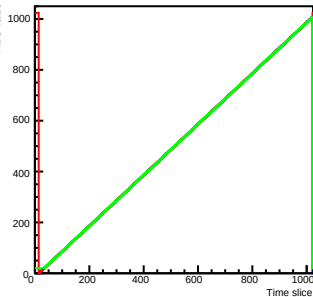
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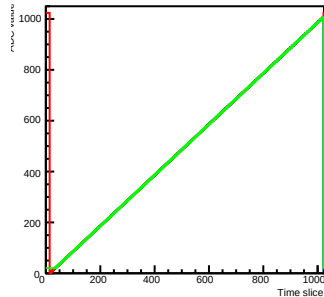
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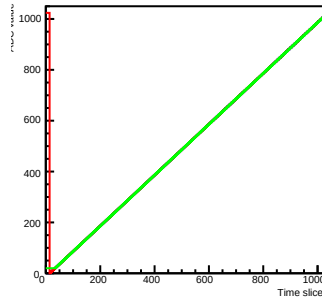
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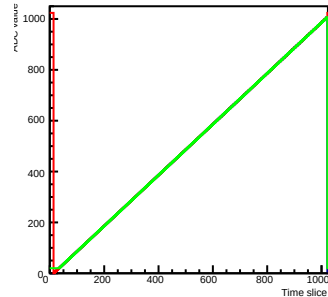
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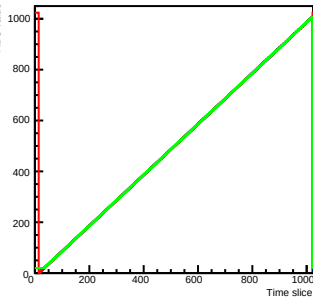
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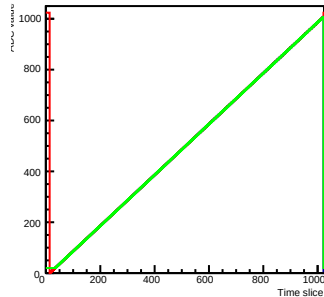
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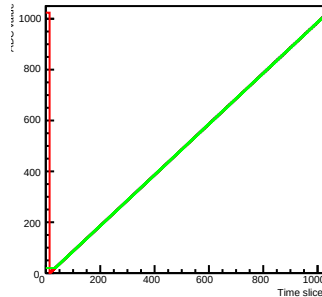
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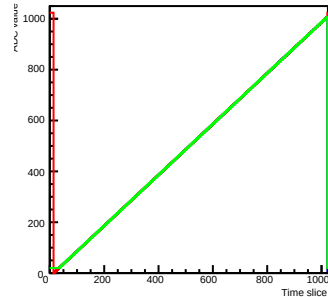
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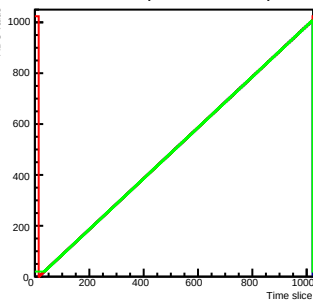
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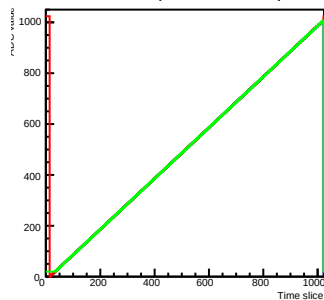
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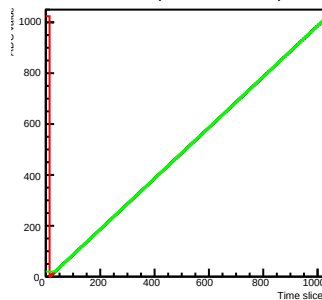
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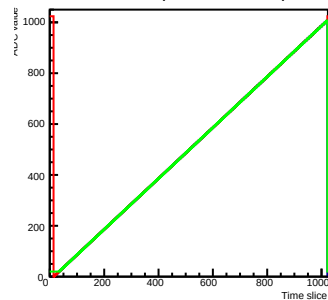
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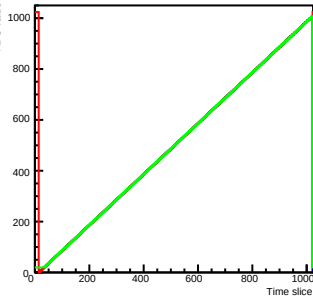
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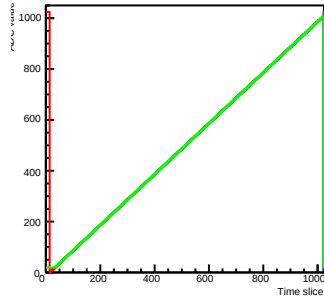
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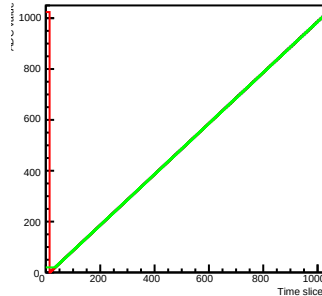
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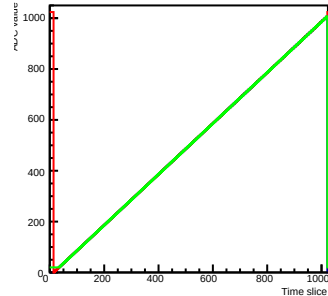
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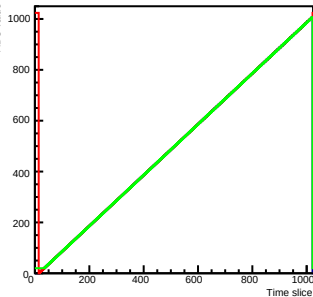
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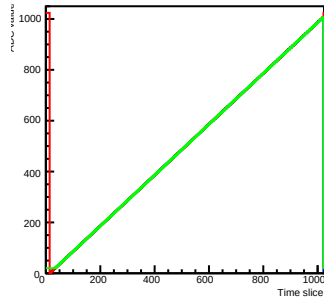
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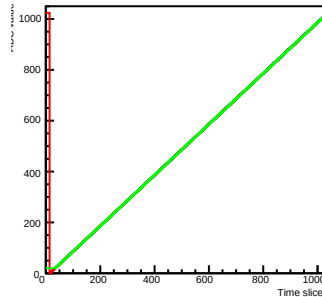
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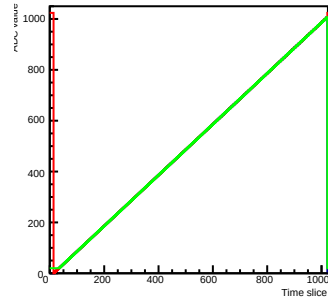
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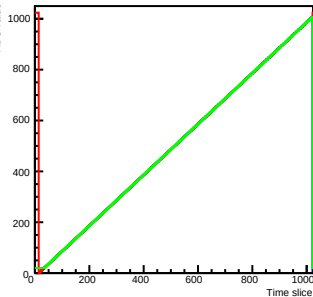
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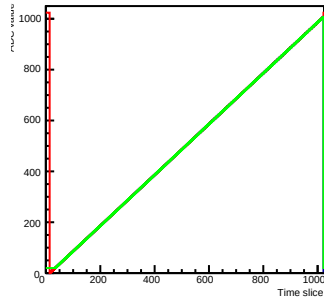
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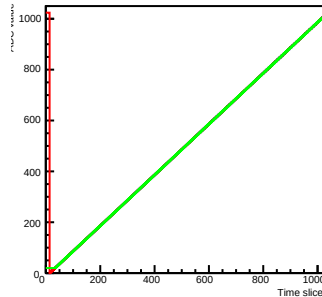
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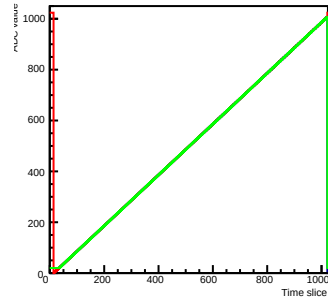
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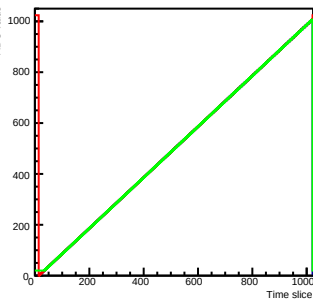
mcm 0 chip 3 chan 14 ramp



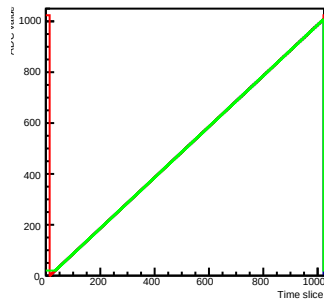
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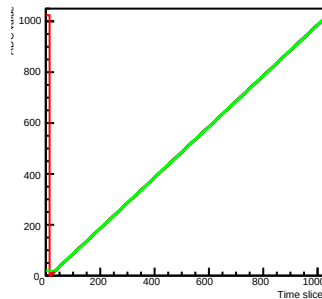
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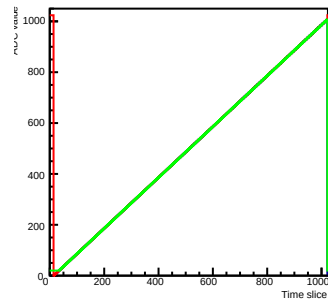
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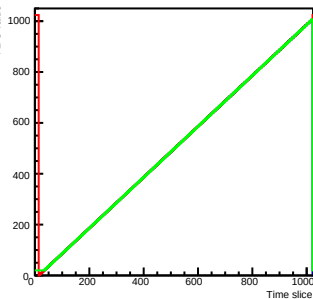
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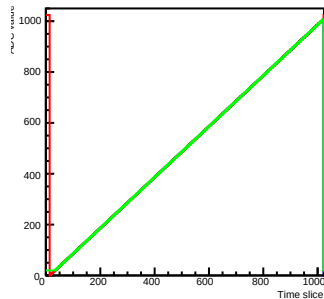
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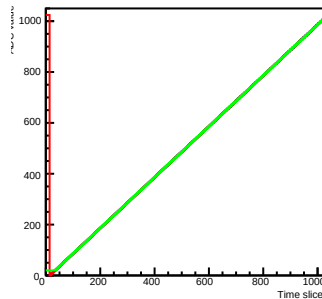
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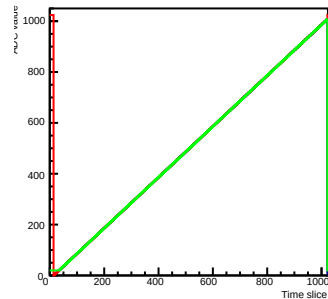
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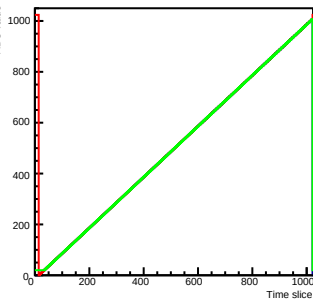
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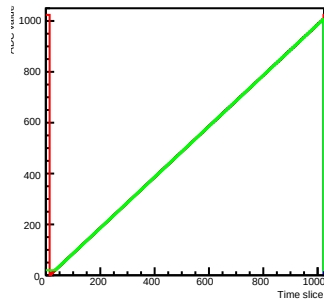
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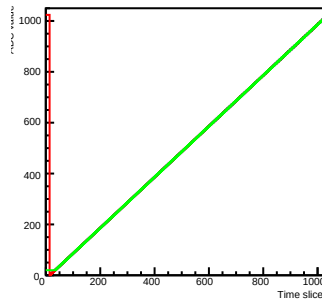
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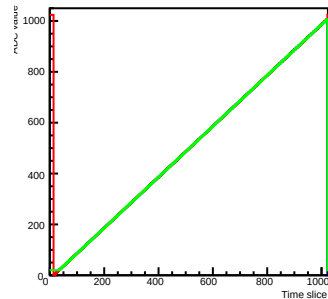
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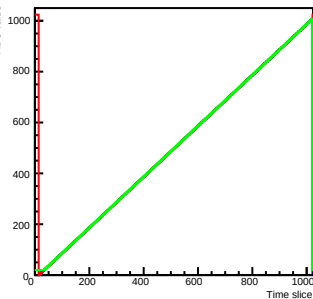
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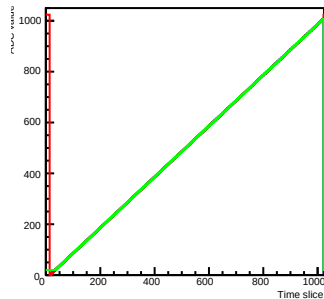
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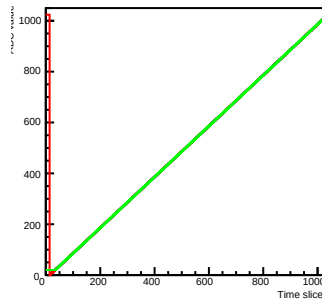
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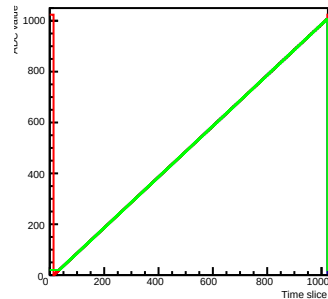
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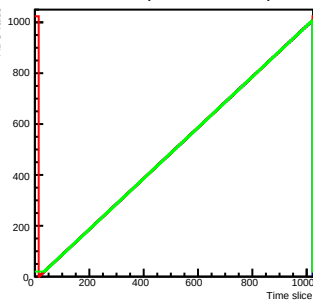
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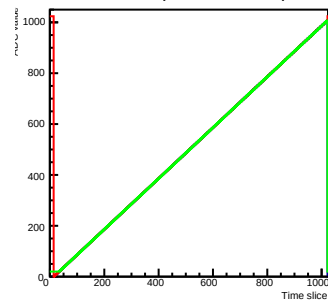
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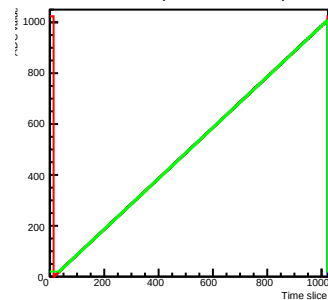
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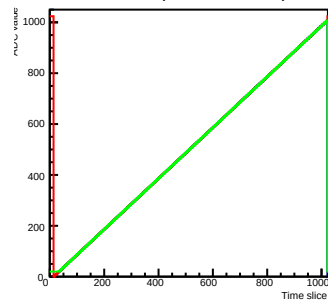
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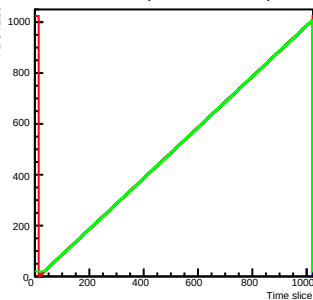
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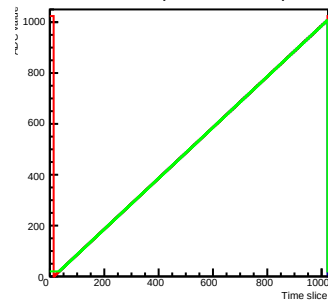
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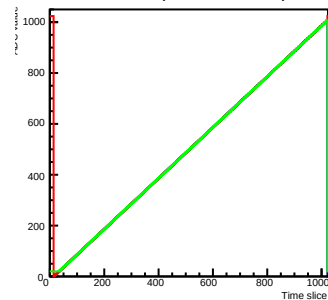
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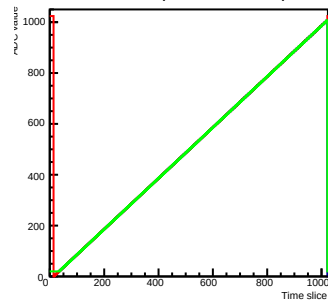
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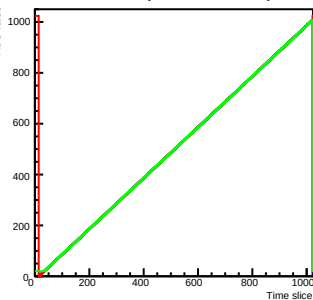
mcm 0 chip 5 chan 6 ramp



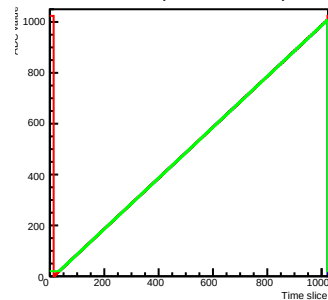
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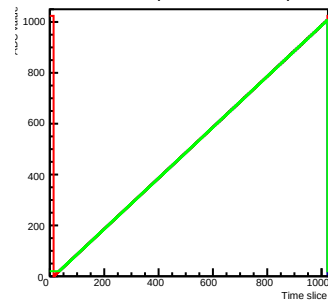
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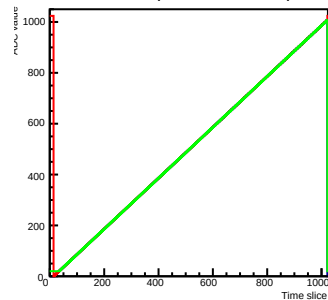
mcm 0 chip 5 chan 9 ramp



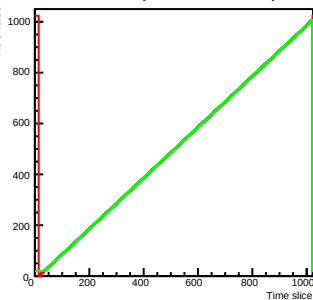
mcm 0 chip 5 chan 10 ramp



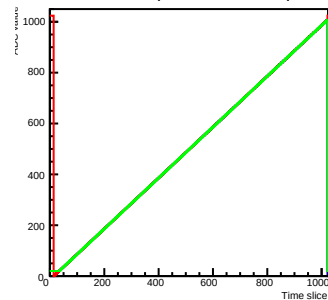
mcm 0 chip 5 chan 11 ramp



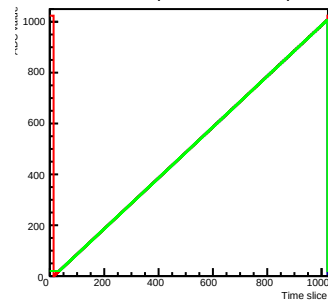
mcm 0 chip 5 chan 12 ramp



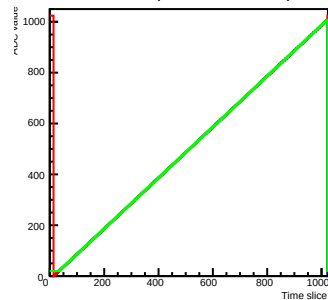
mcm 0 chip 5 chan 13 ramp



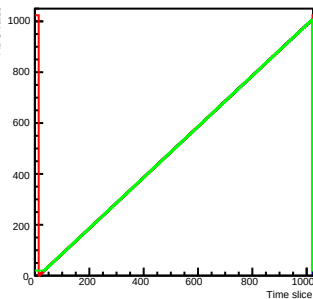
mcm 0 chip 5 chan 14 ramp



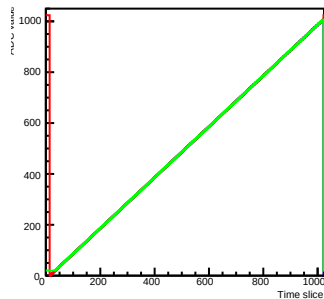
mcm 0 chip 5 chan 15 ramp



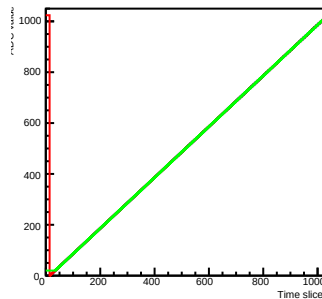
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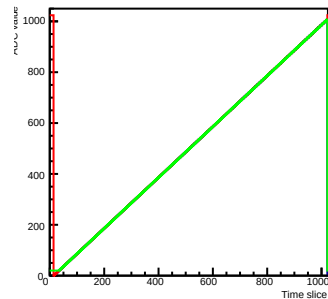
mcm 0 chip 6 chan 1 ramp



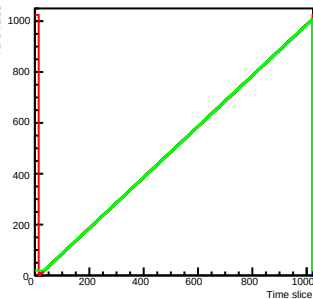
mcm 0 chip 6 chan 2 ramp



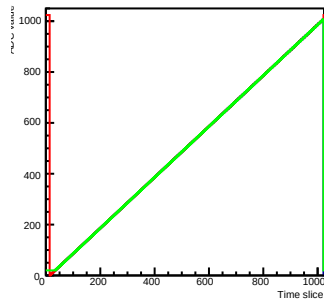
mcm 0 chip 6 chan 3 ramp



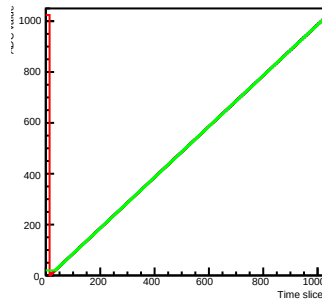
mcm 0 chip 6 chan 4 ramp



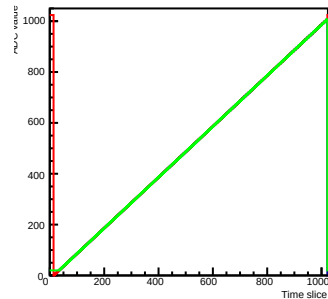
mcm 0 chip 6 chan 5 ramp



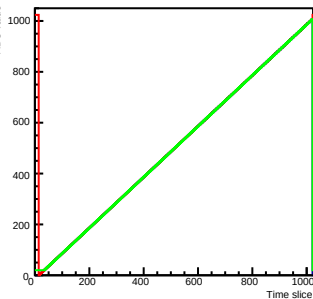
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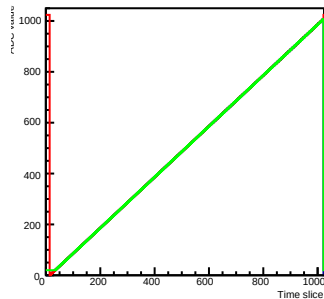
mcm 0 chip 6 chan 7 ramp



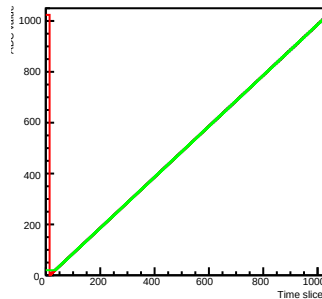
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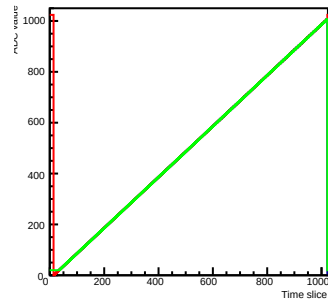
mcm 0 chip 6 chan 9 ramp



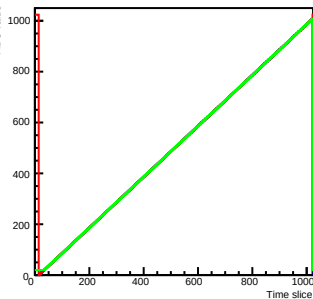
mcm 0 chip 6 chan 10 ramp



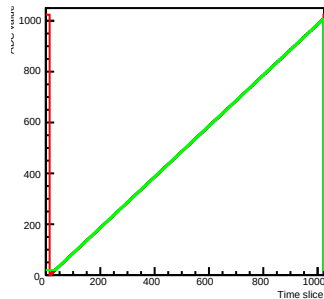
mcm 0 chip 6 chan 11 ramp



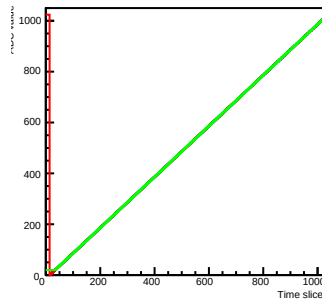
mcm 0 chip 6 chan 12 ramp



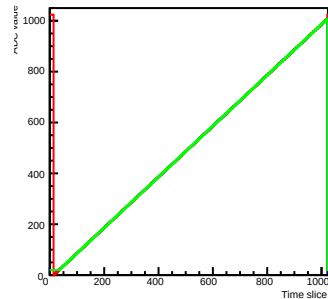
mcm 0 chip 6 chan 13 ramp



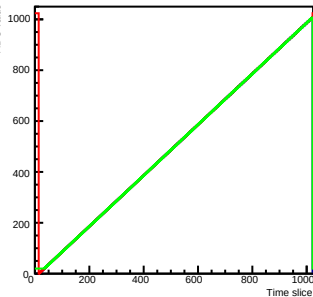
mcm 0 chip 6 chan 14 ramp



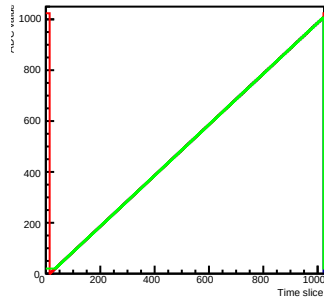
mcm 0 chip 6 chan 15 ramp



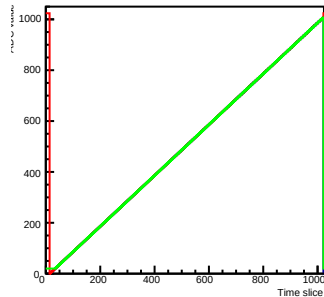
mcm 0 chip 7 chan 0 ramp



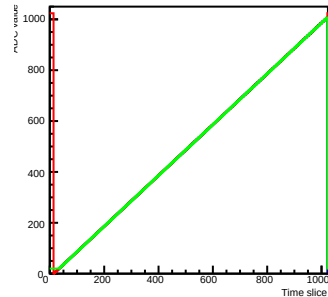
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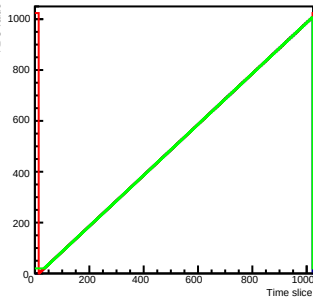
mcm 0 chip 7 chan 2 ramp



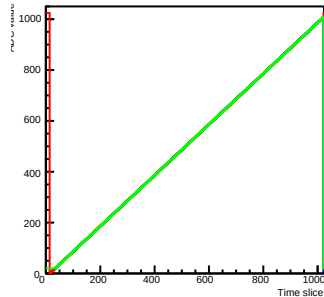
mcm 0 chip 7 chan 3 ramp



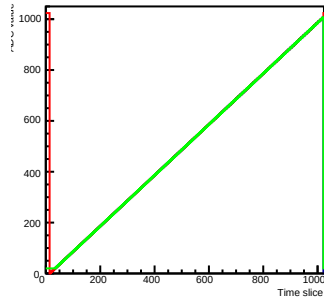
mcm 0 chip 7 chan 4 ramp



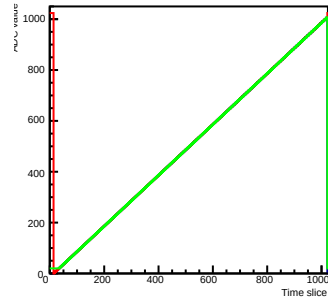
mcm 0 chip 7 chan 5 ramp



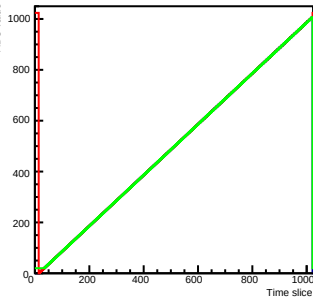
mcm 0 chip 7 chan 6 ramp



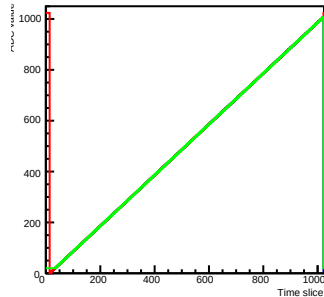
mcm 0 chip 7 chan 7 ramp



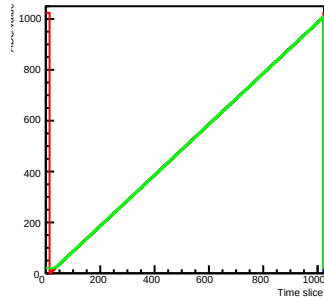
mcm 0 chip 7 chan 8 ramp



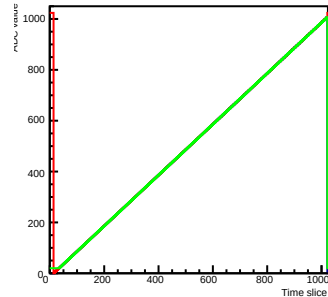
mcm 0 chip 7 chan 9 ramp



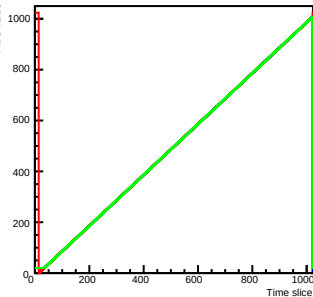
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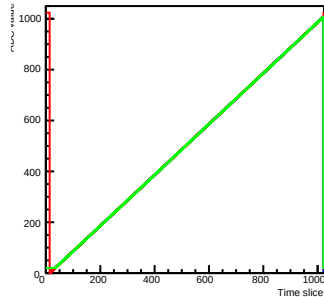
mcm 0 chip 7 chan 11 ramp



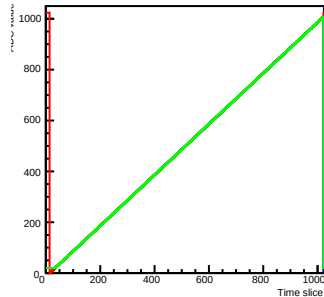
mcm 0 chip 7 chan 12 ramp



mcm 0 chip 7 chan 13 ramp



mcm 0 chip 7 chan 14 ramp



mcm 0 chip 7 chan 15 ramp

