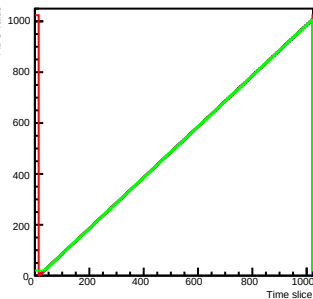
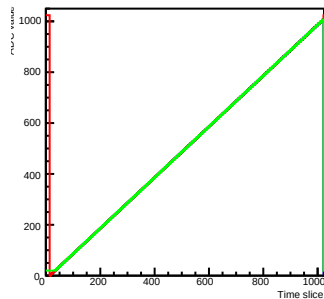


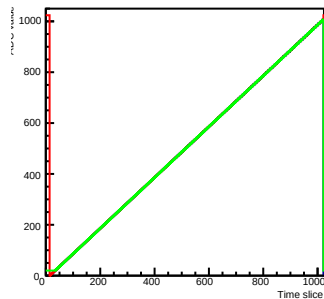
mcm 0 chip 0 chan 0 ramp1



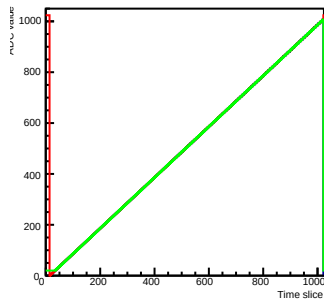
mcm 0 chip 0 chan 1 ramp1



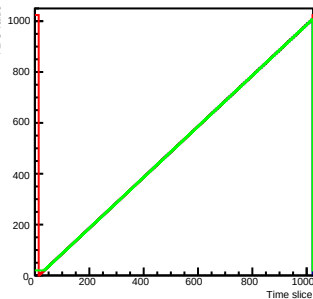
mcm 0 chip 0 chan 2 ramp1



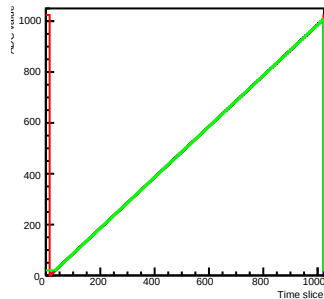
mcm 0 chip 0 chan 3 ramp1



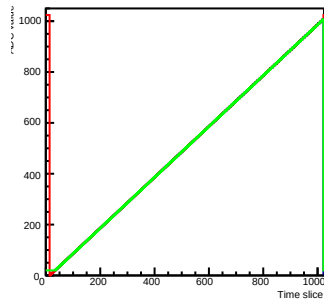
mcm 0 chip 0 chan 4 ramp1



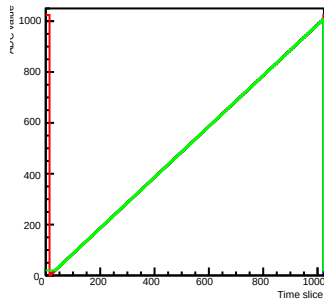
mcm 0 chip 0 chan 5 ramp1



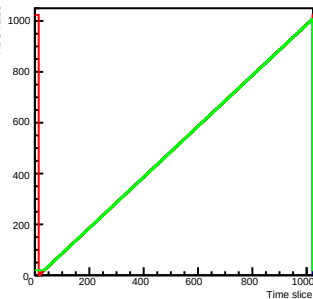
mcm 0 chip 0 chan 6 ramp1



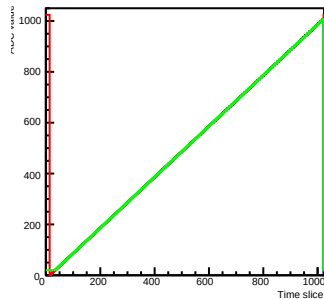
mcm 0 chip 0 chan 7 ramp1



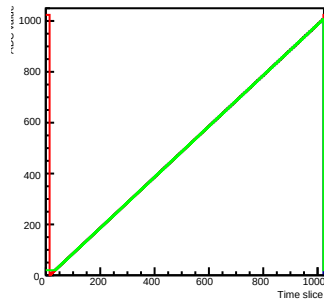
mcm 0 chip 0 chan 8 ramp1



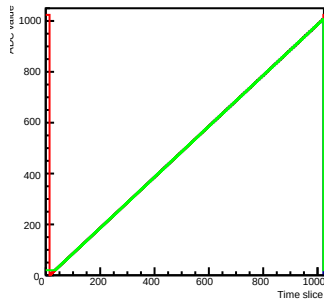
mcm 0 chip 0 chan 9 ramp1



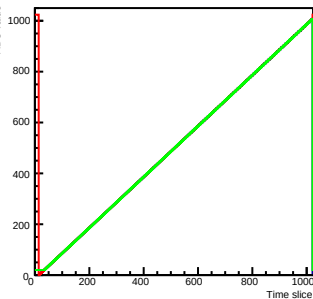
mcm 0 chip 0 chan 10 ramp1



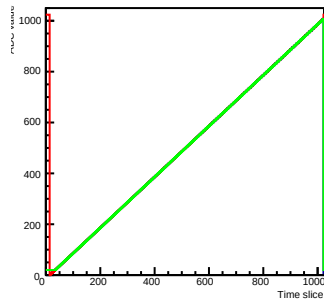
mcm 0 chip 0 chan 11 ramp1



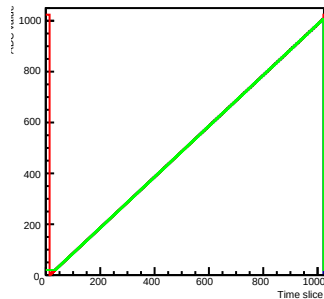
mcm 0 chip 0 chan 12 ramp1



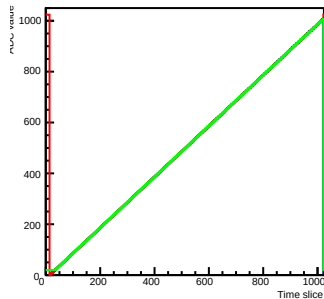
mcm 0 chip 0 chan 13 ramp1



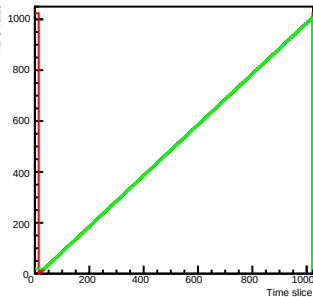
mcm 0 chip 0 chan 14 ramp1



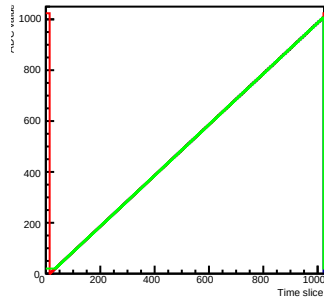
mcm 0 chip 0 chan 15 ramp1



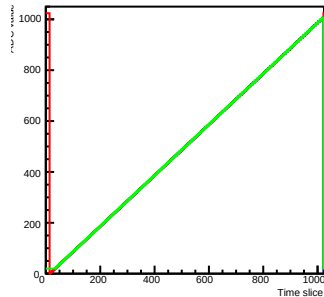
mcm 0 chip 1 chan 0 ramp1



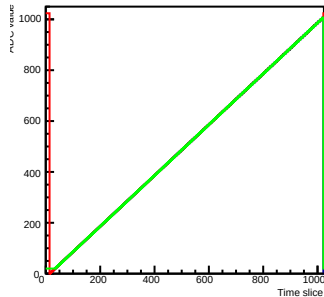
mcm 0 chip 1 chan 1 ramp1



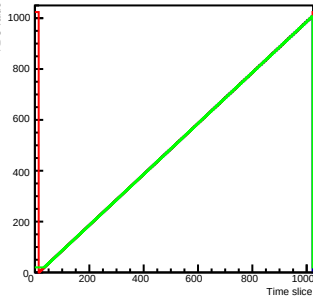
mcm 0 chip 1 chan 2 ramp1



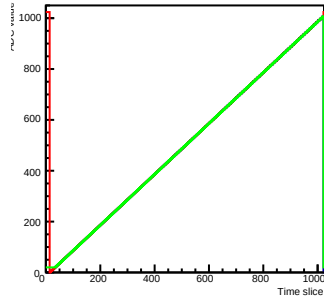
mcm 0 chip 1 chan 3 ramp1



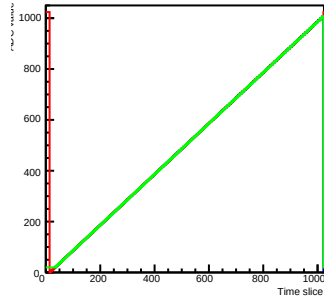
mcm 0 chip 1 chan 4 ramp1



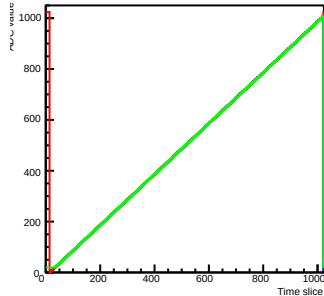
mcm 0 chip 1 chan 5 ramp1



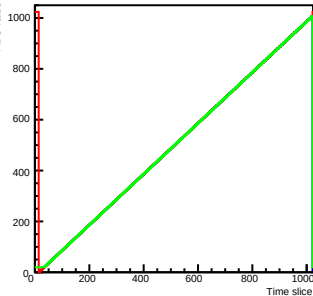
mcm 0 chip 1 chan 6 ramp1



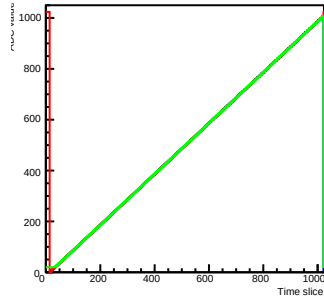
mcm 0 chip 1 chan 7 ramp1



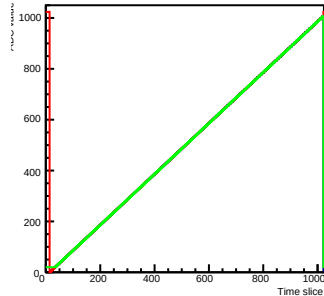
mcm 0 chip 1 chan 8 ramp1



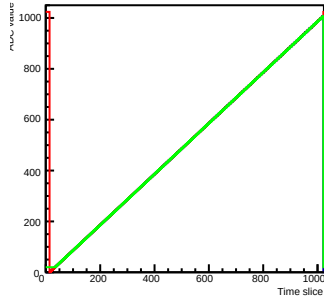
mcm 0 chip 1 chan 9 ramp1



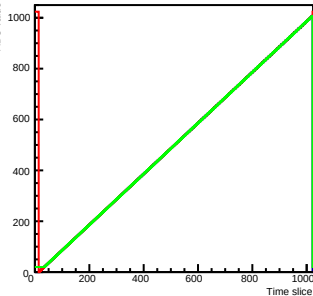
mcm 0 chip 1 chan 10 ramp1



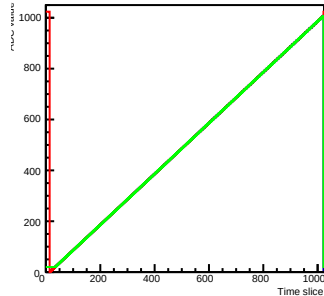
mcm 0 chip 1 chan 11 ramp1



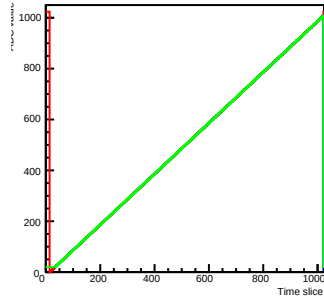
mcm 0 chip 1 chan 12 ramp1



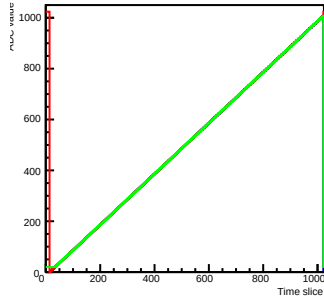
mcm 0 chip 1 chan 13 ramp1



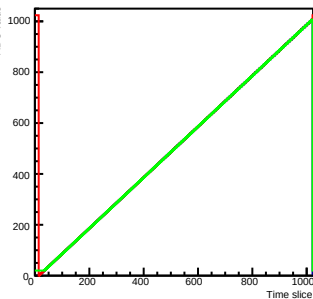
mcm 0 chip 1 chan 14 ramp1



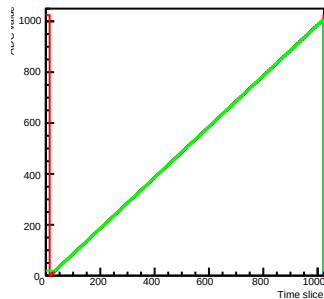
mcm 0 chip 1 chan 15 ramp1



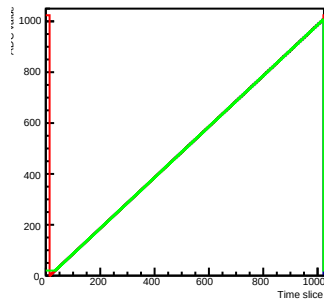
mcm 0 chip 2 chan 0 ramp1



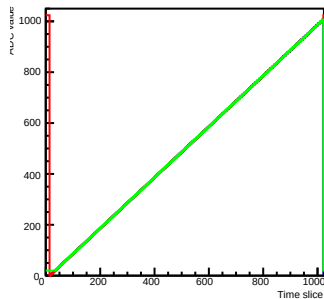
mcm 0 chip 2 chan 1 ramp1



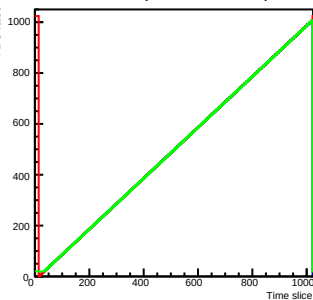
mcm 0 chip 2 chan 2 ramp1



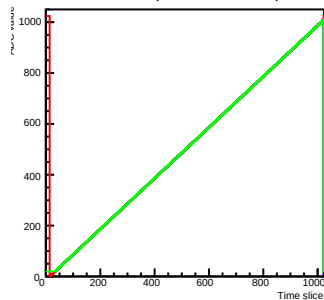
mcm 0 chip 2 chan 3 ramp1



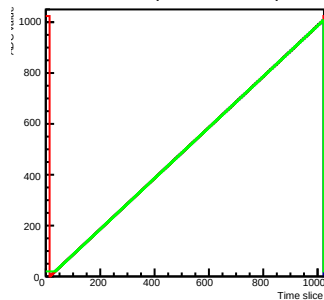
mcm 0 chip 2 chan 4 ramp1



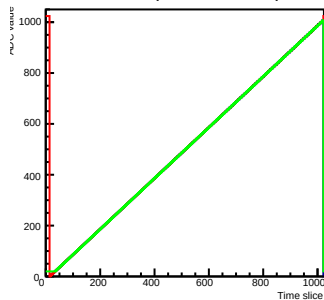
mcm 0 chip 2 chan 5 ramp1



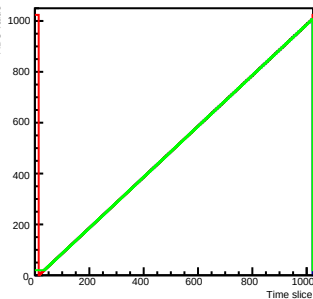
mcm 0 chip 2 chan 6 ramp1



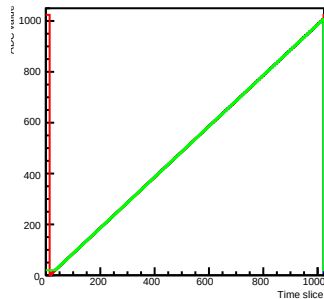
mcm 0 chip 2 chan 7 ramp1



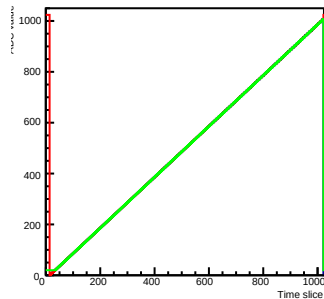
mcm 0 chip 2 chan 8 ramp1



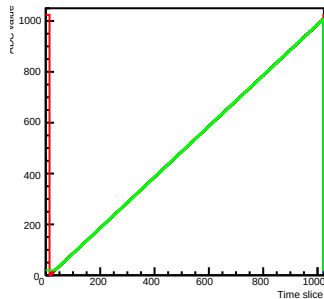
mcm 0 chip 2 chan 9 ramp1



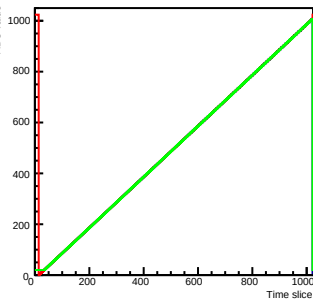
mcm 0 chip 2 chan 10 ramp1



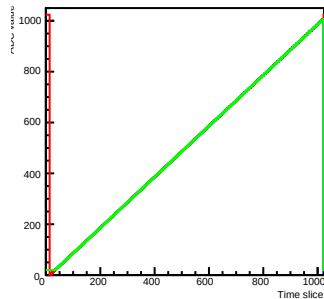
mcm 0 chip 2 chan 11 ramp1



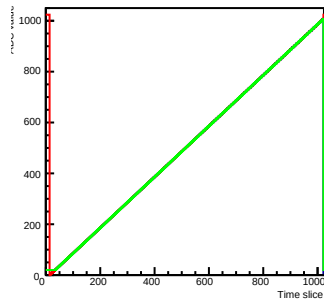
mcm 0 chip 2 chan 12 ramp1



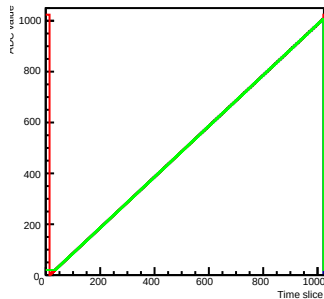
mcm 0 chip 2 chan 13 ramp1

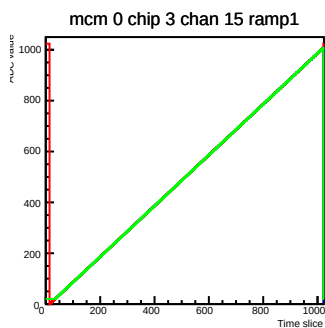
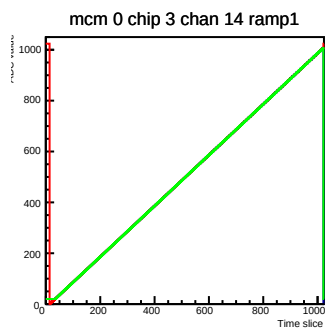
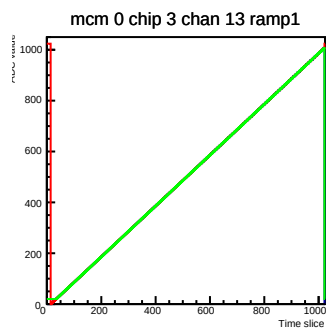
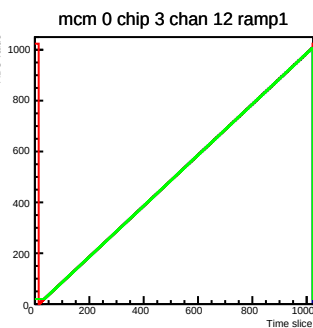
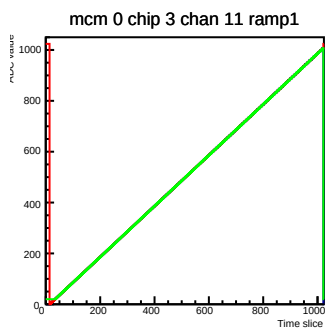
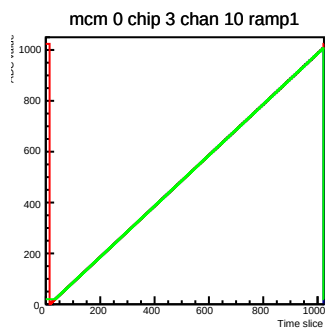
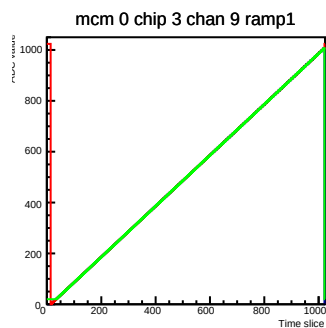
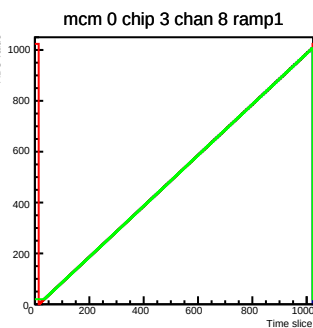
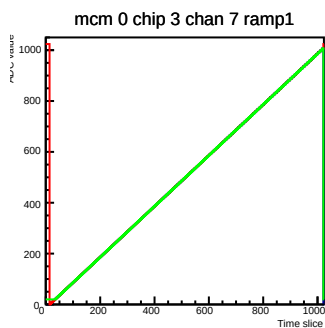
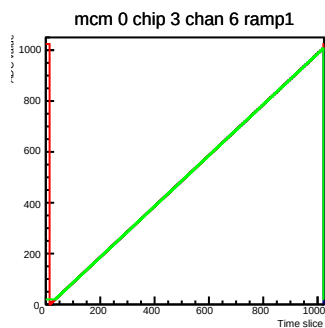
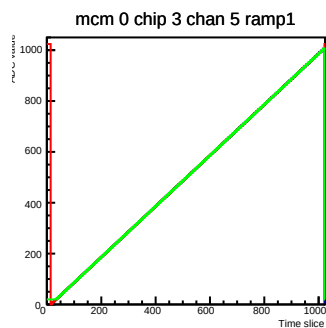
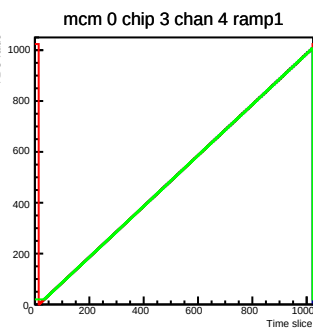
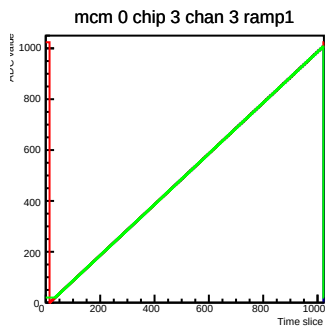
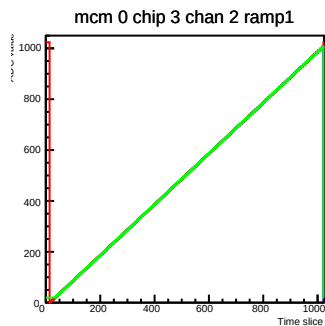
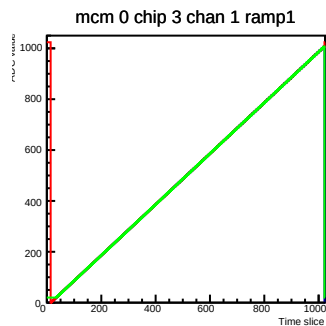
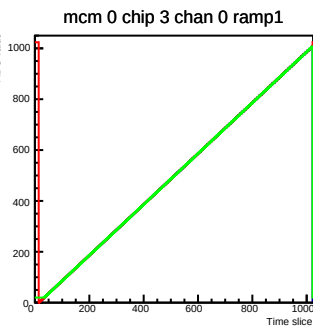


mcm 0 chip 2 chan 14 ramp1

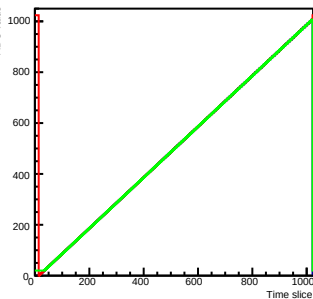


mcm 0 chip 2 chan 15 ramp1

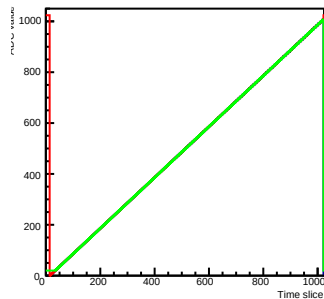




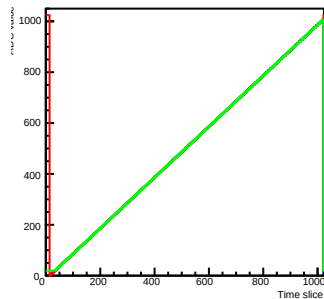
mcm 0 chip 4 chan 0 ramp1



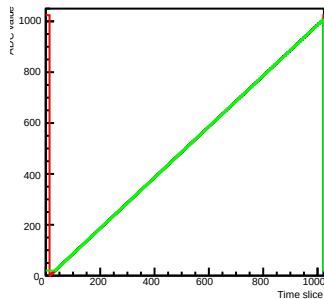
mcm 0 chip 4 chan 1 ramp1



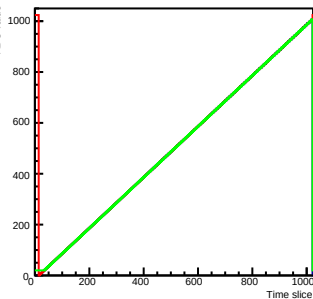
mcm 0 chip 4 chan 2 ramp1



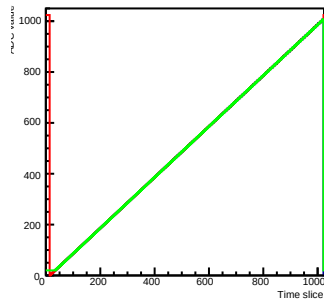
mcm 0 chip 4 chan 3 ramp1



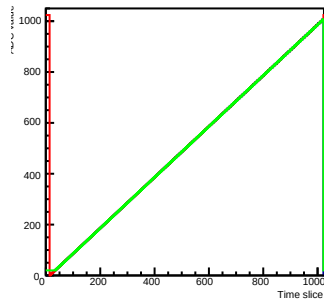
mcm 0 chip 4 chan 4 ramp1



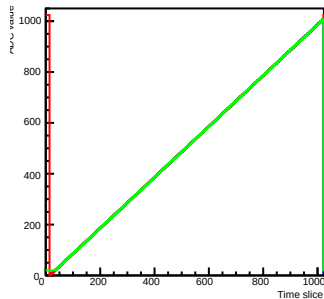
mcm 0 chip 4 chan 5 ramp1



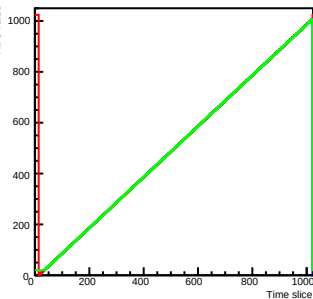
mcm 0 chip 4 chan 6 ramp1



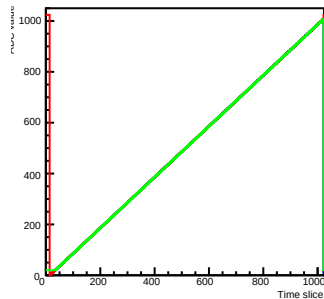
mcm 0 chip 4 chan 7 ramp1



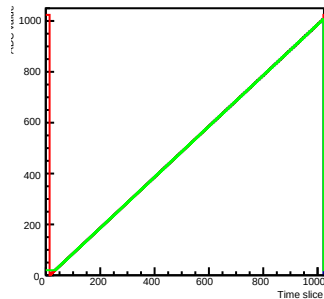
mcm 0 chip 4 chan 8 ramp1



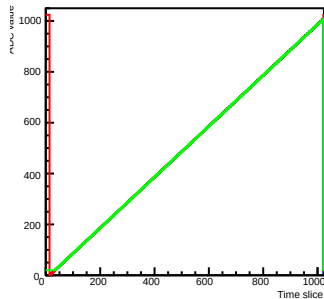
mcm 0 chip 4 chan 9 ramp1



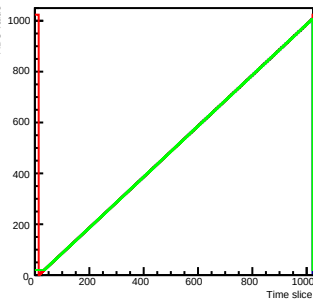
mcm 0 chip 4 chan 10 ramp1



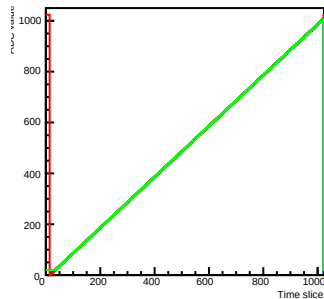
mcm 0 chip 4 chan 11 ramp1



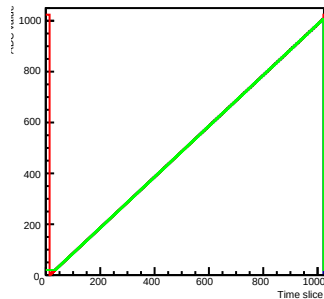
mcm 0 chip 4 chan 12 ramp1



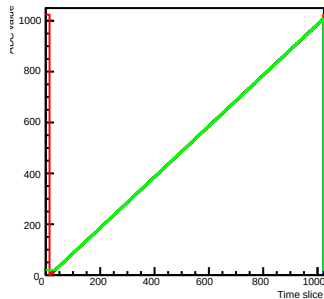
mcm 0 chip 4 chan 13 ramp1



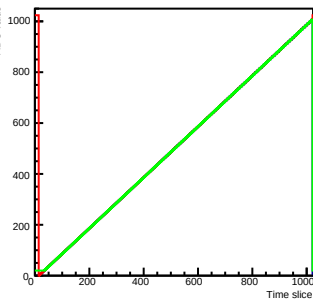
mcm 0 chip 4 chan 14 ramp1



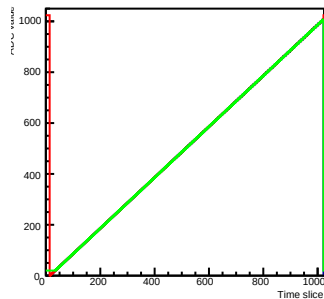
mcm 0 chip 4 chan 15 ramp1



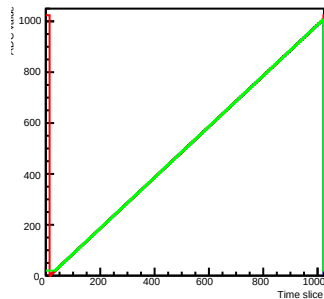
mcm 0 chip 5 chan 0 ramp1



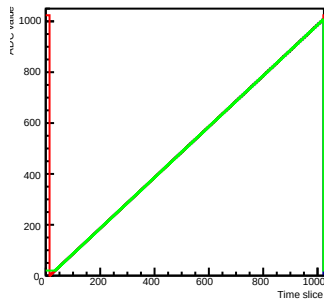
mcm 0 chip 5 chan 1 ramp1



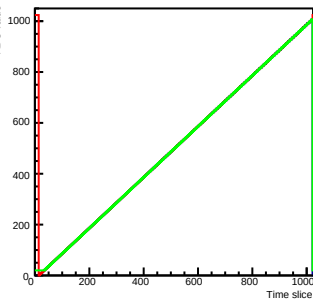
mcm 0 chip 5 chan 2 ramp1



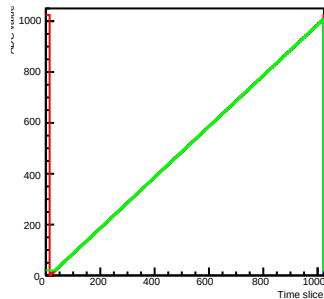
mcm 0 chip 5 chan 3 ramp1



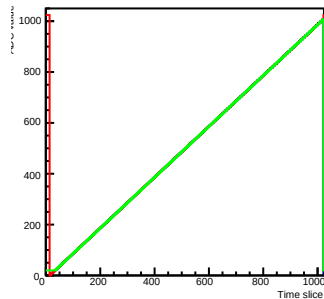
mcm 0 chip 5 chan 4 ramp1



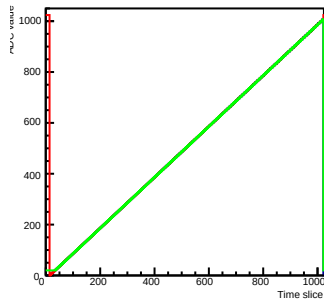
mcm 0 chip 5 chan 5 ramp1



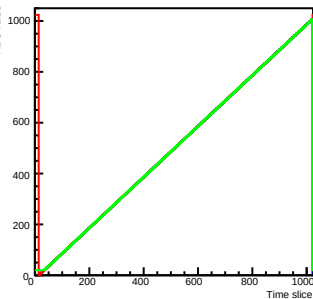
mcm 0 chip 5 chan 6 ramp1



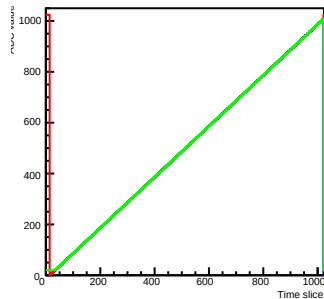
mcm 0 chip 5 chan 7 ramp1



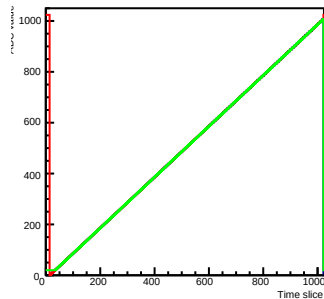
mcm 0 chip 5 chan 8 ramp1



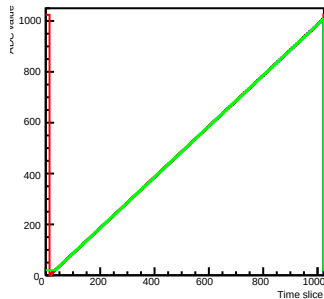
mcm 0 chip 5 chan 9 ramp1



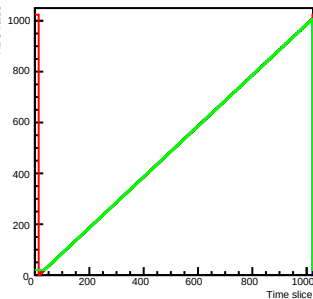
mcm 0 chip 5 chan 10 ramp1



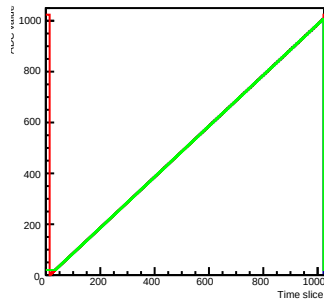
mcm 0 chip 5 chan 11 ramp1



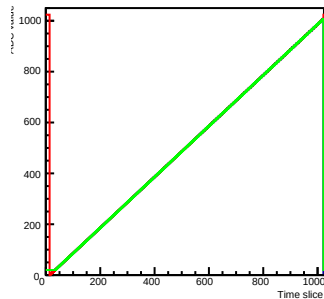
mcm 0 chip 5 chan 12 ramp1



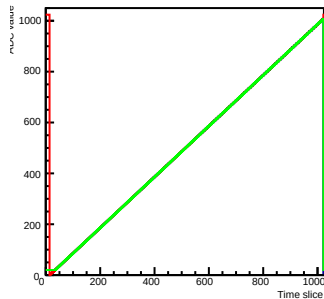
mcm 0 chip 5 chan 13 ramp1

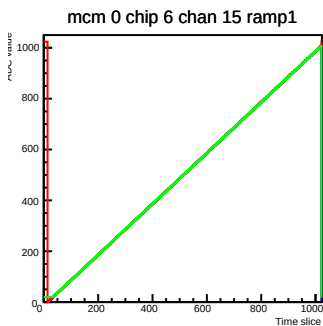
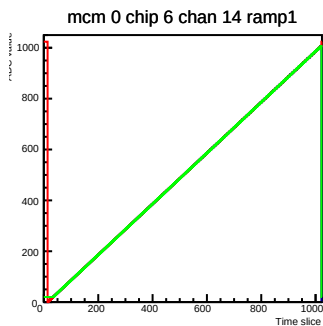
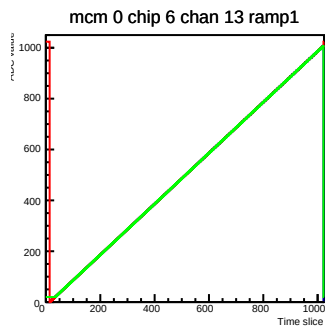
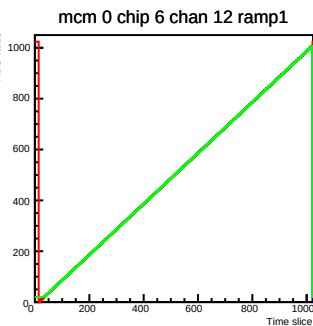
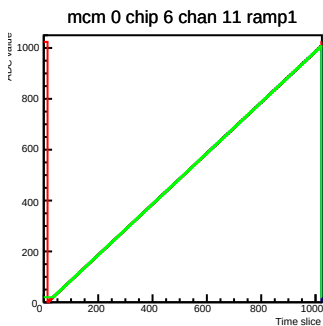
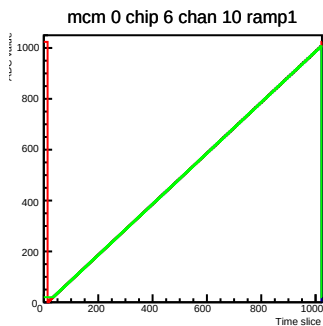
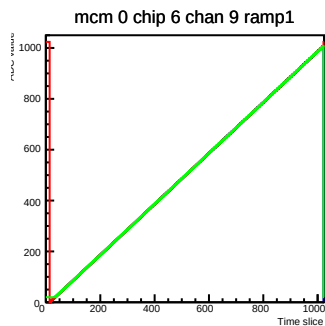
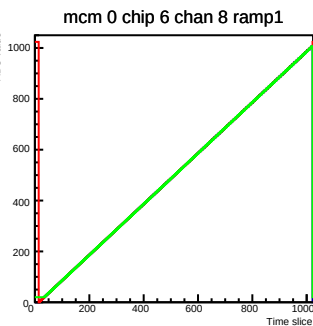
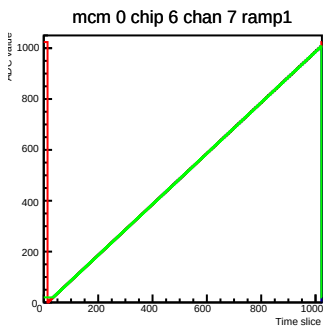
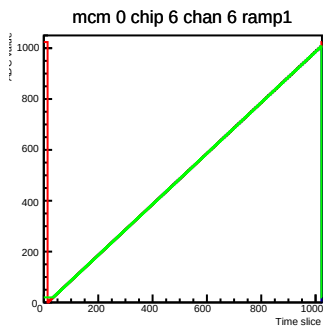
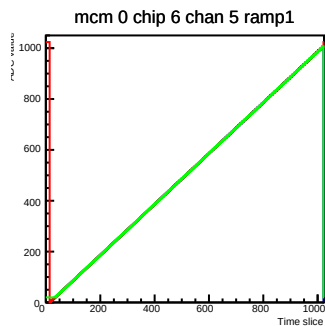
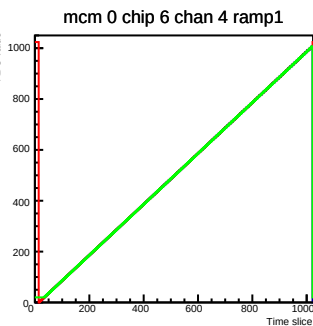
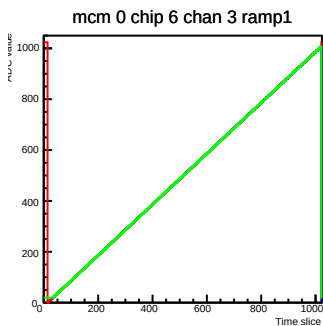
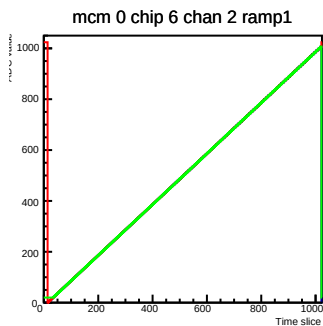
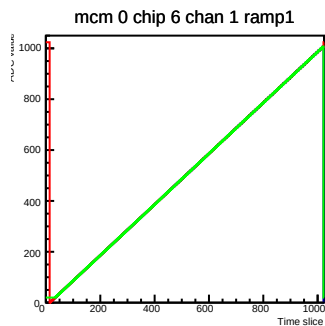
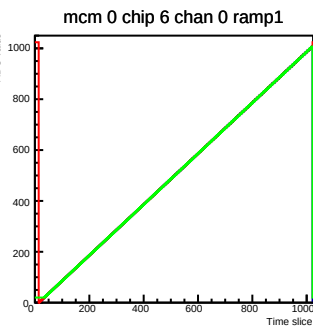


mcm 0 chip 5 chan 14 ramp1

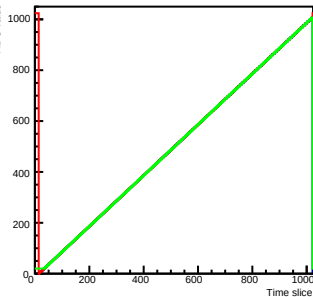


mcm 0 chip 5 chan 15 ramp1

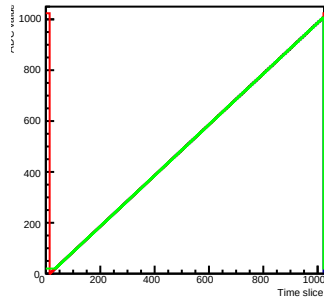




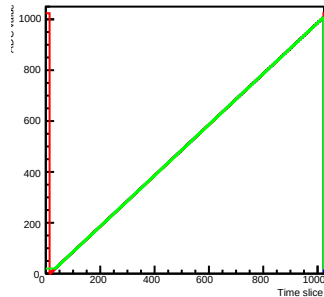
mcm 0 chip 7 chan 0 ramp1



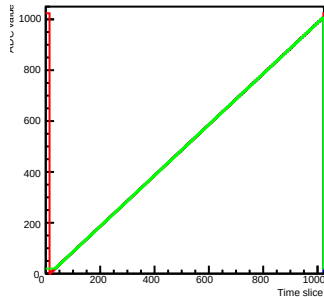
mcm 0 chip 7 chan 1 ramp1



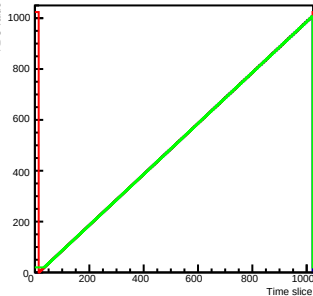
mcm 0 chip 7 chan 2 ramp1



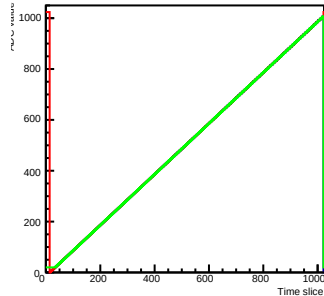
mcm 0 chip 7 chan 3 ramp1



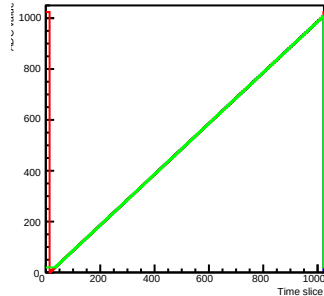
mcm 0 chip 7 chan 4 ramp1



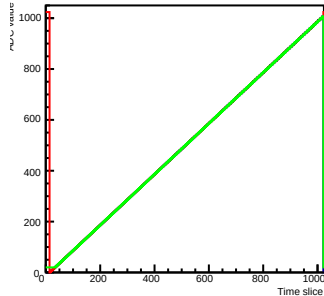
mcm 0 chip 7 chan 5 ramp1



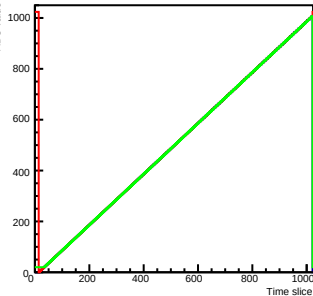
mcm 0 chip 7 chan 6 ramp1



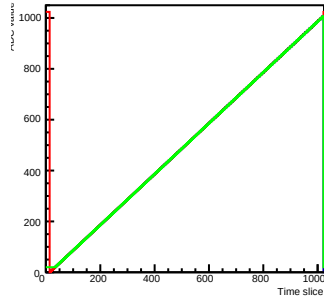
mcm 0 chip 7 chan 7 ramp1



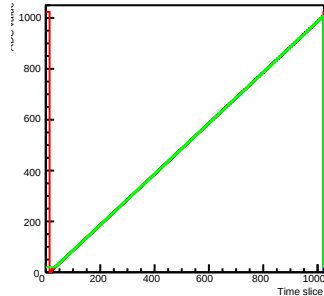
mcm 0 chip 7 chan 8 ramp1



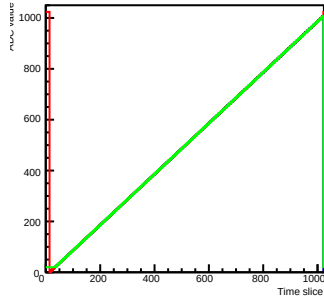
mcm 0 chip 7 chan 9 ramp1



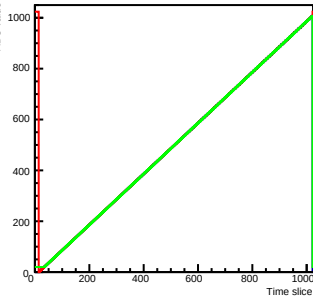
mcm 0 chip 7 chan 10 ramp1



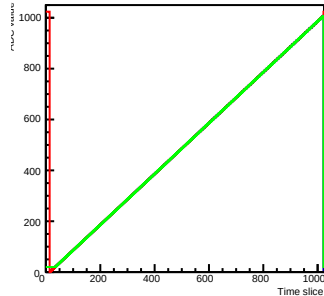
mcm 0 chip 7 chan 11 ramp1



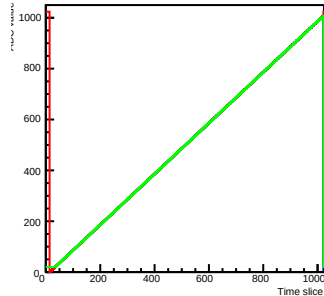
mcm 0 chip 7 chan 12 ramp1



mcm 0 chip 7 chan 13 ramp1



mcm 0 chip 7 chan 14 ramp1



mcm 0 chip 7 chan 15 ramp1

