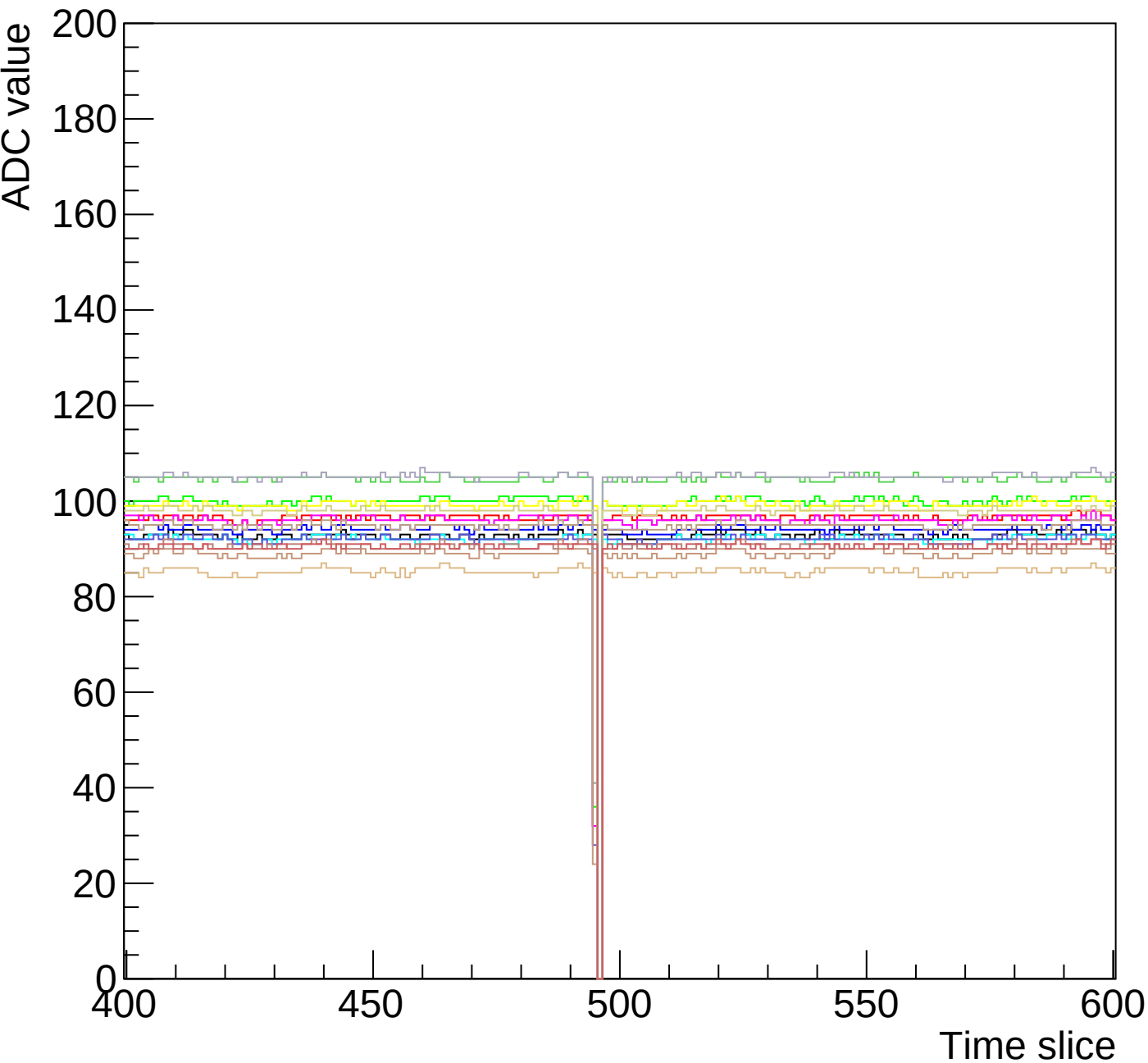
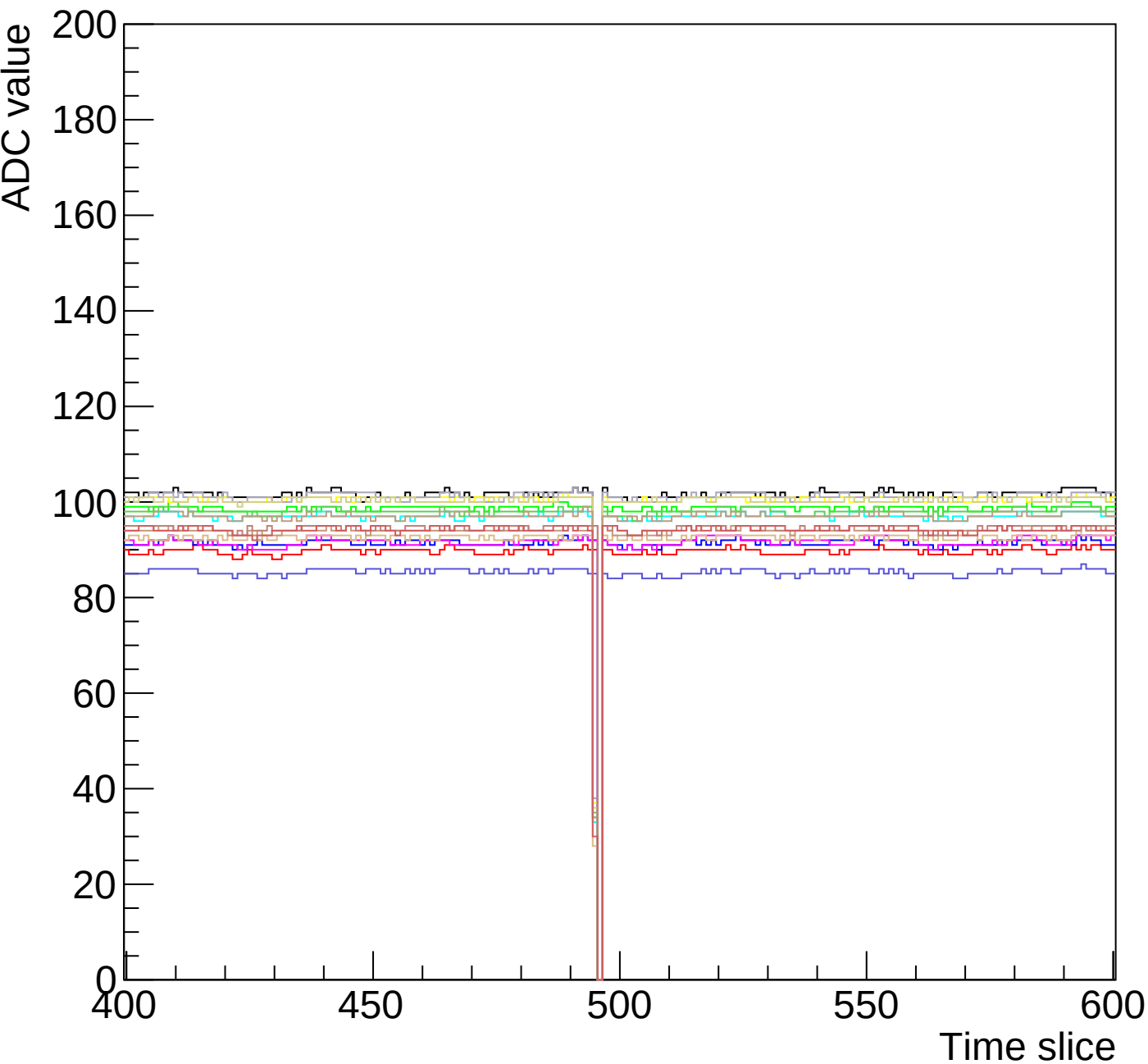


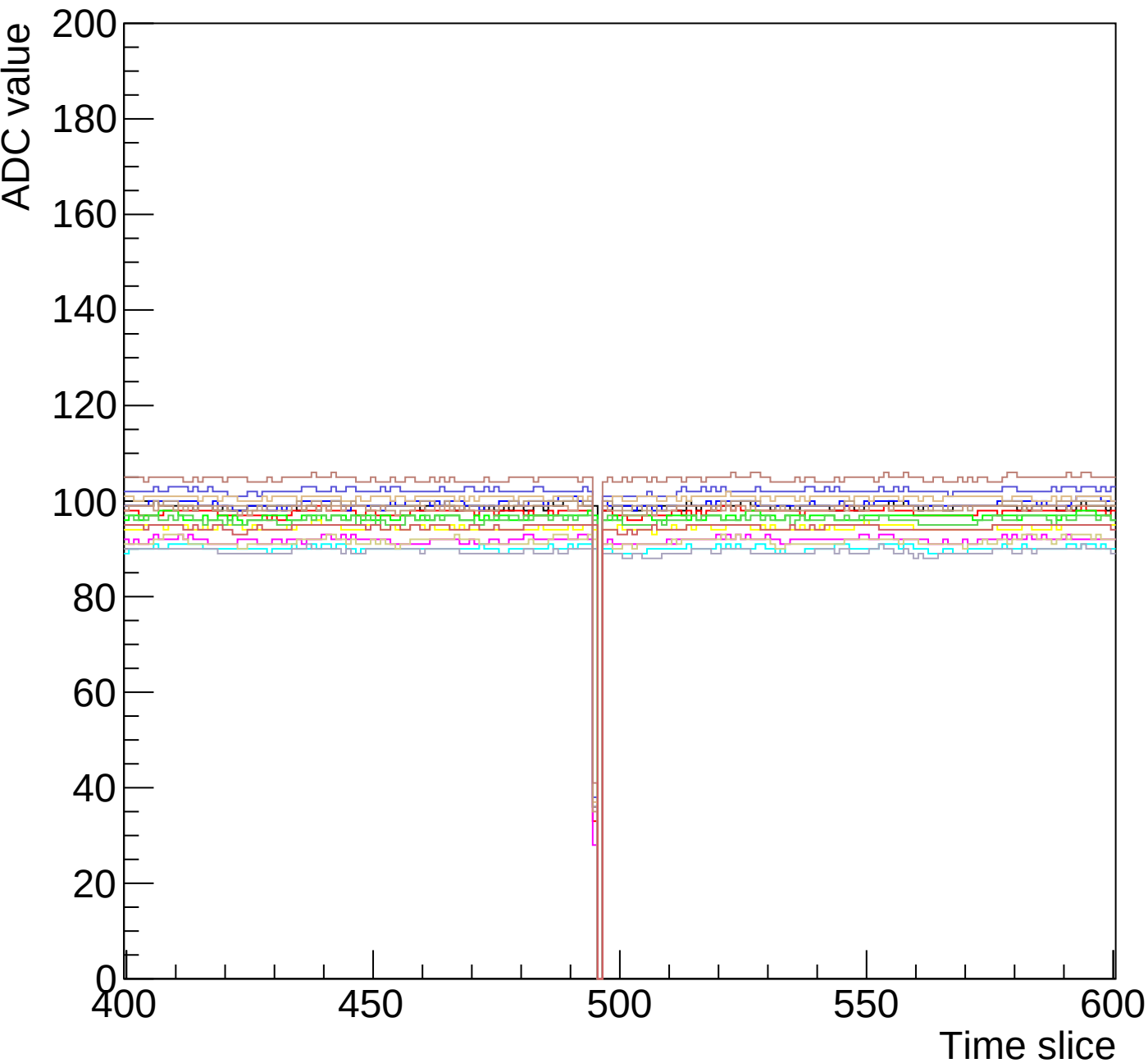
Run 0 event 3 mcm 0 chip 0 mcm11



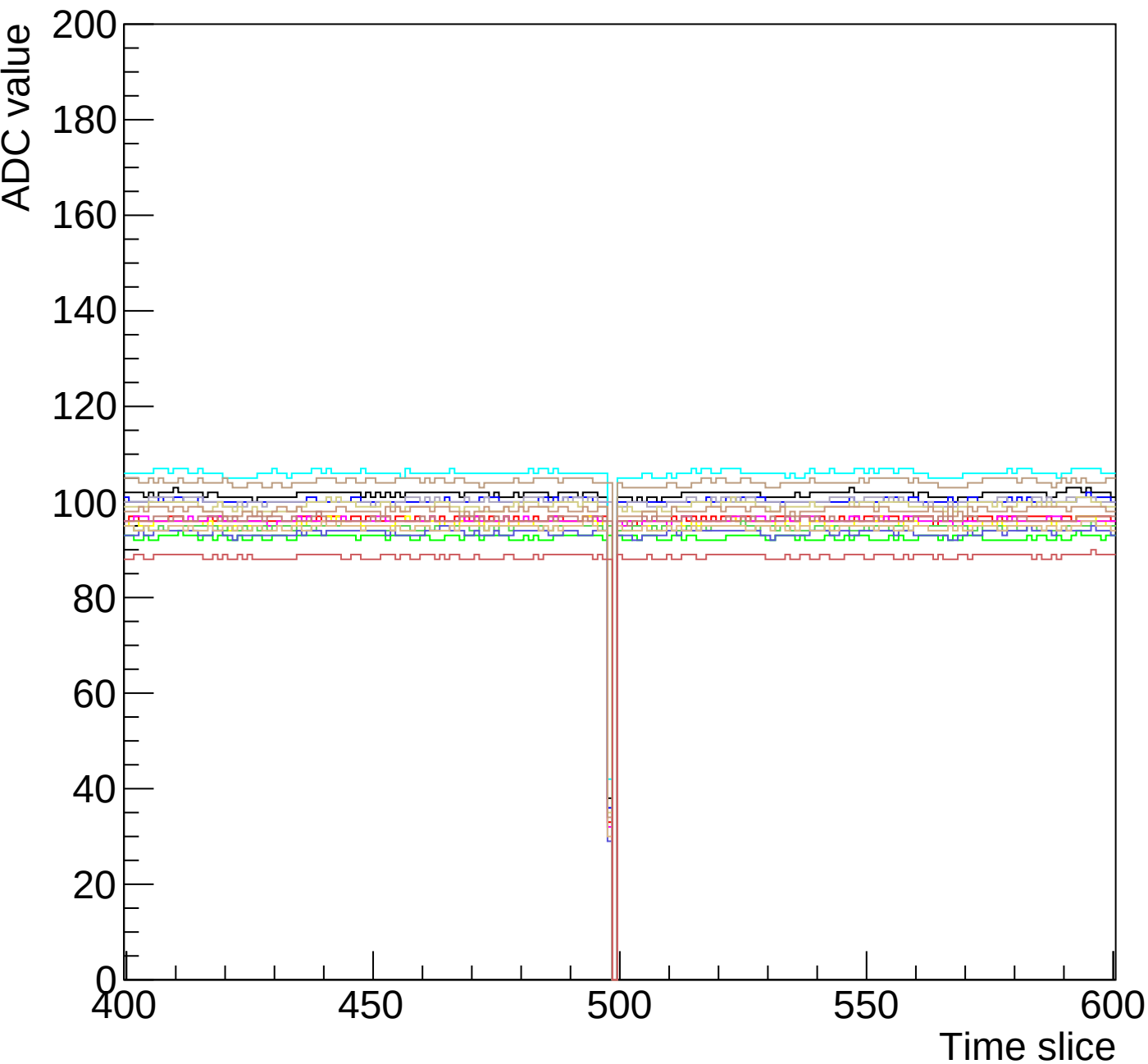
Run 0 event 3 mcm 0 chip 1 mcm11



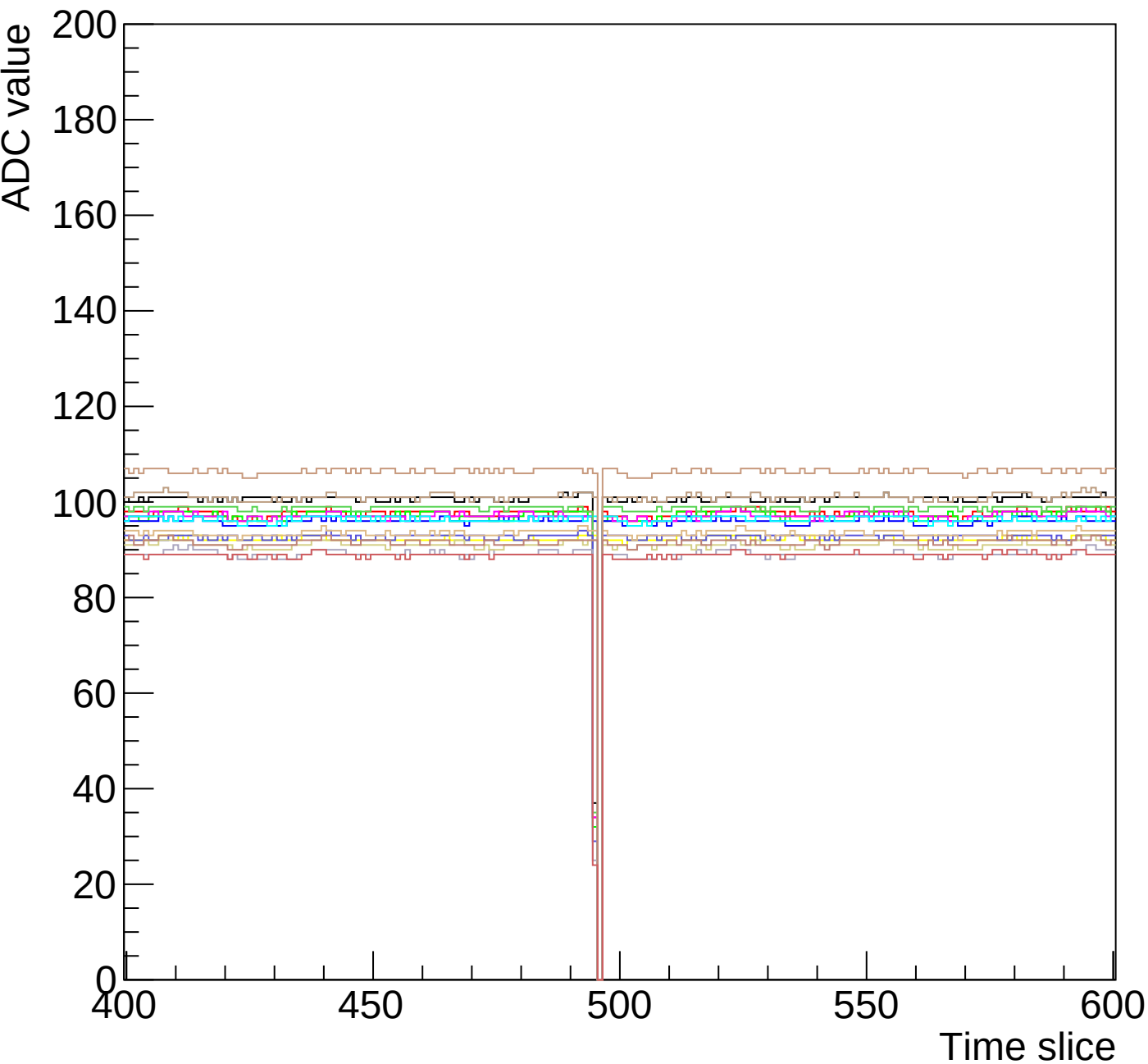
Run 0 event 3 mcm 0 chip 2 mcm11



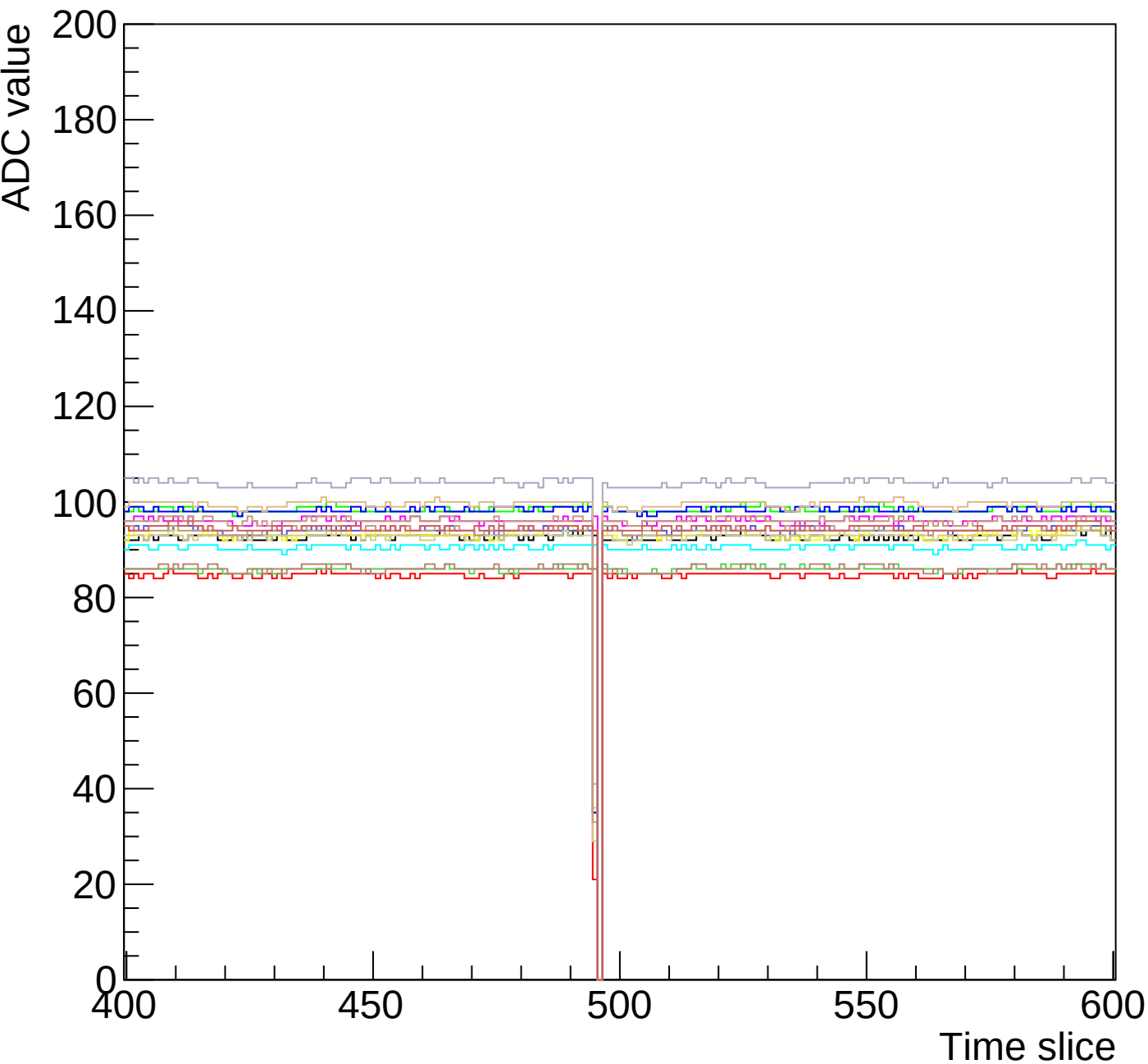
Run 0 event 3 mcm 0 chip 3 mcm11



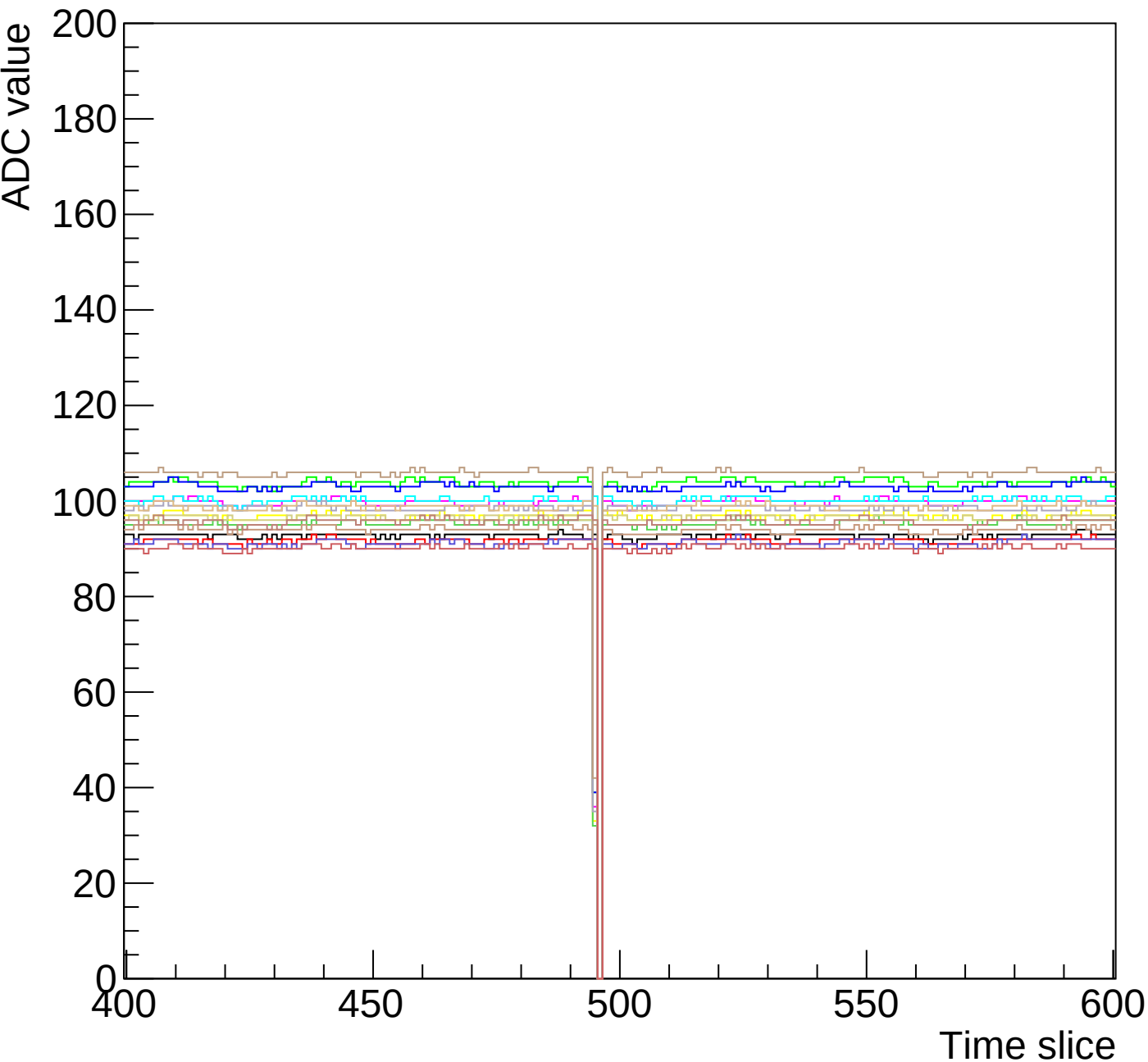
Run 0 event 3 mcm 0 chip 4 mcm11



Run 0 event 3 mcm 0 chip 5 mcm11



Run 0 event 3 mcm 0 chip 6 mcm11



Run 0 event 3 mcm 0 chip 7 mcm11

