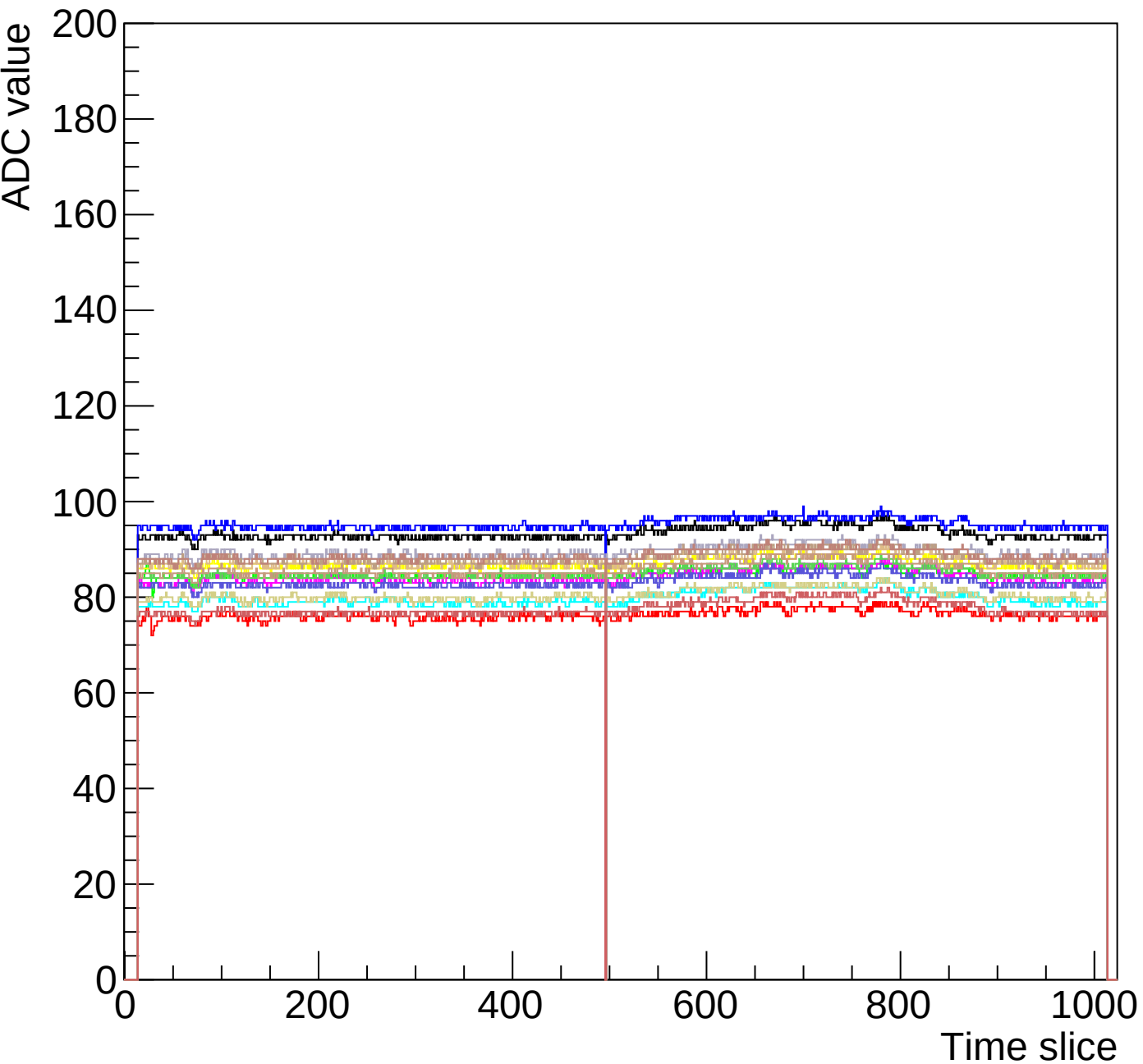
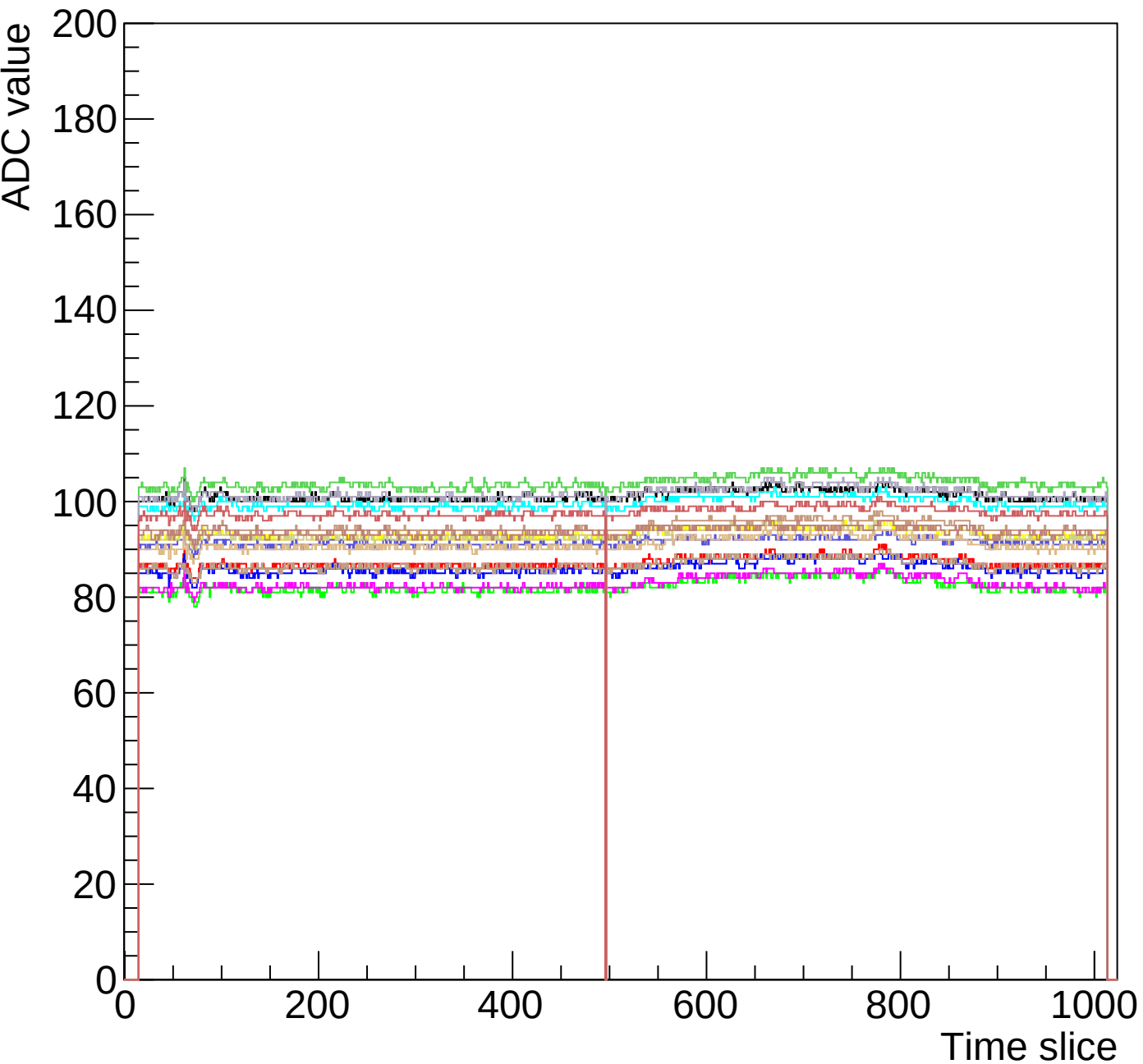


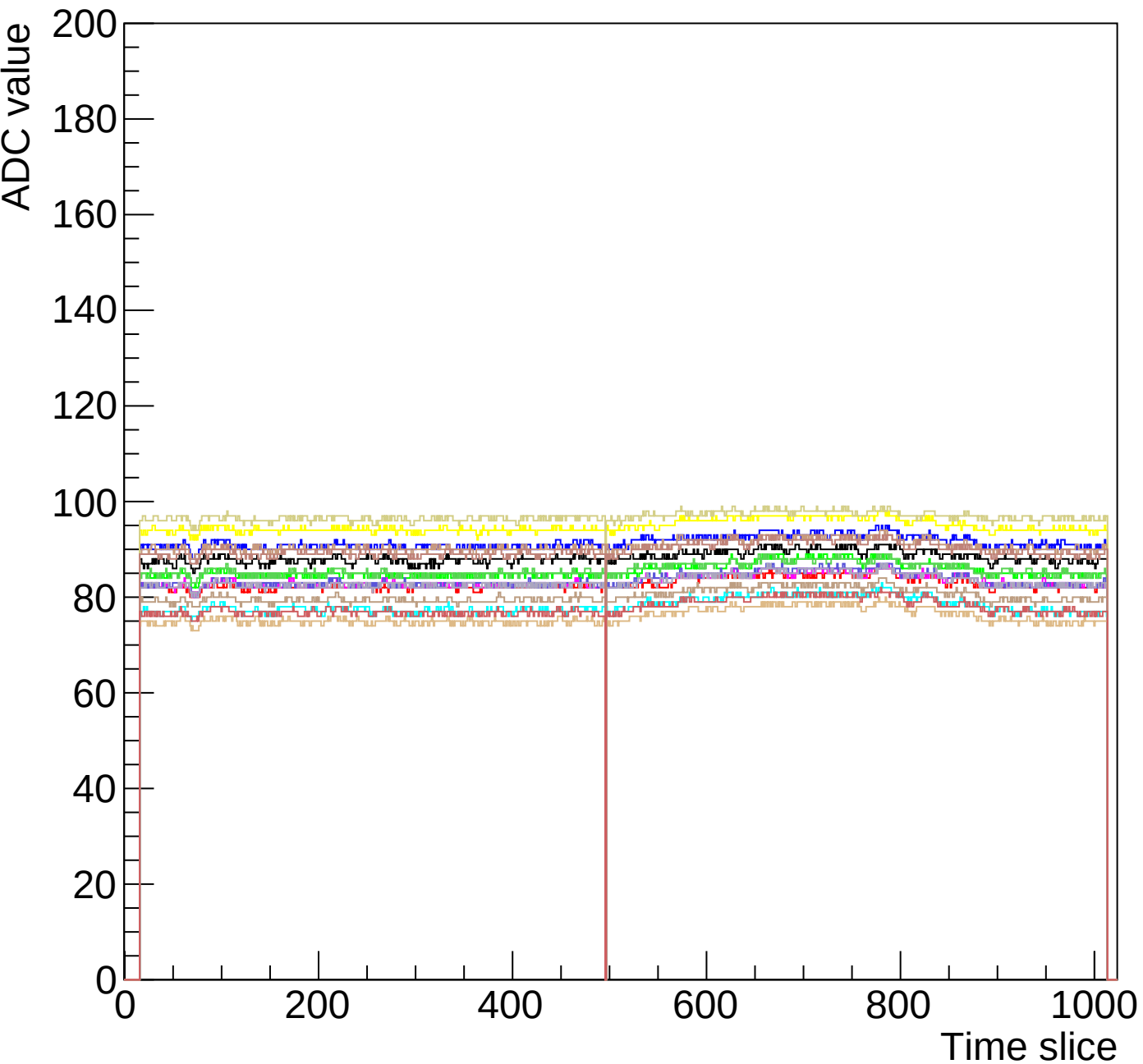
Run 0 event 3 mcm 0 chip 0 mcm12-1000



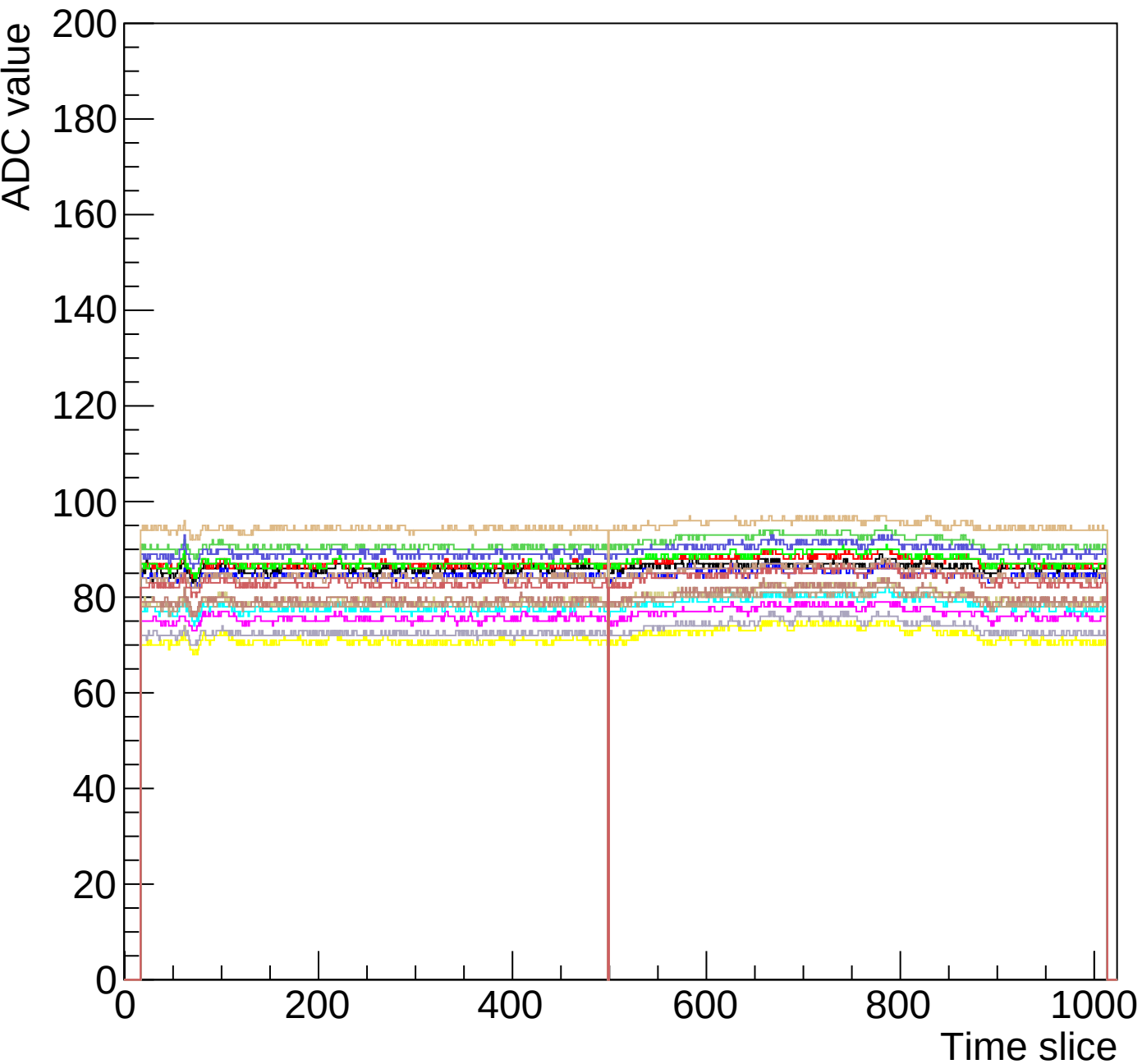
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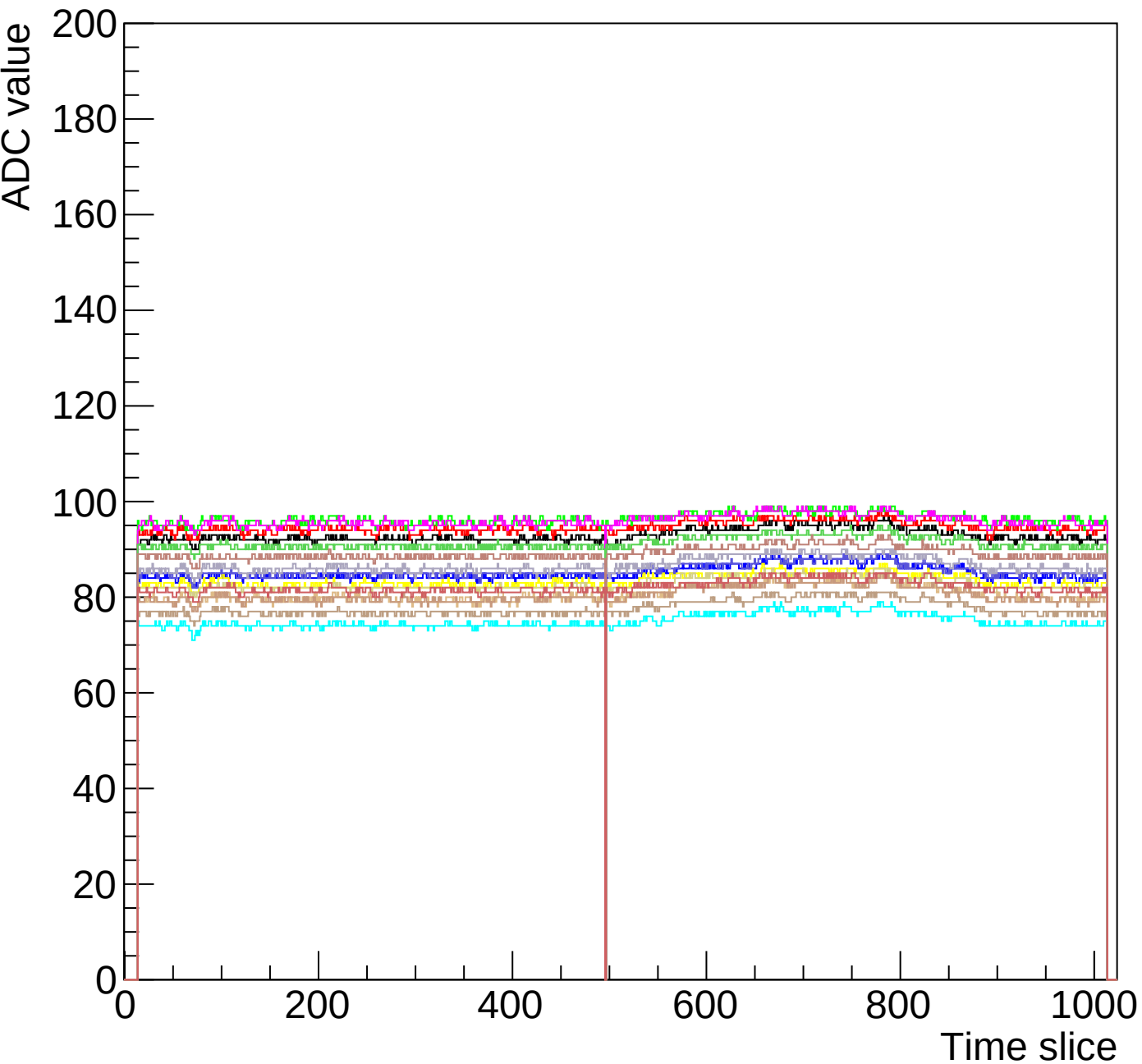
Run 0 event 3 mcm 0 chip 2 mcm12-1000



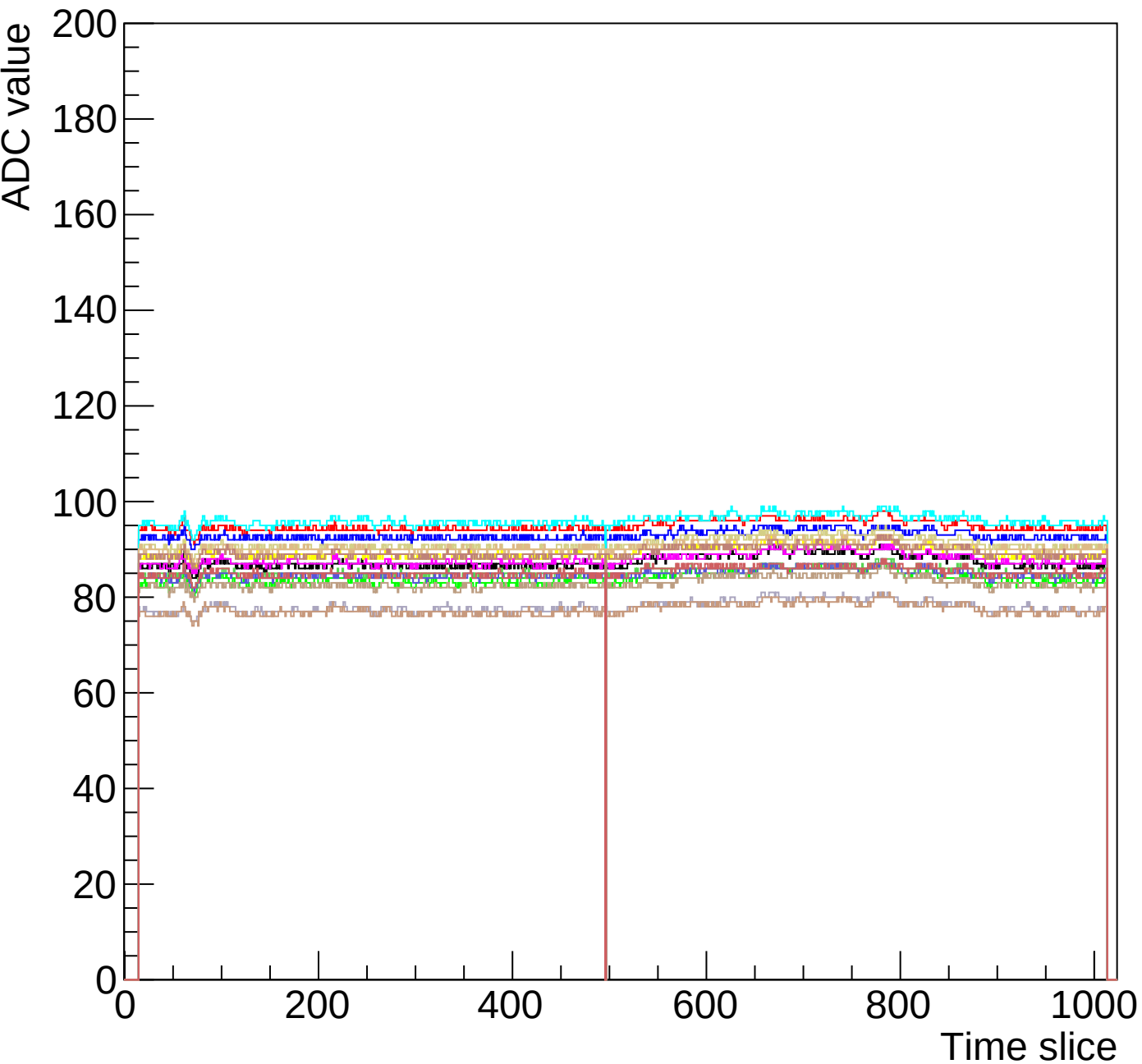
Run 0 event 3 mcm 0 chip 3 mcm12-1000



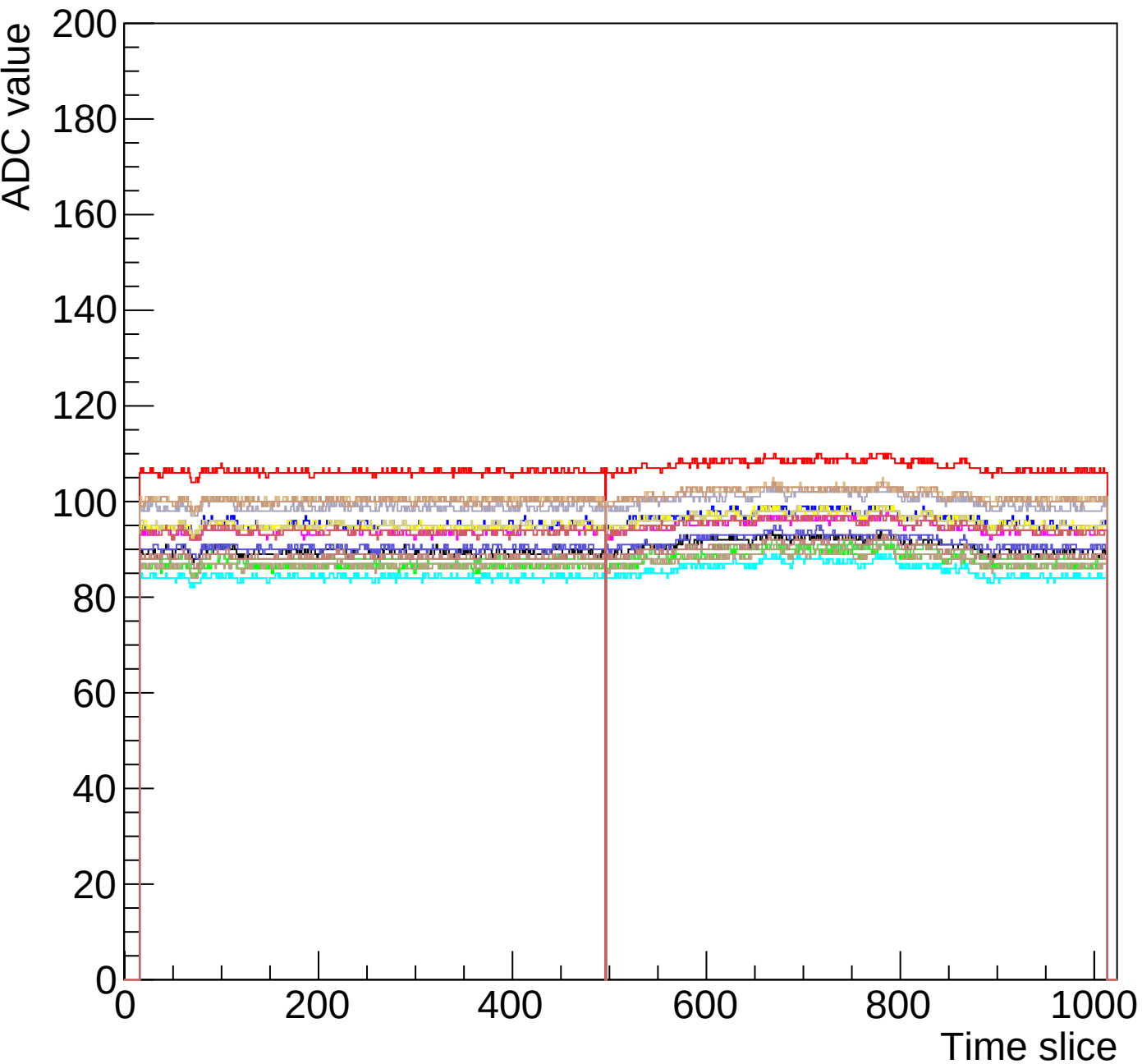
Run 0 event 3 mcm 0 chip 4 mcm12-1000



Run 0 event 3 mcm 0 chip 5 mcm12-1000



Run 0 event 3 mcm 0 chip 6 mcm12-1000



Run 0 event 3 mcm 0 chip 7 mcm12-1000

