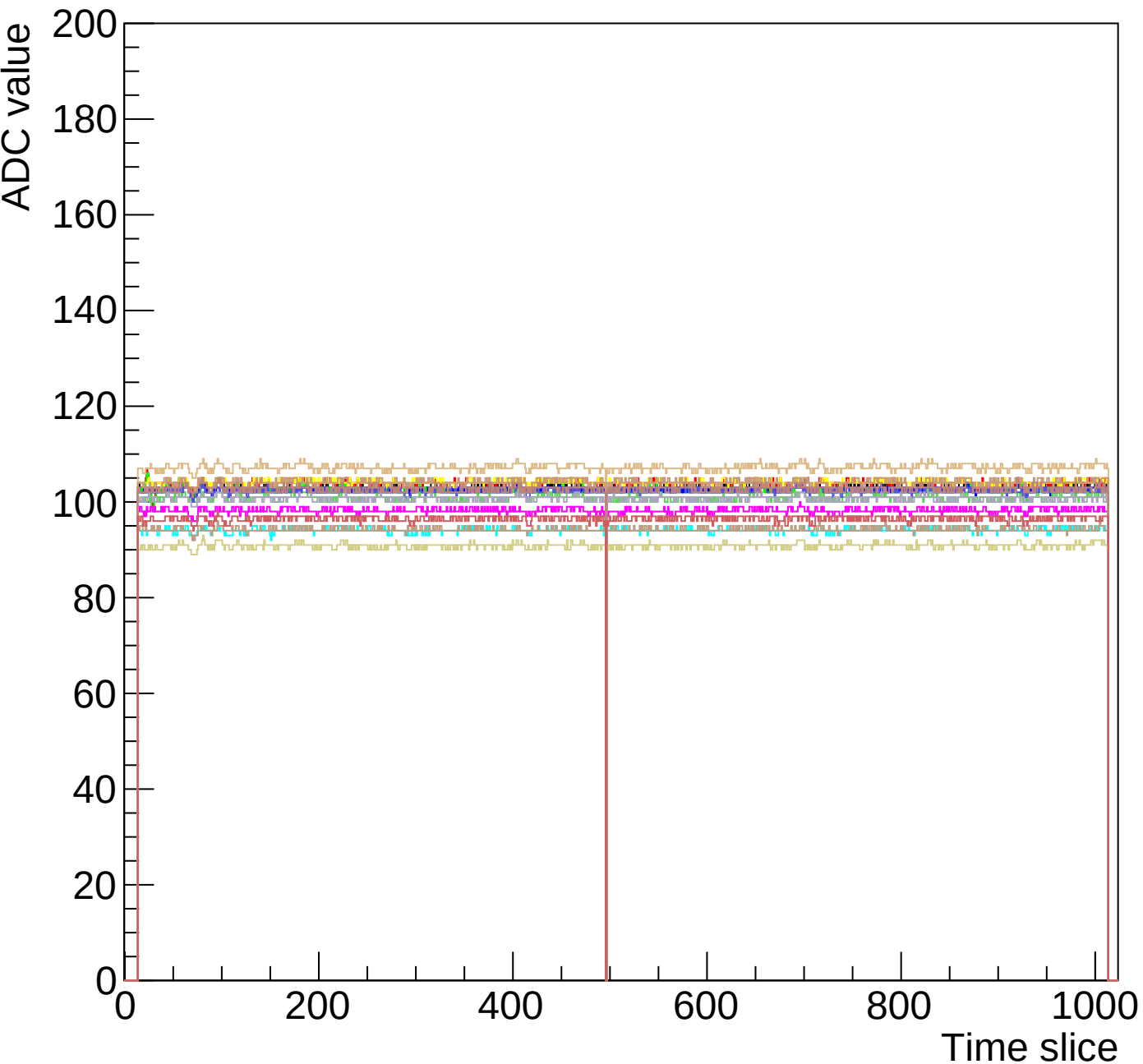
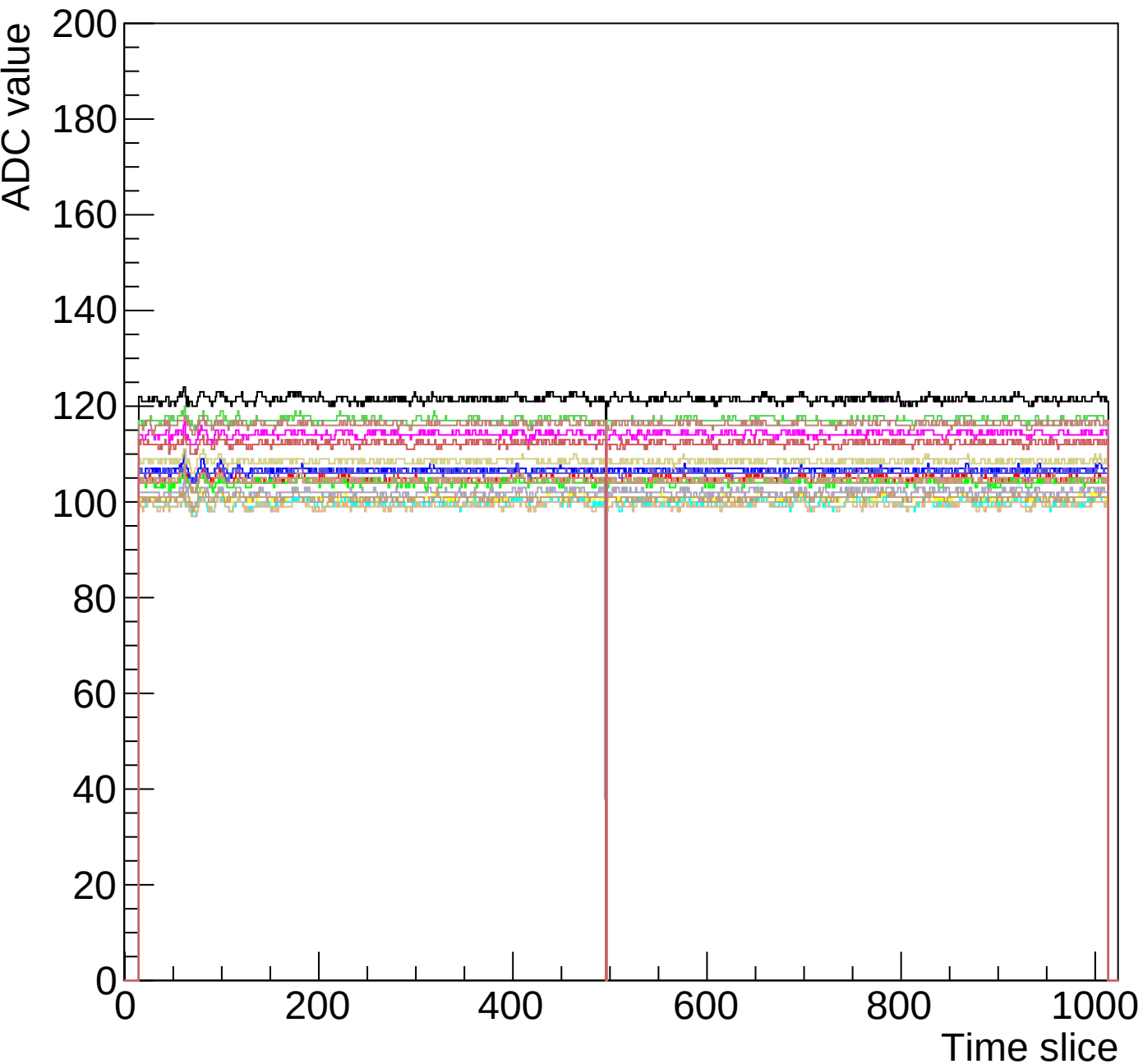


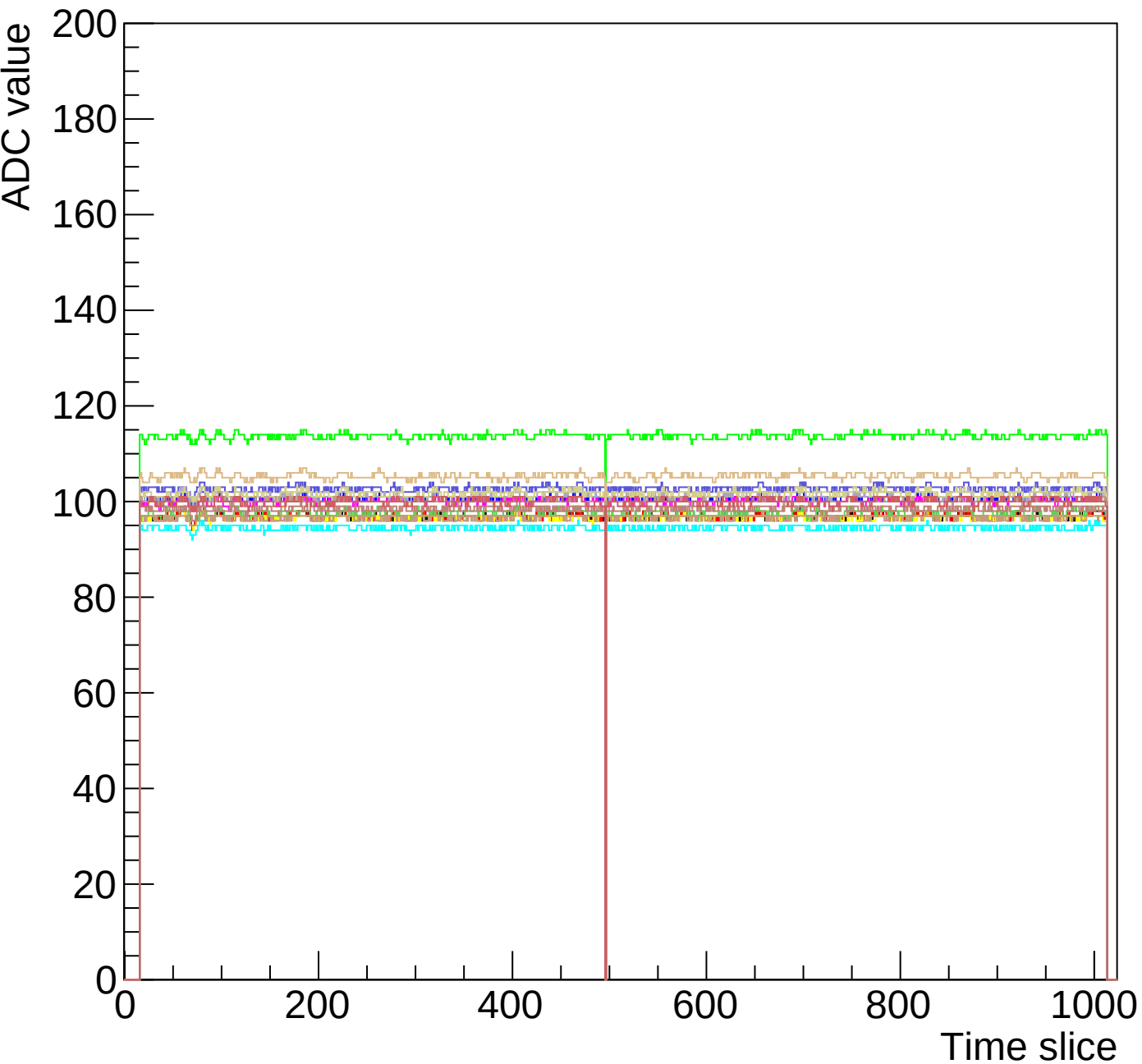
Run 0 event 3 mcm 0 chip 0 mcm21-1000



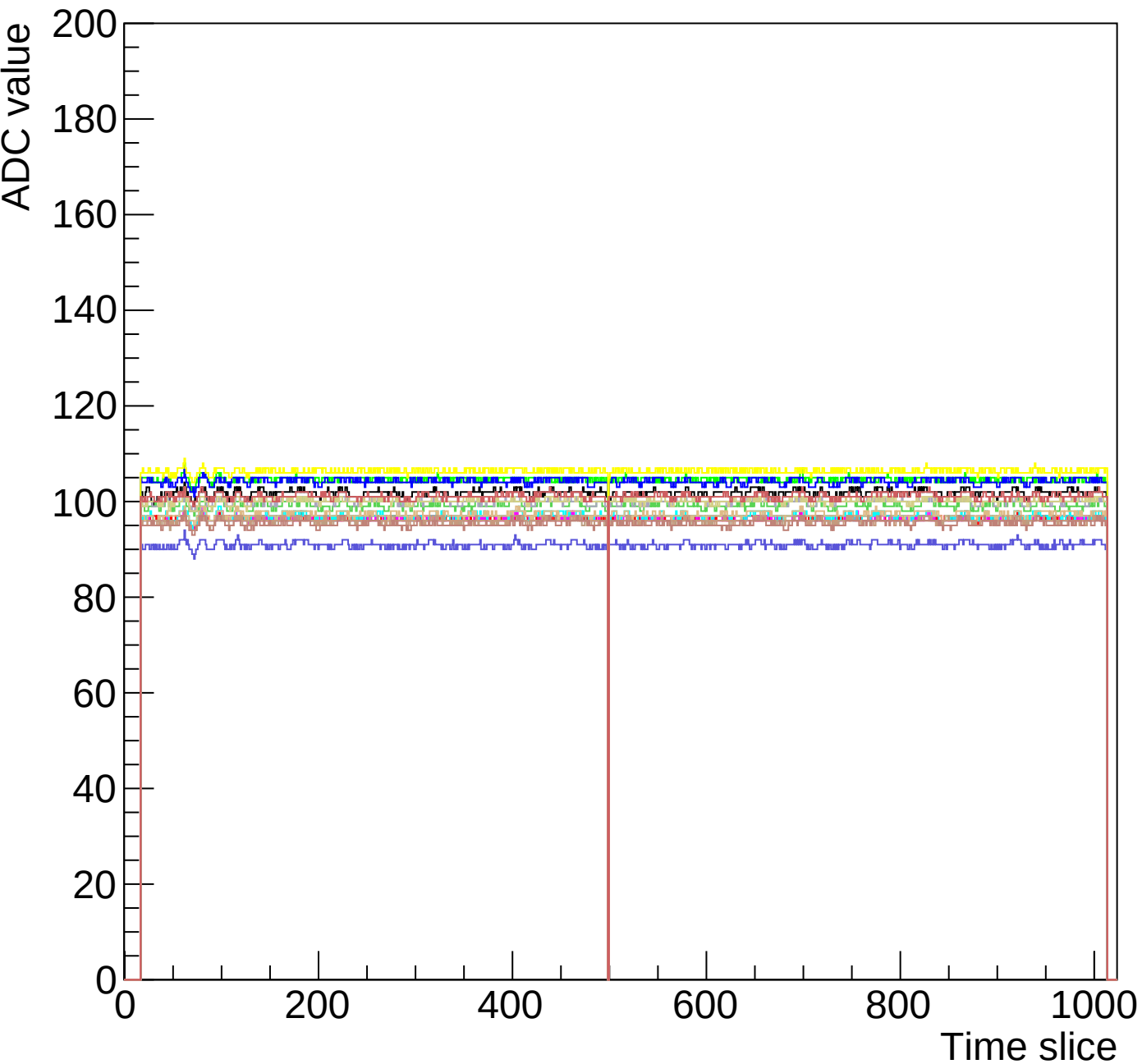
Run 0 event 3 mcm 0 chip 1 mcm21-1000



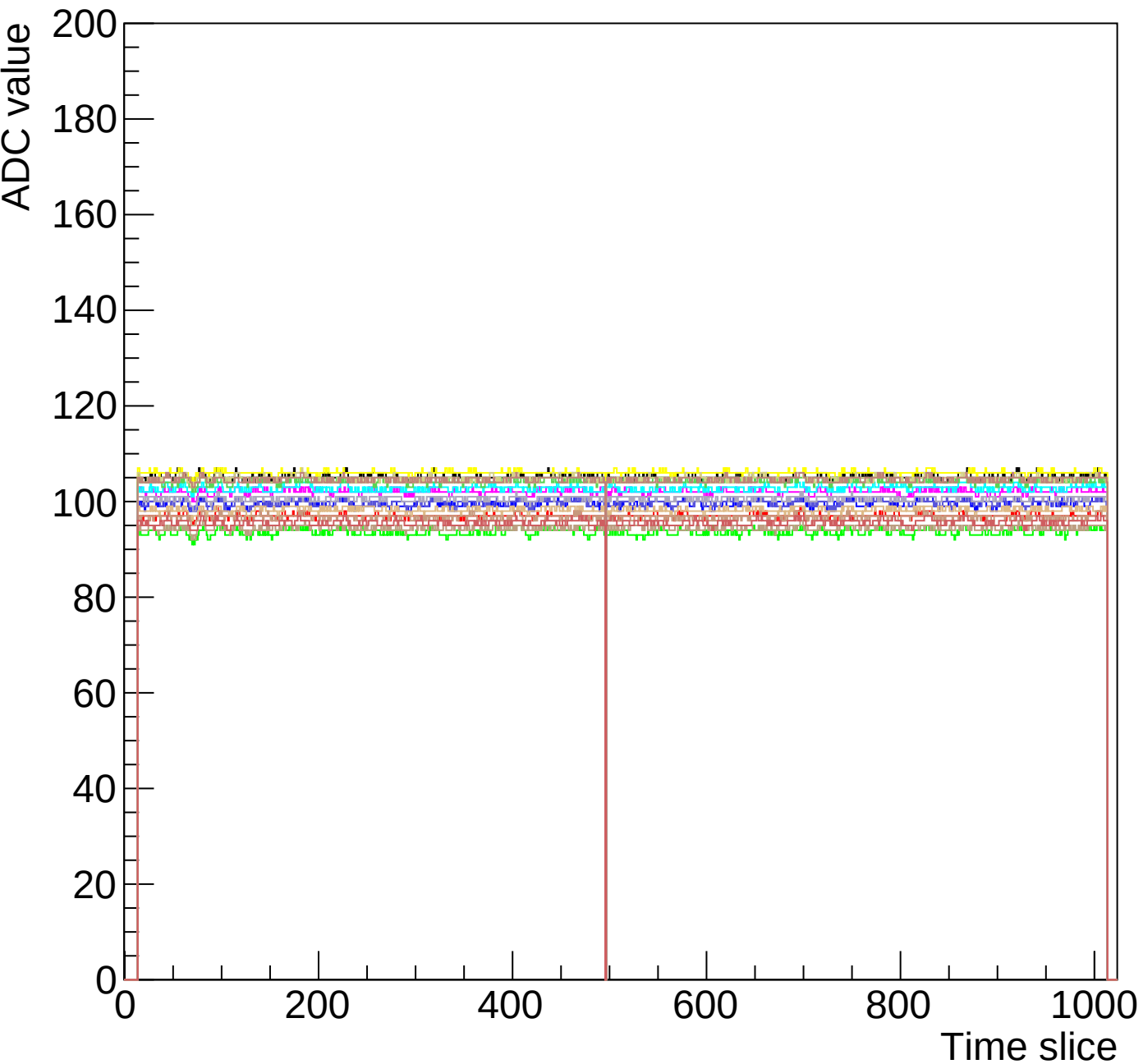
Run 0 event 3 mcm 0 chip 2 mcm21-1000



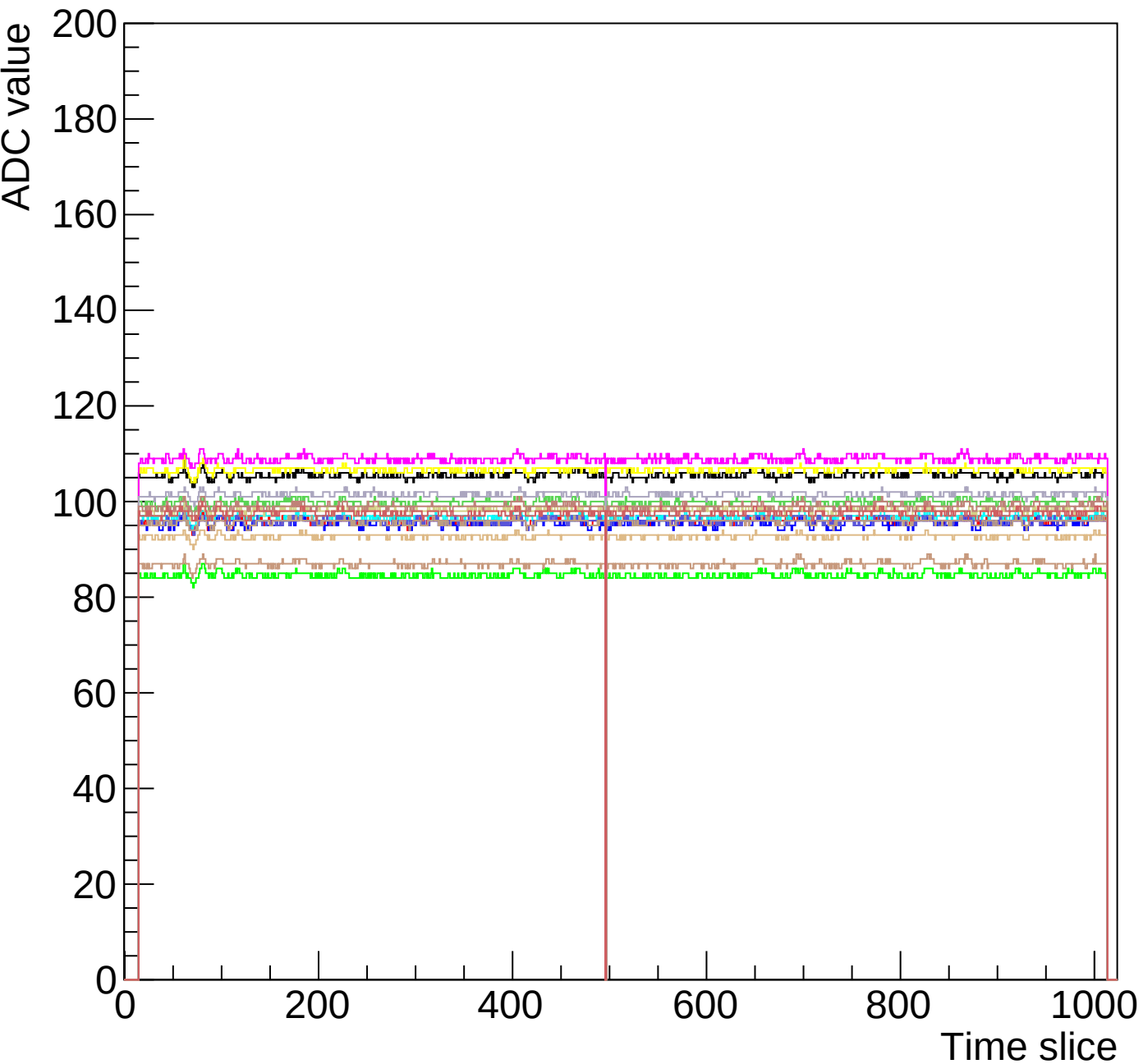
Run 0 event 3 mcm 0 chip 3 mcm21-1000



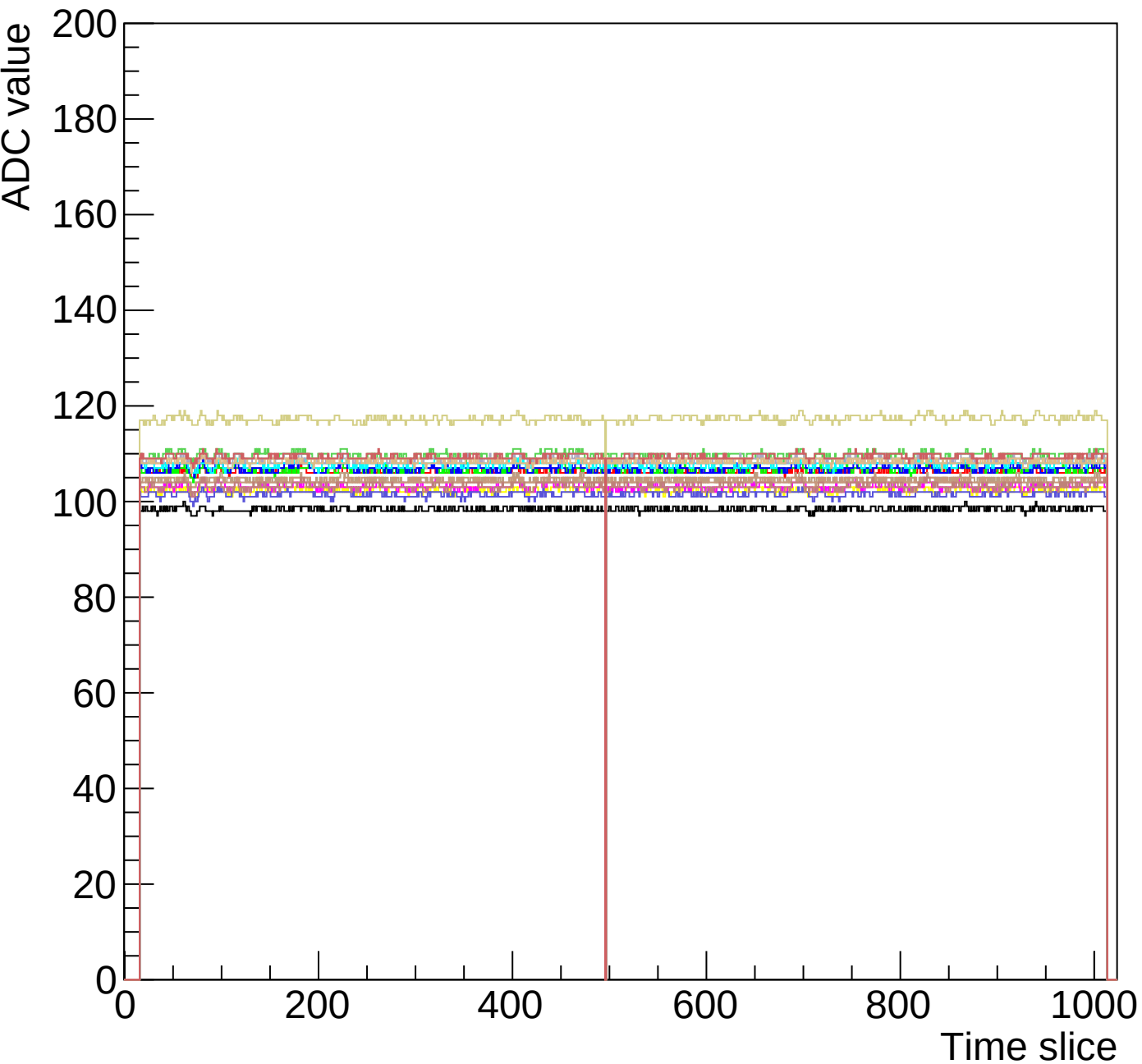
Run 0 event 3 mcm 0 chip 4 mcm21-1000



Run 0 event 3 mcm 0 chip 5 mcm21-1000



Run 0 event 3 mcm 0 chip 6 mcm21-1000



Run 0 event 3 mcm 0 chip 7 mcm21-1000

