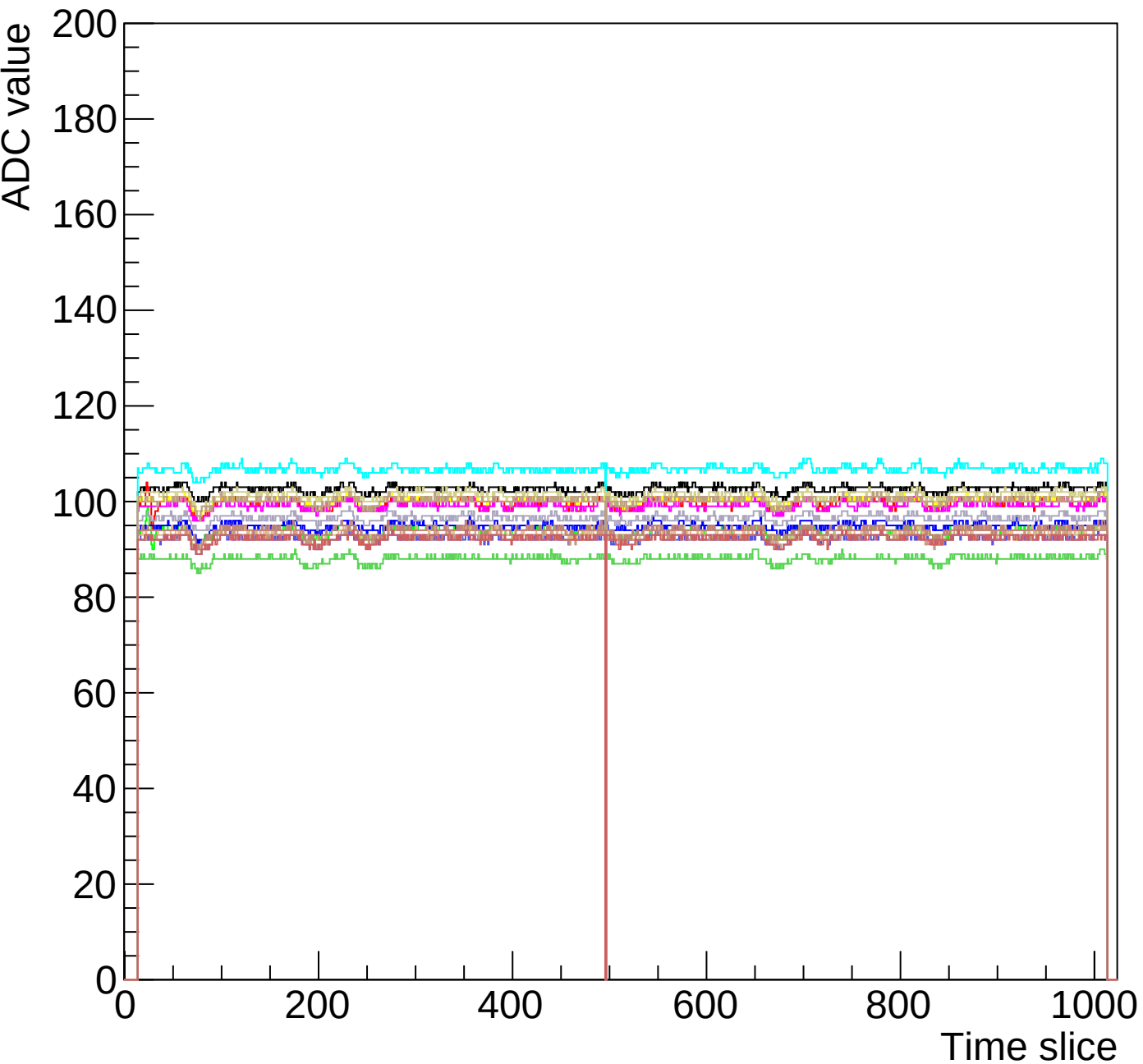
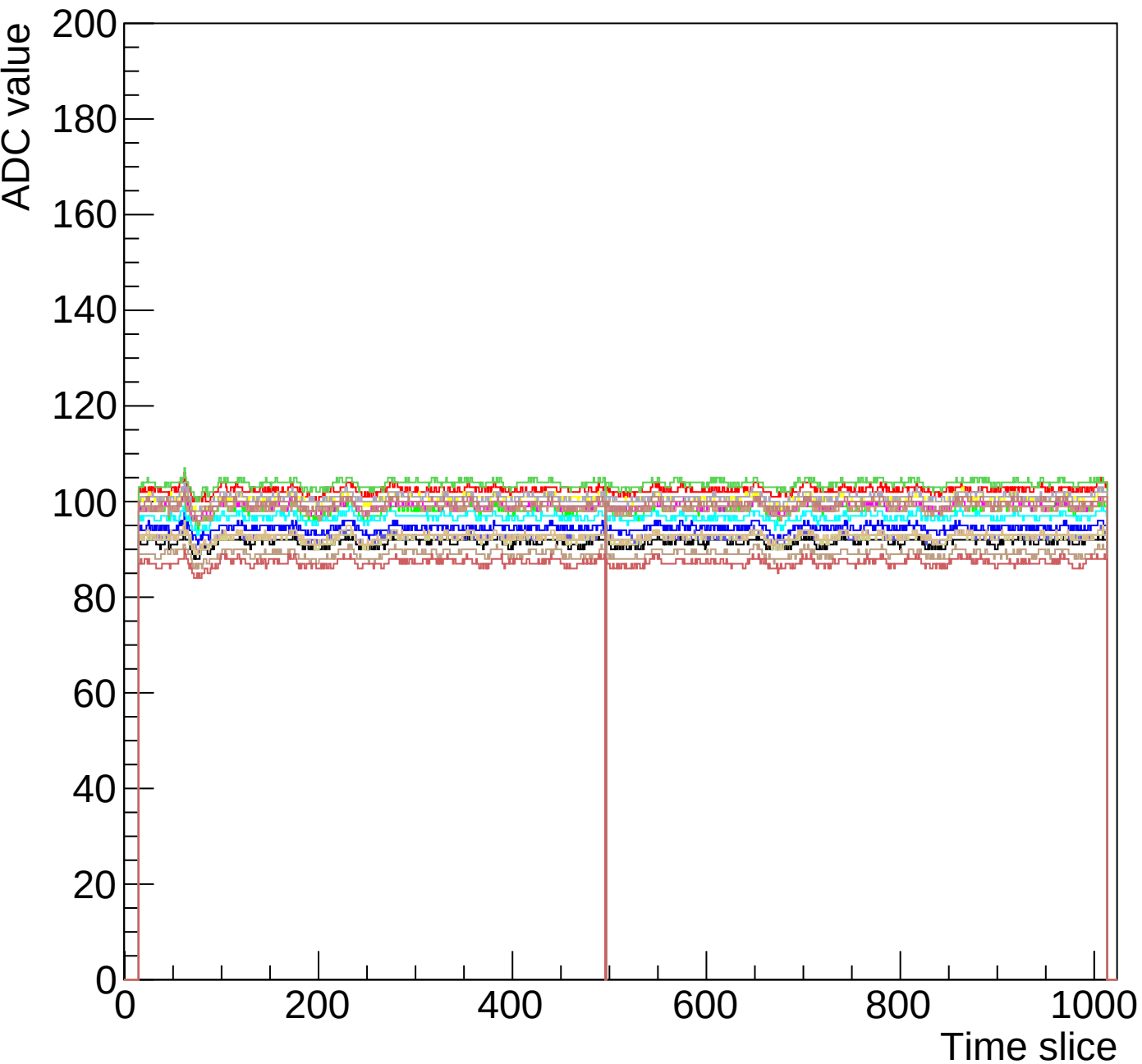


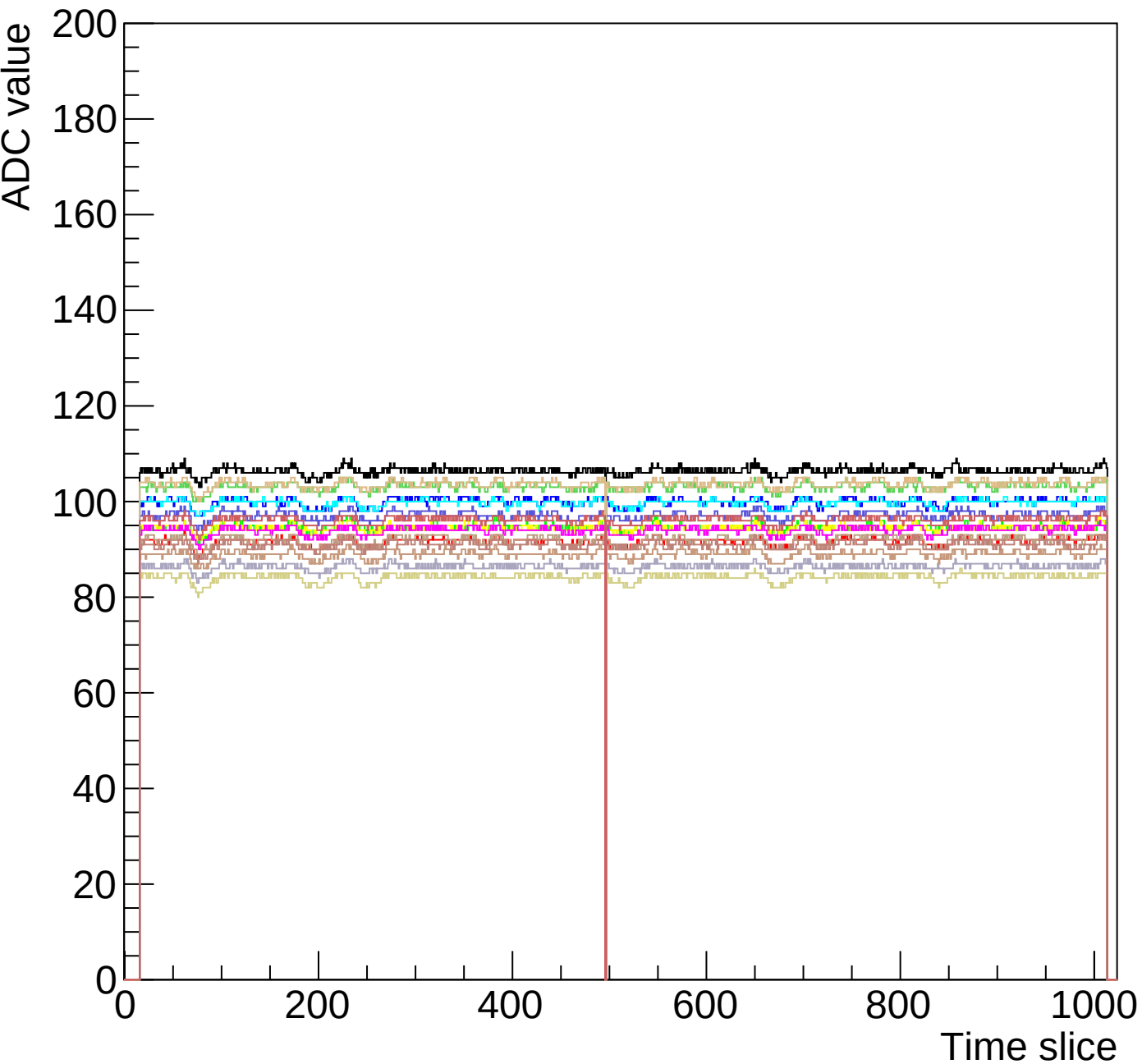
Run 0 event 3 mcm 0 chip 0 mcm22-1000



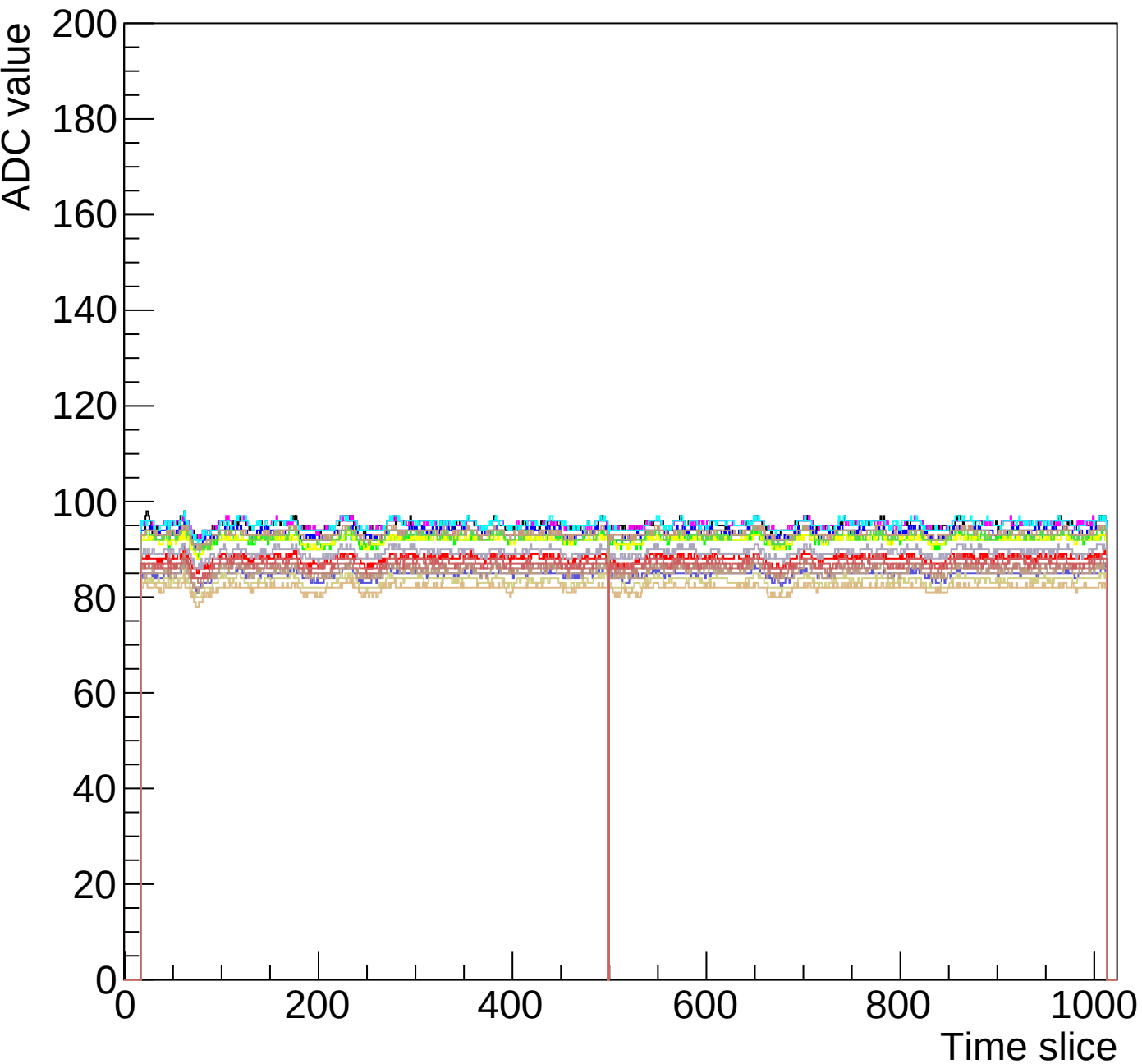
Run 0 event 3 mcm 0 chip 1 mcm22-1000



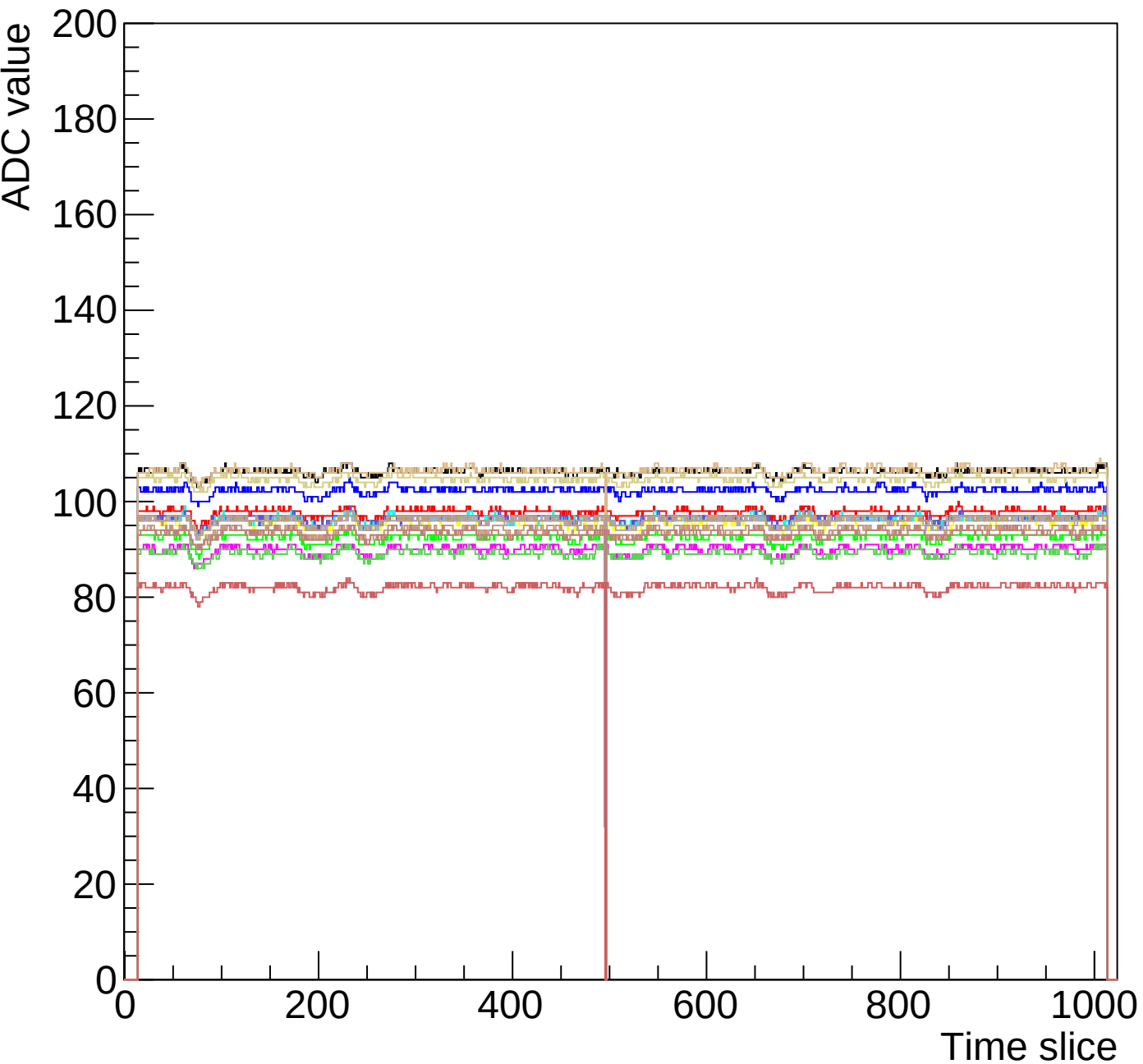
Run 0 event 3 mcm 0 chip 2 mcm22-1000



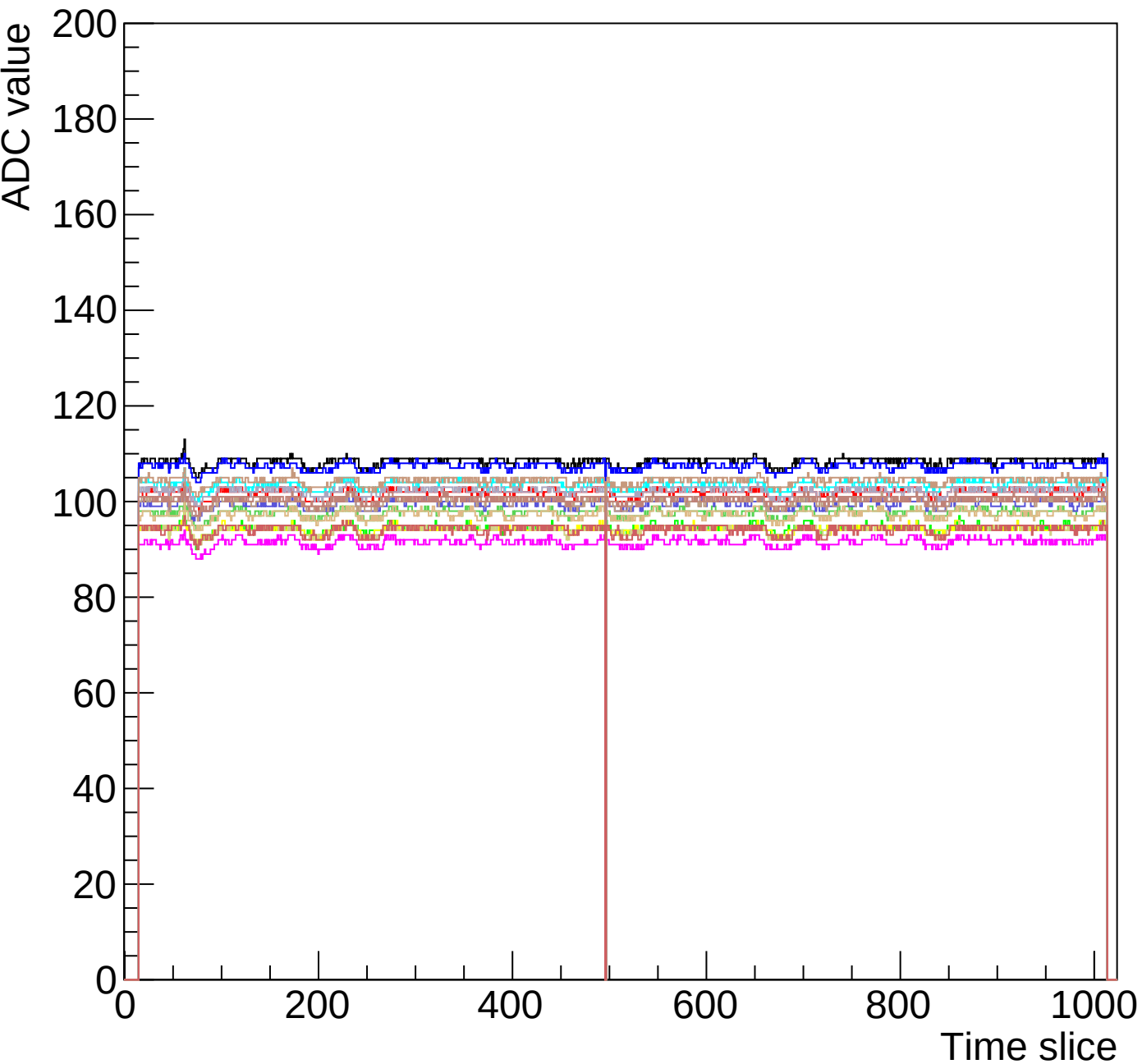
Run 0 event 3 mcm 0 chip 3 mcm22-1000



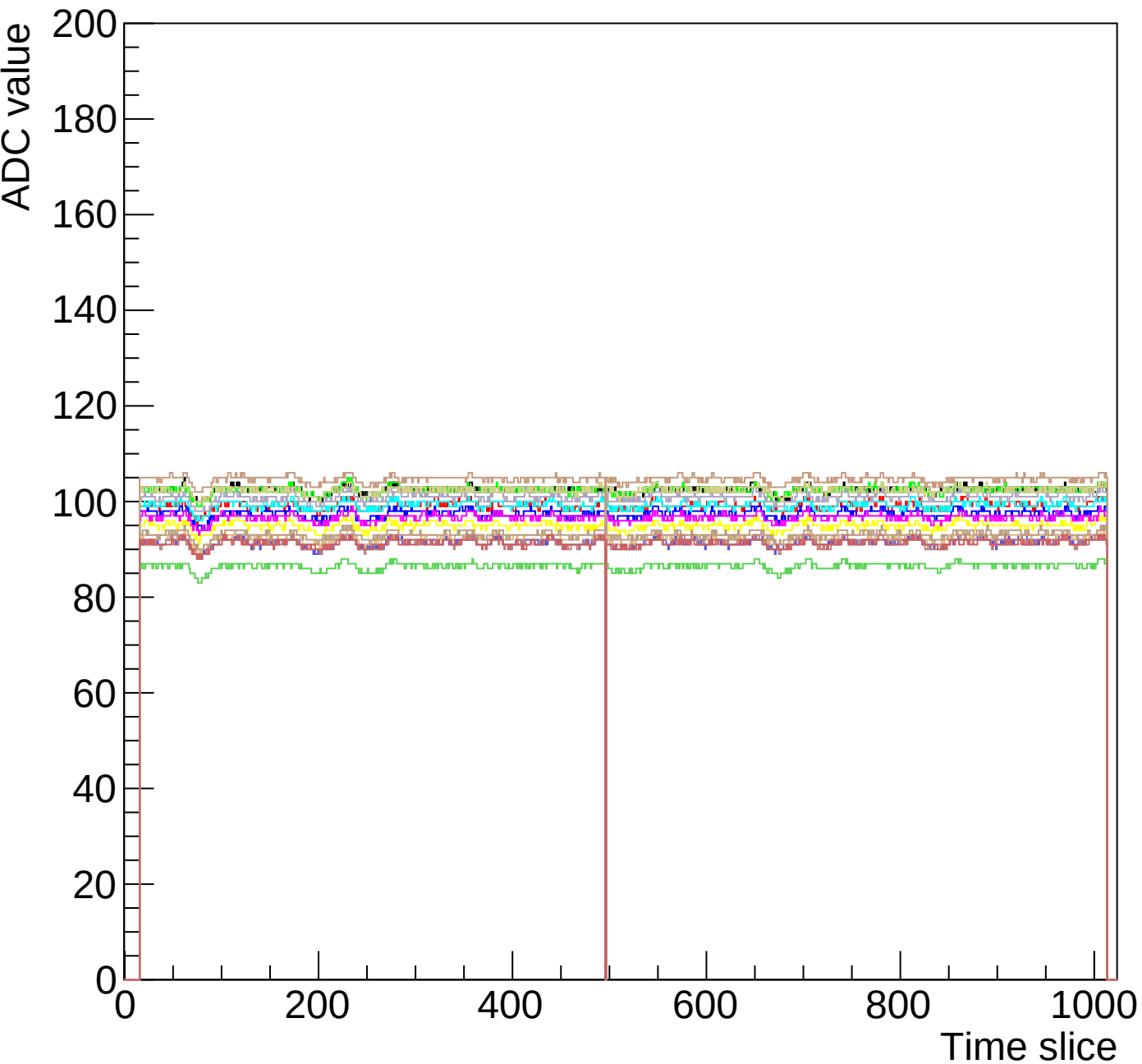
Run 0 event 3 mcm 0 chip 4 mcm22-1000



Run 0 event 3 mcm 0 chip 5 mcm22-1000



Run 0 event 3 mcm 0 chip 6 mcm22-1000



Run 0 event 3 mcm 0 chip 7 mcm22-1000

