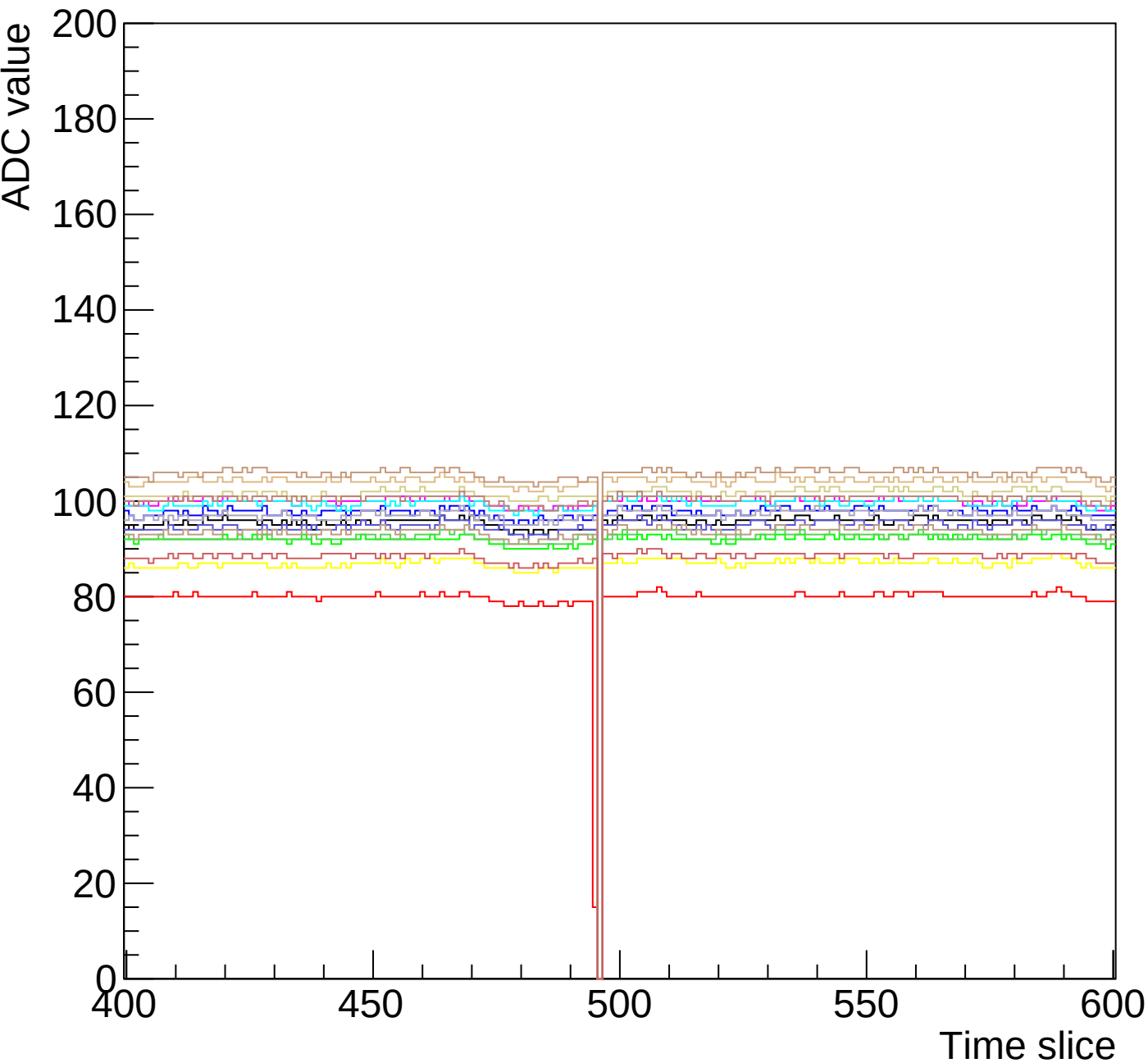
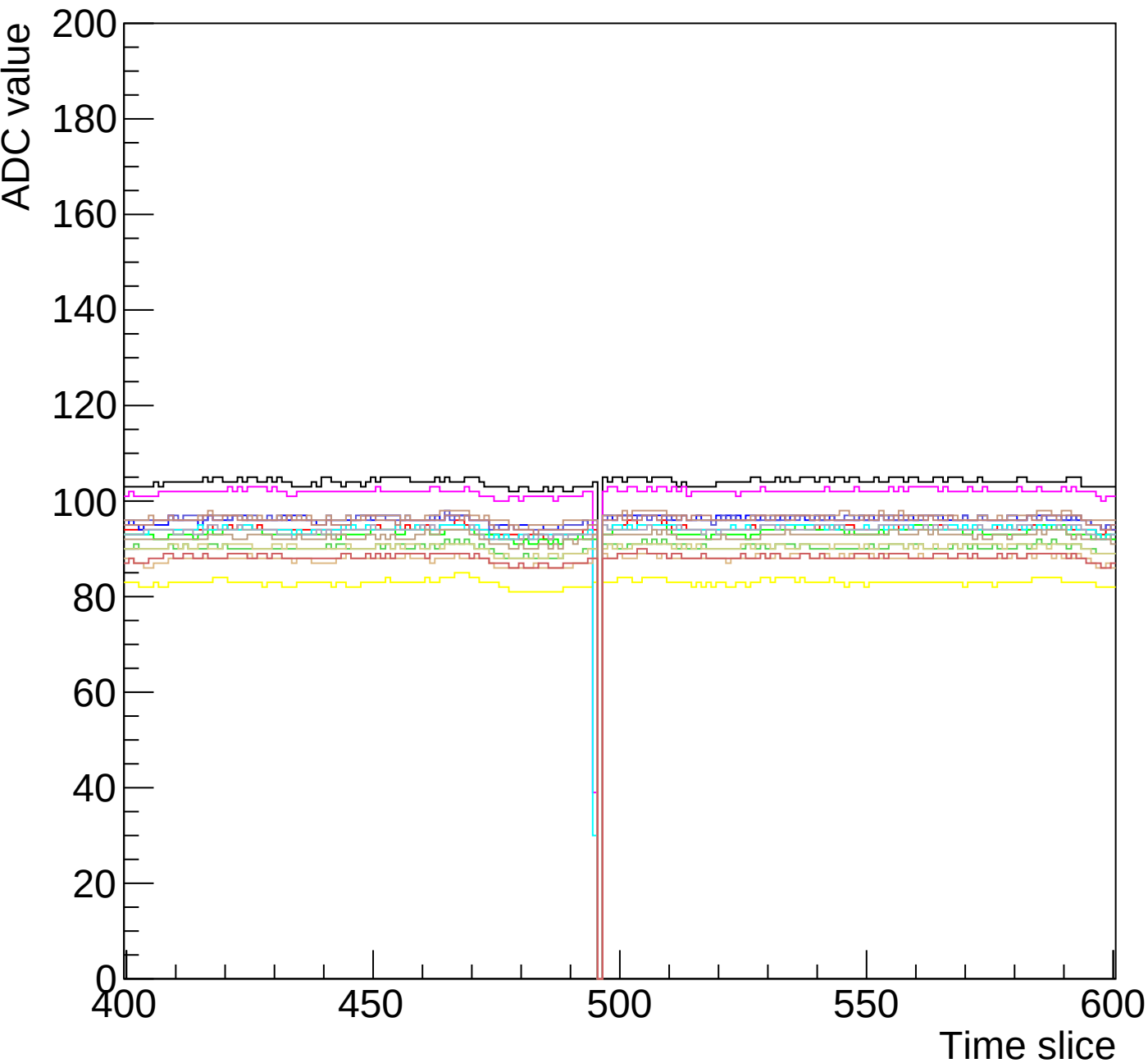


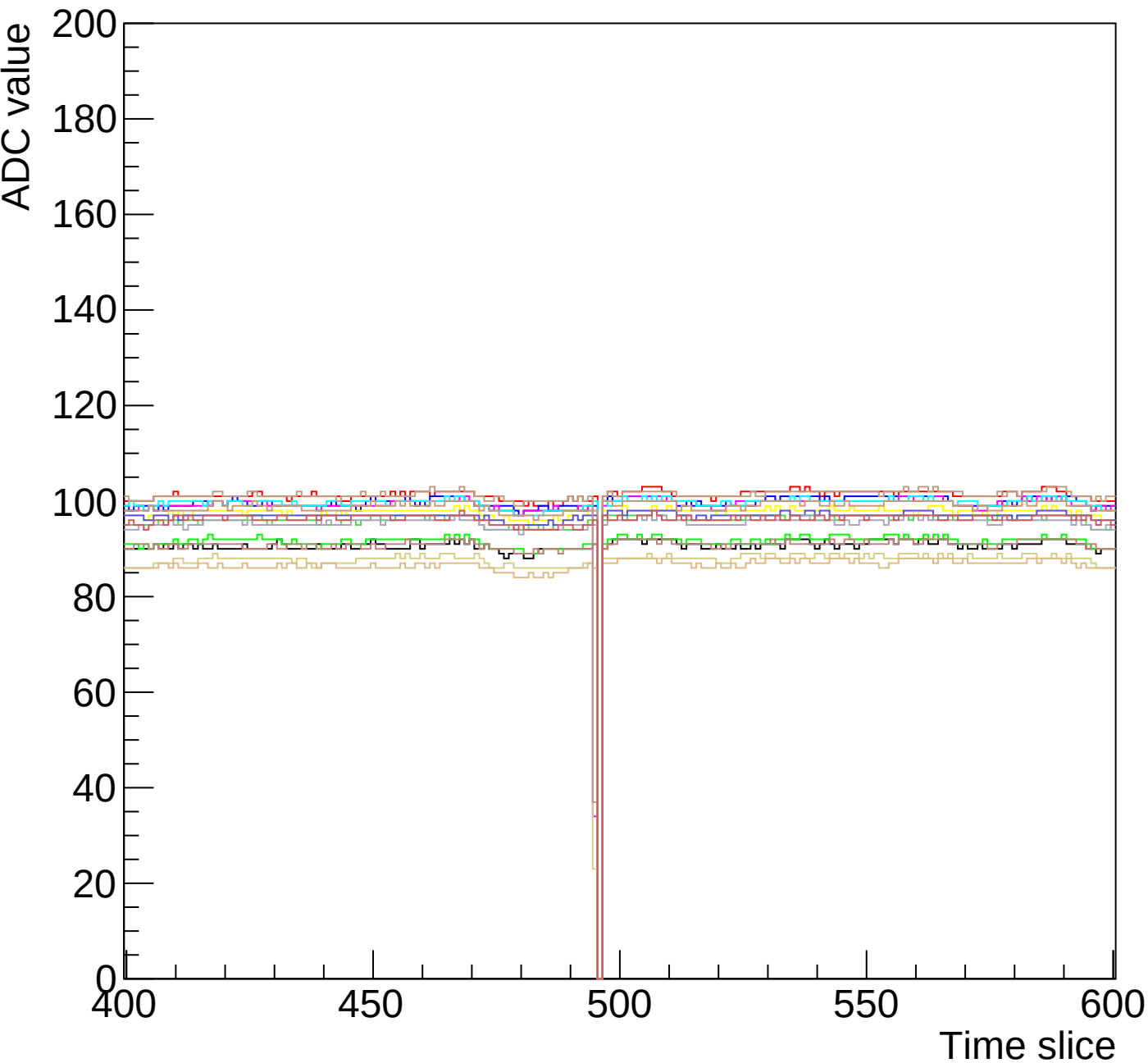
Run 0 event 40 mcm 0 chip 0 mcm10



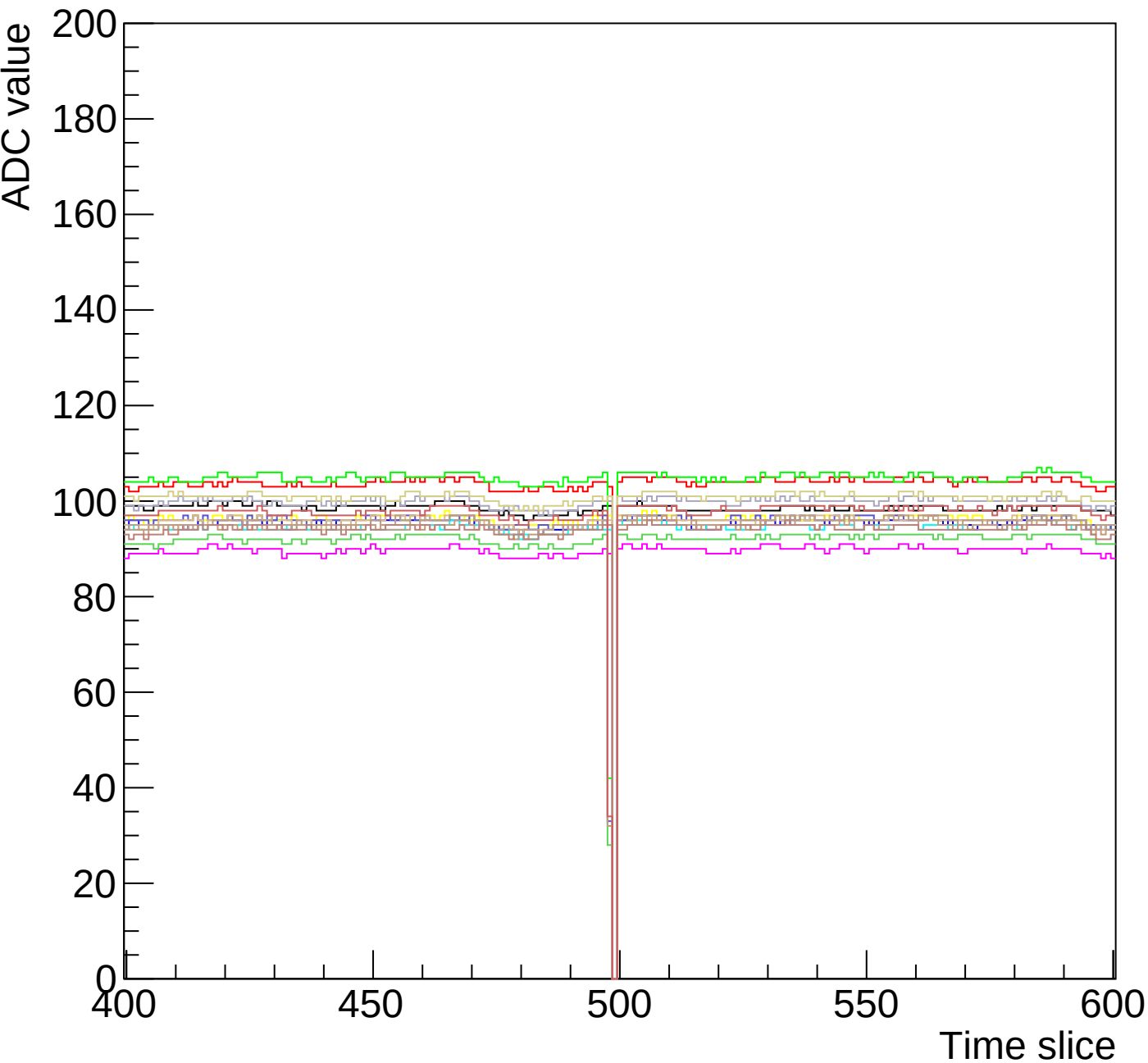
Run 0 event 40 mcm 0 chip 1 mcm10



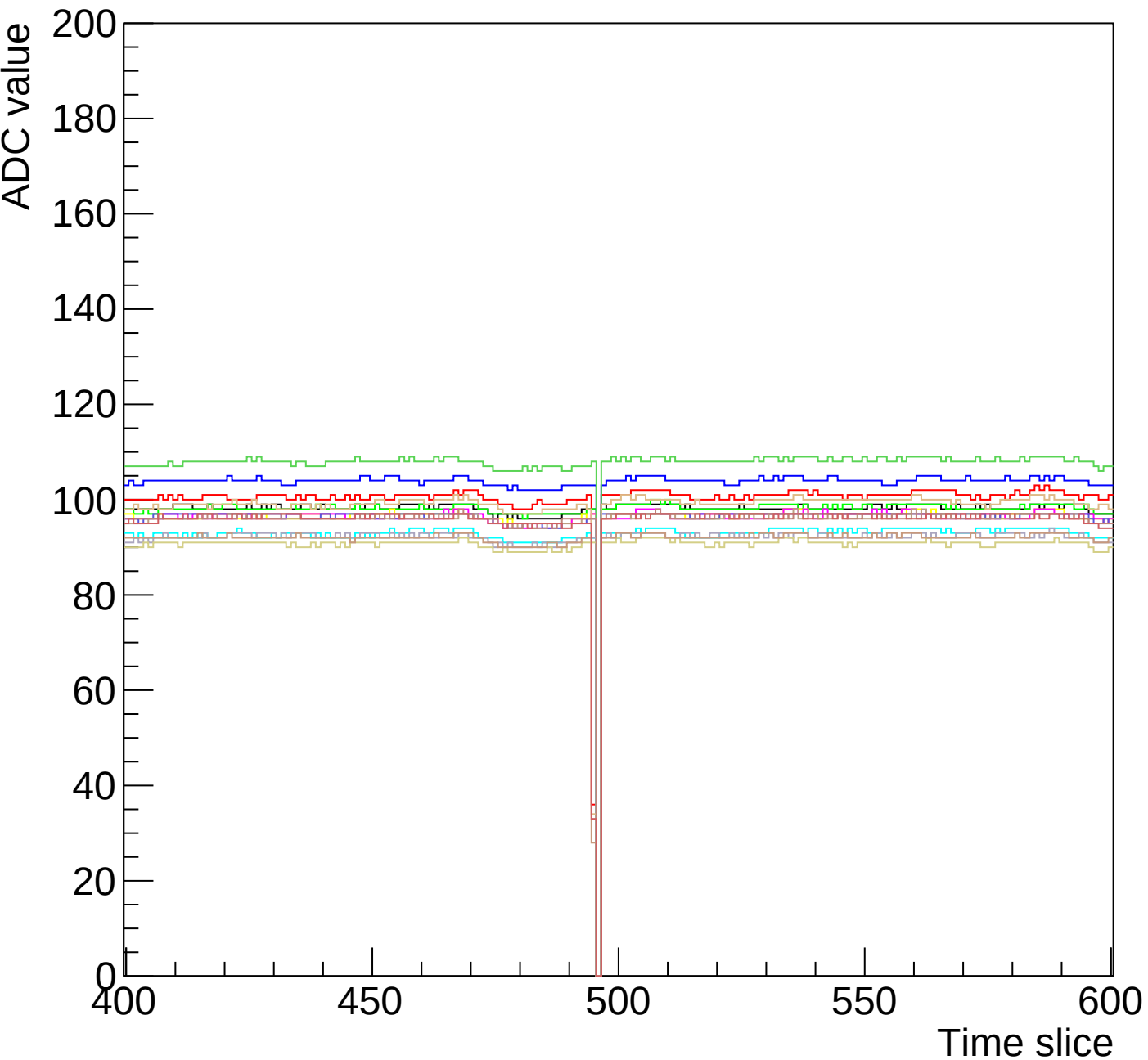
Run 0 event 40 mcm 0 chip 2 mcm10



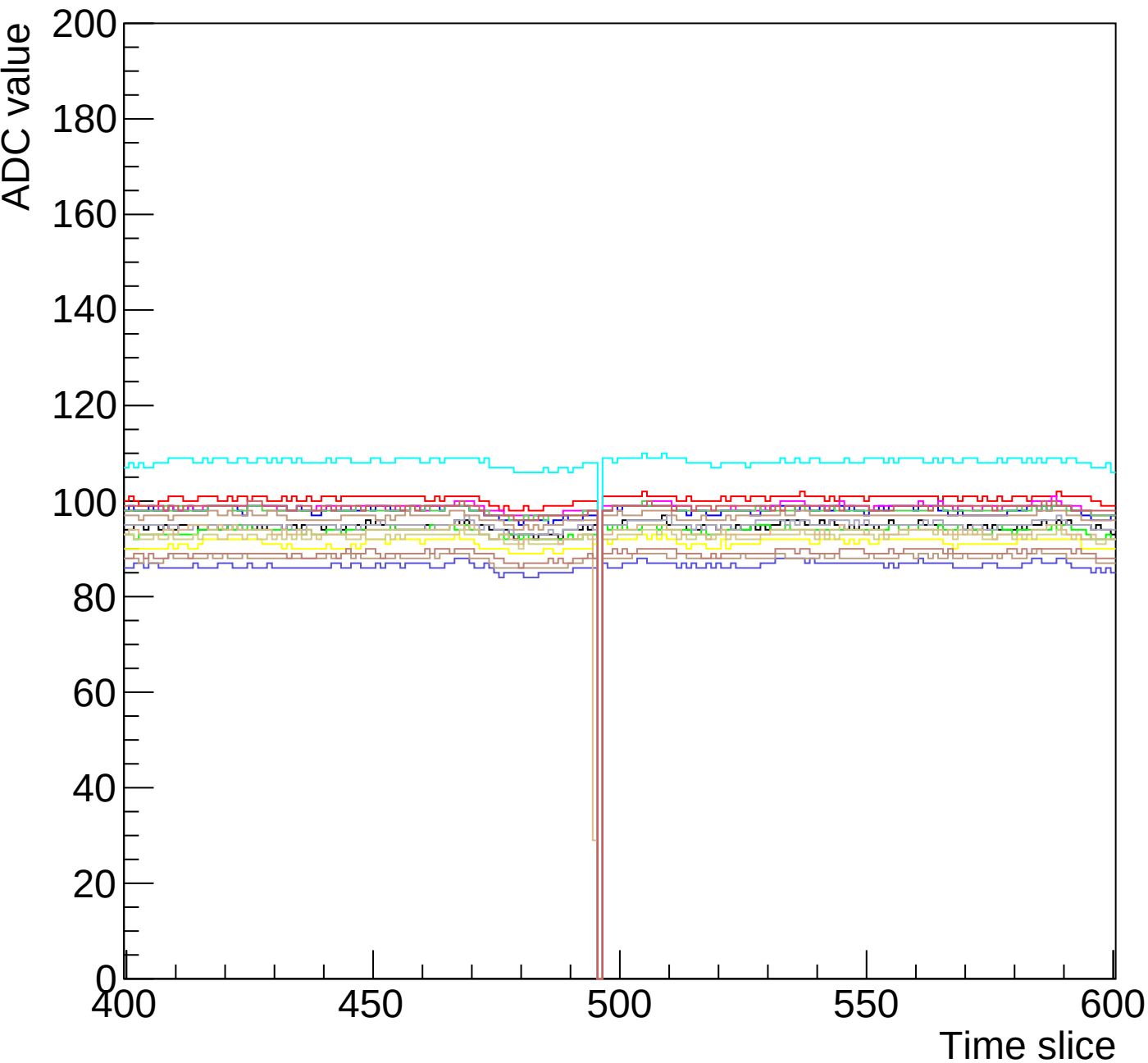
Run 0 event 40 mcm 0 chip 3 mcm10



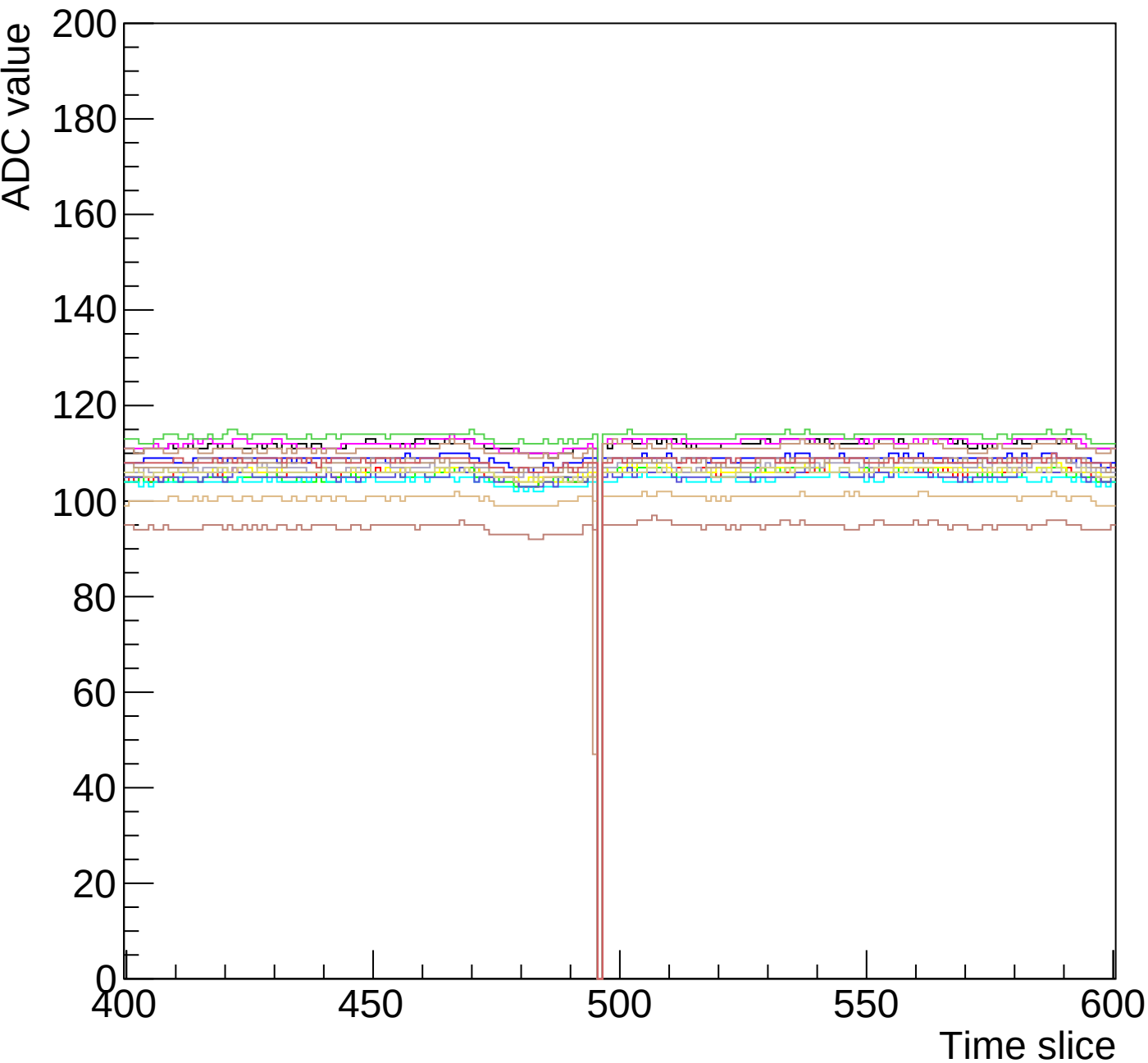
Run 0 event 40 mcm 0 chip 4 mcm10



Run 0 event 40 mcm 0 chip 5 mcm10



Run 0 event 40 mcm 0 chip 6 mcm10



Run 0 event 40 mcm 0 chip 7 mcm10

