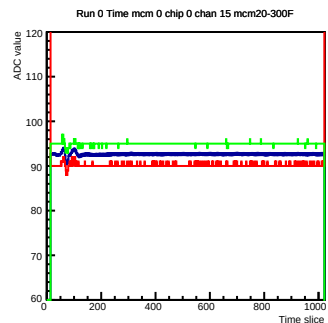
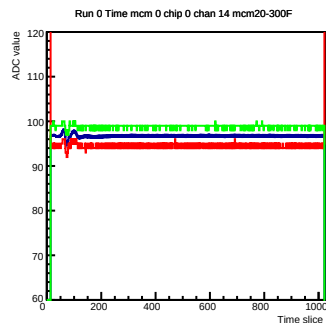
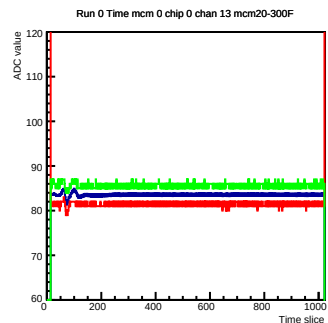
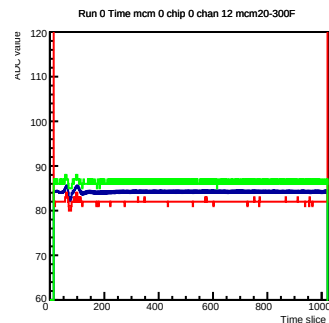
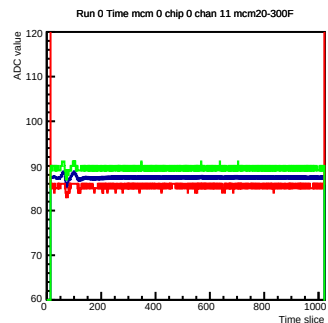
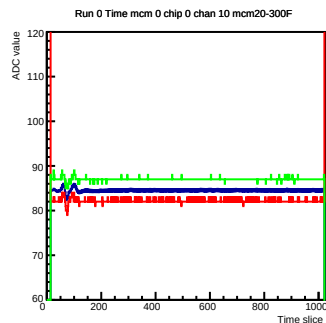
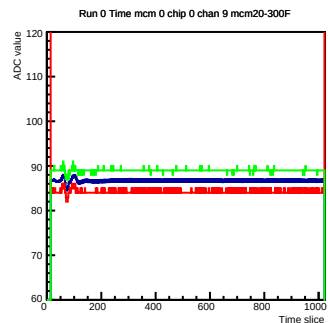
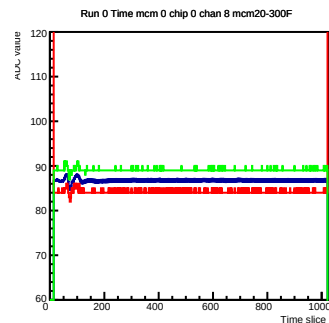
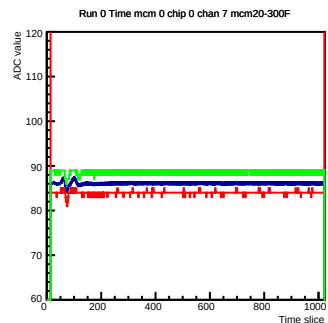
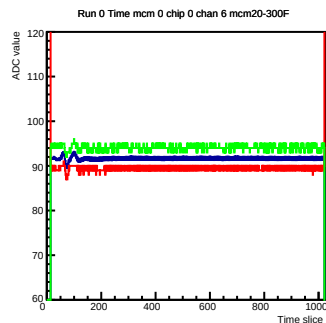
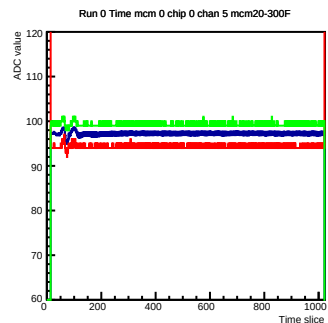
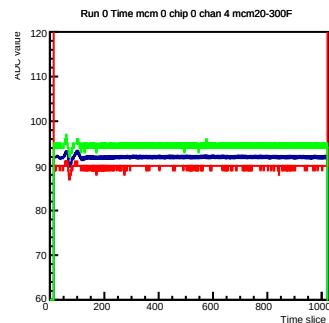
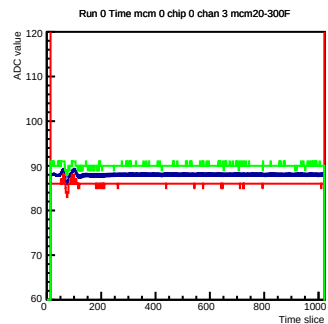
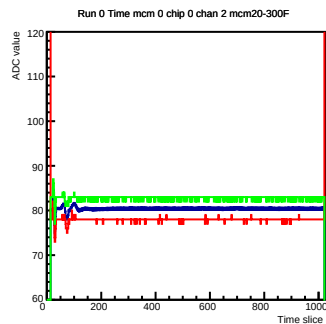
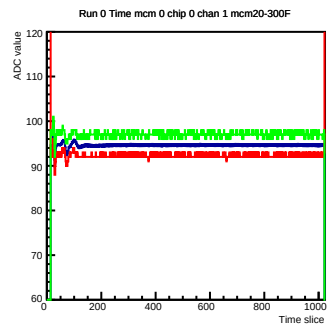
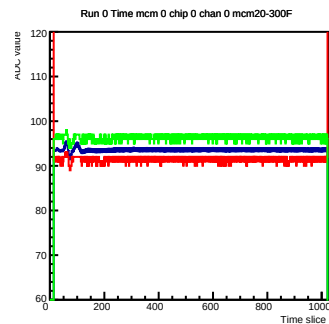
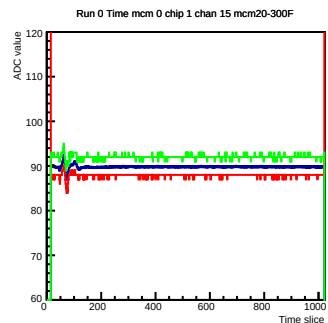
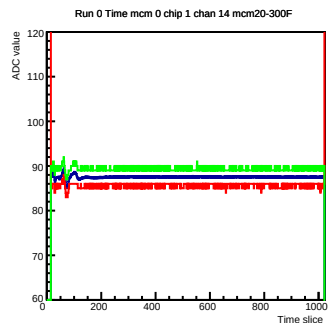
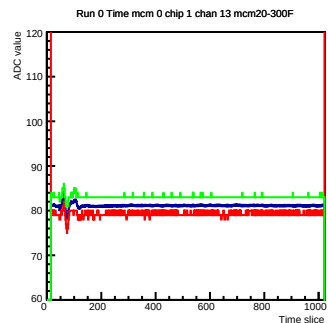
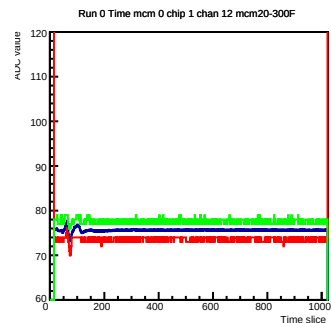
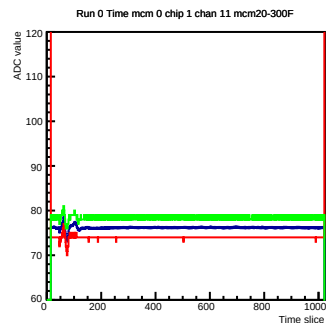
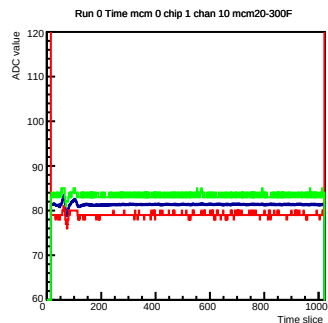
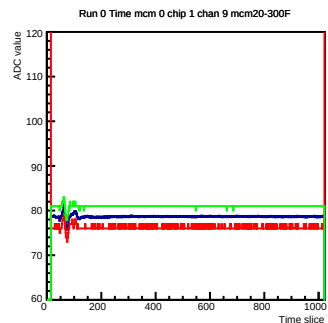
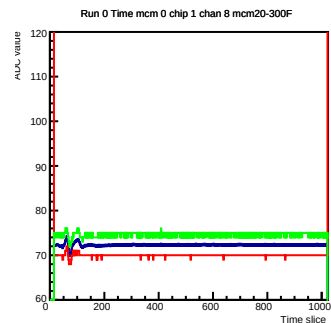
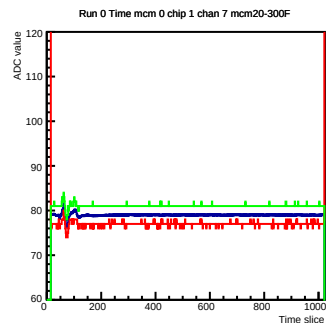
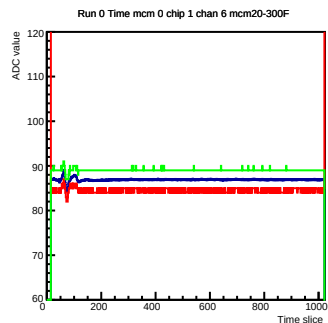
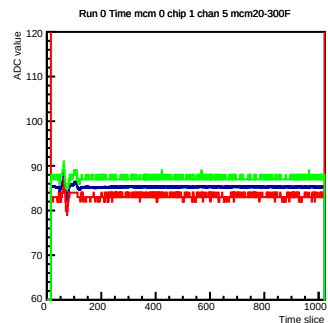
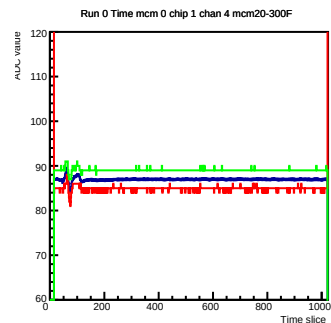
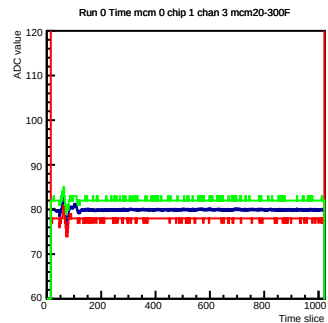
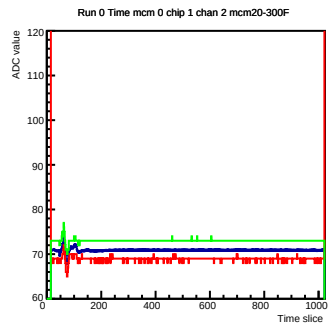
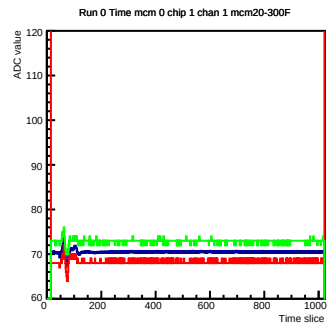
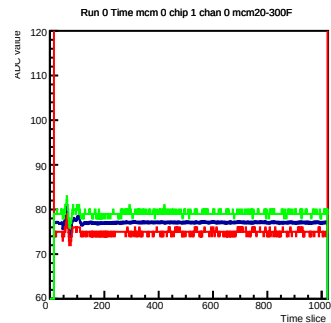
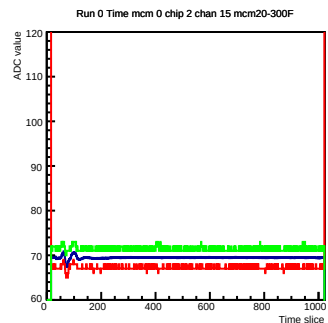
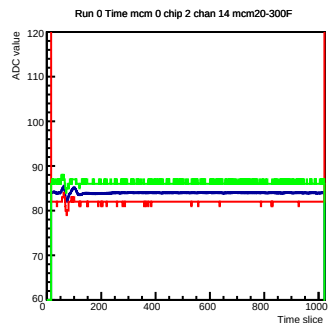
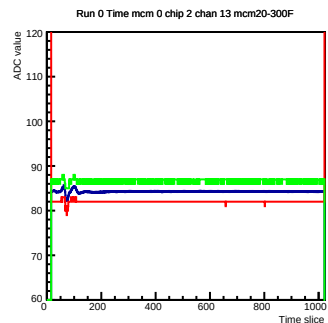
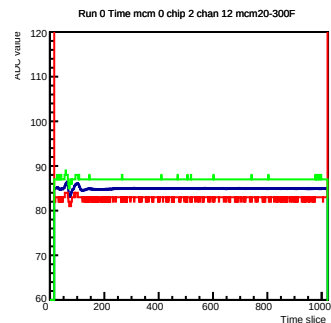
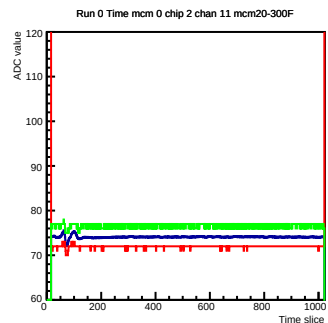
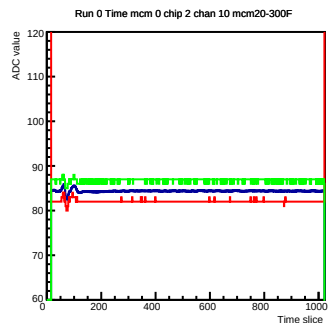
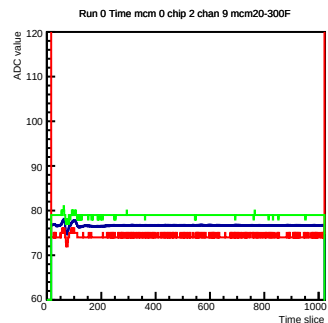
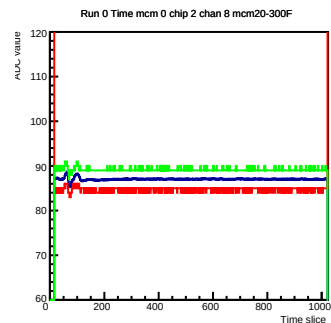
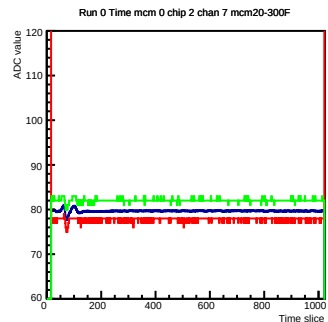
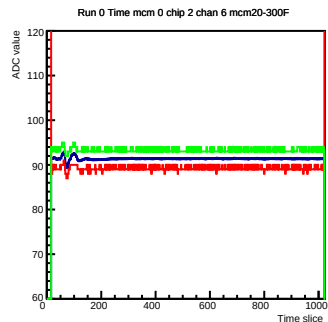
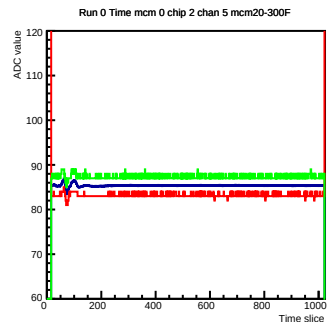
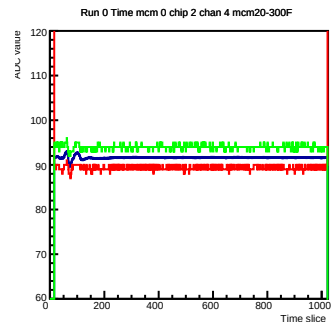
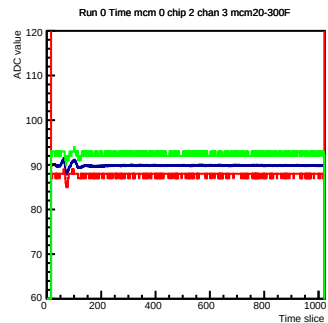
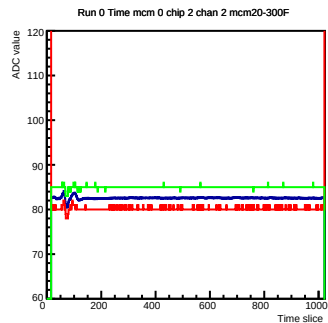
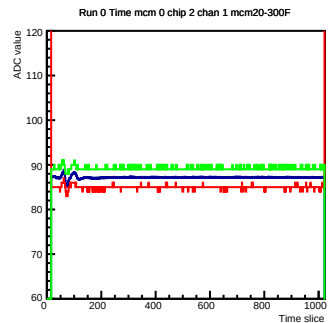
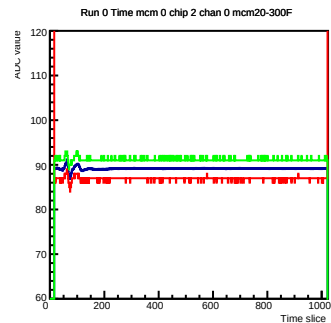
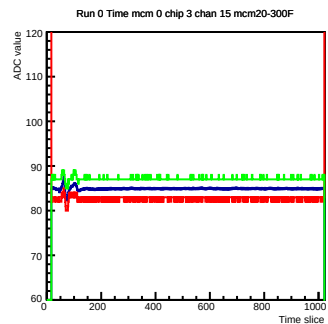
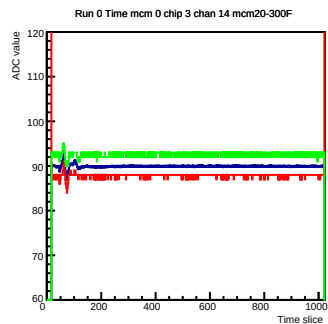
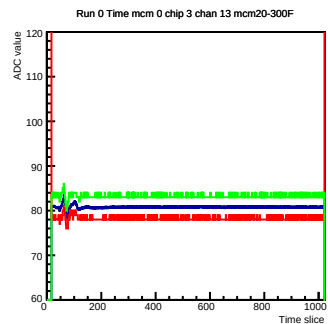
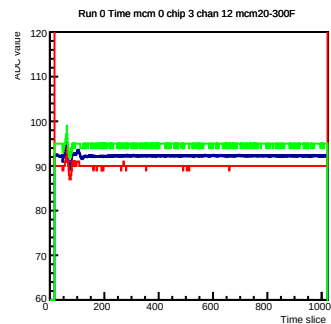
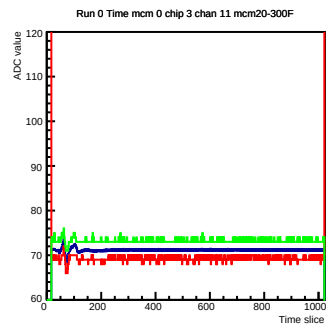
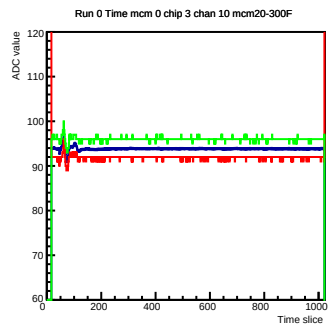
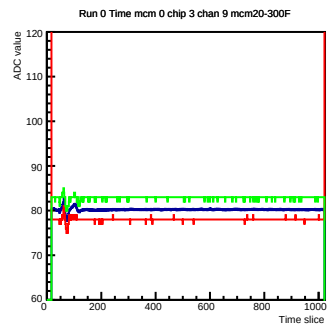
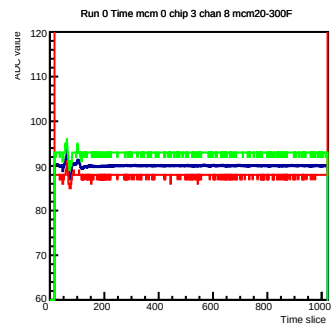
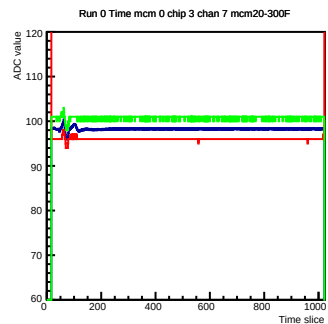
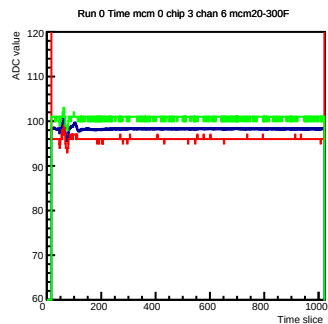
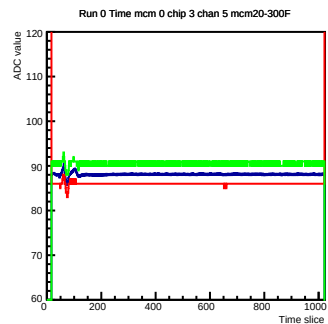
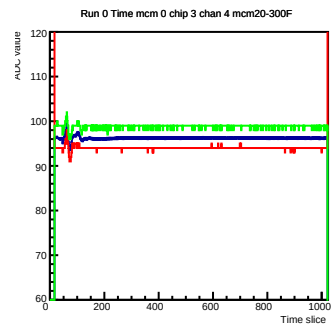
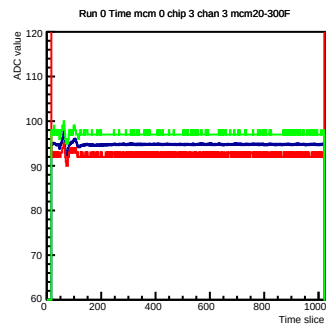
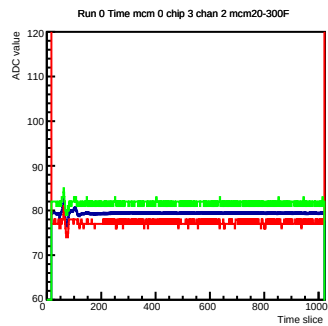
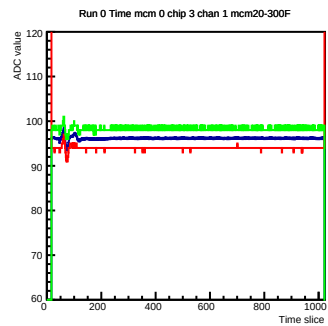
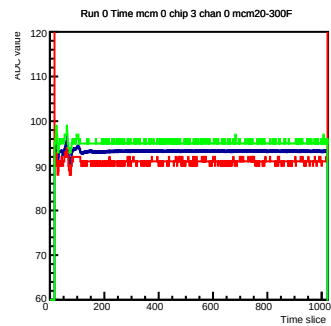


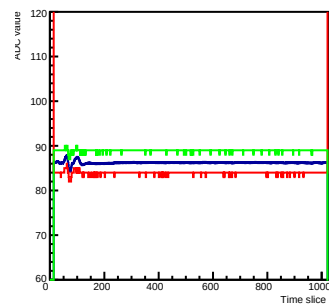




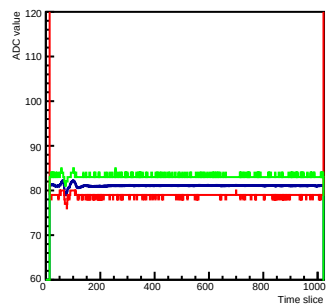




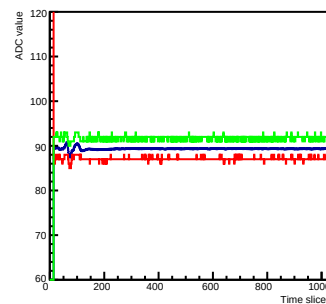
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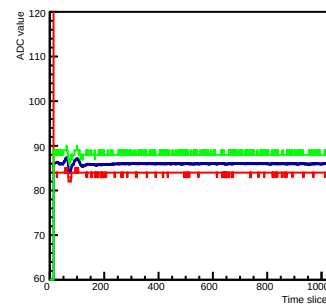
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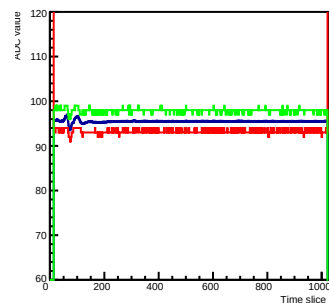
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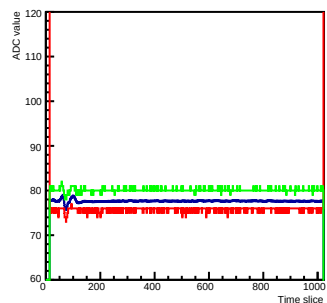
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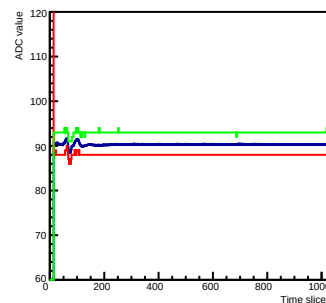
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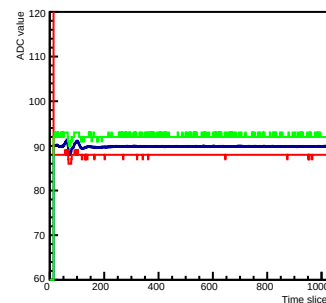
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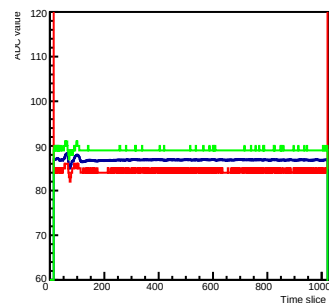
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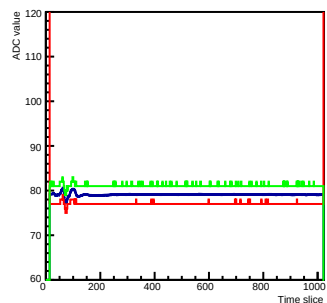
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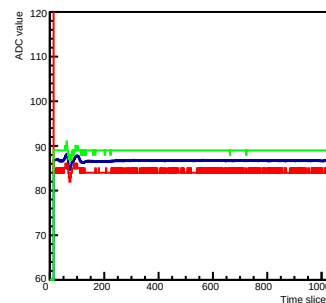
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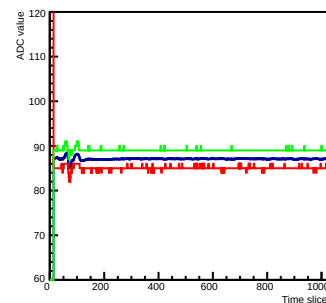
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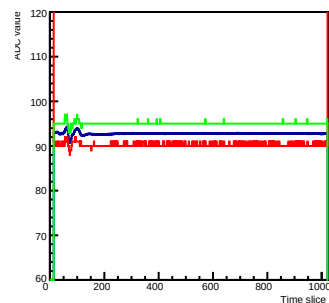
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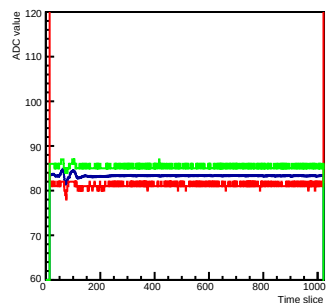
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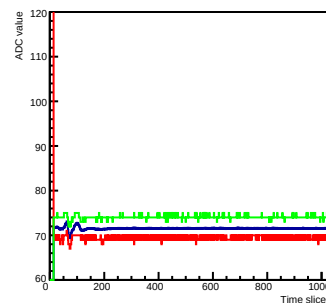
Run 0 Time mcm 0 chip 4 chan 12 mcm20-300F



Run 0 Time mcm 0 chip 4 chan 13 mcm20-300F



Run 0 Time mcm 0 chip 4 chan 14 mcm20-300F



Run 0 Time mcm 0 chip 4 chan 15 mcm20-300F

