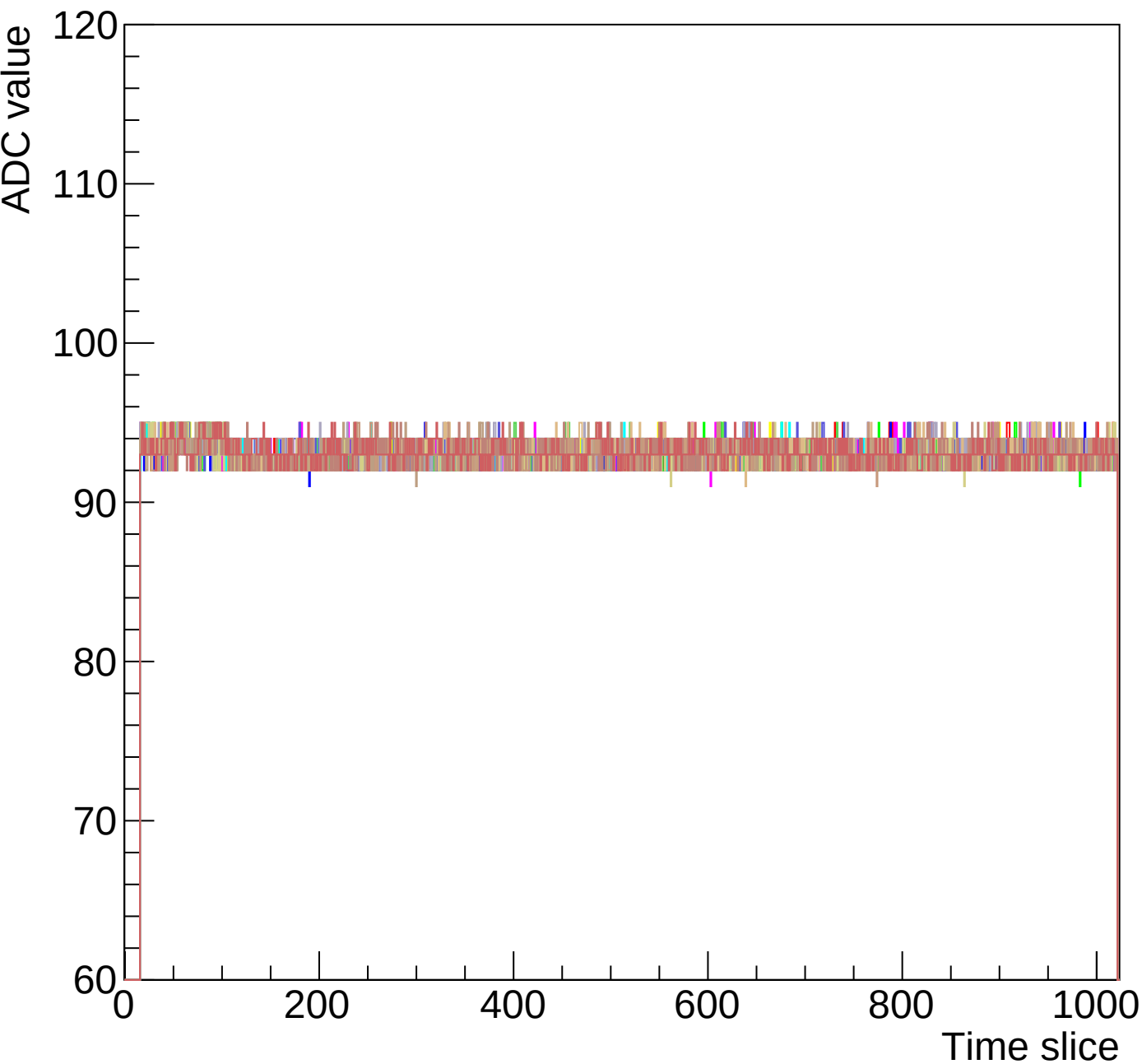
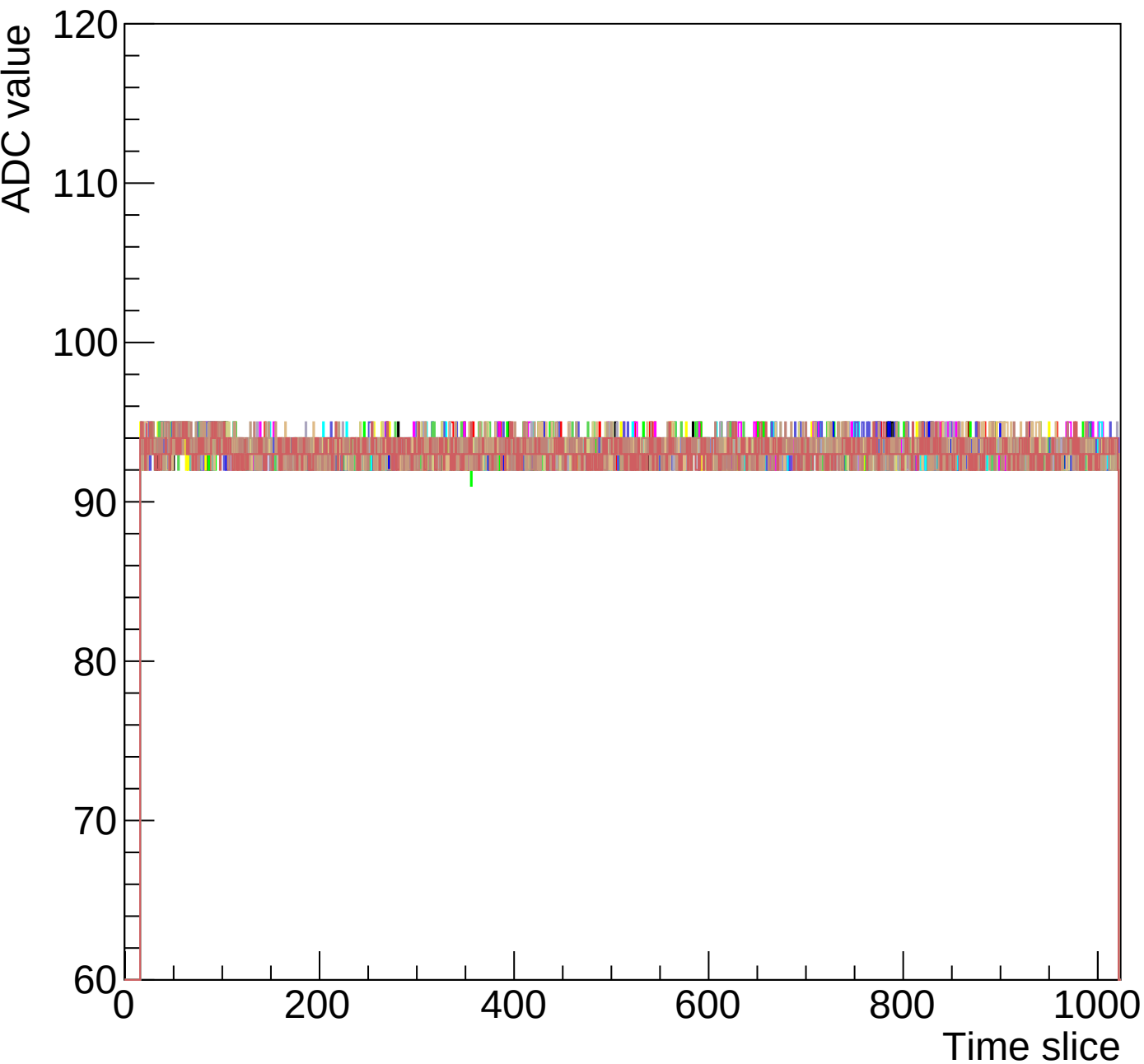


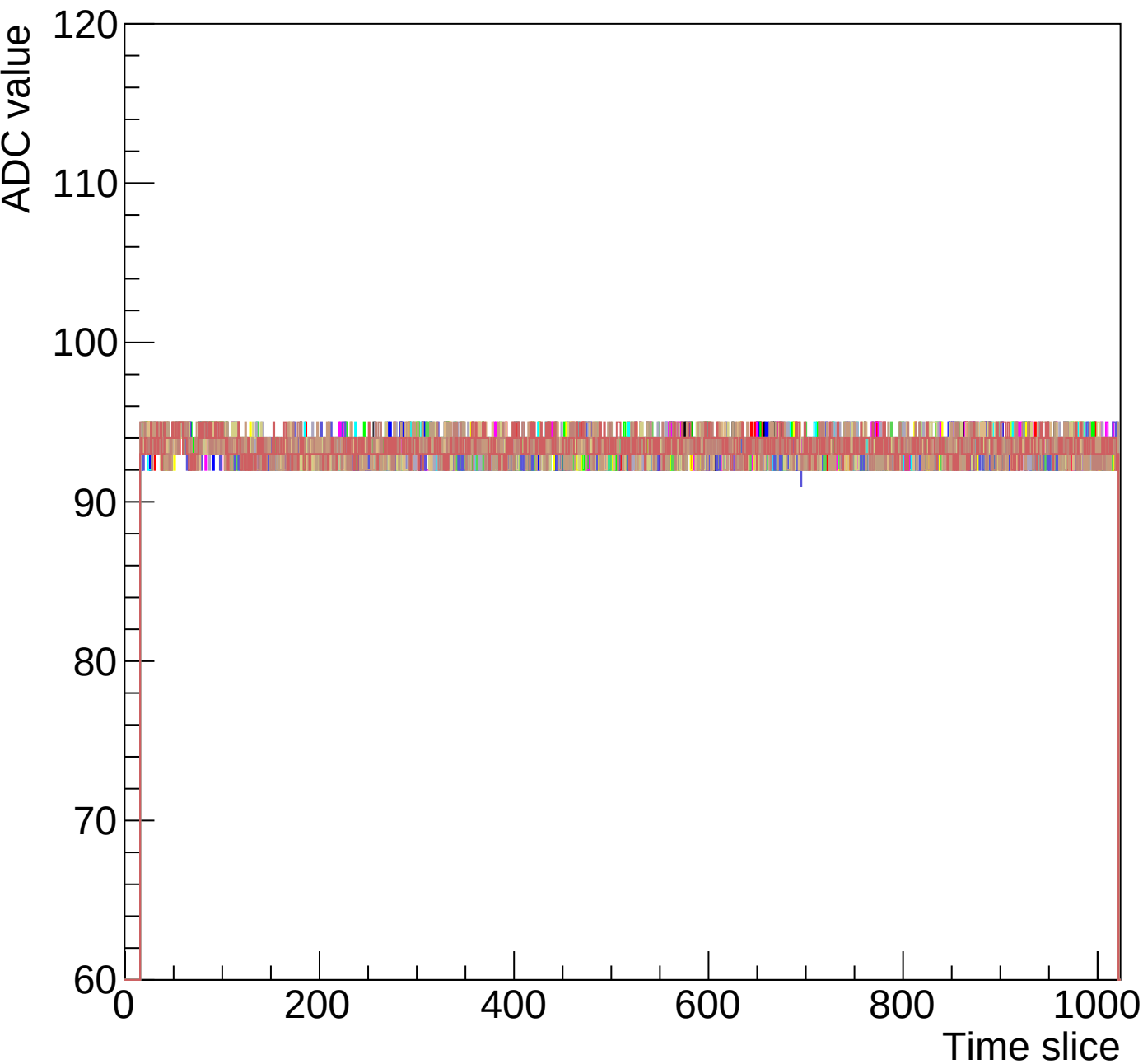
# Event 0-15 mcm 0 chan 96 test2



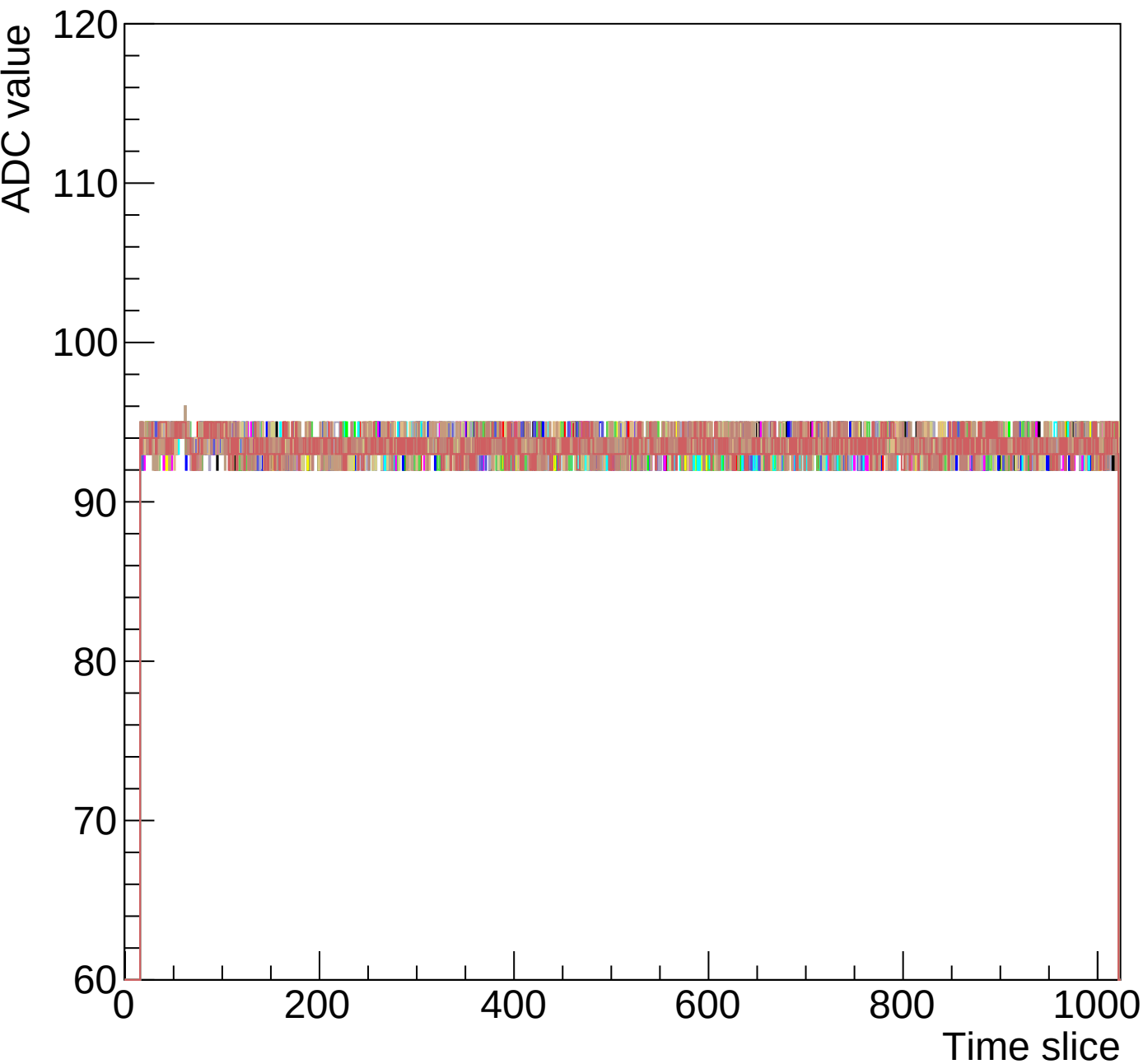
# Event 16-31 mcm 0 chan 96 test2



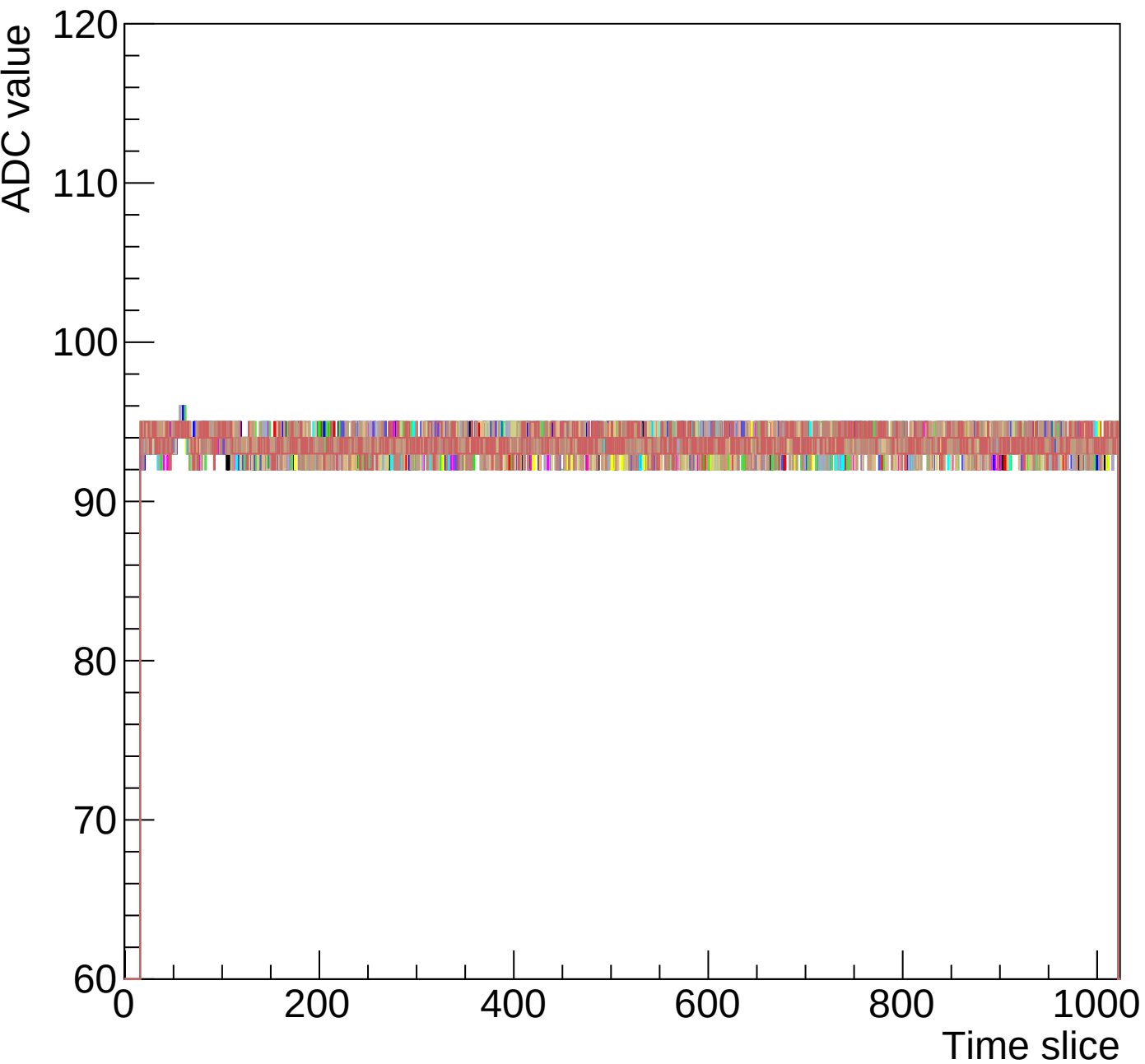
# Event 32-47 mcm 0 chan 96 test2



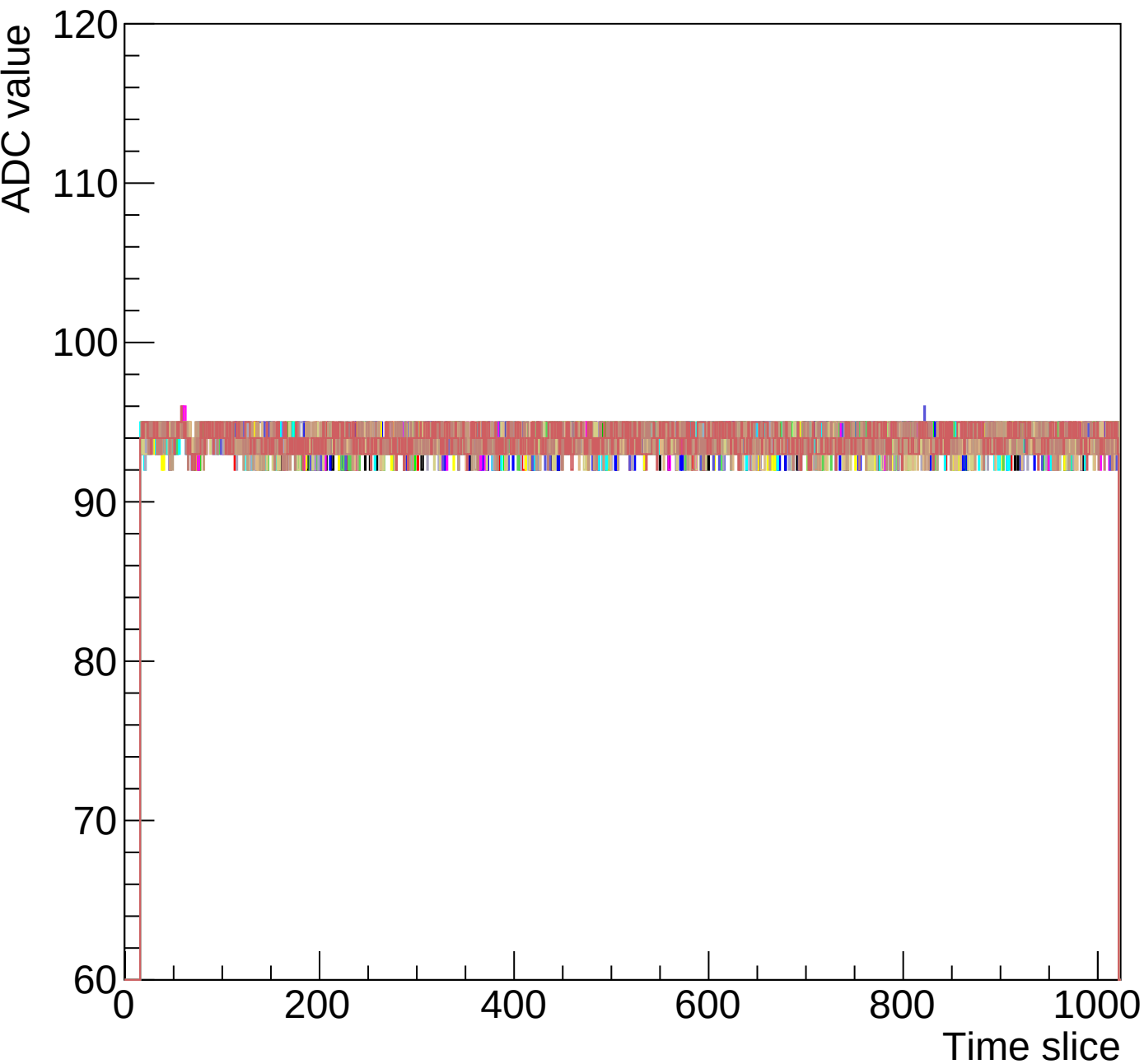
# Event 48-63 mcm 0 chan 96 test2



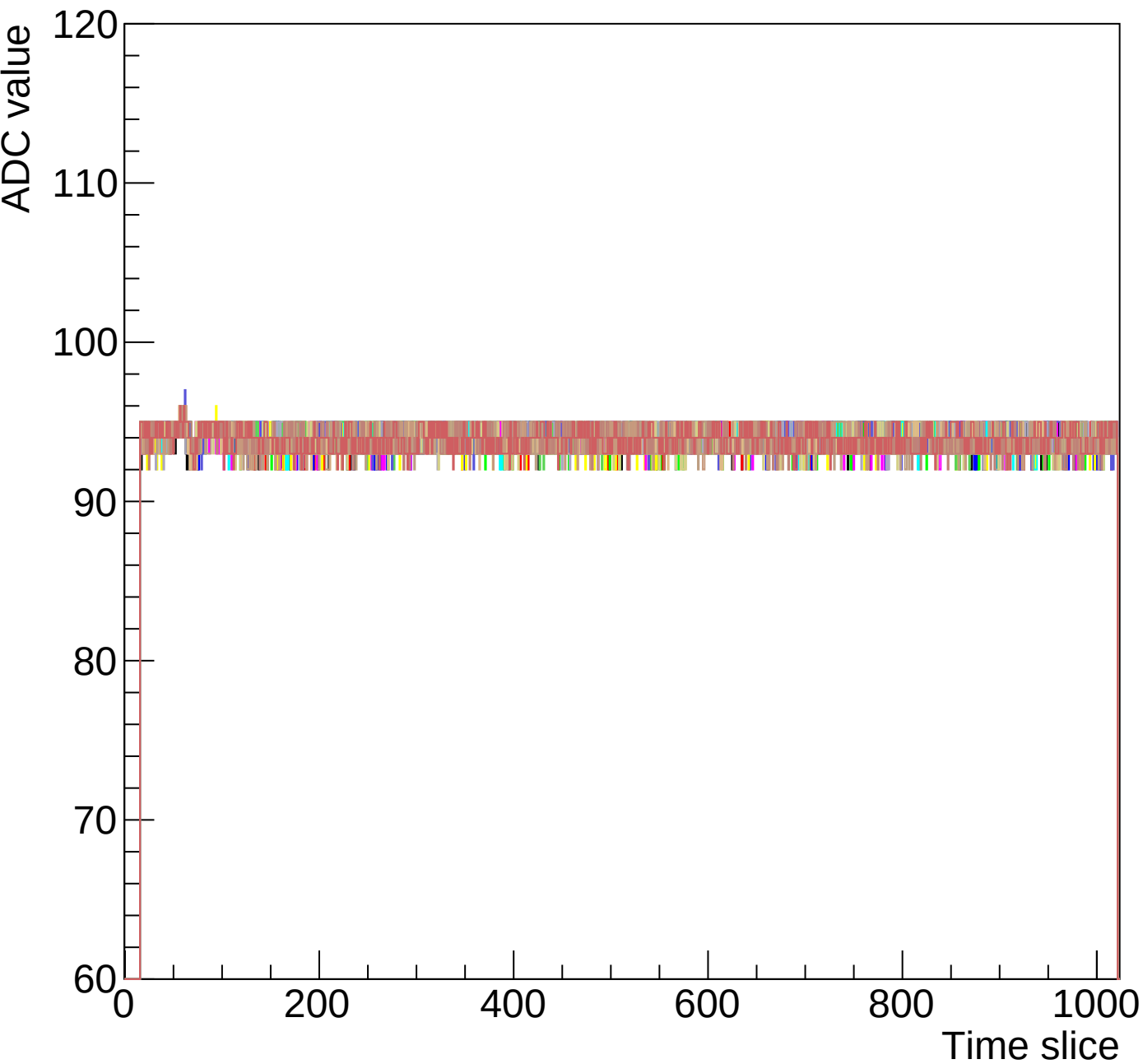
# Event 64-79 mcm 0 chan 96 test2



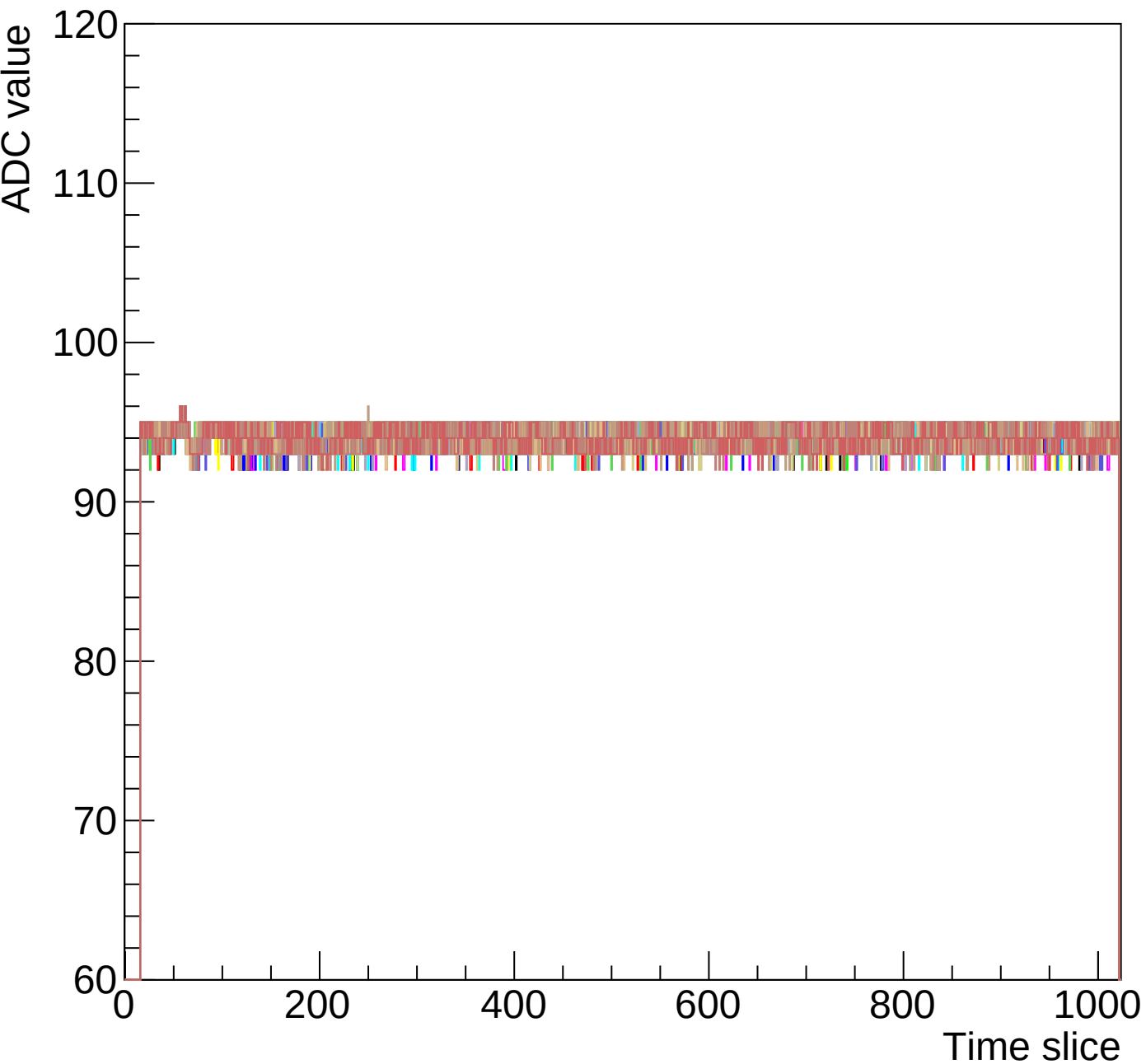
# Event 80-95 mcm 0 chan 96 test2



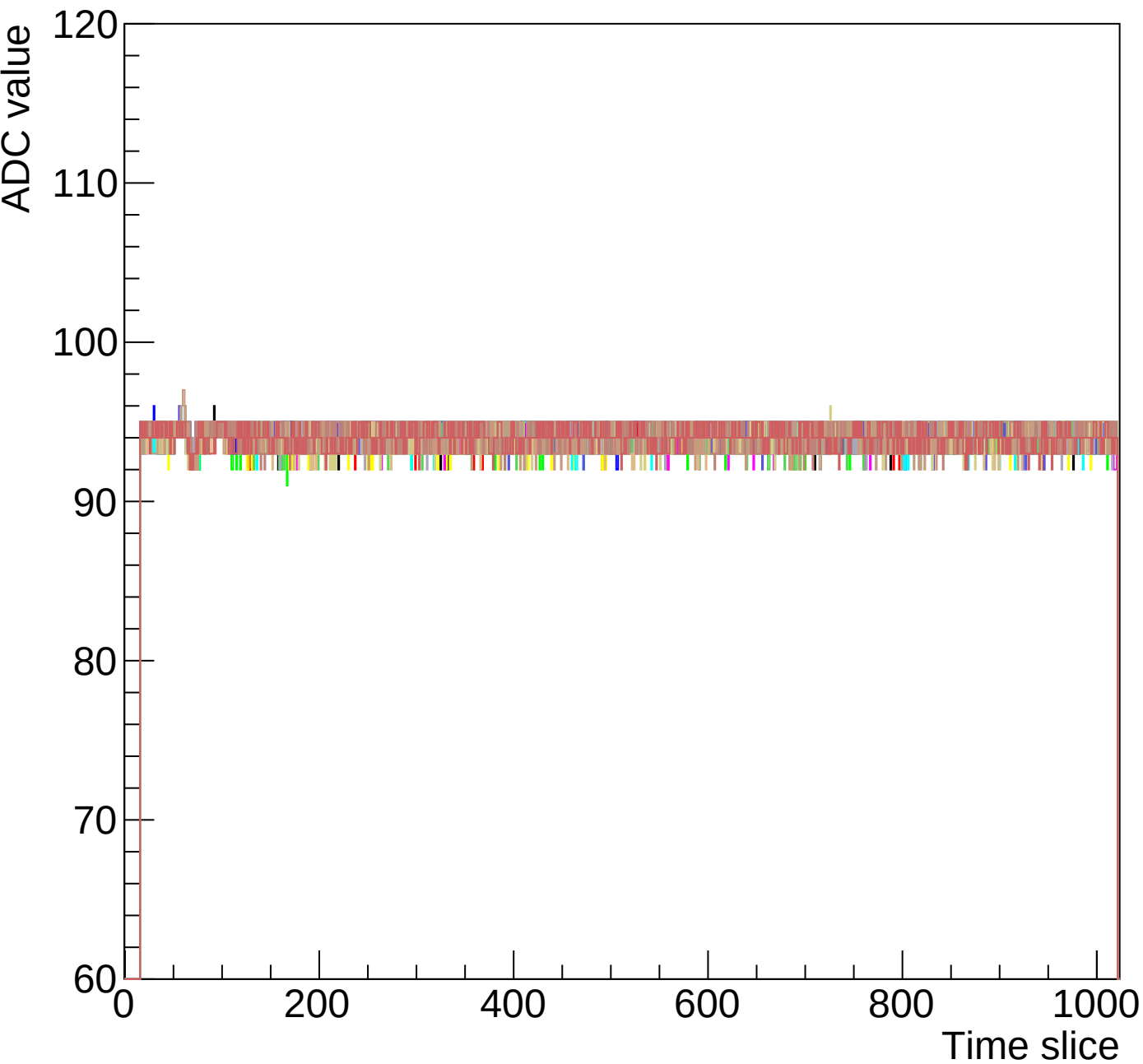
# Event 96-111 mcm 0 chan 96 test2



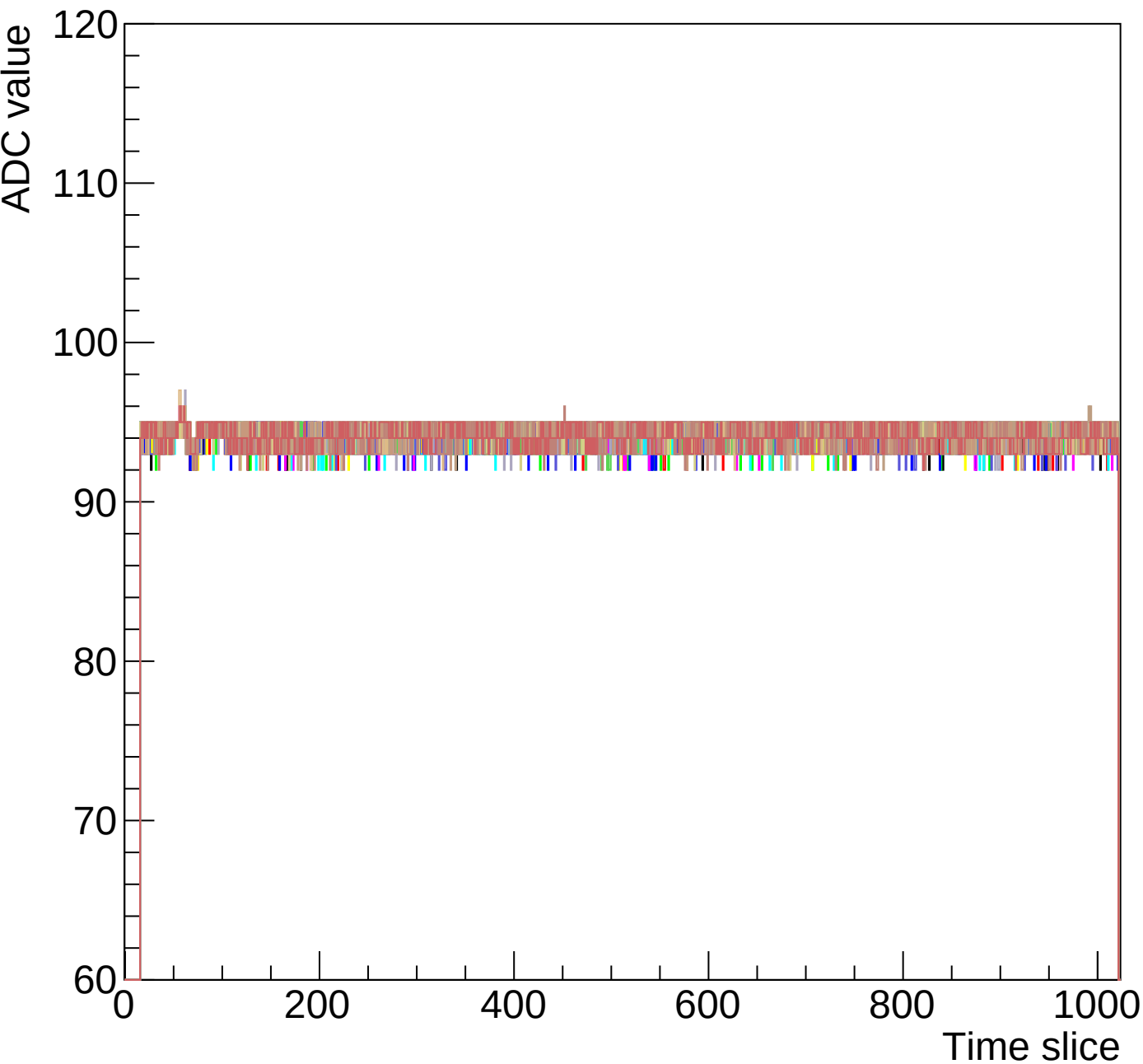
# Event 112-127 mcm 0 chan 96 test2



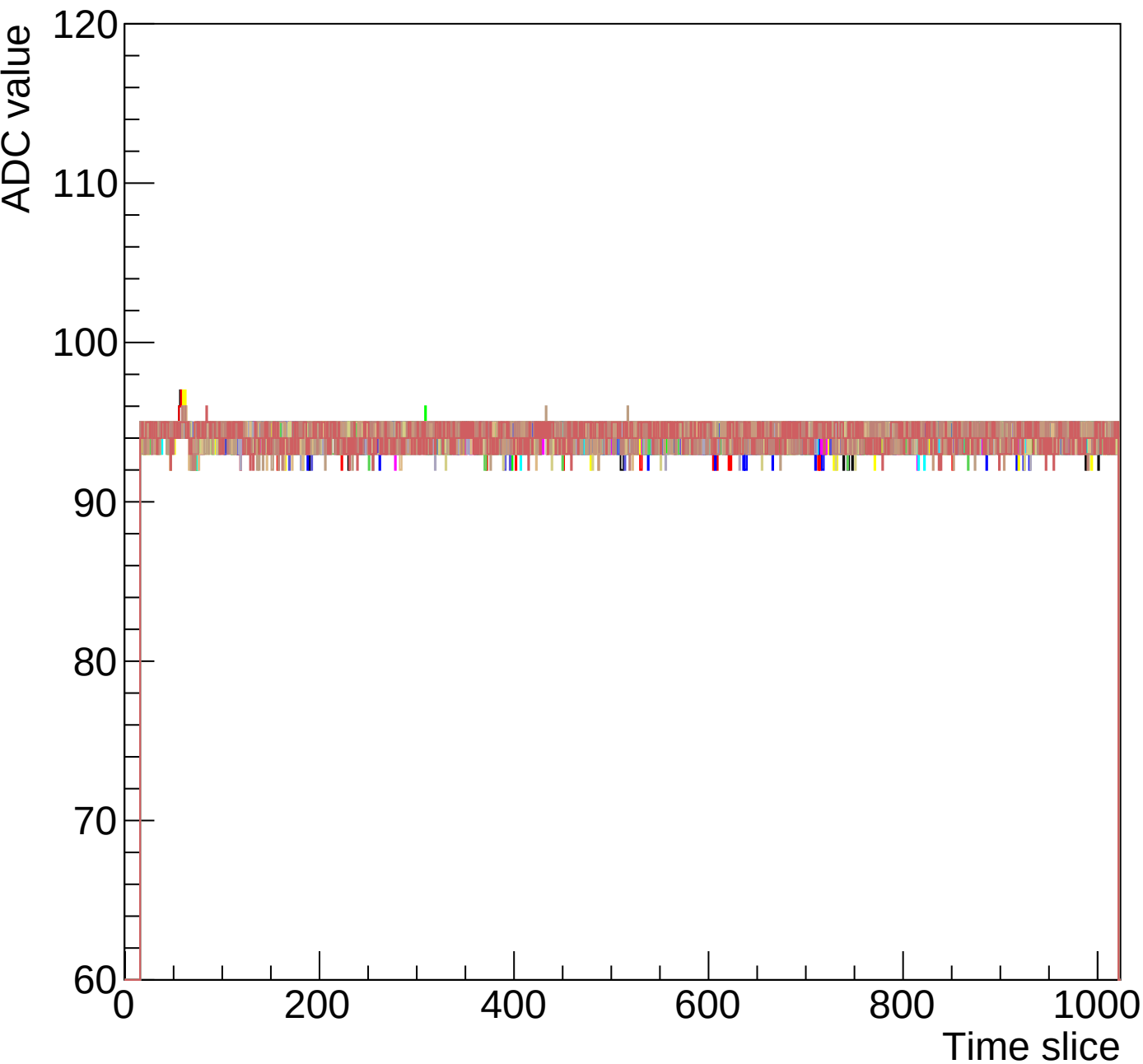
# Event 128-143 mcm 0 chan 96 test2



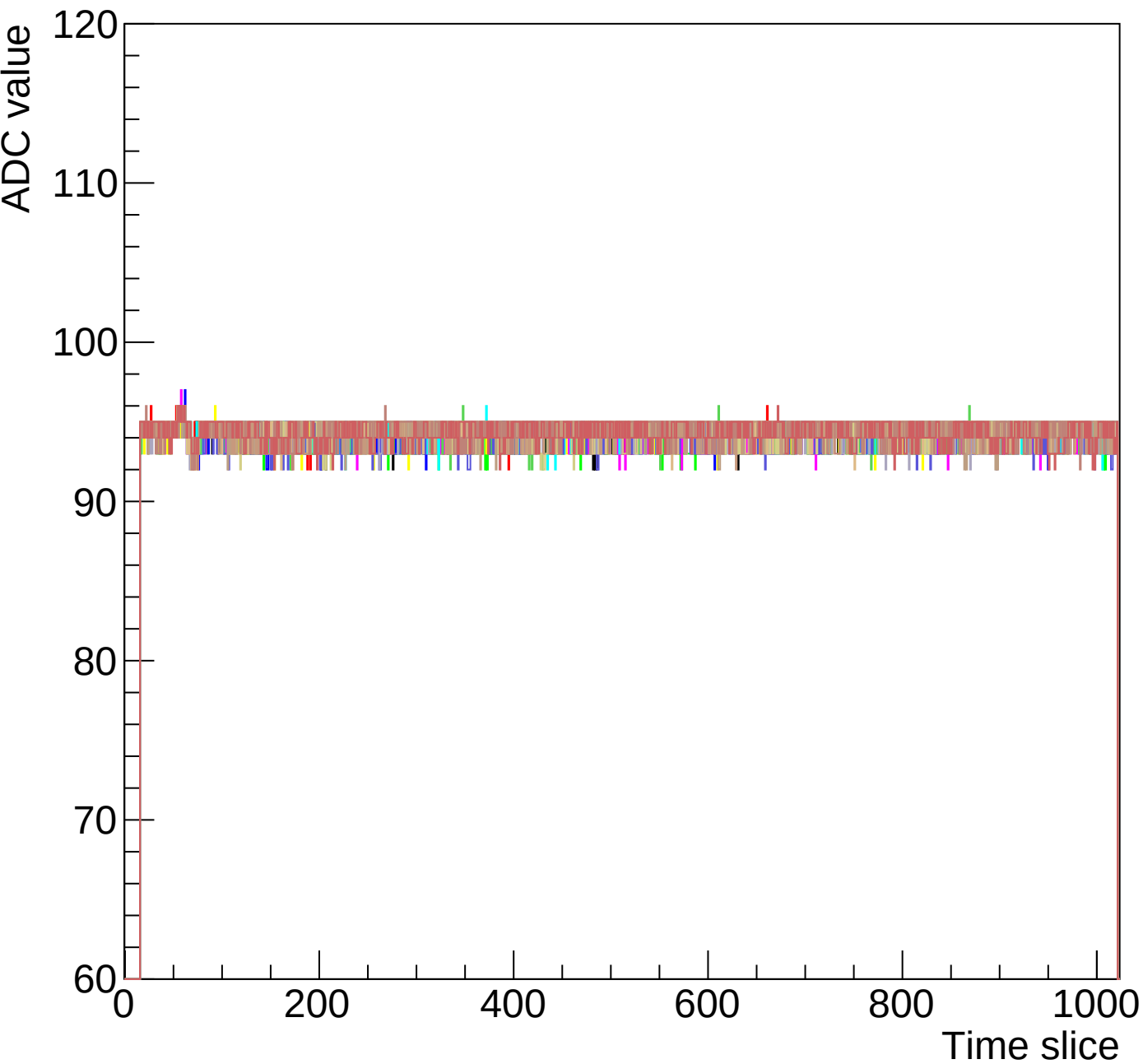
# Event 144-159 mcm 0 chan 96 test2



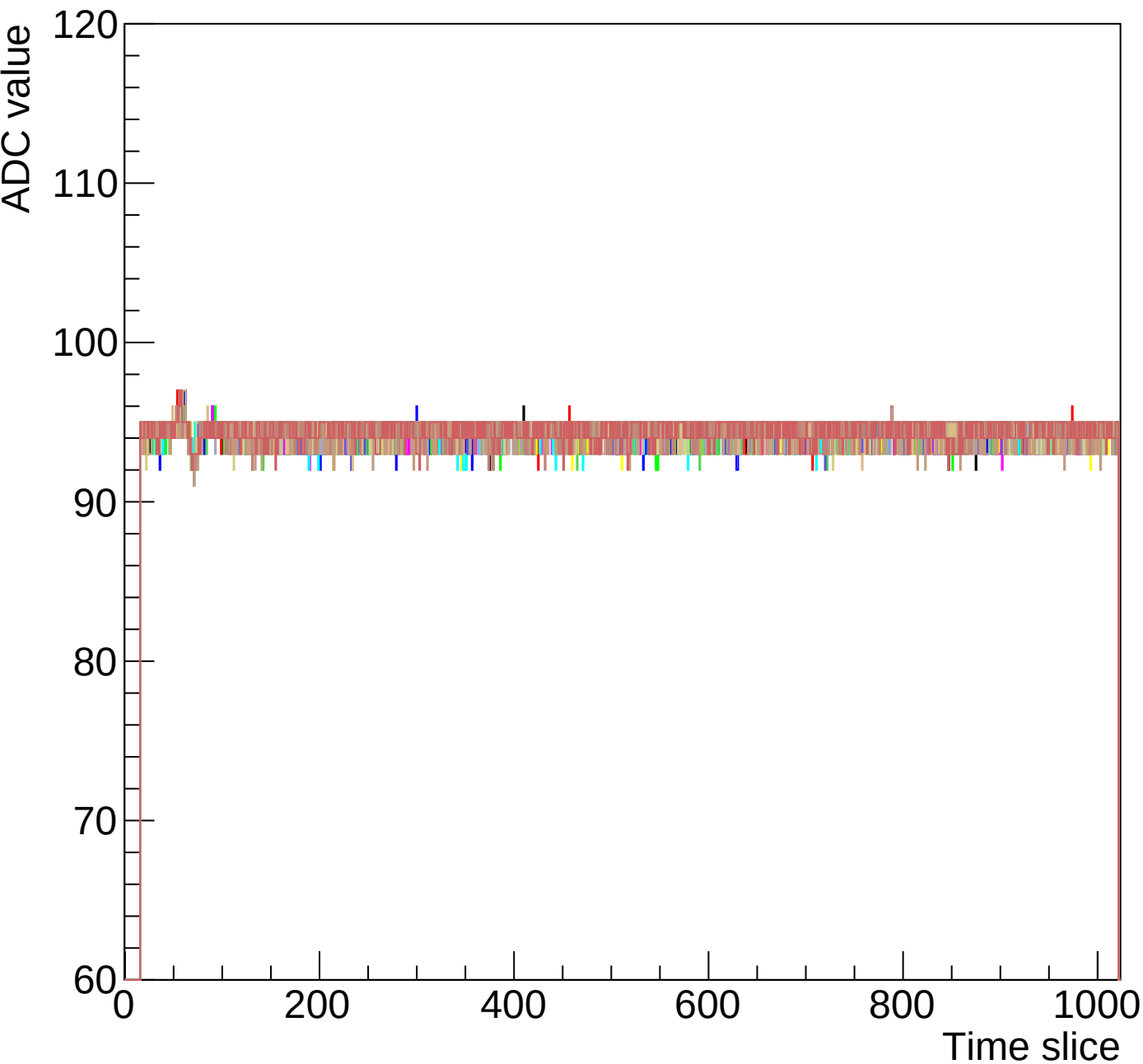
# Event 160-175 mcm 0 chan 96 test2



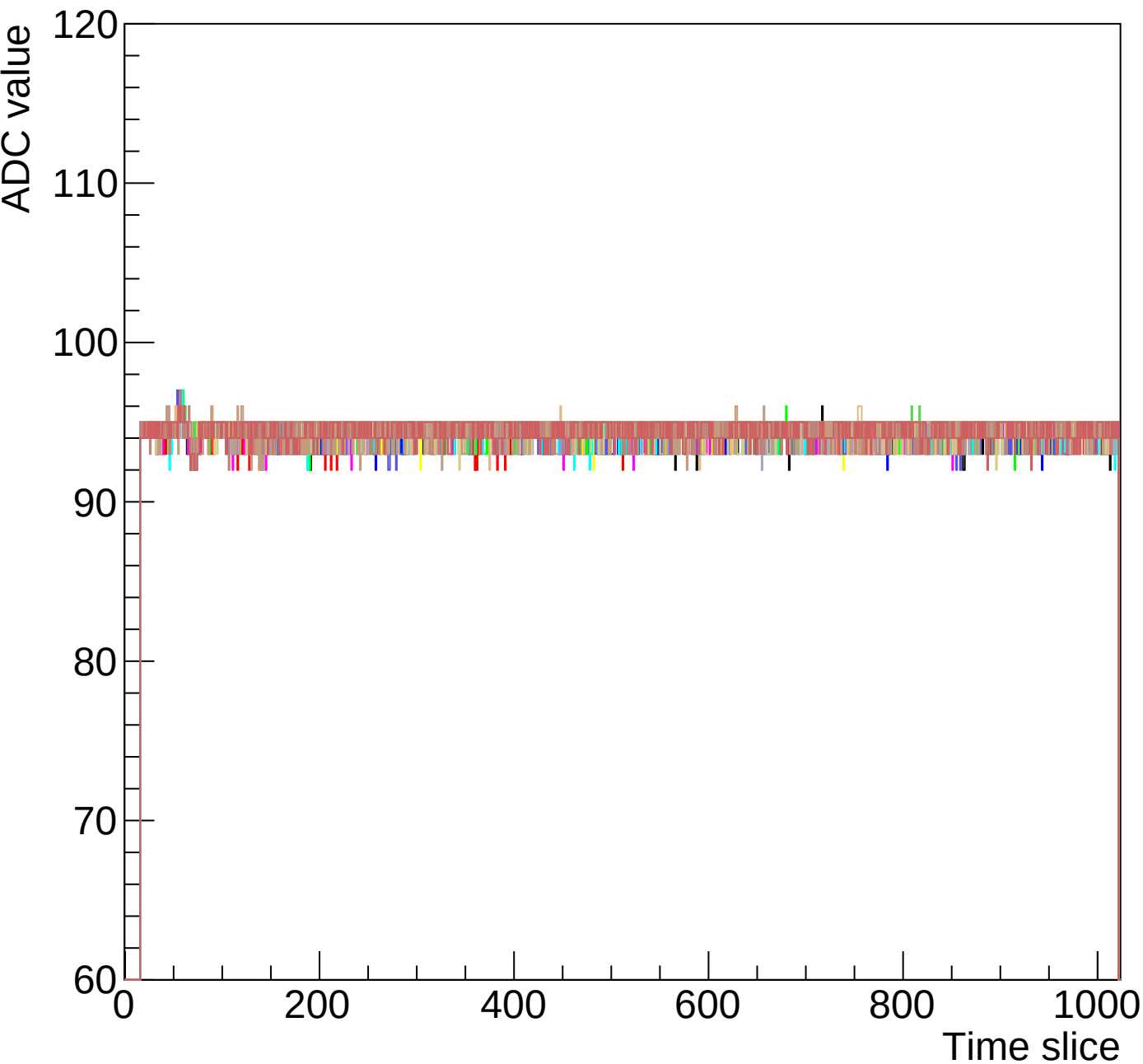
# Event 176-191 mcm 0 chan 96 test2



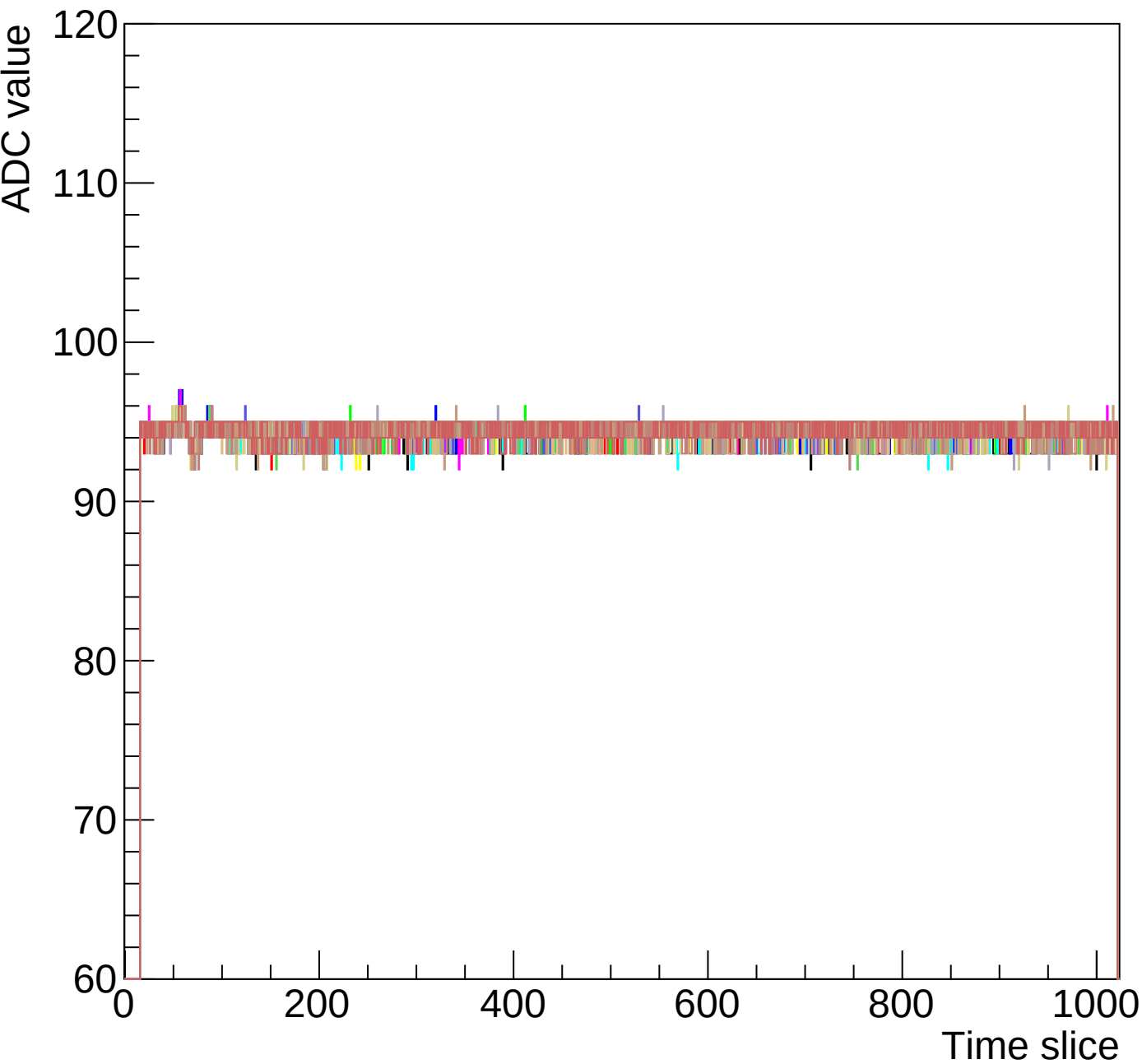
# Event 192-207 mcm 0 chan 96 test2



# Event 208-223 mcm 0 chan 96 test2



# Event 224-239 mcm 0 chan 96 test2



# Event 240-255 mcm 0 chan 96 test2

