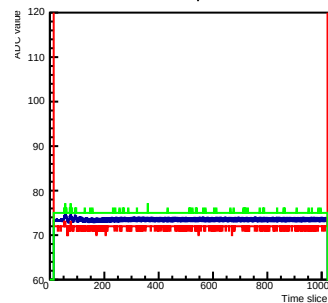
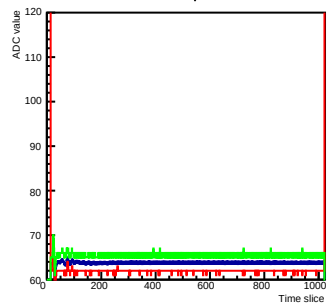


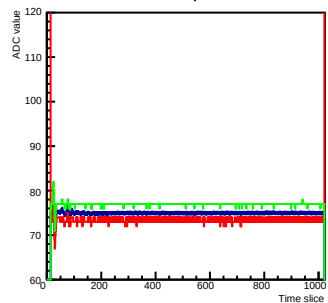
Time mcm 0 chip 0 chan 0 all



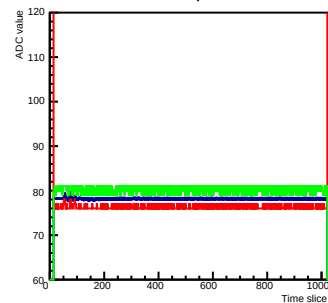
Time mcm 0 chip 0 chan 1 all



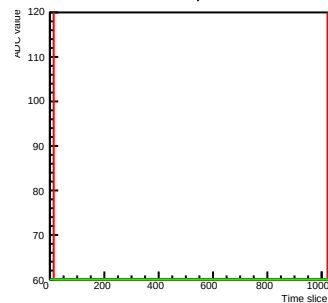
Time mcm 0 chip 0 chan 2 all



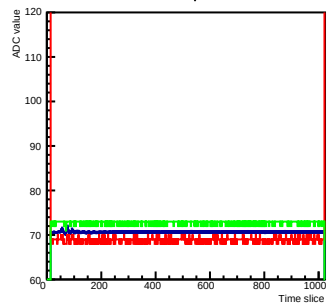
Time mcm 0 chip 0 chan 3 all



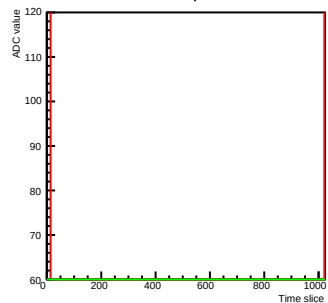
Time mcm 0 chip 0 chan 4 all



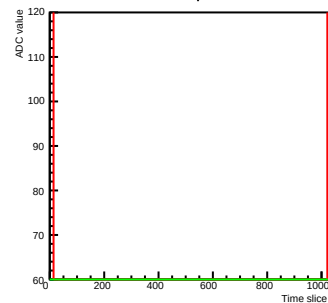
Time mcm 0 chip 0 chan 5 all



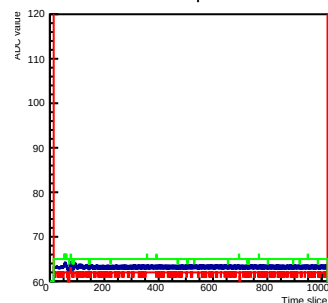
Time mcm 0 chip 0 chan 6 all



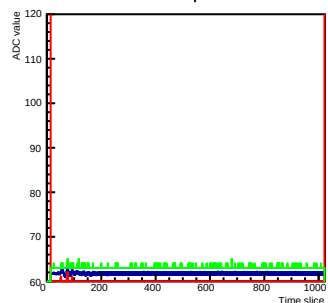
Time mcm 0 chip 0 chan 7 all



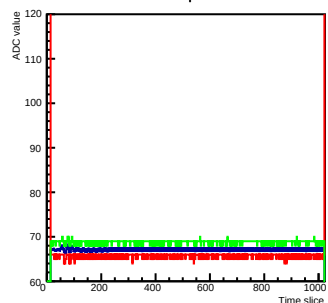
Time mcm 0 chip 0 chan 8 all



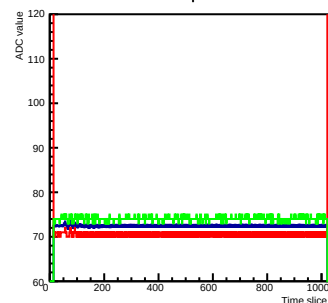
Time mcm 0 chip 0 chan 9 all



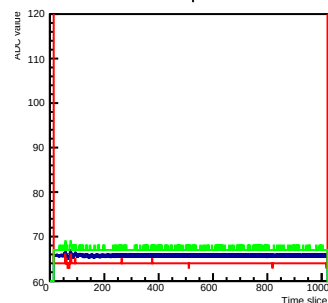
Time mcm 0 chip 0 chan 10 all



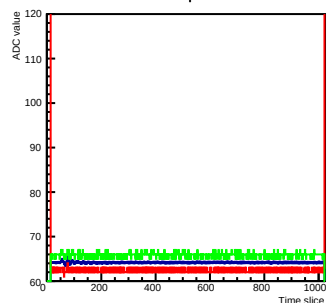
Time mcm 0 chip 0 chan 11 all



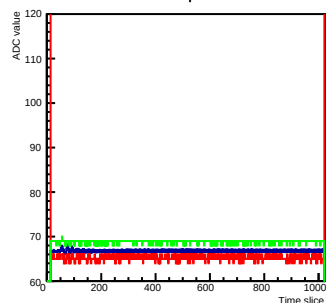
Time mcm 0 chip 0 chan 12 all



Time mcm 0 chip 0 chan 13 all



Time mcm 0 chip 0 chan 14 all



Time mcm 0 chip 0 chan 15 all

