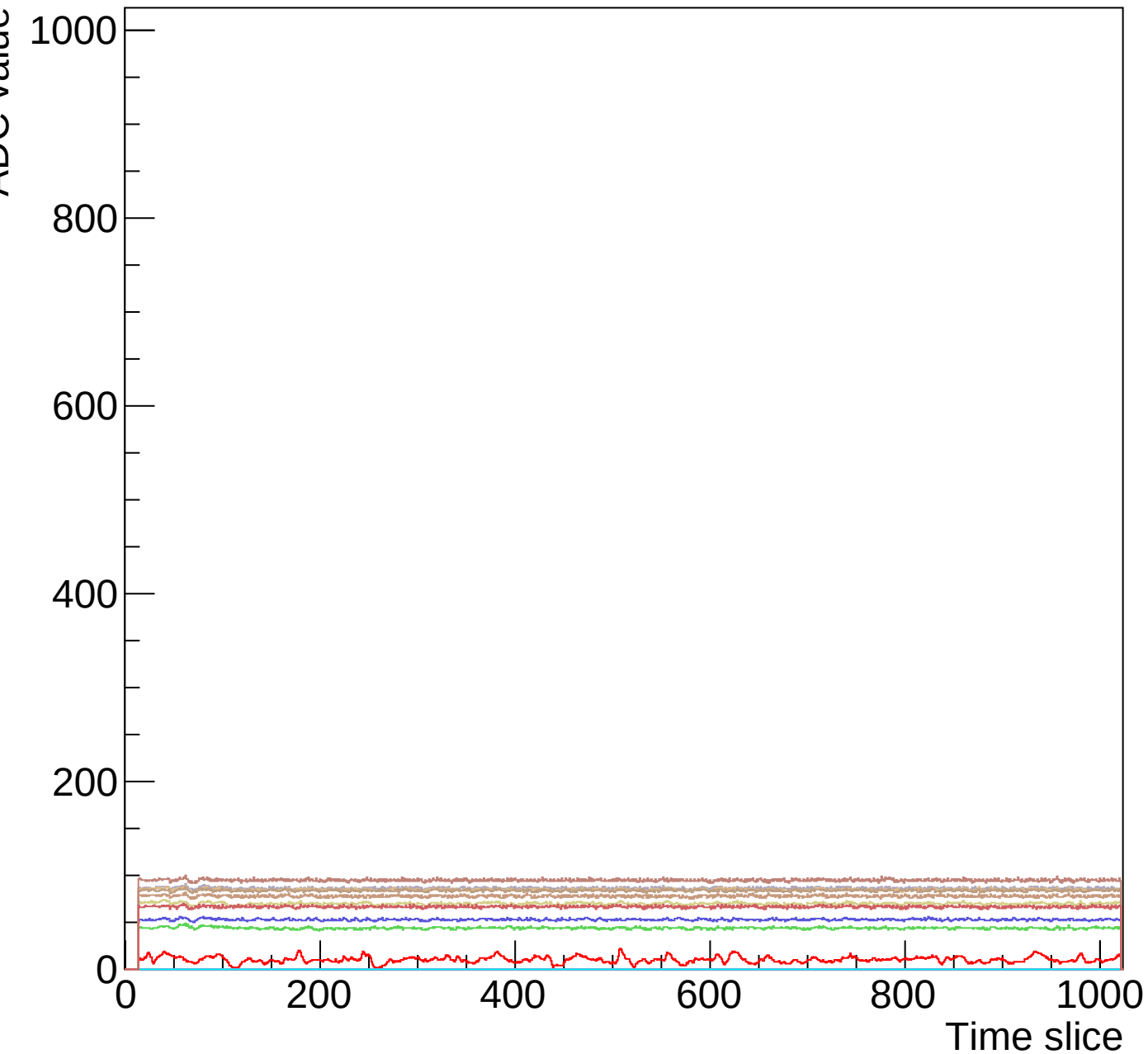
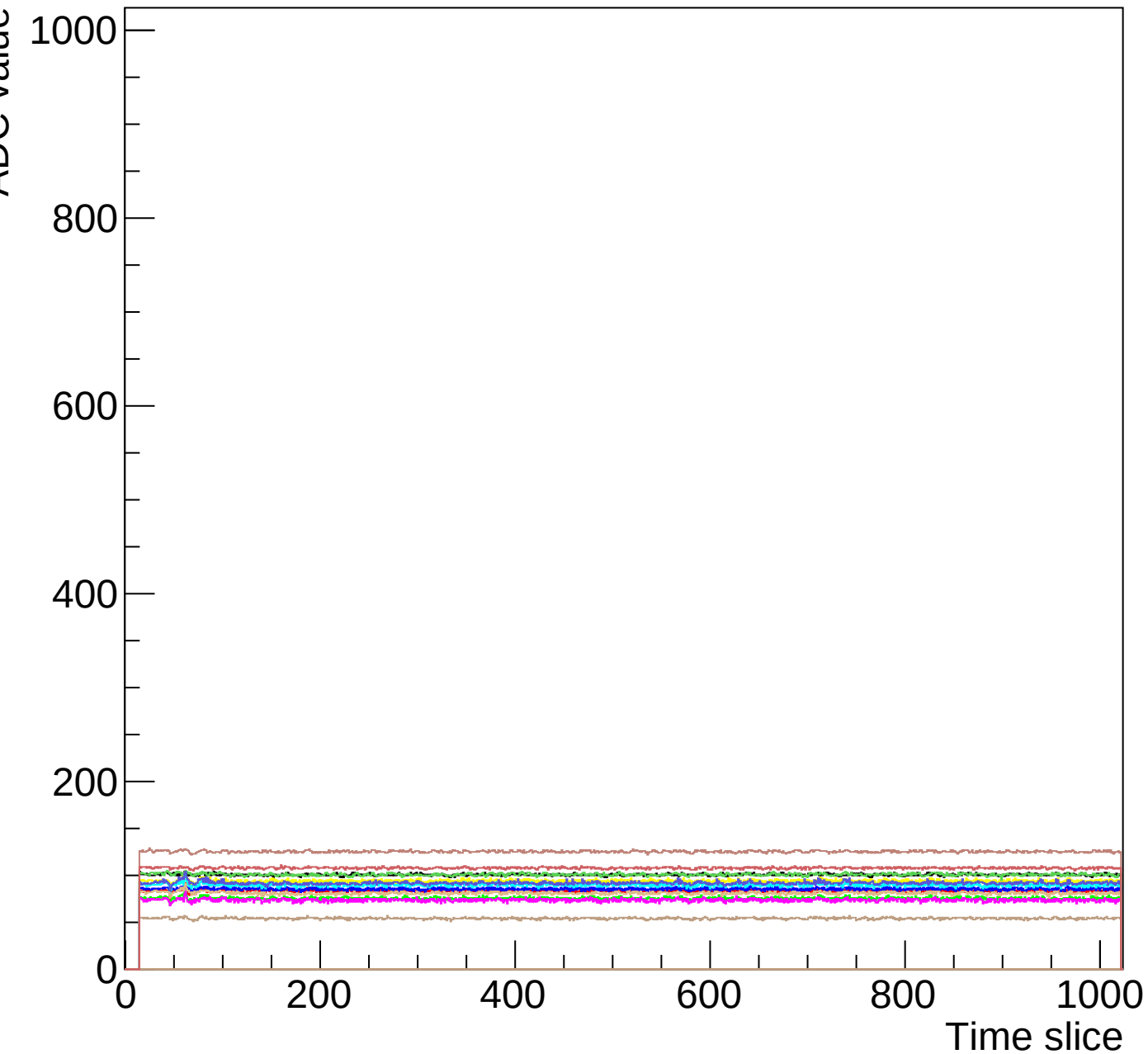


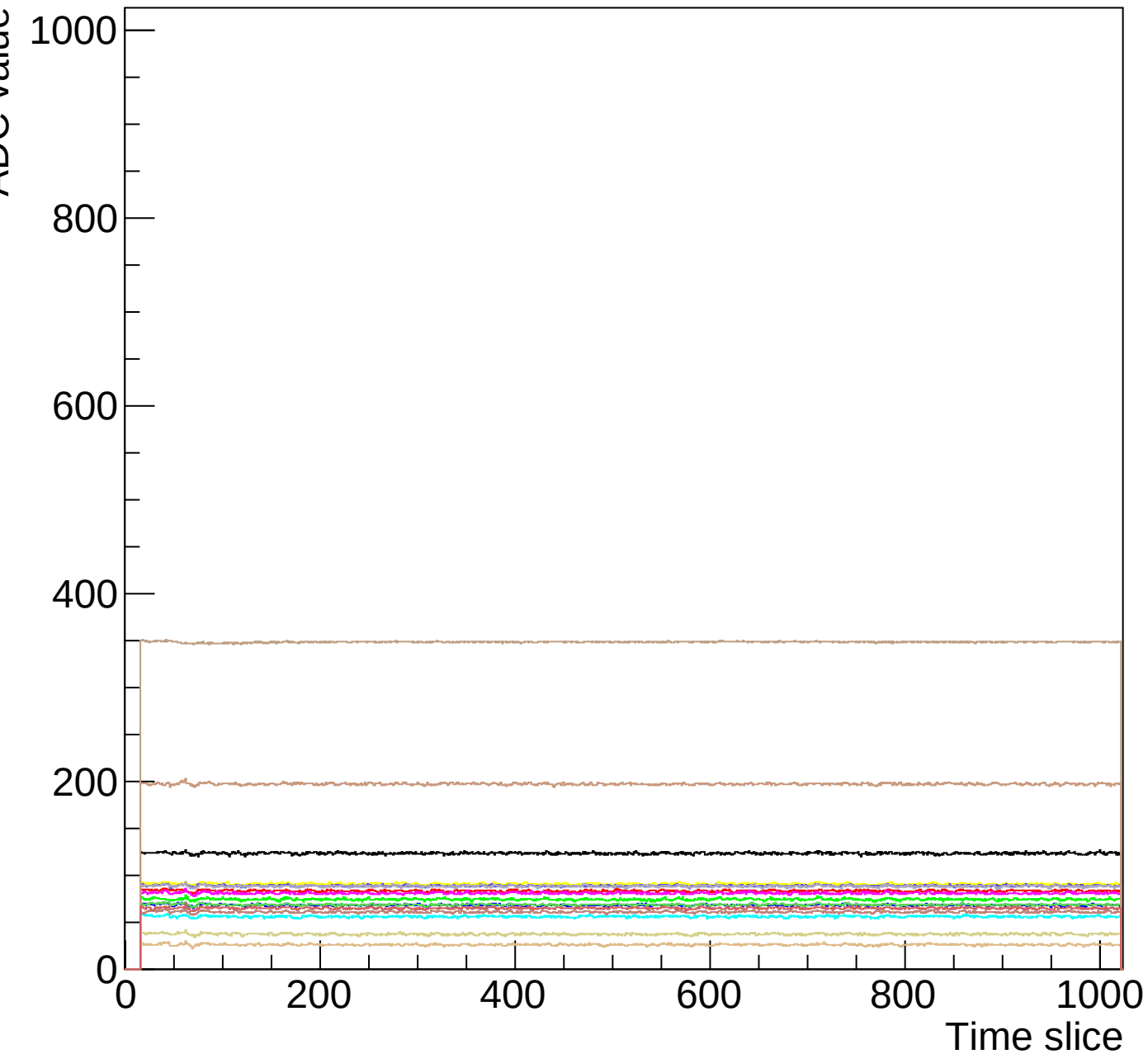
Run 0 event 10 mcm 0 chip 0 s0-test



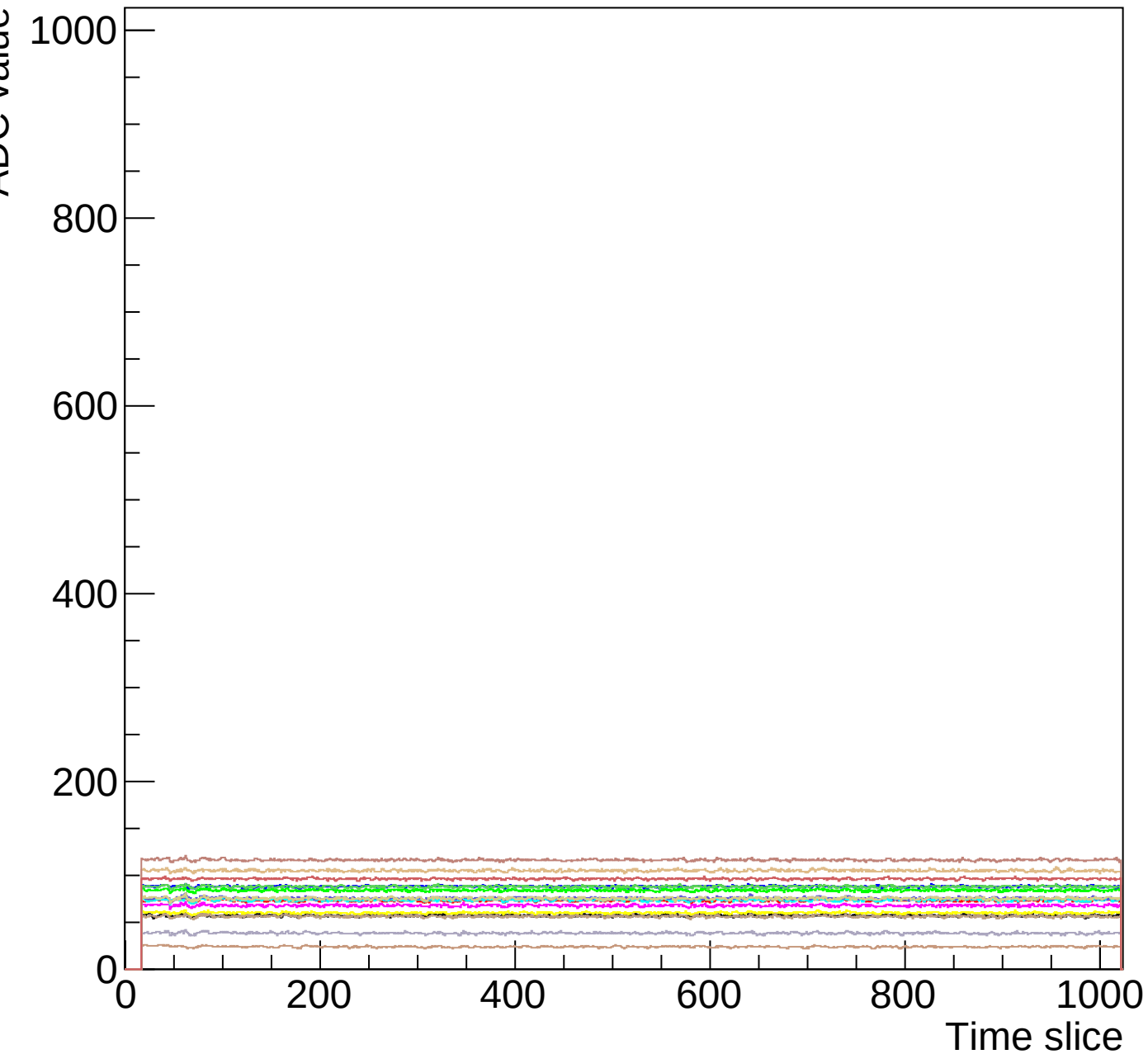
Run 0 event 10 mcm 0 chip 1 s0-test



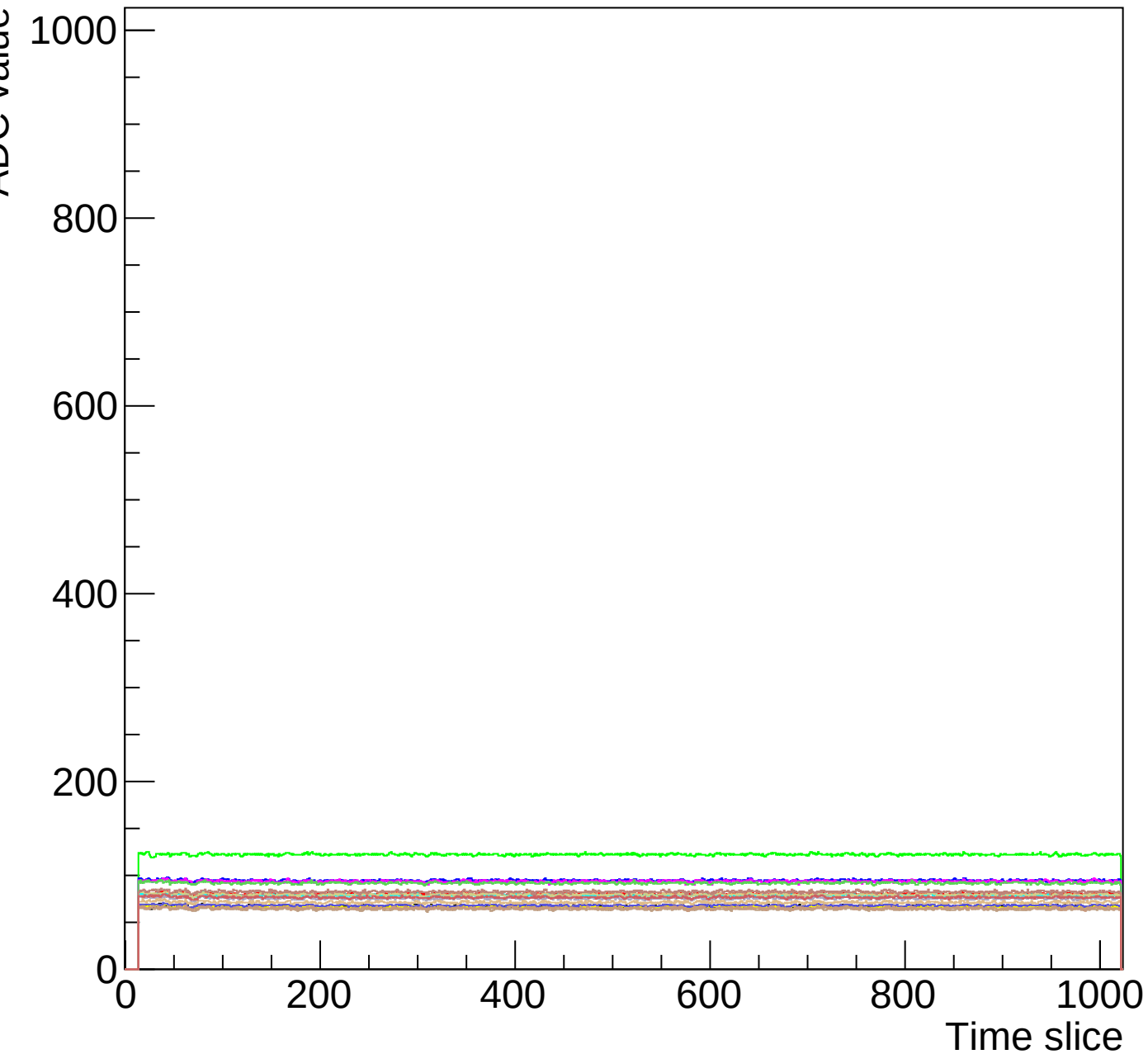
Run 0 event 10 mcm 0 chip 2 s0-test



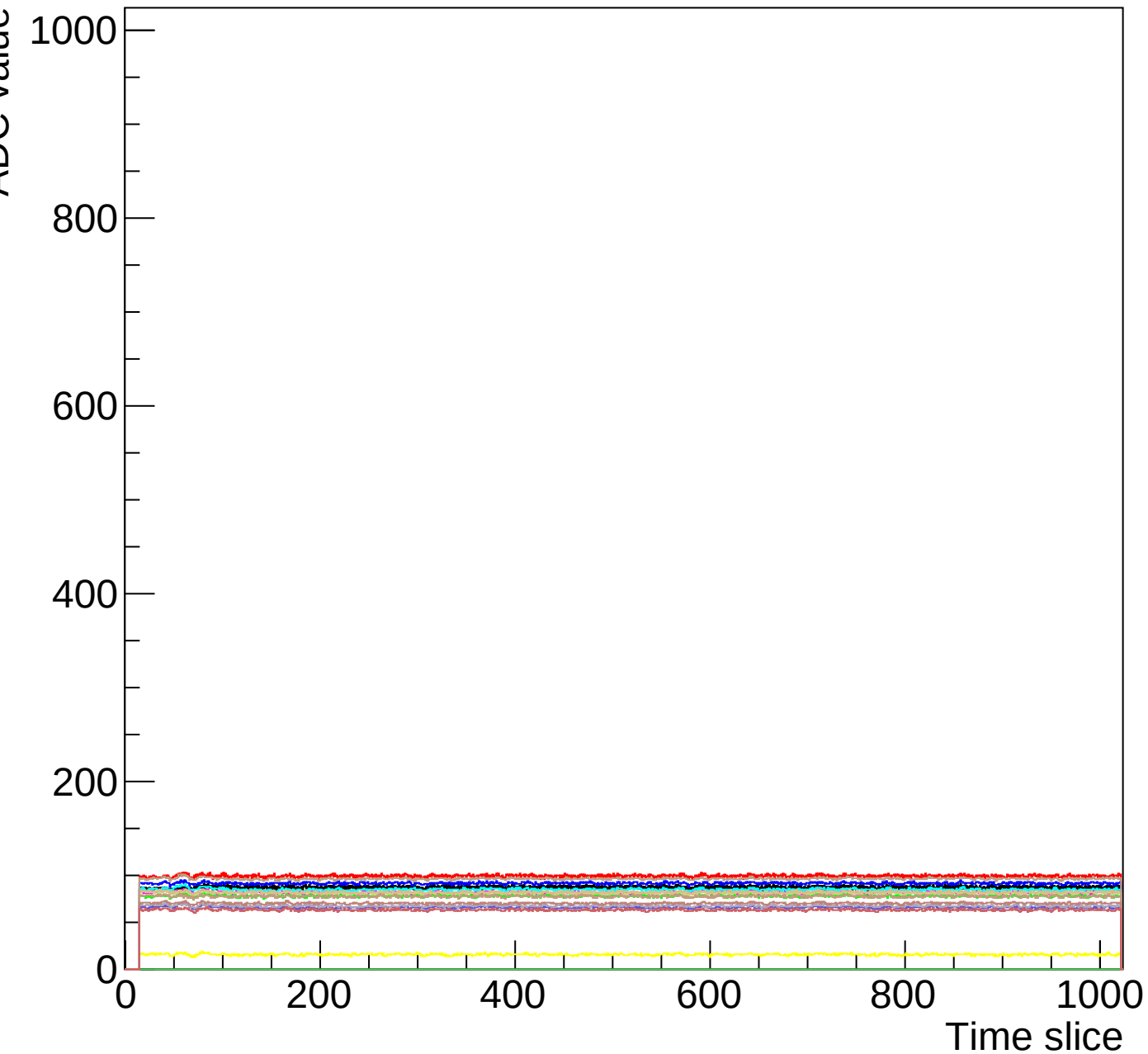
Run 0 event 10 mcm 0 chip 3 s0-test



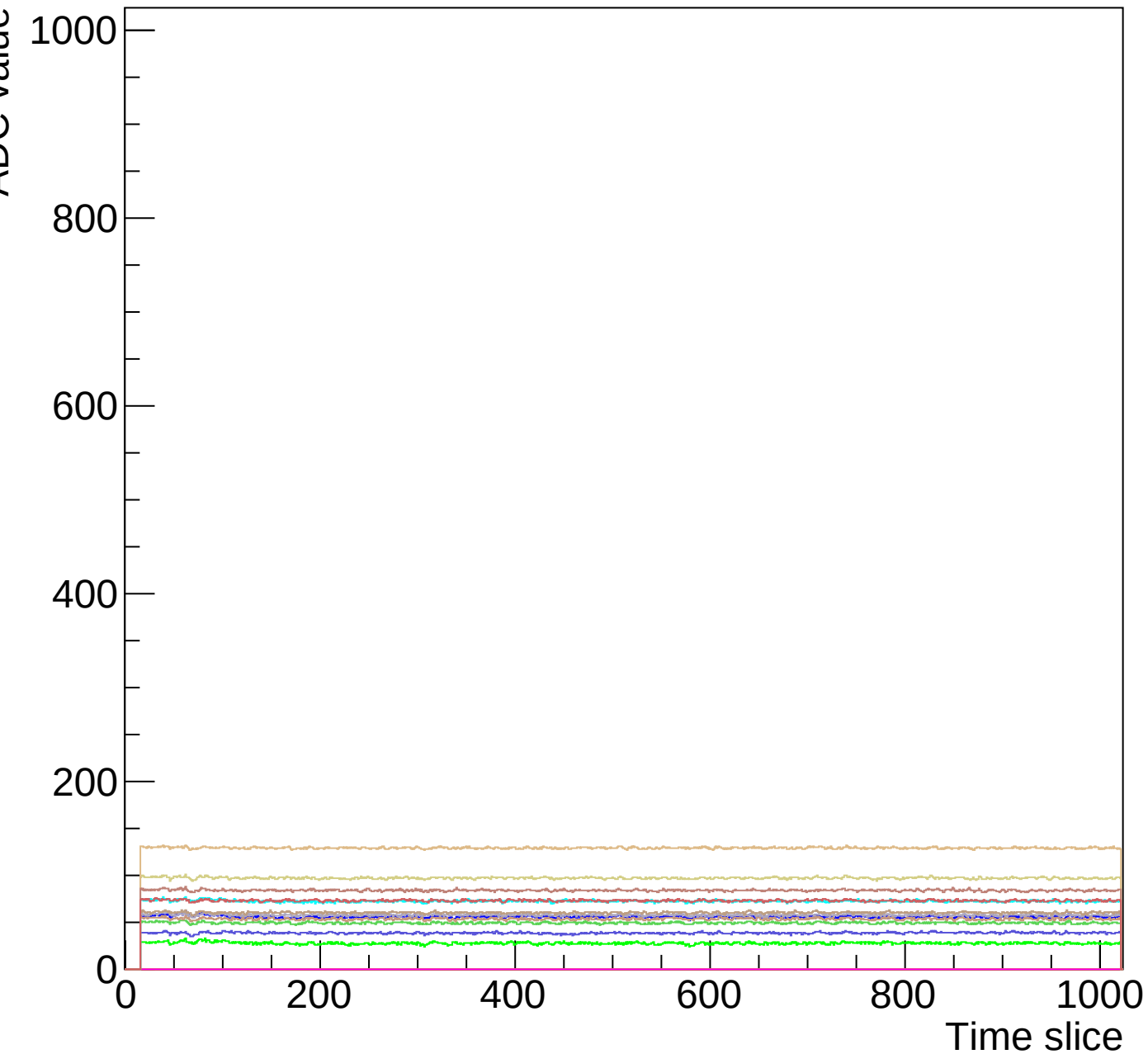
Run 0 event 10 mcm 0 chip 4 s0-test



Run 0 event 10 mcm 0 chip 5 s0-test



Run 0 event 10 mcm 0 chip 6 s0-test



Run 0 event 10 mcm 0 chip 7 s0-test

