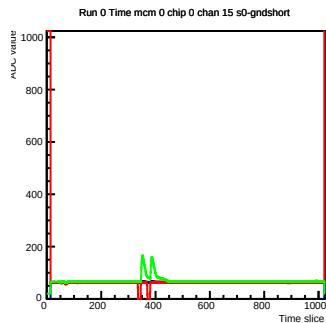
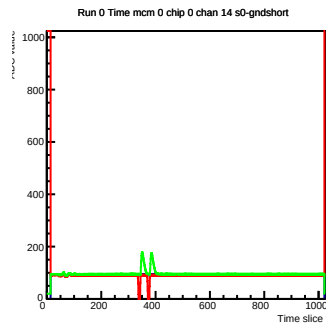
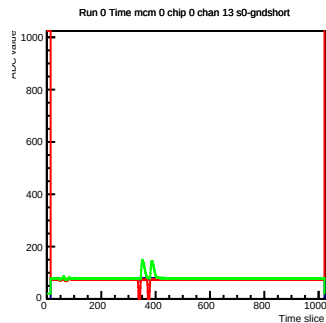
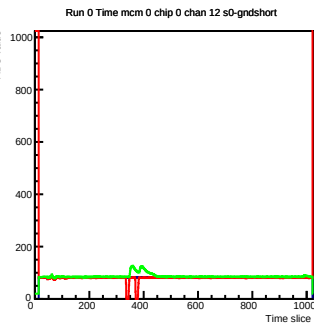
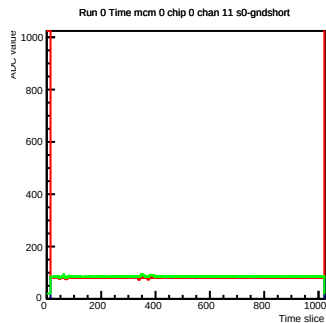
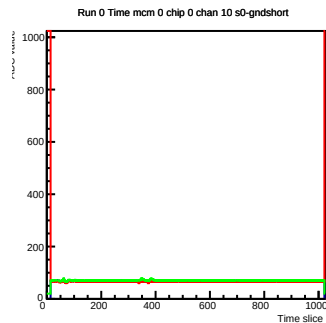
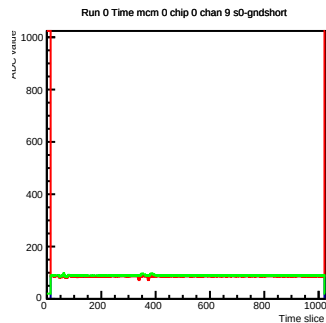
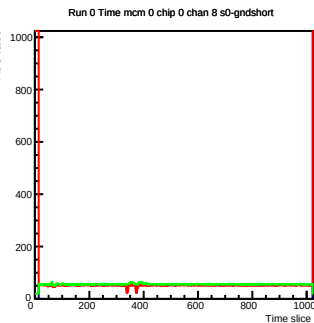
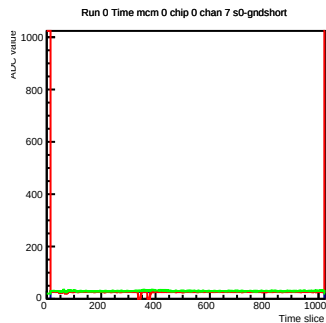
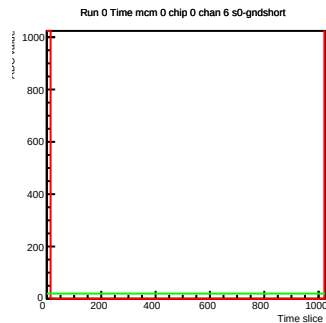
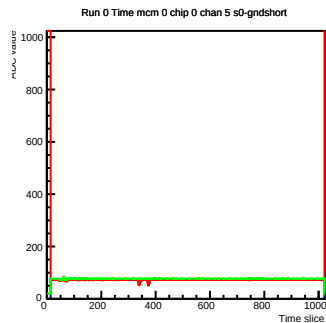
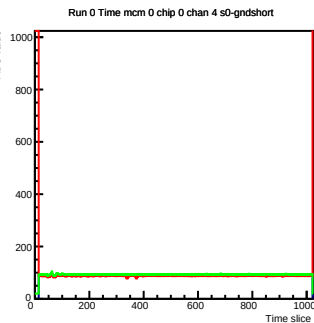
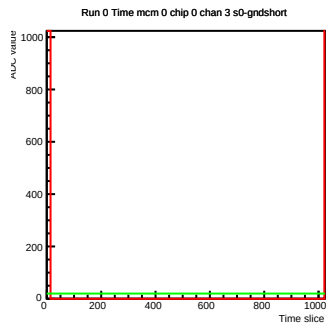
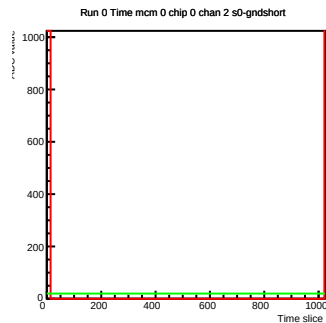
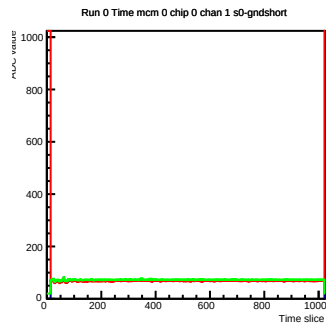
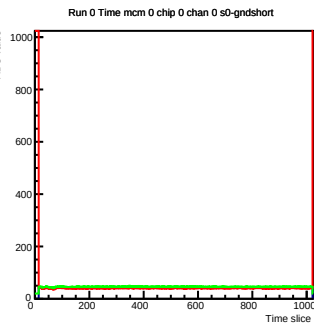
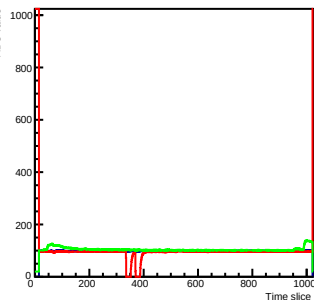
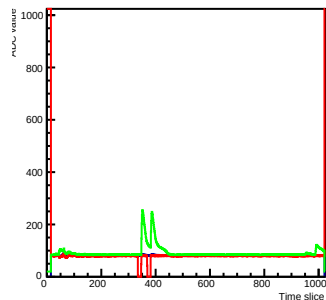




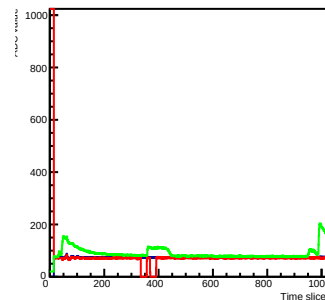
Run 0 Time mcm 0 chip 1 chan 0 s0-gndshort



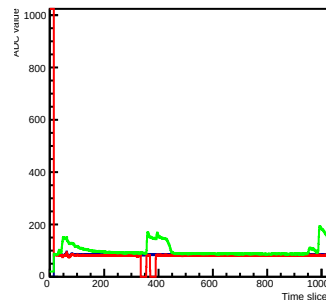
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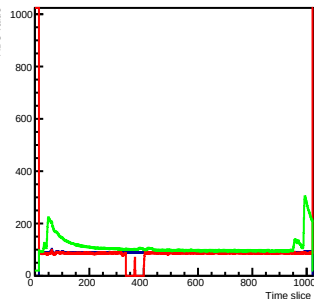
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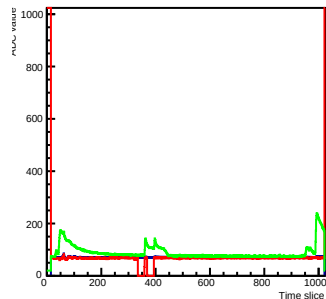
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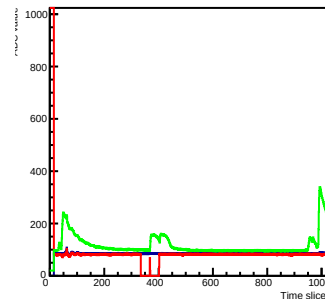
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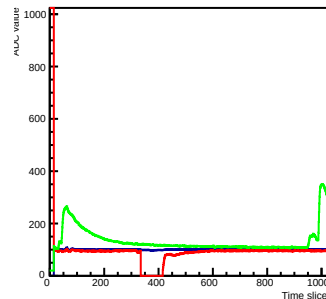
Run 0 Time mcm 0 chip 1 chan 5 s0-gndshort



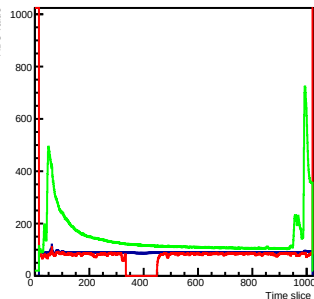
Run 0 Time mcm 0 chip 1 chan 6 s0-gndshort



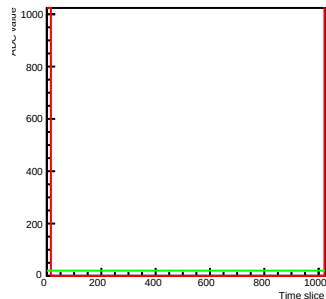
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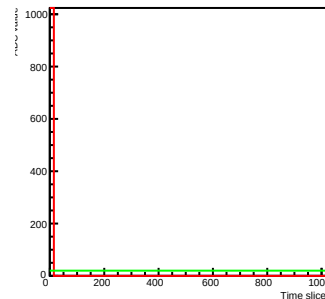
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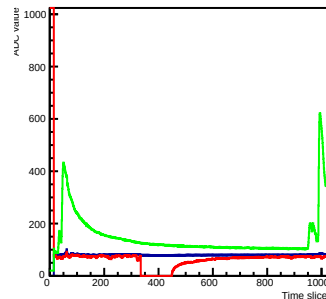
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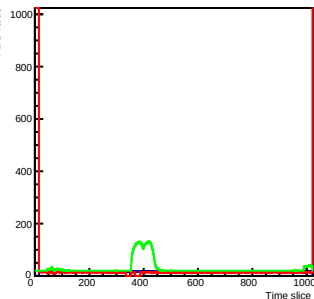
Run 0 Time mcm 0 chip 1 chan 10 s0-gndshort



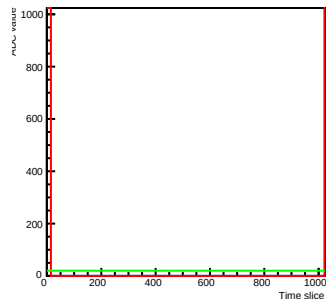
Run 0 Time mcm 0 chip 1 chan 11 s0-gndshort



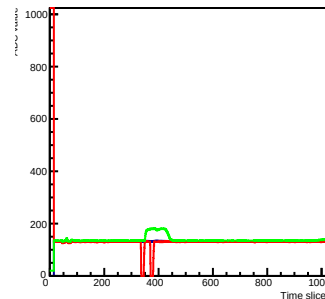
Run 0 Time mcm 0 chip 1 chan 12 s0-gndshort



Run 0 Time mcm 0 chip 1 chan 13 s0-gndshort



Run 0 Time mcm 0 chip 1 chan 14 s0-gndshort



Run 0 Time mcm 0 chip 1 chan 15 s0-gndshort

