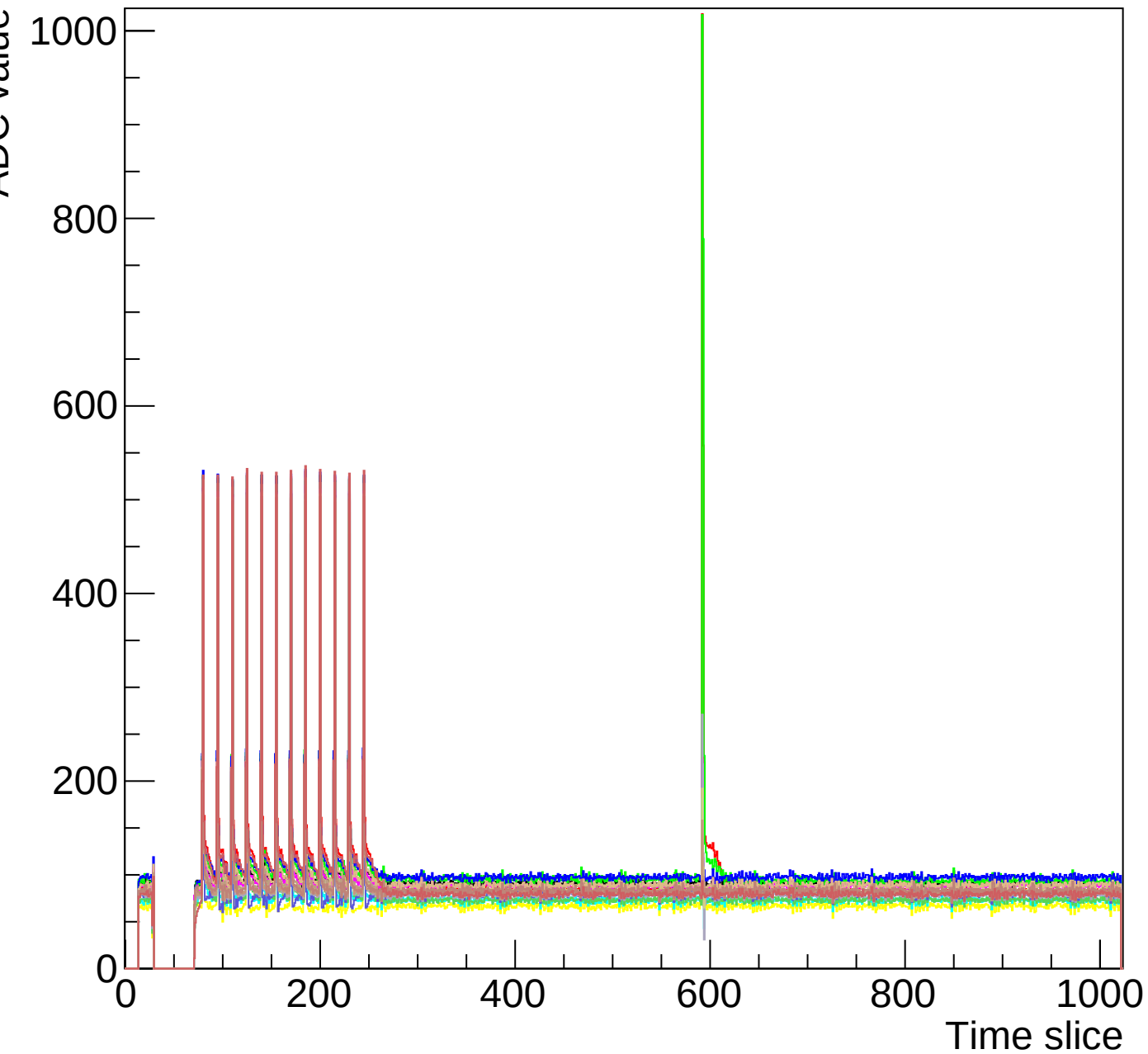
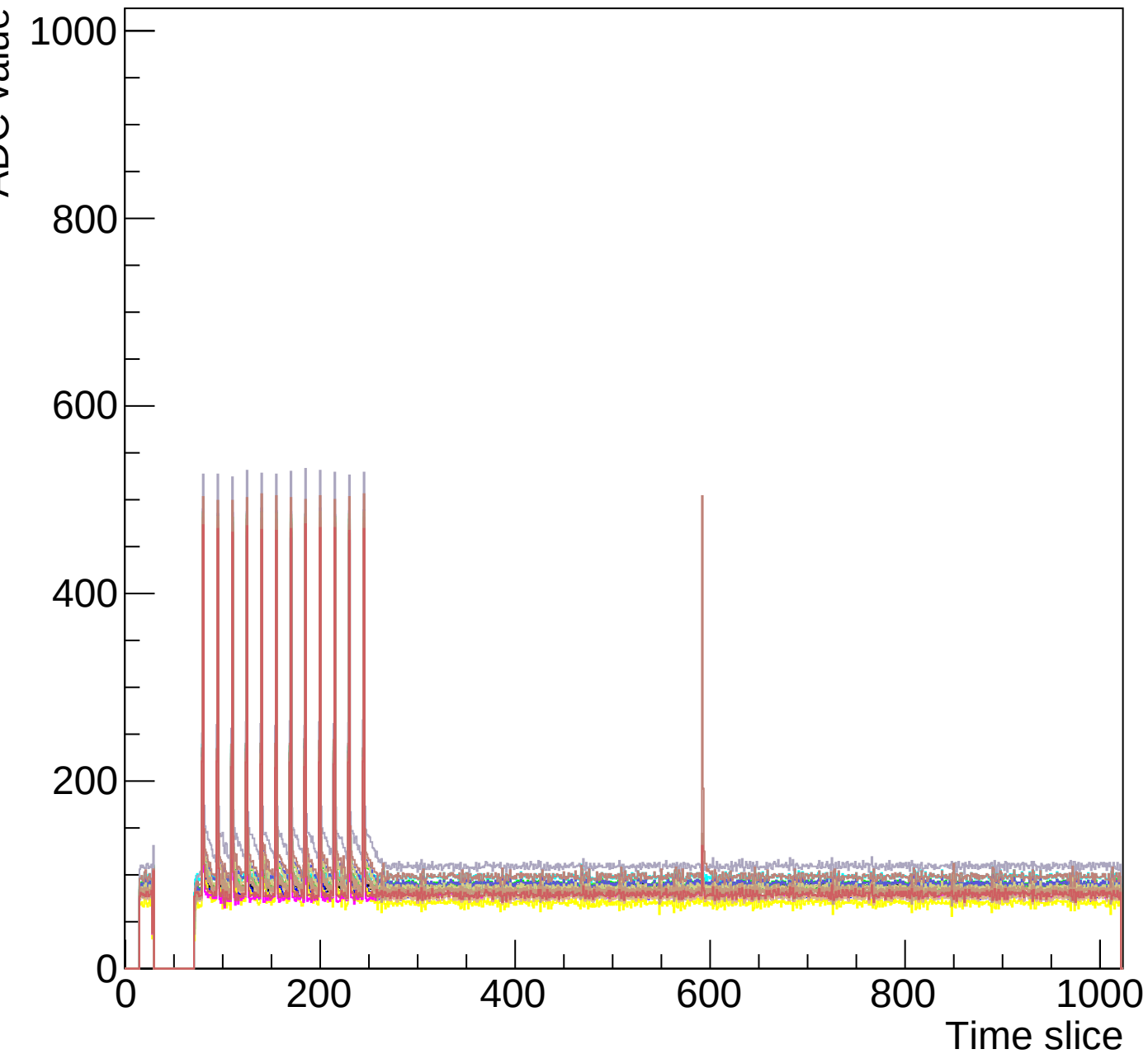


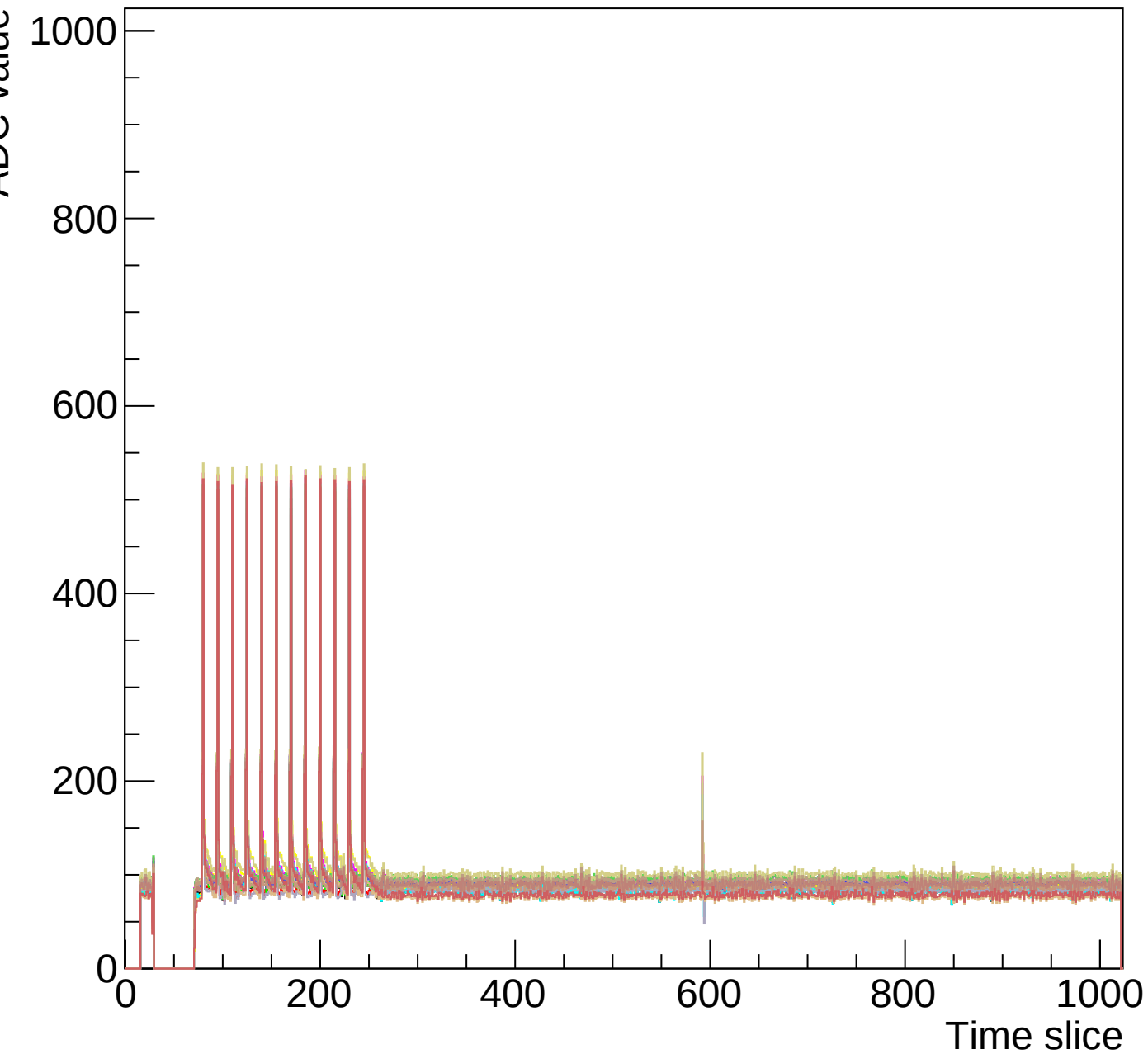
Run 6 event 11 mcm 0 chip 0 clk2-s3-test



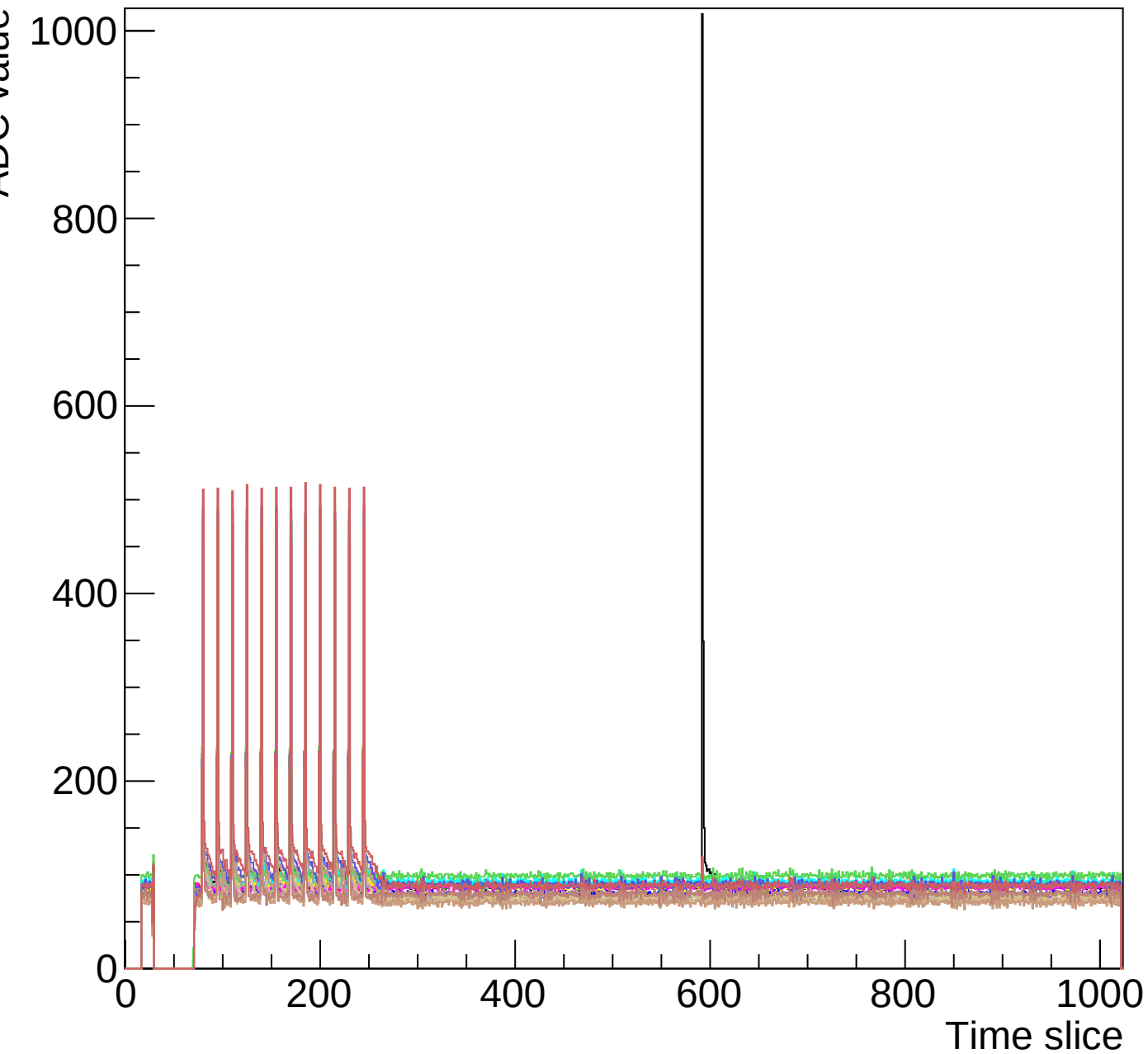
Run 6 event 11 mcm 0 chip 1 clk2-s3-test



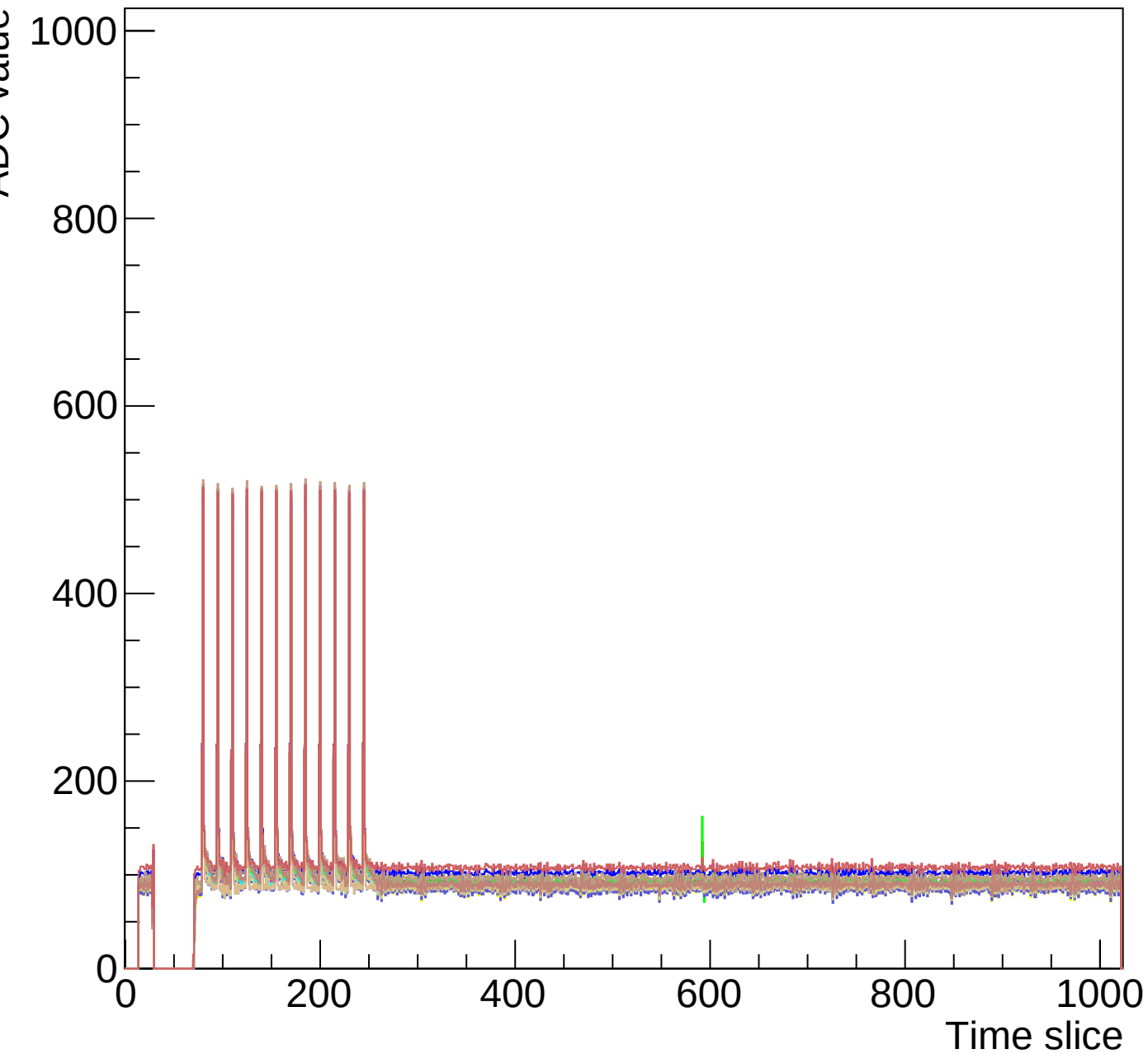
Run 6 event 11 mcm 0 chip 2 clk2-s3-test



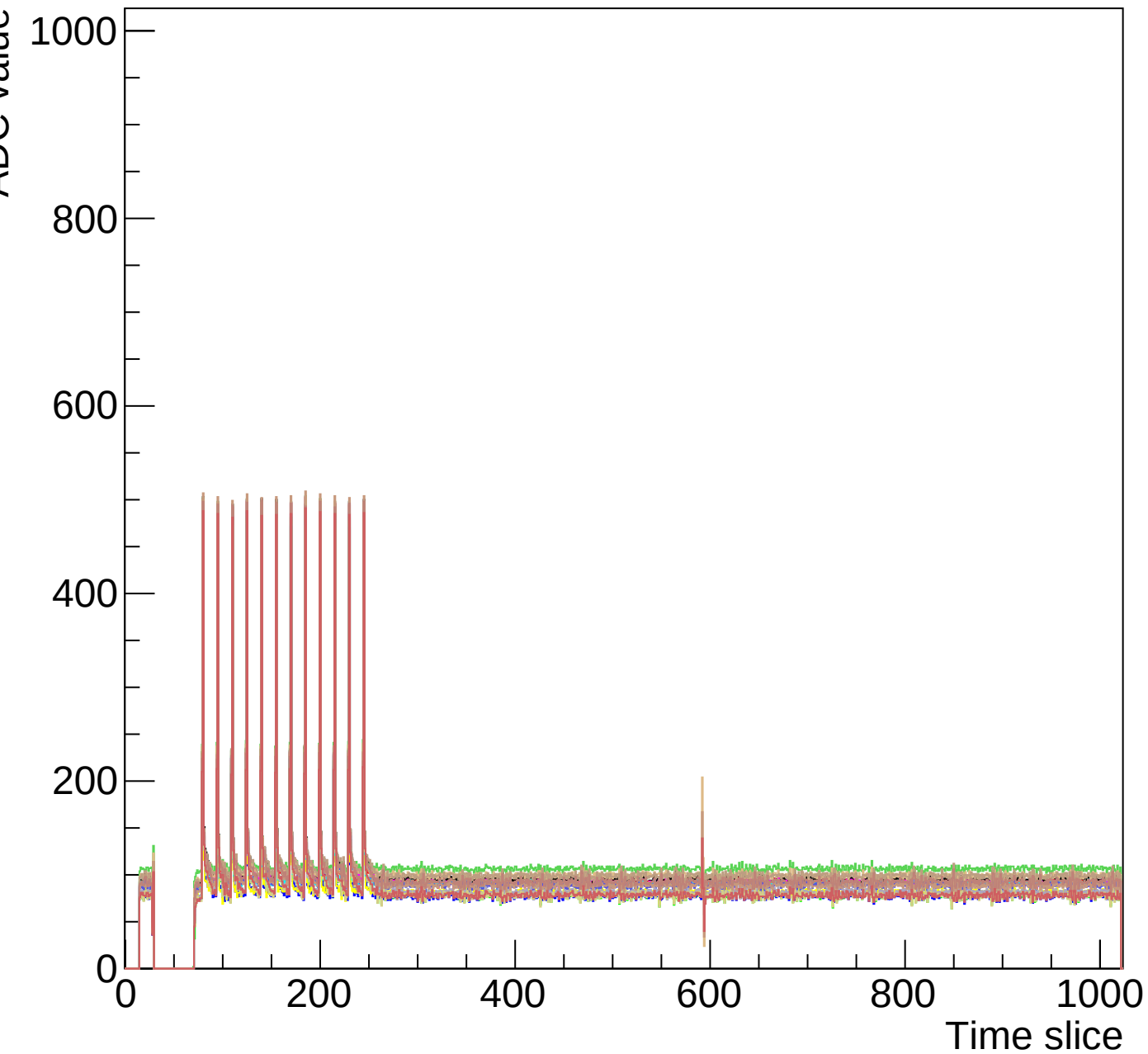
Run 6 event 11 mcm 0 chip 3 clk2-s3-test



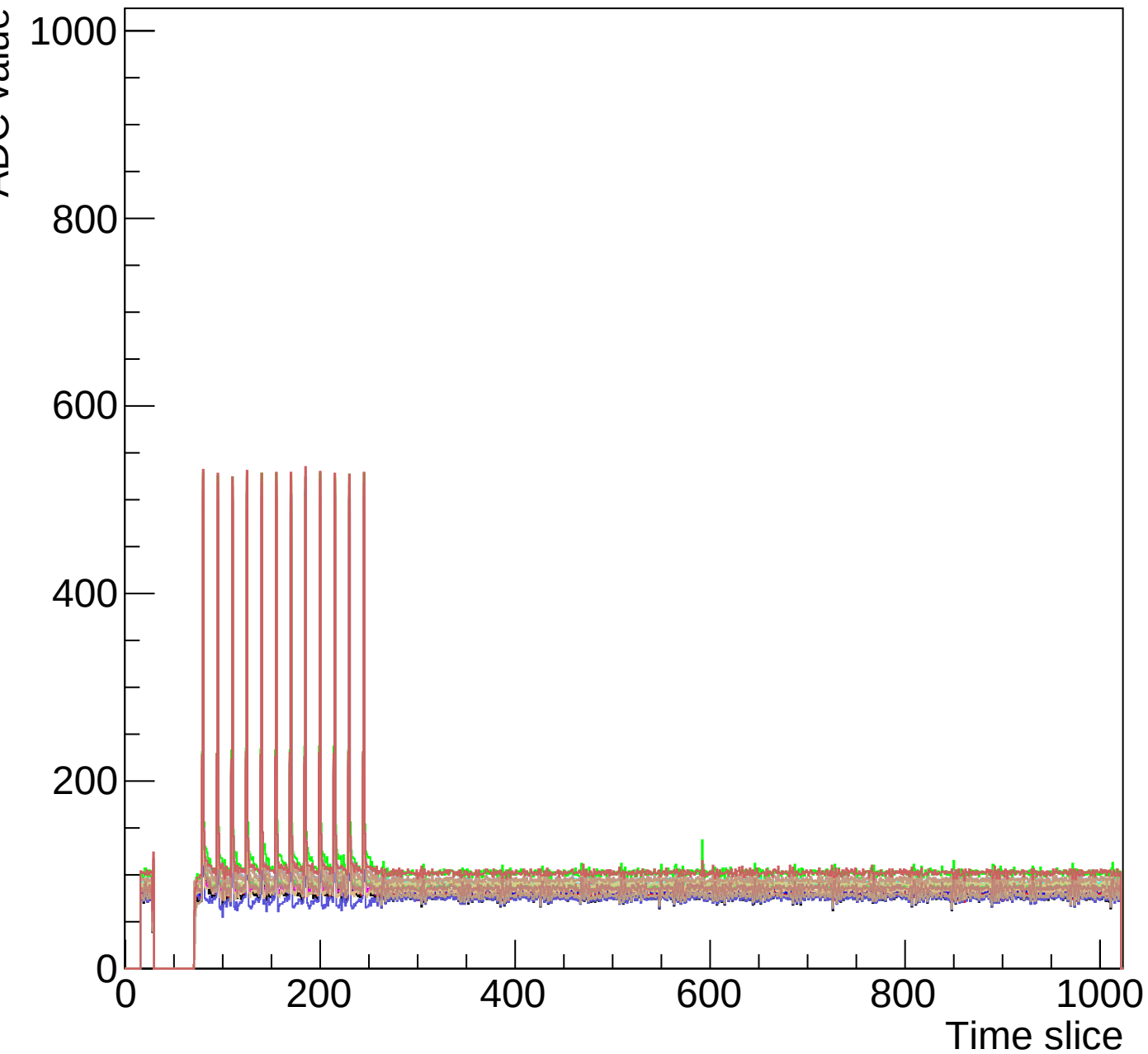
Run 6 event 11 mcm 0 chip 4 clk2-s3-test



Run 6 event 11 mcm 0 chip 5 clk2-s3-test



Run 6 event 11 mcm 0 chip 6 clk2-s3-test



Run 6 event 11 mcm 0 chip 7 clk2-s3-test

