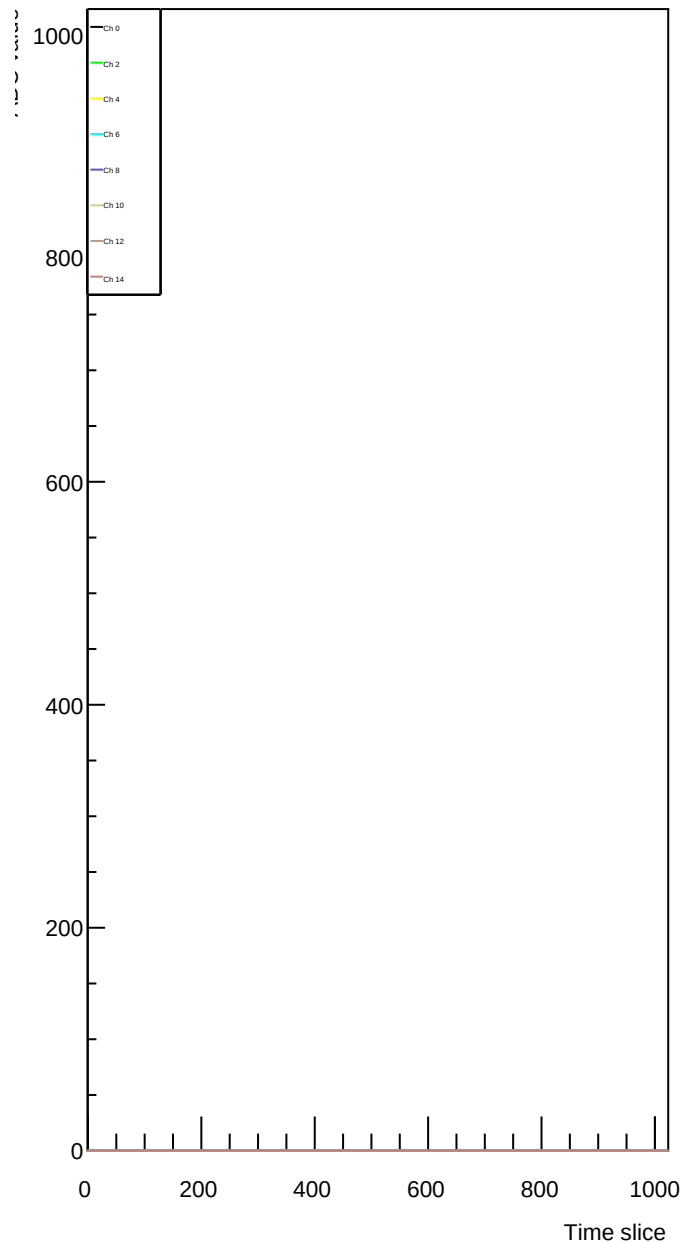
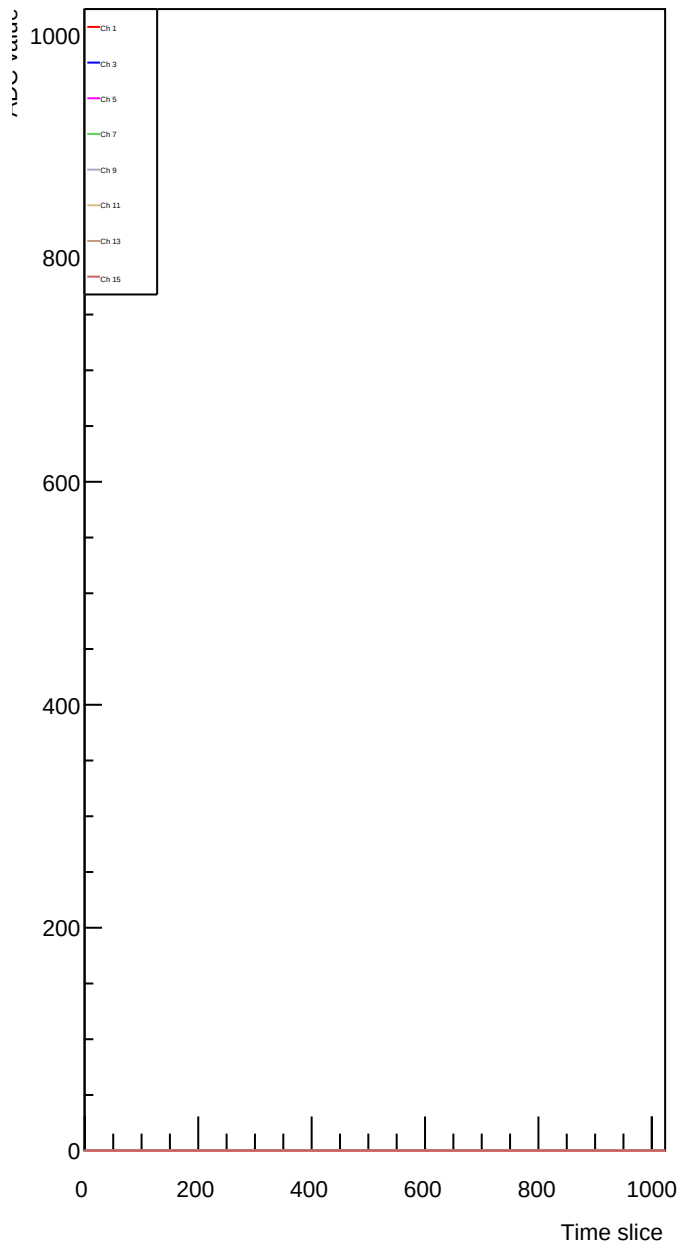


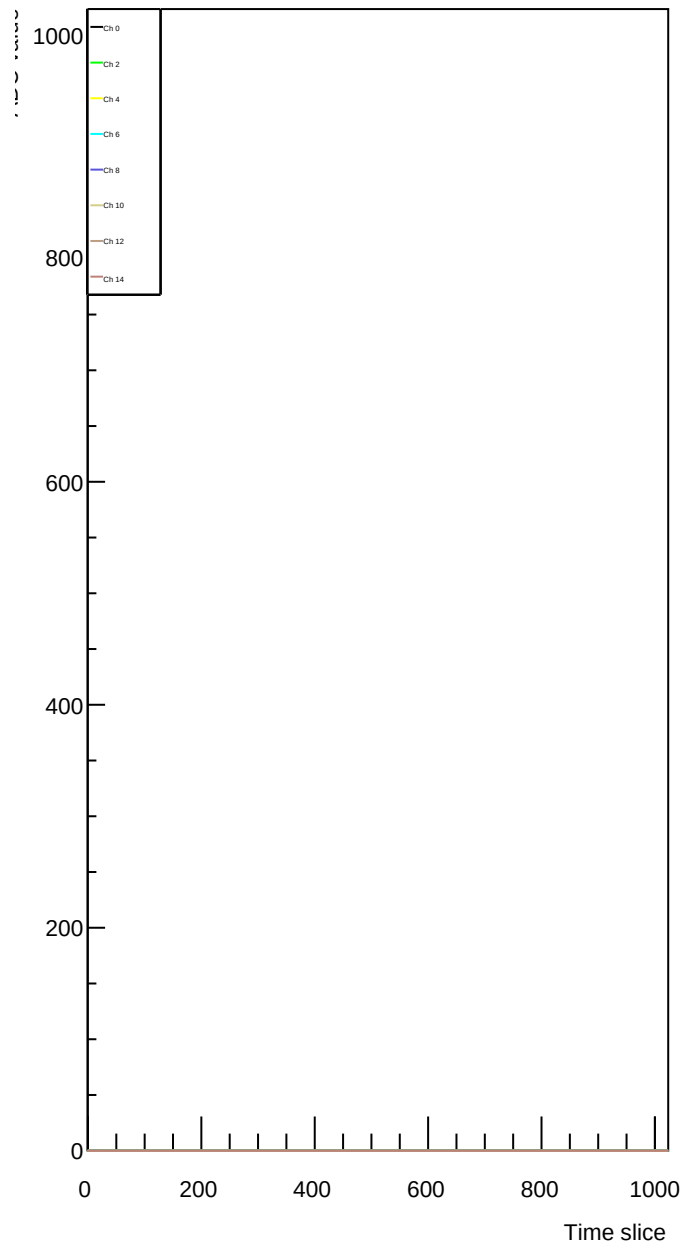
Run 0 event 8 mcm 0 chip 0 s0-clk2 even ch



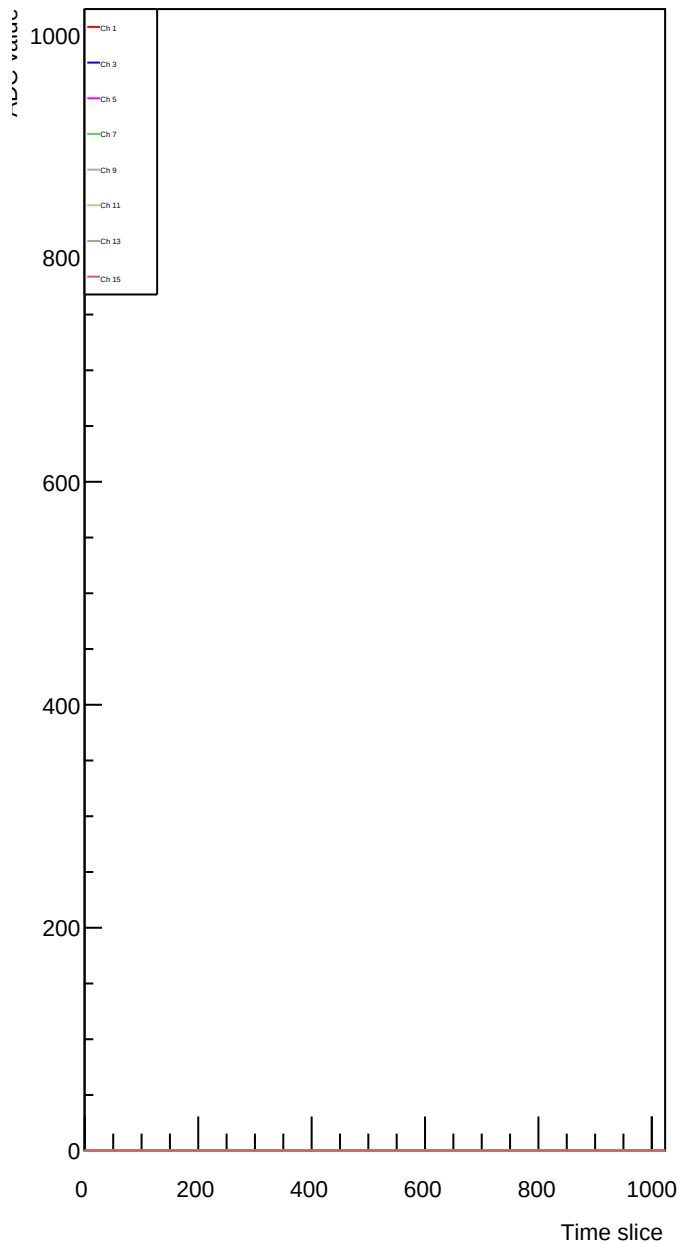
Run 0 event 8 mcm 0 chip 0 s0-clk2 odd ch



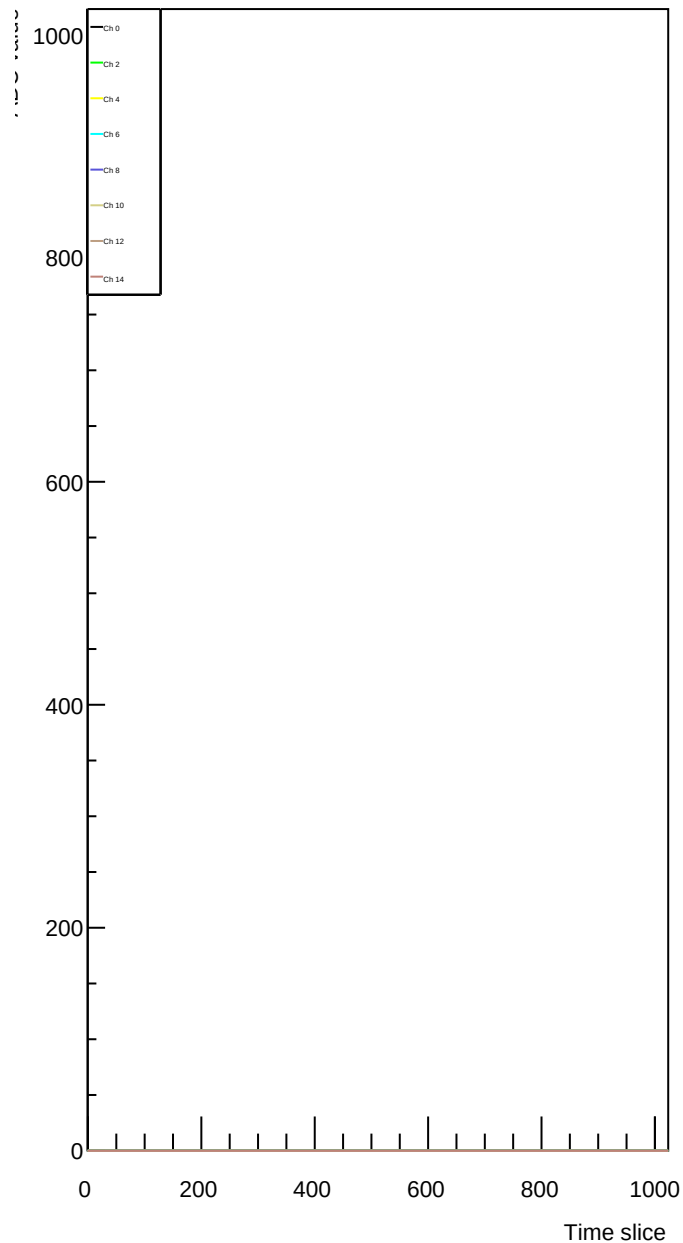
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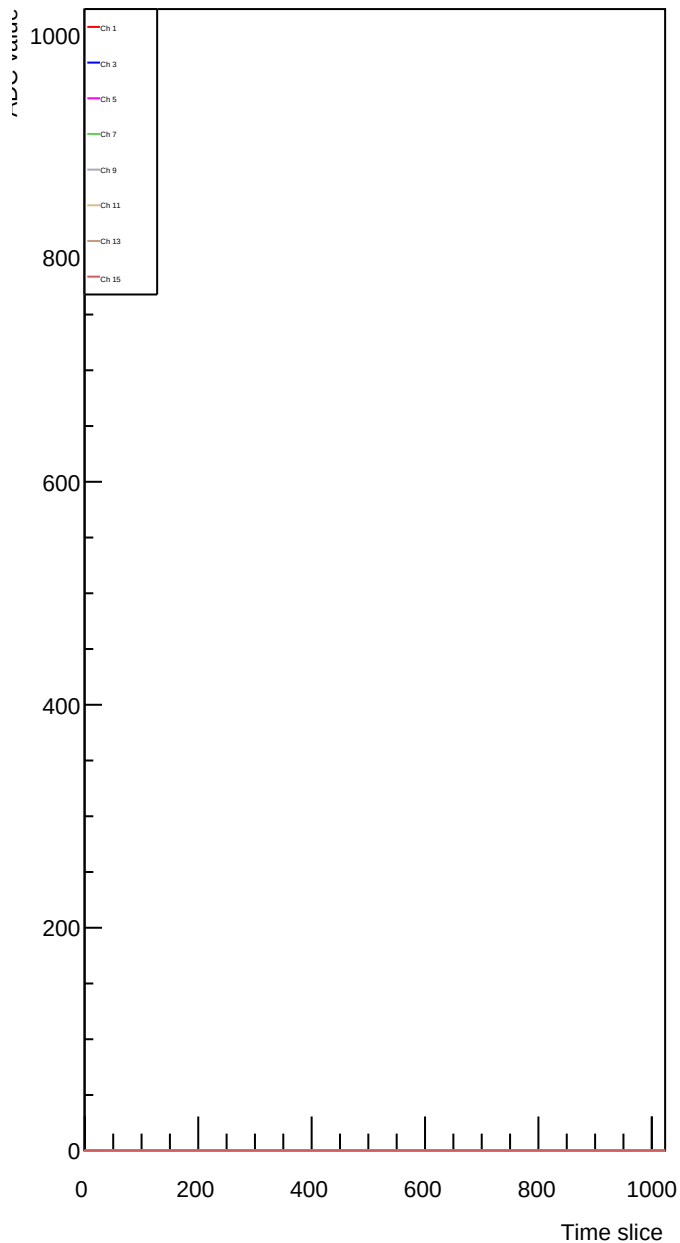
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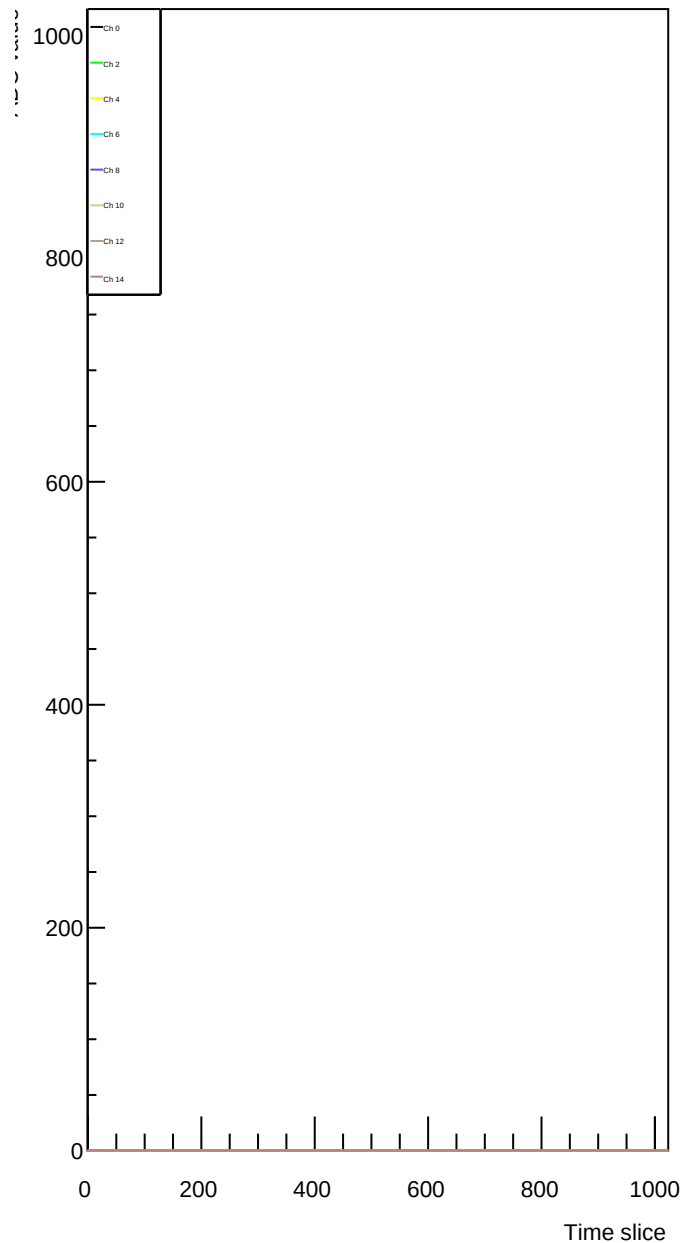
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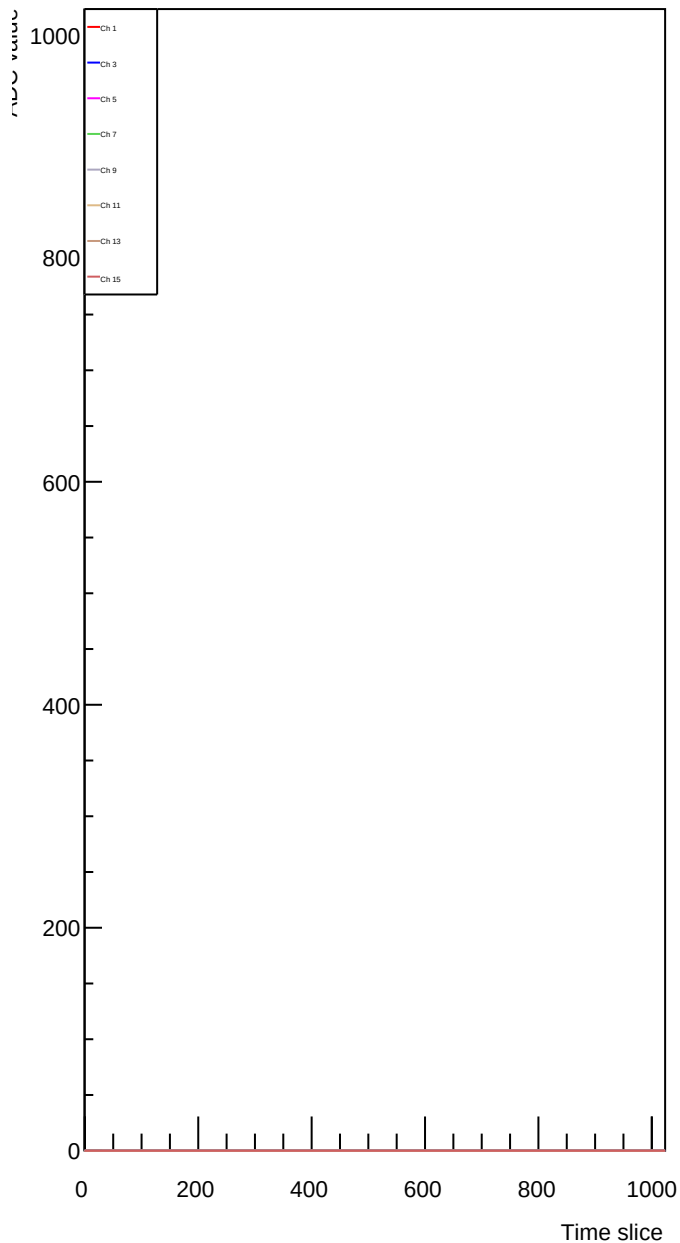
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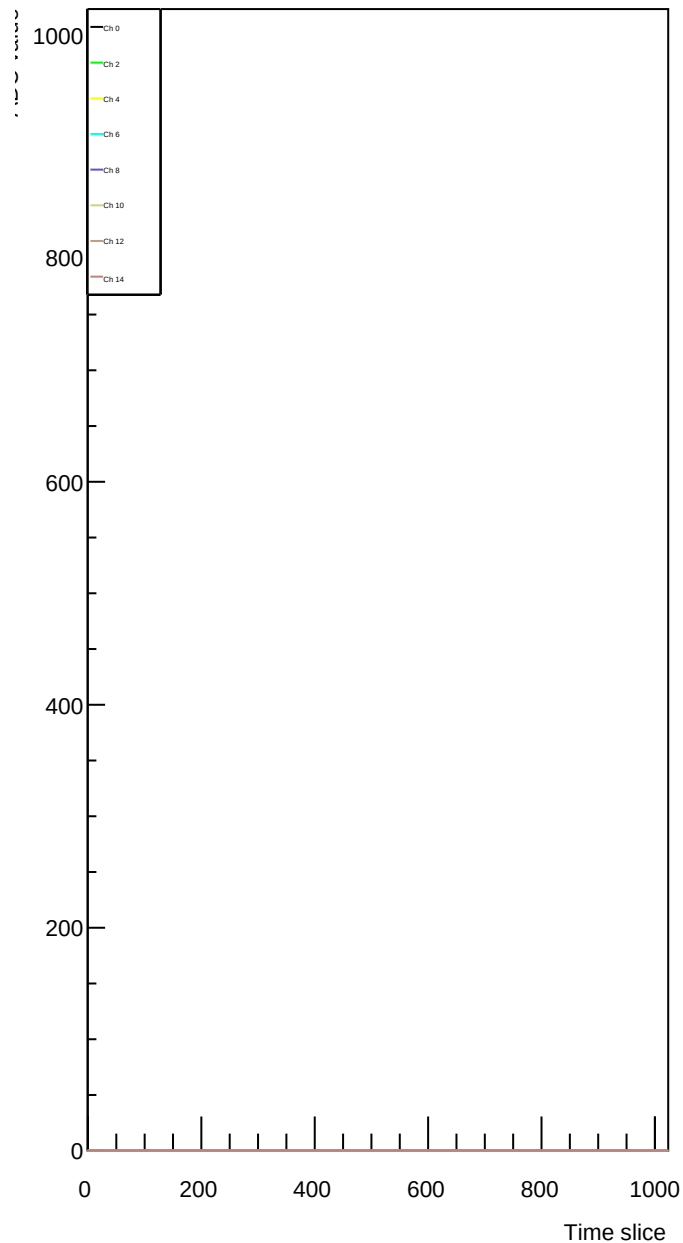
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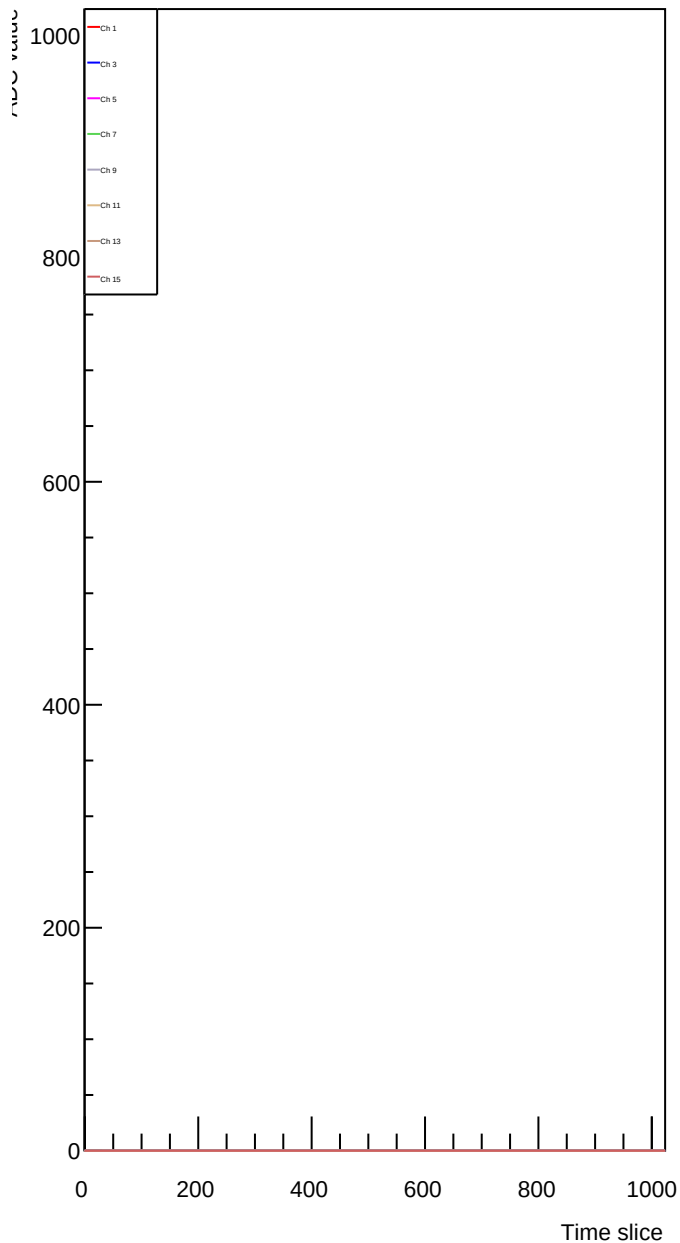
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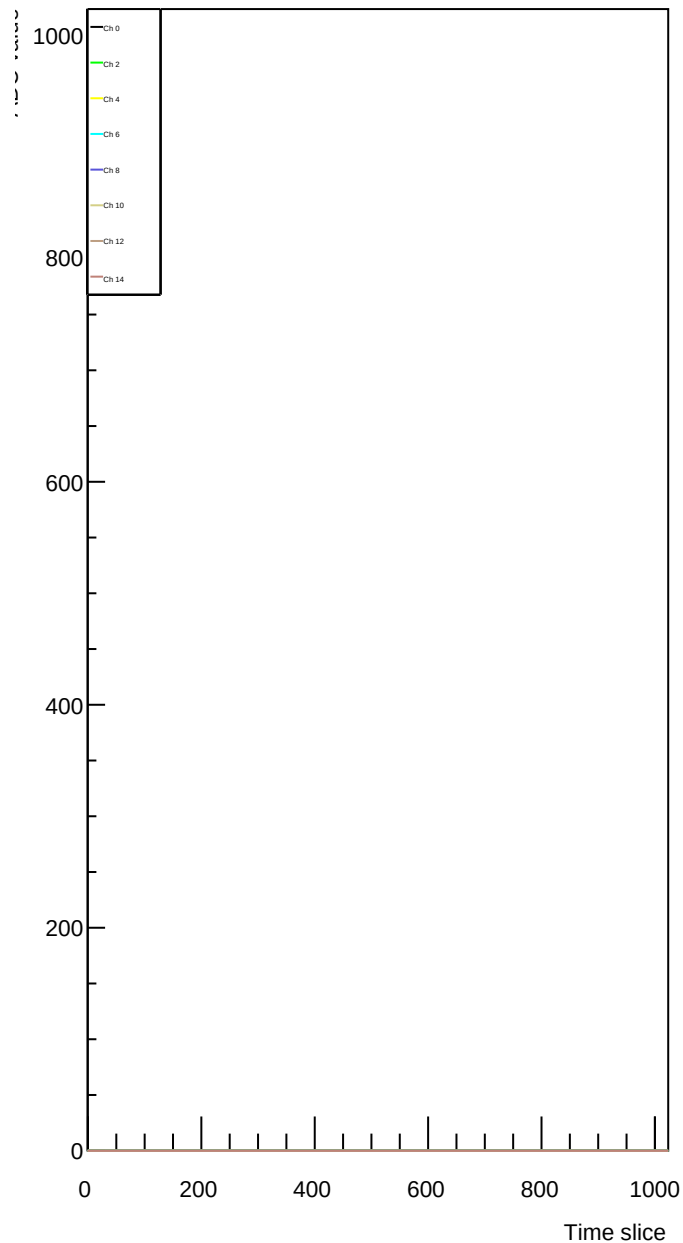
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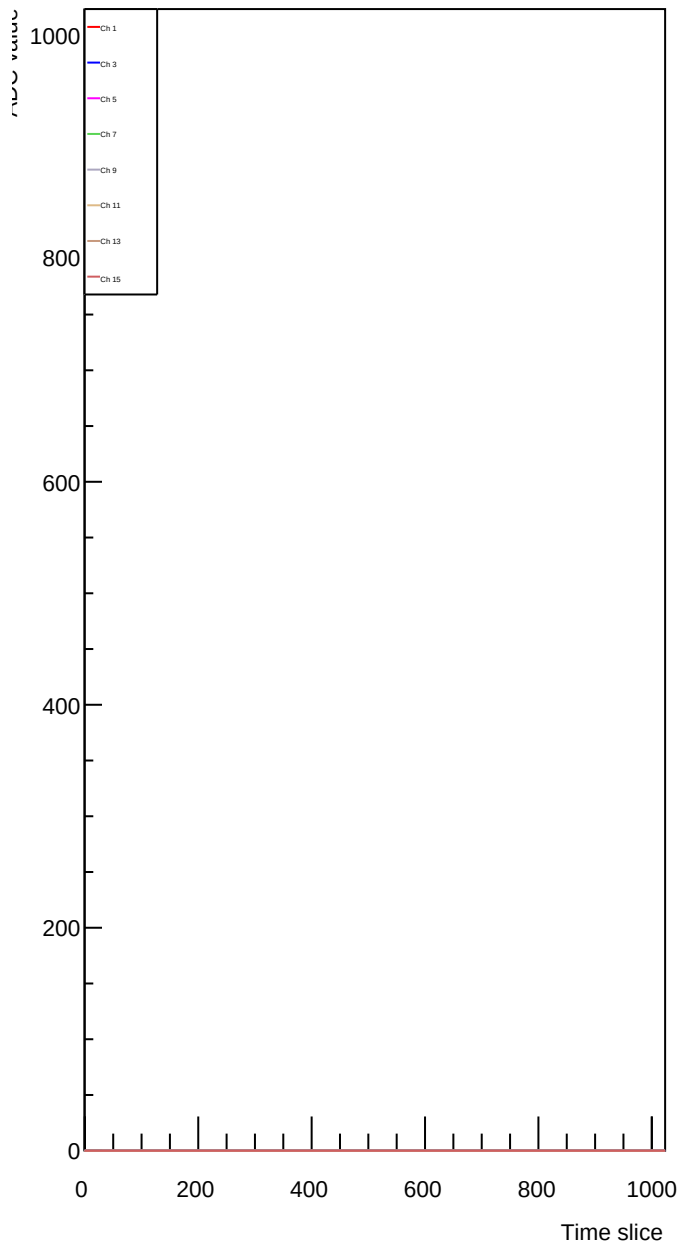
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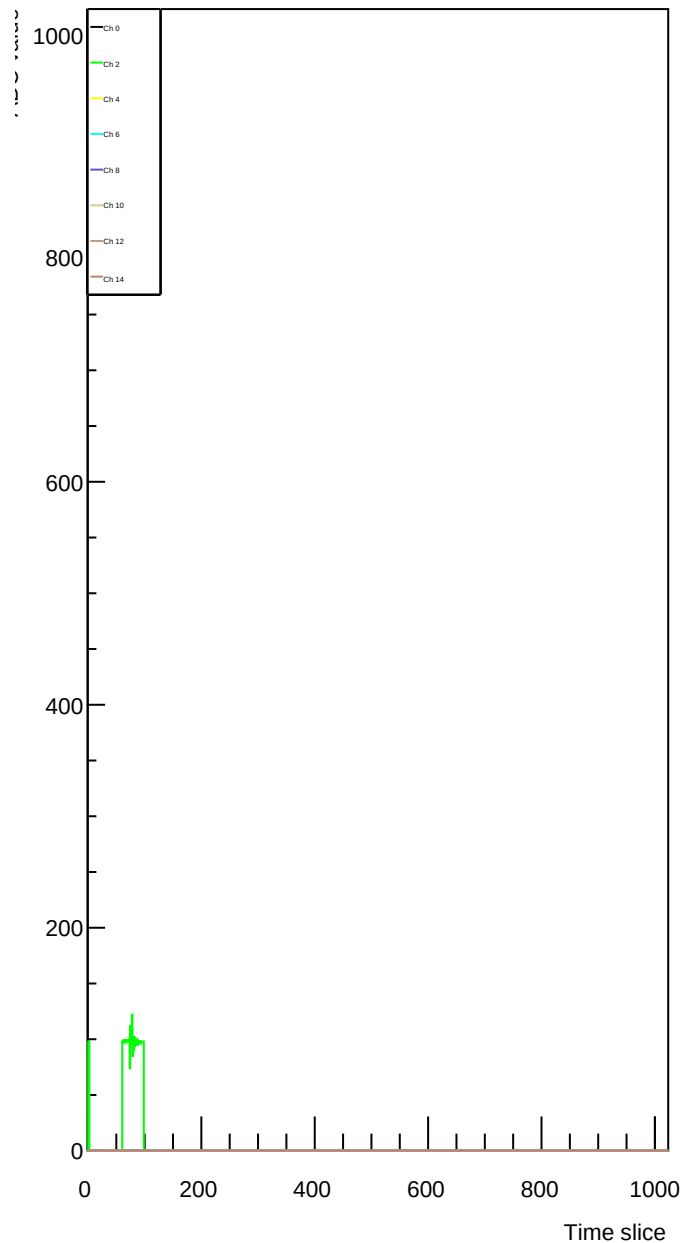
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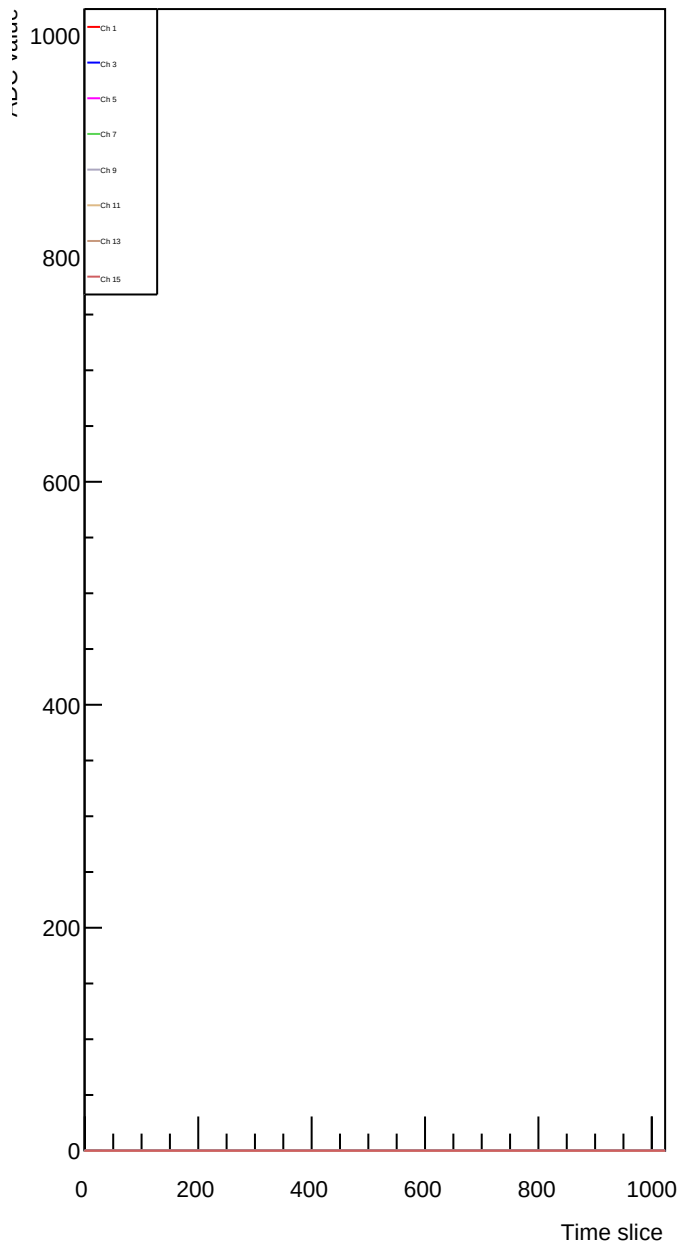
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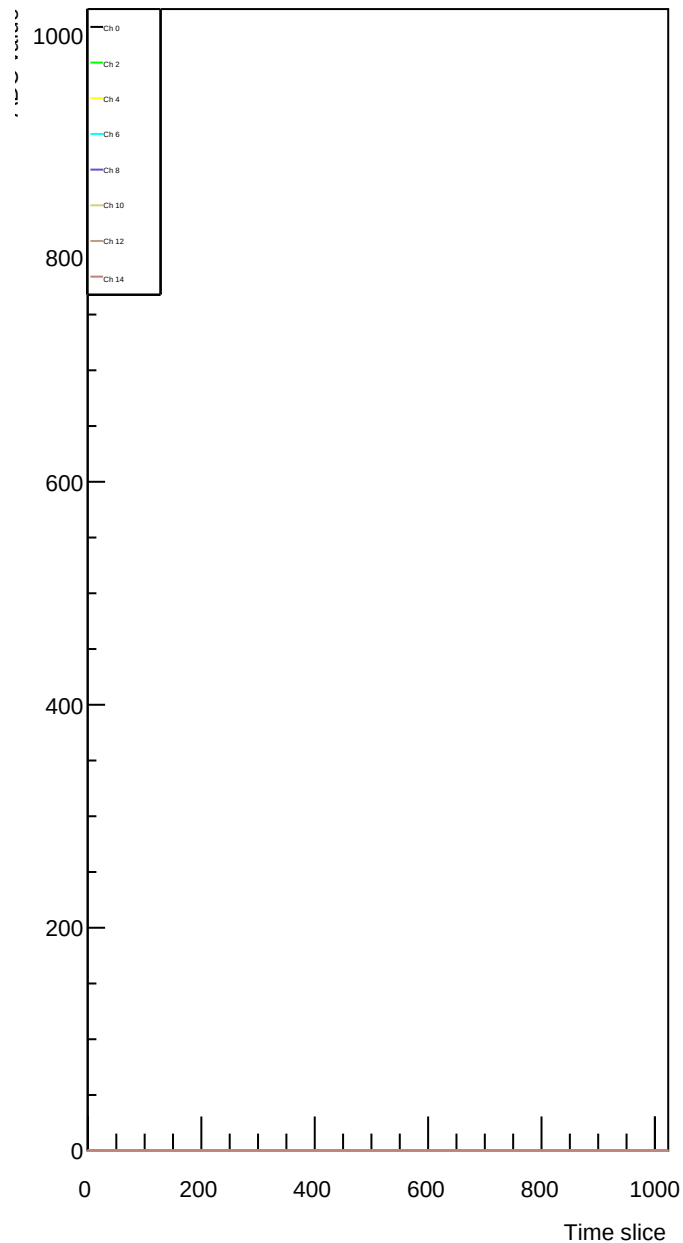
Run 0 event 8 mcm 0 chip 6 s0-clk2 even ch



Run 0 event 8 mcm 0 chip 6 s0-clk2 odd ch



Run 0 event 8 mcm 0 chip 7 s0-clk2 even ch



Run 0 event 8 mcm 0 chip 7 s0-clk2 odd ch

