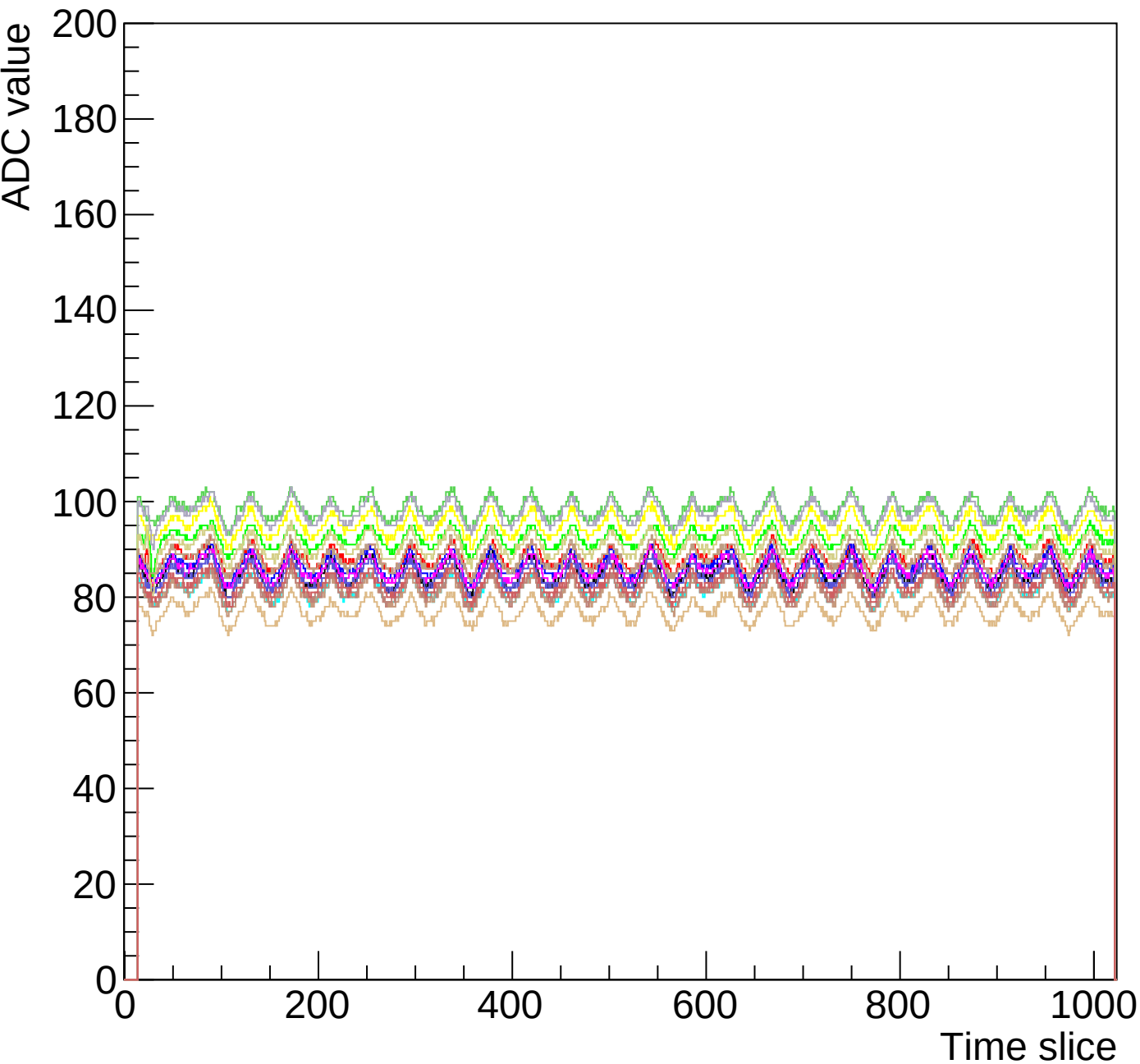
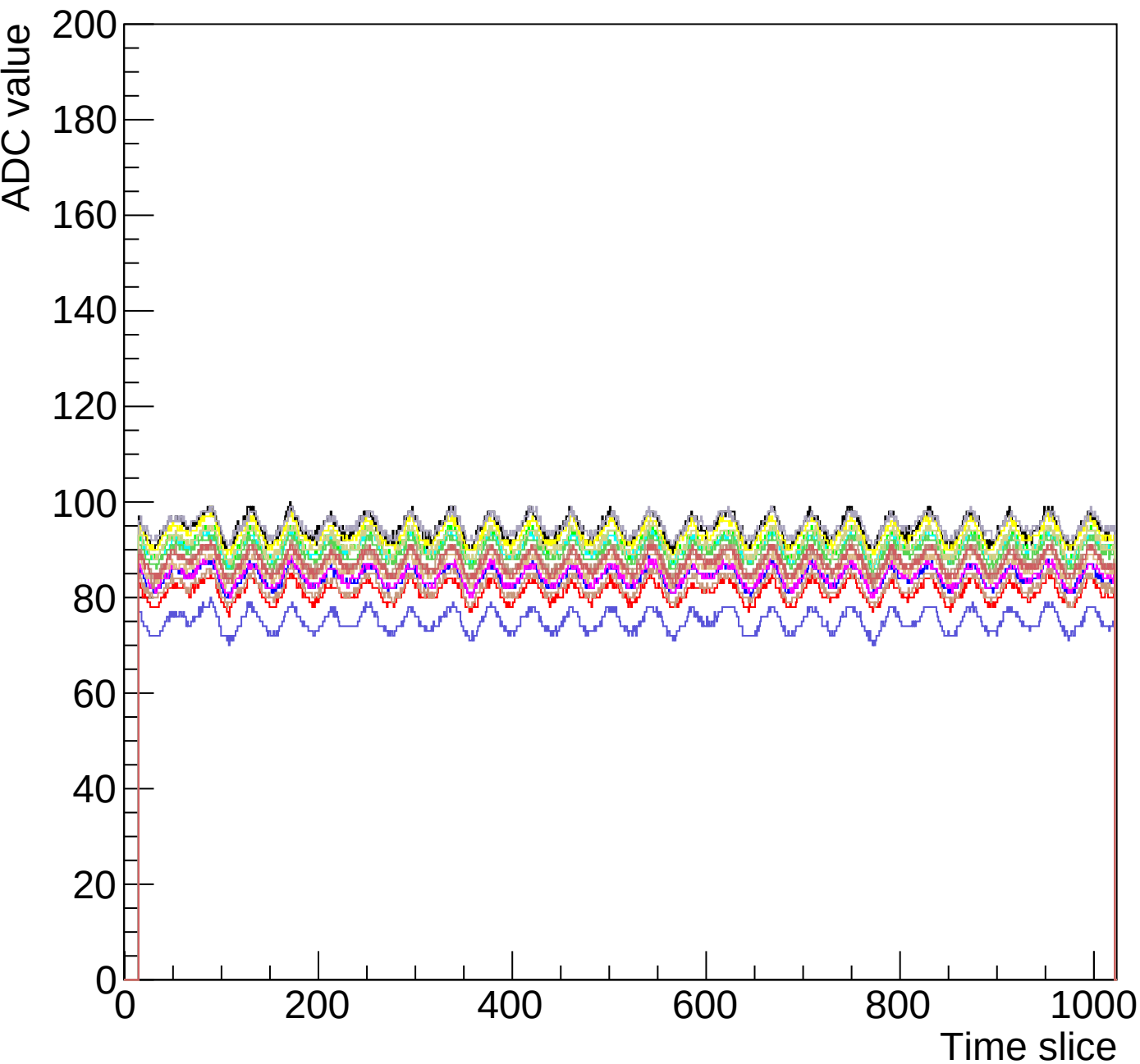


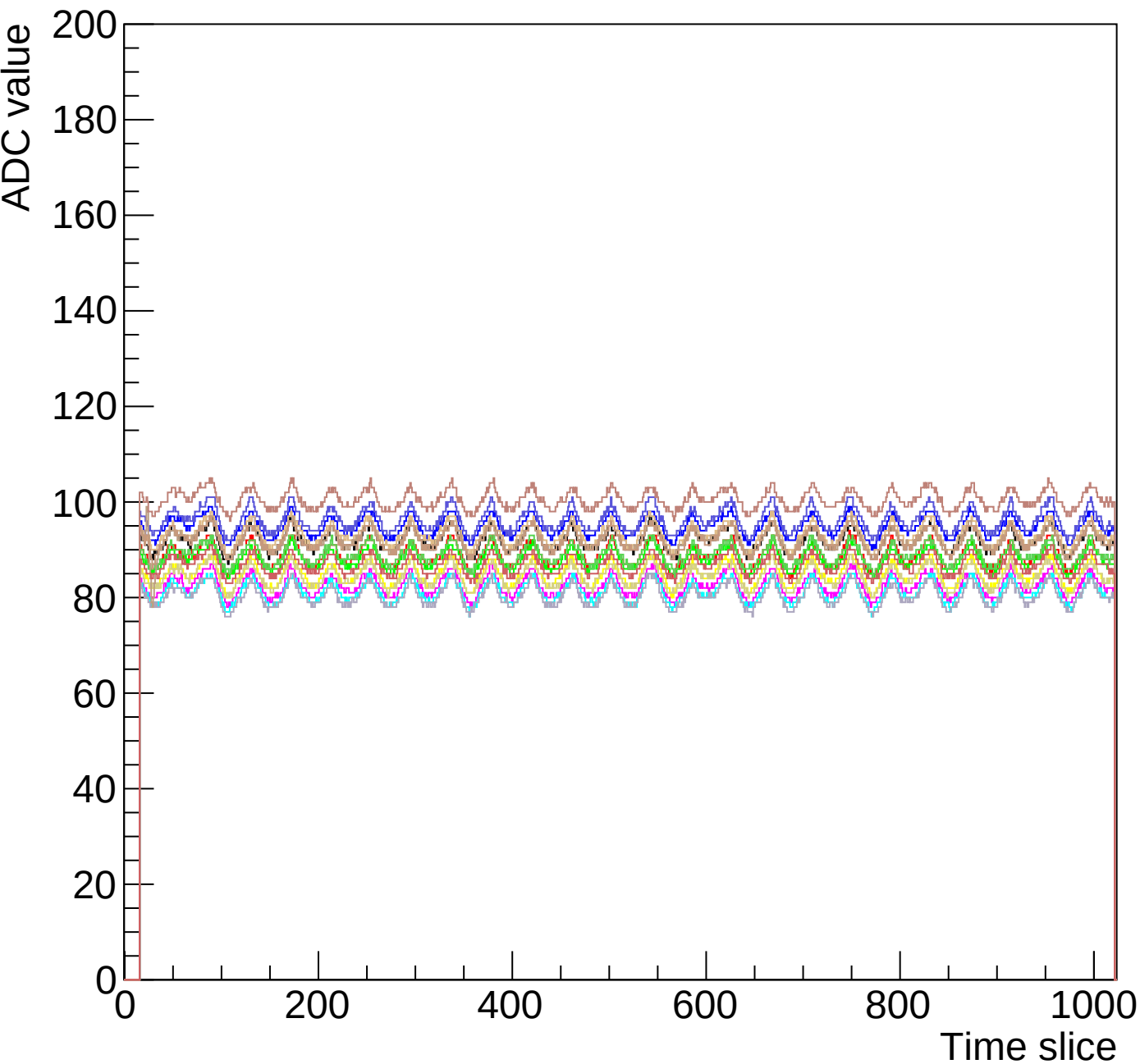
Run 0 event 100 mcm 0 chip 0 debug



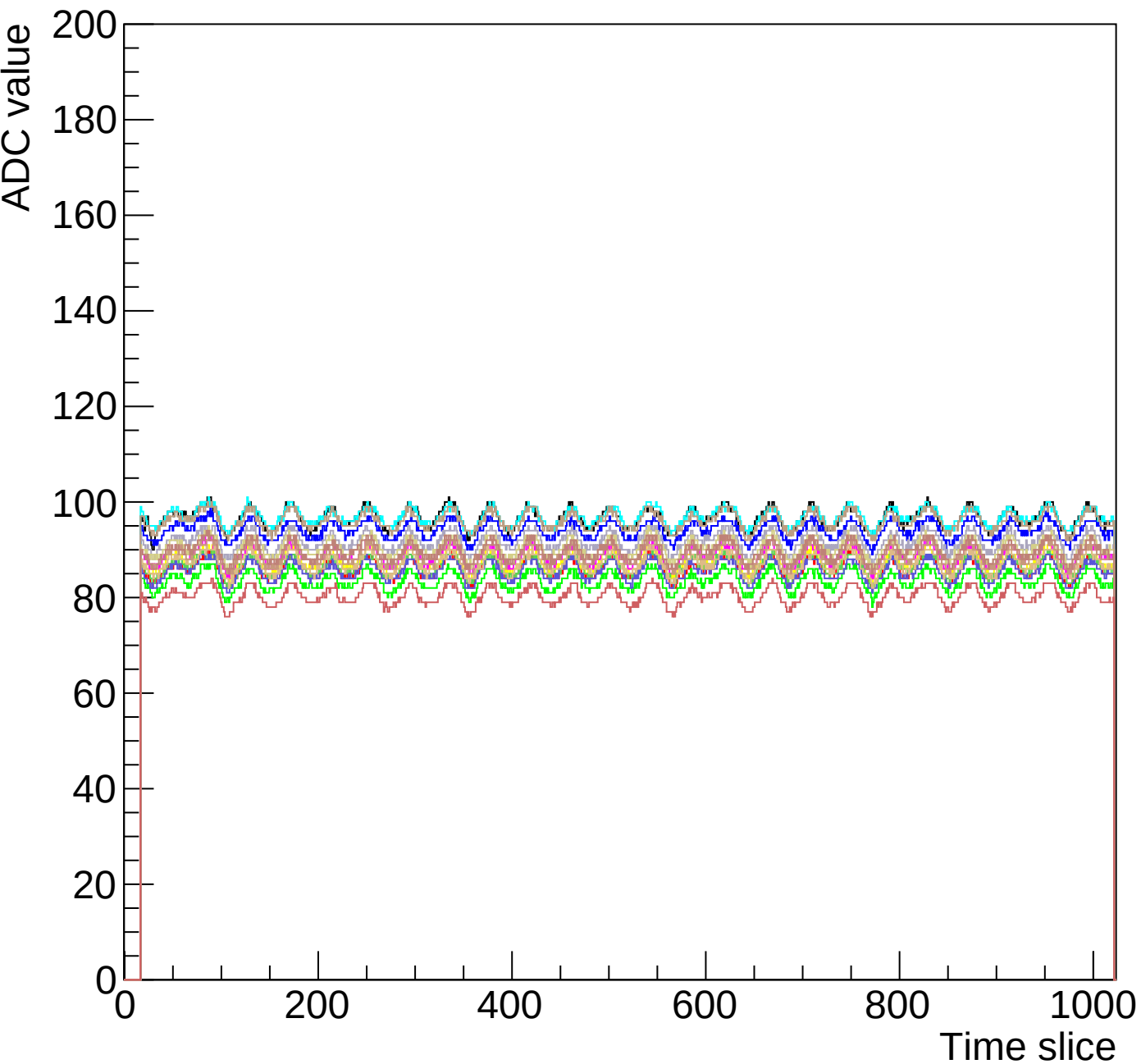
Run 0 event 100 mcm 0 chip 1 debug



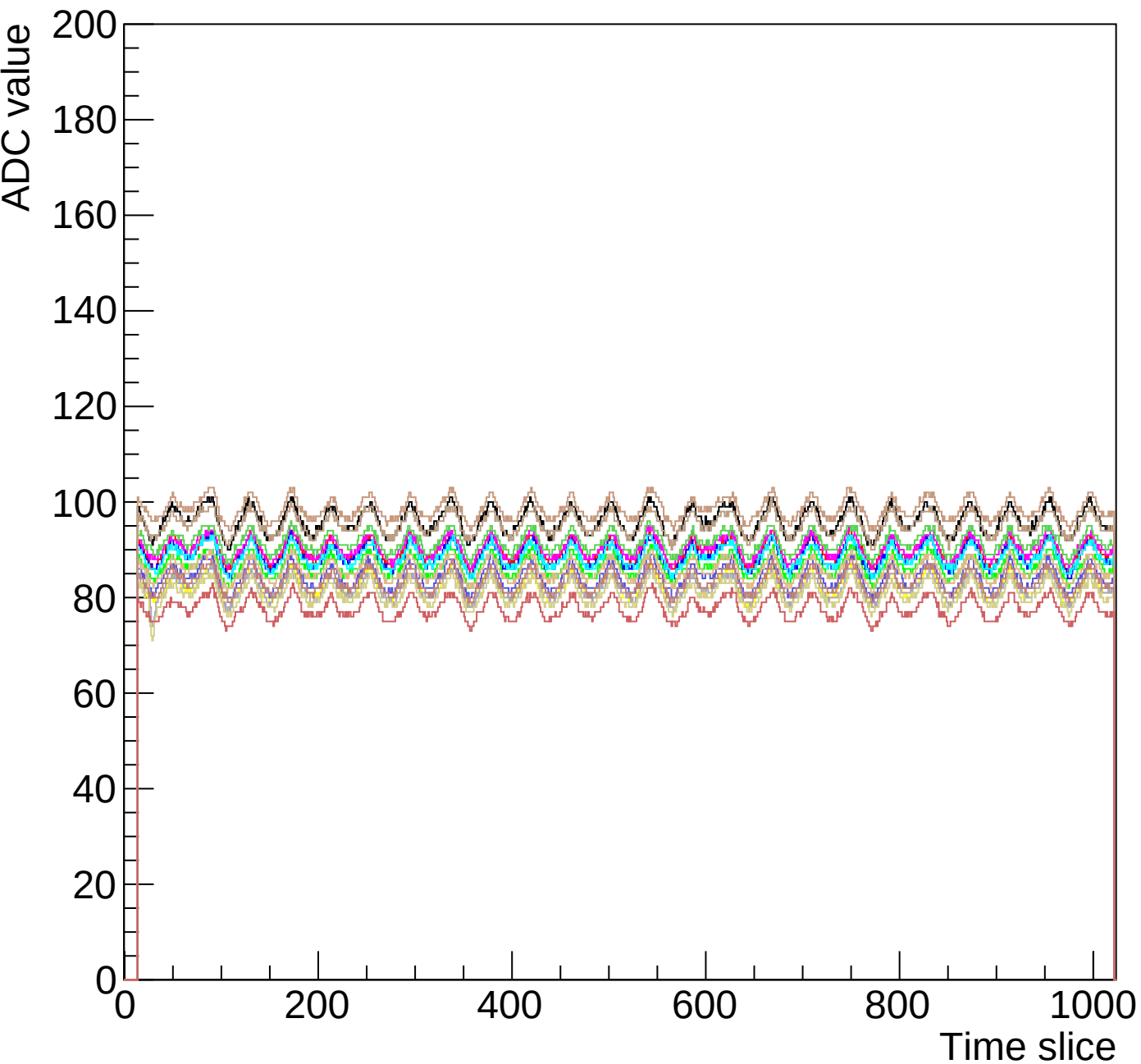
Run 0 event 100 mcm 0 chip 2 debug



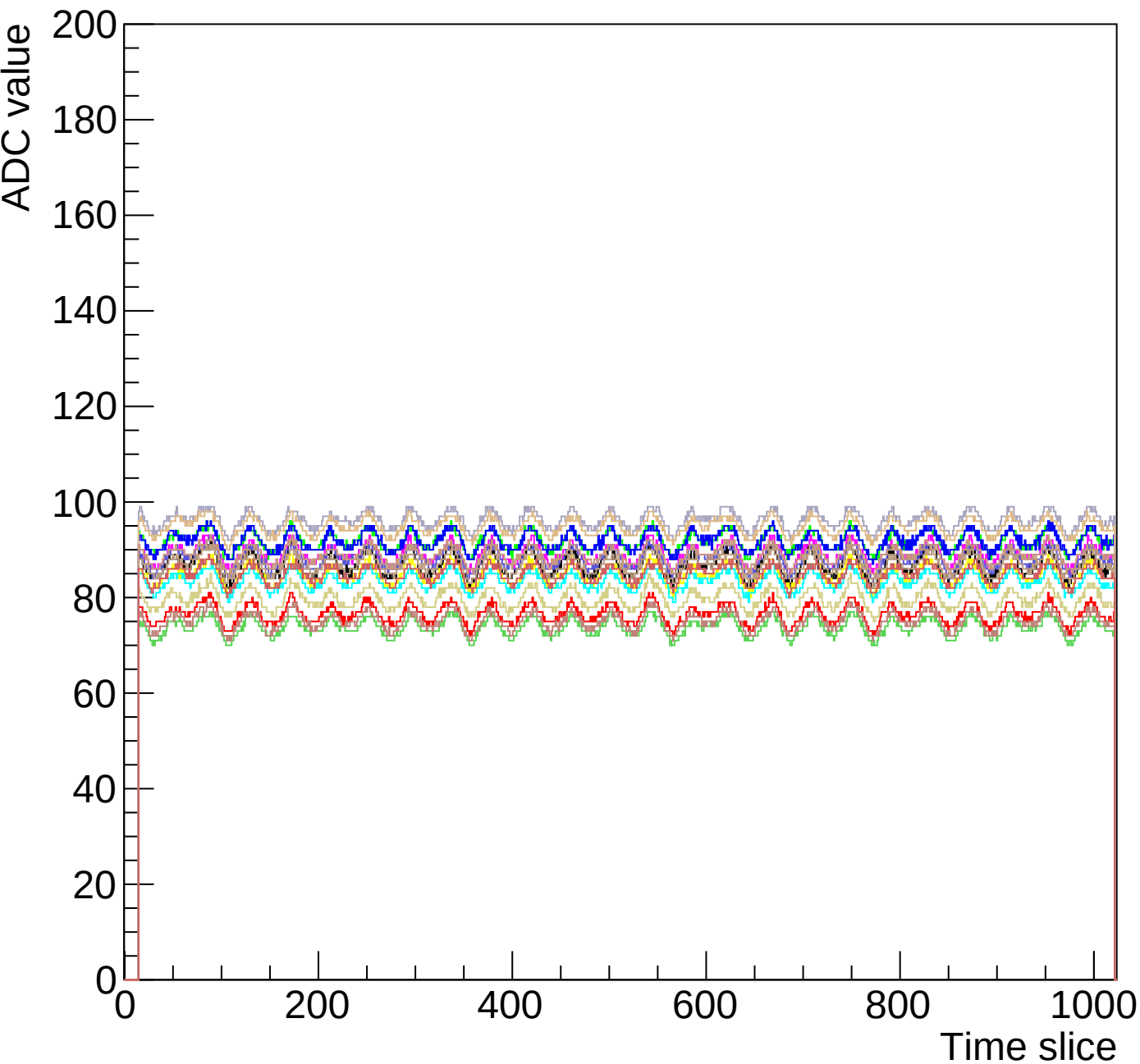
Run 0 event 100 mcm 0 chip 3 debug



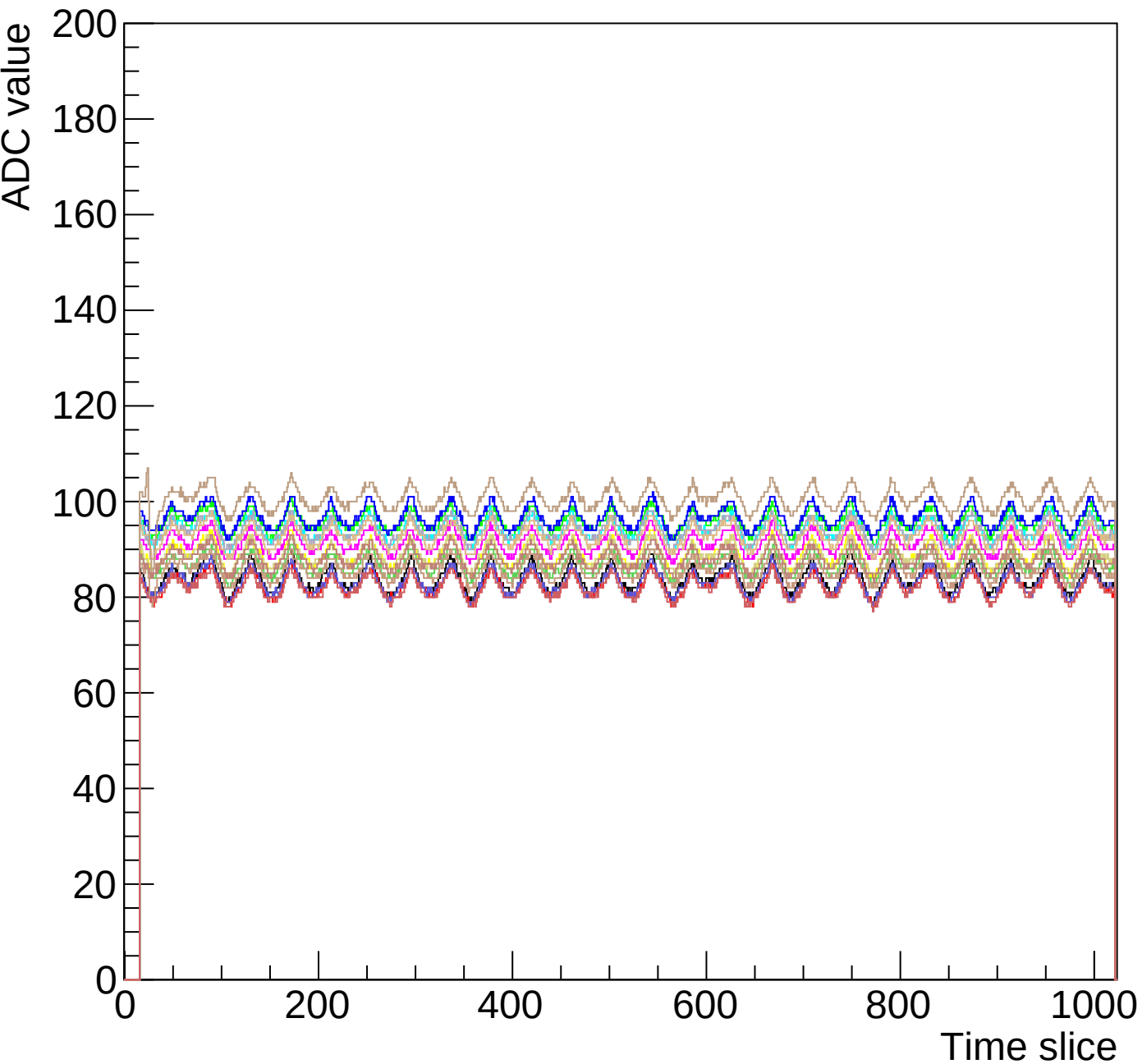
Run 0 event 100 mcm 0 chip 4 debug



Run 0 event 100 mcm 0 chip 5 debug



Run 0 event 100 mcm 0 chip 6 debug



Run 0 event 100 mcm 0 chip 7 debug

